

Towards Certified Slicing

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March 12, 2013

Abstract

Slicing is a widely-used technique with applications in e.g. compiler technology and software security. Thus verification of algorithms in these areas is often based on the correctness of slicing, which should ideally be proven independent of concrete programming languages and with the help of well-known verifying techniques such as proof assistants. As a first step in this direction, this contribution presents a framework for dynamic [2] and static intraprocedural slicing [1] based on control flow and program dependence graphs. Abstracting from concrete syntax we base the framework on a graph representation of the program fulfilling certain structural and well-formedness properties.

We provide two instantiations to show the validity of the framework: a simple While language and the sophisticated object-oriented byte code language from Jinja [3].

0.1 Auxiliary lemmas

theory *AuxLemmas* **imports** *Main* **begin**

abbreviation *arbitrary* == *undefined*

Lemmas about left- and rightmost elements in lists

lemma *leftmost-element-property*:

assumes $\exists x \in \text{set } xs. P x$

obtains $zs\ x'\ ys$ **where** $xs = zs @ x' \# ys$ **and** $P\ x'$ **and** $\forall z \in \text{set } zs. \neg P\ z$
<proof>

lemma *rightmost-element-property*:

assumes $\exists x \in \text{set } xs. P x$

obtains $ys\ x'\ zs$ **where** $xs = ys @ x' \# zs$ **and** $P\ x'$ **and** $\forall z \in \text{set } zs. \neg P\ z$
<proof>

Lemma concerning maps and @

lemma *map-append-append-maps*:
assumes $\text{map}:\text{map } f \text{ } xs = ys @ zs$
obtains $xs' \text{ } xs''$ **where** $\text{map } f \text{ } xs' = ys$ **and** $\text{map } f \text{ } xs'' = zs$ **and** $xs = xs' @ xs''$
 $\langle \text{proof} \rangle$

Lemma concerning splitting of *lists*

lemma *path-split-general*:
assumes $\text{all}:\forall zs. xs \neq ys @ zs$
obtains $j \text{ } zs$ **where** $xs = (\text{take } j \text{ } ys) @ zs$ **and** $j < \text{length } ys$
and $\forall k > j. \forall zs'. xs \neq (\text{take } k \text{ } ys) @ zs'$
 $\langle \text{proof} \rangle$

end

Chapter 1

The Framework

As slicing is a program analysis that can be completely based on the information given in the CFG, we want to provide a framework which allows us to formalize and prove properties of slicing regardless of the actual programming language. So the starting point for the formalization is the definition of an abstract CFG, i.e. without considering features specific for certain languages. By doing so we ensure that our framework is as generic as possible since all proofs hold for every language whose CFG conforms to this abstract CFG. This abstract CFG can be used as a basis for static intraprocedural slicing as well as for dynamic slicing, if in the dynamic case all method calls are inlined (i.e., abstract CFG paths conform to traces).

1.1 Basic Definitions

theory *BasicDefs* **imports** *AuxLemmas* **begin**

1.1.1 Edge kinds

datatype 'state *edge-kind* = Update 'state $\Rightarrow$ 'state $\quad (\uparrow-)$
| Predicate 'state $\Rightarrow$ bool $\quad (('-')_{\sqrt{}})$

1.1.2 Transfer and predicate functions

fun *transfer* :: 'state *edge-kind* $\Rightarrow$ 'state $\Rightarrow$ 'state
where *transfer* ($\uparrow f$) *s* = *f s*
| *transfer* (*P*) $_{\sqrt{}}$ *s* = *s*

fun *transfers* :: 'state *edge-kind list* $\Rightarrow$ 'state $\Rightarrow$ 'state
where *transfers* [] *s* = *s*
| *transfers* (*e#es*) *s* = *transfers es (transfer e s)*

fun *pred* :: 'state *edge-kind* $\Rightarrow$ 'state $\Rightarrow$ bool
where *pred* ($\uparrow f$) *s* = *True*
| *pred* (*P*) $_{\sqrt{}}$ *s* = (*P s*)

fun *preds* :: 'state *edge-kind list* $\Rightarrow$ 'state $\Rightarrow$ bool

where $\text{preds } [] \ s = \text{True}$
 $| \text{preds } (e \# es) \ s = (\text{pred } e \ s \wedge \text{preds } es \ (\text{transfer } e \ s))$

lemma *transfers-split*:
 $(\text{transfers } (ets @ ets') \ s) = (\text{transfers } ets' \ (\text{transfers } ets \ s))$
 $\langle \text{proof} \rangle$

lemma *preds-split*:
 $(\text{preds } (ets @ ets') \ s) = (\text{preds } ets \ s \wedge \text{preds } ets' \ (\text{transfers } ets \ s))$
 $\langle \text{proof} \rangle$

lemma *transfers-id-no-influence*:
 $\text{transfers } [et \leftarrow ets. et \neq \uparrow id] \ s = \text{transfers } ets \ s$
 $\langle \text{proof} \rangle$

lemma *preds-True-no-influence*:
 $\text{preds } [et \leftarrow ets. et \neq (\lambda s. \text{True})_{\surd}] \ s = \text{preds } ets \ s$
 $\langle \text{proof} \rangle$

end

1.2 CFG

theory *CFG* **imports** *BasicDefs* **begin**

1.2.1 The abstract CFG

locale *CFG* =
fixes *sourcenode* :: 'edge $\Rightarrow$ 'node
fixes *targetnode* :: 'edge $\Rightarrow$ 'node
fixes *kind* :: 'edge $\Rightarrow$ 'state edge-kind
fixes *valid-edge* :: 'edge $\Rightarrow$ bool
fixes *Entry*::'node ('('Entry'-'))
assumes *Entry-target* [*dest*]: $\llbracket \text{valid-edge } a; \text{targetnode } a = (-\text{Entry}-) \rrbracket \Longrightarrow \text{False}$
and *edge-det*:
 $\llbracket \text{valid-edge } a; \text{valid-edge } a'; \text{sourcenode } a = \text{sourcenode } a';$
 $\text{targetnode } a = \text{targetnode } a' \rrbracket \Longrightarrow a = a'$

begin

definition *valid-node* :: 'node $\Rightarrow$ bool
where *valid-node* $n \equiv$
 $(\exists a. \text{valid-edge } a \wedge (n = \text{sourcenode } a \vee n = \text{targetnode } a))$

lemma [*simp*]: $\text{valid-edge } a \Longrightarrow \text{valid-node } (\text{sourcenode } a)$

$\langle proof \rangle$

lemma $[simp]$: $valid-edge\ a \implies valid-node\ (targetnode\ a)$
 $\langle proof \rangle$

1.2.2 CFG paths and lemmas

inductive $path :: 'node \Rightarrow 'edge\ list \Rightarrow 'node \Rightarrow bool$
 $(- \dashrightarrow^* - [51,0,0] 80)$

where

$empty-path: valid-node\ n \implies n - [] \rightarrow^* n$

| $Cons-path$:

$\llbracket n'' - as \rightarrow^* n'; valid-edge\ a; sourcenode\ a = n; targetnode\ a = n' \rrbracket$
 $\implies n - a \# as \rightarrow^* n'$

lemma $path-valid-node$:

assumes $n - as \rightarrow^* n'$ **shows** $valid-node\ n$ **and** $valid-node\ n'$
 $\langle proof \rangle$

lemma $empty-path-nodes\ [dest]: n - [] \rightarrow^* n' \implies n = n'$
 $\langle proof \rangle$

lemma $path-valid-edges: n - as \rightarrow^* n' \implies \forall a \in set\ as. valid-edge\ a$
 $\langle proof \rangle$

lemma $path-edge: valid-edge\ a \implies sourcenode\ a - [a] \rightarrow^* targetnode\ a$
 $\langle proof \rangle$

lemma $path-Entry-target\ [dest]$:

assumes $n - as \rightarrow^* (-Entry-)$
shows $n = (-Entry-)$ **and** $as = []$
 $\langle proof \rangle$

lemma $path-Append: \llbracket n - as \rightarrow^* n''; n'' - as' \rightarrow^* n' \rrbracket$
 $\implies n - as @ as' \rightarrow^* n'$
 $\langle proof \rangle$

lemma $path-split$:

assumes $n - as @ a \# as' \rightarrow^* n'$
shows $n - as \rightarrow^* sourcenode\ a$ **and** $valid-edge\ a$ **and** $targetnode\ a - as' \rightarrow^* n'$
 $\langle proof \rangle$

lemma *path-split-Cons*:

assumes $n - as \rightarrow^* n'$ and $as \neq []$
 obtains $a' as'$ where $as = a' \# as'$ and $n = \text{sourcenode } a'$
 and *valid-edge* a' and $\text{targetnode } a' - as' \rightarrow^* n'$
 $\langle \text{proof} \rangle$

lemma *path-split-snoc*:

assumes $n - as \rightarrow^* n'$ and $as \neq []$
 obtains $a' as'$ where $as = as' @ [a]$ and $n - as' \rightarrow^* \text{sourcenode } a'$
 and *valid-edge* a' and $n' = \text{targetnode } a'$
 $\langle \text{proof} \rangle$

lemma *path-split-second*:

assumes $n - as @ a \# as' \rightarrow^* n'$ shows $\text{sourcenode } a - a \# as' \rightarrow^* n'$
 $\langle \text{proof} \rangle$

lemma *path-Entry-Cons*:

assumes $(-Entry-) - as \rightarrow^* n'$ and $n' \neq (-Entry-)$
 obtains $n a$ where $\text{sourcenode } a = (-Entry-)$ and $\text{targetnode } a = n$
 and $n - tl \ as \rightarrow^* n'$ and *valid-edge* a and $a = \text{hd } as$
 $\langle \text{proof} \rangle$

lemma *path-det*:

$\llbracket n - as \rightarrow^* n'; n - as \rightarrow^* n'' \rrbracket \implies n' = n''$
 $\langle \text{proof} \rangle$

definition

sourcenodes :: 'edge list $\Rightarrow$ 'node list
 where *sourcenodes* $xs \equiv \text{map } \text{sourcenode } xs$

definition

kinds :: 'edge list $\Rightarrow$ 'state edge-kind list
 where *kinds* $xs \equiv \text{map } \text{kind } xs$

definition

targetnodes :: 'edge list $\Rightarrow$ 'node list
 where *targetnodes* $xs \equiv \text{map } \text{targetnode } xs$

lemma *path-sourcenode*:

$\llbracket n - as \rightarrow^* n'; as \neq [] \rrbracket \implies \text{hd } (\text{sourcenodes } as) = n$
 $\langle \text{proof} \rangle$

lemma *path-targetnode*:
 $\llbracket n - as \rightarrow^* n'; as \neq [] \rrbracket \implies \text{last } (\text{targetnodes } as) = n'$
 $\langle \text{proof} \rangle$

lemma *sourcenodes-is-n-Cons-butlast-targetnodes*:
 $\llbracket n - as \rightarrow^* n'; as \neq [] \rrbracket \implies$
 $\text{sourcenodes } as = n \# (\text{butlast } (\text{targetnodes } as))$
 $\langle \text{proof} \rangle$

lemma *targetnodes-is-tl-sourcenodes-App-n'*:
 $\llbracket n - as \rightarrow^* n'; as \neq [] \rrbracket \implies$
 $\text{targetnodes } as = (\text{tl } (\text{sourcenodes } as)) @ [n']$
 $\langle \text{proof} \rangle$

lemma *Entry-sourcenode-hd*:
assumes $n - as \rightarrow^* n'$ **and** $(-Entry-) \in \text{set } (\text{sourcenodes } as)$
shows $n = (-Entry-)$ **and** $(-Entry-) \notin \text{set } (\text{sourcenodes } (\text{tl } as))$
 $\langle \text{proof} \rangle$

end

end

theory *CFGExit* **imports** *CFG* **begin**

1.2.3 Adds an exit node to the abstract CFG

locale *CFGExit* = *CFG* *sourcenode* *targetnode* *kind* *valid-edge* *Entry*
for *sourcenode* :: 'edge $\Rightarrow$ 'node **and** *targetnode* :: 'edge $\Rightarrow$ 'node
and *kind* :: 'edge $\Rightarrow$ 'state *edge-kind* **and** *valid-edge* :: 'edge $\Rightarrow$ bool
and *Entry* :: 'node $\Rightarrow$ ('(-Entry'-)) +
fixes *Exit*::'node $\Rightarrow$ ('(-Exit'-))
assumes *Exit-source* [dest]: $\llbracket \text{valid-edge } a; \text{sourcenode } a = (-Exit-) \rrbracket \implies \text{False}$
and *Entry-Exit-edge*: $\exists a. \text{valid-edge } a \wedge \text{sourcenode } a = (-Entry-) \wedge$
 $\text{targetnode } a = (-Exit-) \wedge \text{kind } a = (\lambda s. \text{False})_{\checkmark}$

begin

lemma *Entry-noteq-Exit* [dest]:
assumes $\text{eq}:(-Entry-) = (-Exit-)$ **shows** *False*
 $\langle \text{proof} \rangle$

lemma *Exit-noteq-Entry* [dest]: $(-Exit-) = (-Entry-) \implies \text{False}$

$\langle \text{proof} \rangle$

lemma [simp]: *valid-node* (-Entry-)
 $\langle \text{proof} \rangle$

lemma [simp]: *valid-node* (-Exit-)
 $\langle \text{proof} \rangle$

definition *inner-node* :: 'node $\Rightarrow$ bool
where *inner-node-def*:
inner-node $n \equiv \text{valid-node } n \wedge n \neq (-\text{Entry-}) \wedge n \neq (-\text{Exit-})$

lemma *inner-is-valid*:
inner-node $n \implies \text{valid-node } n$
 $\langle \text{proof} \rangle$

lemma [dest]:
inner-node (-Entry-) $\implies \text{False}$
 $\langle \text{proof} \rangle$

lemma [dest]:
inner-node (-Exit-) $\implies \text{False}$
 $\langle \text{proof} \rangle$

lemma [simp]: $\llbracket \text{valid-edge } a; \text{targetnode } a \neq (-\text{Exit-}) \rrbracket$
 $\implies \text{inner-node } (\text{targetnode } a)$
 $\langle \text{proof} \rangle$

lemma [simp]: $\llbracket \text{valid-edge } a; \text{sourcenode } a \neq (-\text{Entry-}) \rrbracket$
 $\implies \text{inner-node } (\text{sourcenode } a)$
 $\langle \text{proof} \rangle$

lemma *valid-node-cases* [consumes 1, case-names Entry Exit inner]:
 $\llbracket \text{valid-node } n; n = (-\text{Entry-}) \implies Q; n = (-\text{Exit-}) \implies Q; \text{inner-node } n \implies Q \rrbracket \implies Q$
 $\langle \text{proof} \rangle$

lemma *path-Exit-source* [dest]:
assumes (-Exit-) $-as \rightarrow^* n'$ **shows** $n' = (-\text{Exit-})$ **and** $as = []$
 $\langle \text{proof} \rangle$

lemma *Exit-no-sourcenode* [dest]:
assumes *isin*:(-Exit-) $\in \text{set } (\text{sourcenodes } as)$ **and** *path*: $n -as \rightarrow^* n'$
shows *False*
 $\langle \text{proof} \rangle$

end

end

1.3 Postdomination

theory *Postdomination* **imports** *CFGExit* **begin**

1.3.1 Standard Postdomination

locale *Postdomination* = *CFGExit* *sourcenode* *targetnode* *kind* *valid-edge* *Entry* *Exit*

for *sourcenode* :: 'edge $\Rightarrow$ 'node **and** *targetnode* :: 'edge $\Rightarrow$ 'node
and *kind* :: 'edge $\Rightarrow$ 'state edge-kind **and** *valid-edge* :: 'edge $\Rightarrow$ bool
and *Entry* :: 'node ('(-Entry'-)) **and** *Exit* :: 'node ('(-Exit'-)) +
assumes *Entry-path:valid-node* $n \implies \exists as. (-Entry-) -as \rightarrow^* n$
and *Exit-path:valid-node* $n \implies \exists as. n -as \rightarrow^* (-Exit-)$

begin

definition *postdominate* :: 'node $\Rightarrow$ 'node $\Rightarrow$ bool (- *postdominates* - [51,0])

where *postdominate-def*: n' *postdominates* $n \equiv$
(*valid-node* $n \wedge$ *valid-node* $n' \wedge$
($(\forall as. n -as \rightarrow^* (-Exit-) \longrightarrow n' \in \text{set } (\text{sourcenodes } as)))$)

lemma *postdominate-implies-path*:

assumes n' *postdominates* n **obtains** *as* **where** $n -as \rightarrow^* n'$
(*proof*)

lemma *postdominate-refl*:

assumes *valid:valid-node* n **and** *notExit*: $n \neq (-Exit-)$
shows n *postdominates* n
(*proof*)

lemma *postdominate-trans*:

assumes $pd1:n'$ *postdominates* n **and** $pd2:n'$ *postdominates* n''
shows n' *postdominates* n
(*proof*)

lemma *postdominate-antisym*:

assumes $pd1:n'$ *postdominates* n **and** $pd2:n$ *postdominates* n'
shows $n = n'$

$\langle proof \rangle$

lemma *postdominate-path-branch*:

assumes $n - as \rightarrow^* n''$ **and** n' *postdominates* n'' **and** $\neg n'$ *postdominates* n
obtains a as' as'' **where** $as = as' @ a \# as''$ **and** *valid-edge* a
and $\neg n'$ *postdominates* (*sourcenode* a) **and** n' *postdominates* (*targetnode* a)
 $\langle proof \rangle$

lemma *Exit-no-postdominator*:

(*-Exit*-) *postdominates* $n \implies False$
 $\langle proof \rangle$

lemma *postdominate-path-targetnode*:

assumes n' *postdominates* n **and** $n - as \rightarrow^* n''$ **and** $n' \notin \text{set}(\text{sourcenodes } as)$
shows n' *postdominates* n''
 $\langle proof \rangle$

lemma *not-postdominate-source-not-postdominate-target*:

assumes $\neg n$ *postdominates* (*sourcenode* a) **and** *valid-node* n **and** *valid-edge* a
obtains ax **where** *sourcenode* $a = \text{sourcenode } ax$ **and** *valid-edge* ax
and $\neg n$ *postdominates* *targetnode* ax
 $\langle proof \rangle$

lemma *inner-node-Entry-edge*:

assumes *inner-node* n
obtains a **where** *valid-edge* a **and** *inner-node* (*targetnode* a)
and *sourcenode* $a = (-\text{Entry}-)$
 $\langle proof \rangle$

lemma *inner-node-Exit-edge*:

assumes *inner-node* n
obtains a **where** *valid-edge* a **and** *inner-node* (*sourcenode* a)
and *targetnode* $a = (-\text{Exit}-)$
 $\langle proof \rangle$

end

1.3.2 Strong Postdomination

locale *StrongPostdomination* =

Postdomination sourcenode targetnode kind valid-edge Entry Exit
for sourcenode :: 'edge $\Rightarrow$ 'node **and** targetnode :: 'edge $\Rightarrow$ 'node
and kind :: 'edge $\Rightarrow$ 'state edge-kind **and** valid-edge :: 'edge $\Rightarrow$ bool
and Entry :: 'node (('(-Entry'-')) **and** Exit :: 'node (('(-Exit'-')) +
assumes successor-set-finite: valid-node $n \implies$
 finite $\{n'. \exists a'. \text{valid-edge } a' \wedge \text{sourcenode } a' = n \wedge \text{targetnode } a' = n'\}$

begin

definition strong-postdominate :: 'node $\Rightarrow$ 'node $\Rightarrow$ bool
 (- strongly-postdominates - [51,0])
where strong-postdominate-def: $n' \text{ strongly-postdominates } n \equiv$
 ($n' \text{ postdominates } n \wedge$
 $(\exists k \geq 1. \forall as \ nx. n -as \rightarrow^* nx \wedge$
 $\text{length } as \geq k \longrightarrow n' \in \text{set}(\text{sourcenodes } as)))$

lemma strong-postdominate-prop-smaller-path:
assumes all: $\forall as \ nx. n -as \rightarrow^* nx \wedge \text{length } as \geq k \longrightarrow n' \in \text{set}(\text{sourcenodes } as)$
and $n -as \rightarrow^* n''$ **and** $\text{length } as \geq k$
obtains $as' \ as''$ **where** $n -as' \rightarrow^* n'$ **and** $\text{length } as' < k$ **and** $n' -as'' \rightarrow^* n''$
and $as = as' @ as''$
 <proof>

lemma strong-postdominate-refl:
assumes valid-node n **and** $n \neq (-Exit-)$
shows $n \text{ strongly-postdominates } n$
 <proof>

lemma strong-postdominate-trans:
assumes $n'' \text{ strongly-postdominates } n$ **and** $n' \text{ strongly-postdominates } n''$
shows $n' \text{ strongly-postdominates } n$
 <proof>

lemma strong-postdominate-antisym:
 $\llbracket n' \text{ strongly-postdominates } n; n \text{ strongly-postdominates } n' \rrbracket \implies n = n'$
 <proof>

lemma strong-postdominate-path-branch:
assumes $n -as \rightarrow^* n''$ **and** $n' \text{ strongly-postdominates } n''$
and $\neg n' \text{ strongly-postdominates } n$
obtains $a \ as' \ as''$ **where** $as = as' @ a \# as''$ **and** valid-edge a
and $\neg n' \text{ strongly-postdominates } (\text{sourcenode } a)$
and $n' \text{ strongly-postdominates } (\text{targetnode } a)$

<proof>

lemma *Exit-no-strongly-postdominator:*

$\llbracket (-Exit-) \text{ strongly-postdominates } n; n -as \rightarrow^* (-Exit-) \rrbracket \implies \text{False}$
<proof>

lemma *strong-postdominate-path-targetnode:*

assumes $n' \text{ strongly-postdominates } n$ **and** $n -as \rightarrow^* n''$
and $n' \notin \text{set}(\text{sourcenodes } as)$
shows $n' \text{ strongly-postdominates } n''$
<proof>

lemma *not-strong-postdominate-successor-set:*

assumes $\neg n \text{ strongly-postdominates } (\text{sourcenode } a)$ **and** *valid-node* n
and *valid-edge* a
and $\text{all}:\forall nx \in N. \exists a'. \text{valid-edge } a' \wedge \text{sourcenode } a' = \text{sourcenode } a \wedge$
 $\text{targetnode } a' = nx \wedge n \text{ strongly-postdominates } nx$
obtains a' **where** *valid-edge* a' **and** $\text{sourcenode } a' = \text{sourcenode } a$
and $\text{targetnode } a' \notin N$
<proof>

lemma *not-strong-postdominate-predecessor-successor:*

assumes $\neg n \text{ strongly-postdominates } (\text{sourcenode } a)$
and *valid-node* n **and** *valid-edge* a
obtains a' **where** *valid-edge* a' **and** $\text{sourcenode } a' = \text{sourcenode } a$
and $\neg n \text{ strongly-postdominates } (\text{targetnode } a')$
<proof>

end

end

1.4 CFG well-formedness

theory *CFG-wf* **imports** *CFG begin*

1.4.1 Well-formedness of the abstract CFG

locale *CFG-wf* = *CFG* *sourcenode targetnode kind valid-edge Entry*
for *sourcenode* :: 'edge $\Rightarrow$ 'node **and** *targetnode* :: 'edge $\Rightarrow$ 'node
and *kind* :: 'edge $\Rightarrow$ 'state edge-kind **and** *valid-edge* :: 'edge $\Rightarrow$ bool

```

and Entry :: 'node ('(-Entry'-)) +
fixes Def::'node  $\Rightarrow$  'var set
fixes Use::'node  $\Rightarrow$  'var set
fixes state-val::'state  $\Rightarrow$  'var  $\Rightarrow$  'val
assumes Entry-empty:Def (-Entry-) = {}  $\wedge$  Use (-Entry-) = {}
and CFG-edge-no-Def-equal:
   $\llbracket \text{valid-edge } a; V \notin \text{Def } (\text{sourcenode } a); \text{pred } (\text{kind } a) \ s \rrbracket$ 
   $\implies \text{state-val } (\text{transfer } (\text{kind } a) \ s) \ V = \text{state-val } s \ V$ 
and CFG-edge-transfer-uses-only-Use:
   $\llbracket \text{valid-edge } a; \forall V \in \text{Use } (\text{sourcenode } a). \text{state-val } s \ V = \text{state-val } s' \ V;$ 
   $\text{pred } (\text{kind } a) \ s; \text{pred } (\text{kind } a) \ s' \rrbracket$ 
   $\implies \forall V \in \text{Def } (\text{sourcenode } a). \text{state-val } (\text{transfer } (\text{kind } a) \ s) \ V =$ 
   $\text{state-val } (\text{transfer } (\text{kind } a) \ s') \ V$ 
and CFG-edge-Uses-pred-equal:
   $\llbracket \text{valid-edge } a; \text{pred } (\text{kind } a) \ s;$ 
   $\forall V \in \text{Use } (\text{sourcenode } a). \text{state-val } s \ V = \text{state-val } s' \ V \rrbracket$ 
   $\implies \text{pred } (\text{kind } a) \ s'$ 
and deterministic: $\llbracket \text{valid-edge } a; \text{valid-edge } a'; \text{sourcenode } a = \text{sourcenode } a';$ 
   $\text{targetnode } a \neq \text{targetnode } a' \rrbracket$ 
   $\implies \exists Q \ Q'. \text{kind } a = (Q)_{\vee} \wedge \text{kind } a' = (Q')_{\vee} \wedge$ 
   $(\forall s. (Q \ s \longrightarrow \neg Q' \ s) \wedge (Q' \ s \longrightarrow \neg Q \ s))$ 

begin

lemma [dest!]:  $V \in \text{Use } (-\text{Entry-}) \implies \text{False}$ 
  <proof>

lemma [dest!]:  $V \in \text{Def } (-\text{Entry-}) \implies \text{False}$ 
  <proof>

lemma CFG-path-no-Def-equal:
   $\llbracket n -as \rightarrow^* n'; \forall n \in \text{set } (\text{sourcenodes } as). V \notin \text{Def } n; \text{preds } (\text{kinds } as) \ s \rrbracket$ 
   $\implies \text{state-val } (\text{transfers } (\text{kinds } as) \ s) \ V = \text{state-val } s \ V$ 
  <proof>

end

end

theory CFGEExit-wf imports CFGEExit CFG-wf begin

1.4.2 New well-formedness lemmas using (-Exit-)

locale CFGEExit-wf =
  CFG-wf sourcenode targetnode kind valid-edge Entry Def Use state-val +
  CFGEExit sourcenode targetnode kind valid-edge Entry Exit
  for sourcenode :: 'edge  $\Rightarrow$  'node and targetnode :: 'edge  $\Rightarrow$  'node

```

```

and kind :: 'edge  $\Rightarrow$  'state edge-kind and valid-edge :: 'edge  $\Rightarrow$  bool
and Entry :: 'node (('(-Entry'-)) and Def :: 'node  $\Rightarrow$  'var set
and Use :: 'node  $\Rightarrow$  'var set and state-val :: 'state  $\Rightarrow$  'var  $\Rightarrow$  'val
and Exit :: 'node (('(-Exit'-)) +
assumes Exit-empty:Def (-Exit-) = {}  $\wedge$  Use (-Exit-) = {}

```

begin

```

lemma Exit-Use-empty [dest!]:  $V \in Use (-Exit-) \Rightarrow False$ 
<proof>

```

```

lemma Exit-Def-empty [dest!]:  $V \in Def (-Exit-) \Rightarrow False$ 
<proof>

```

end

end

1.5 CFG and semantics conform

theory *SemanticsCFG* **imports** *CFG* **begin**

```

locale CFG-semantics-wf = CFG sourcenode targetnode kind valid-edge Entry
  for sourcenode :: 'edge  $\Rightarrow$  'node and targetnode :: 'edge  $\Rightarrow$  'node
  and kind :: 'edge  $\Rightarrow$  'state edge-kind and valid-edge :: 'edge  $\Rightarrow$  bool
  and Entry :: 'node (('(-Entry'-)) +
  fixes sem::'com  $\Rightarrow$  'state  $\Rightarrow$  'com  $\Rightarrow$  'state  $\Rightarrow$  bool
    (((1<-,->)  $\Rightarrow$  / (1<-,->))) [0,0,0,0] 81)
  fixes identifies::'node  $\Rightarrow$  'com  $\Rightarrow$  bool (-  $\triangleq$  - [51,0] 80)
  assumes fundamental-property:
     $\llbracket n \triangleq c; \langle c, s \rangle \Rightarrow \langle c', s' \rangle \rrbracket \Rightarrow$ 
       $\exists n' as. n -as \rightarrow^* n' \wedge \text{transfers } (kinds \ as) \ s = s' \wedge \text{preds } (kinds \ as) \ s \wedge$ 
       $n' \triangleq c'$ 

```

end

1.6 Dynamic data dependence

theory *DynDataDependence* **imports** *CFG-wf* **begin**

context *CFG-wf* **begin**

```

definition dyn-data-dependence ::
  'node  $\Rightarrow$  'var  $\Rightarrow$  'node  $\Rightarrow$  'edge list  $\Rightarrow$  bool (- influences - in - via - [51,0,0])
where n influences V in n' via as  $\equiv$ 
   $((V \in Def \ n) \wedge (V \in Use \ n') \wedge (n -as \rightarrow^* n') \wedge$ 

```

$$(\exists a' as'. (as = a' \# as') \wedge (\forall n'' \in \text{set}(\text{sourcenodes } as'). V \notin \text{Def } n''))$$

lemma *dyn-influence-Cons-source:*

n influences V in n' via a#as $\implies$ sourcenode a = n

<proof>

lemma *dyn-influence-source-notin-tl-edges:*

assumes *n influences V in n' via a#as*

shows *n $\notin$ set (sourcenodes as)*

<proof>

lemma *dyn-influence-only-first-edge:*

assumes *n influences V in n' via a#as* **and** *preds (kinds (a#as)) s*

shows *state-val (transfers (kinds (a#as)) s) V =*

state-val (transfer (kind a) s) V

<proof>

end

end

1.7 Dynamic Standard Control Dependence

theory *DynStandardControlDependence* **imports** *Postdomination* **begin**

context *Postdomination* **begin**

definition

dyn-standard-control-dependence :: 'node $\Rightarrow$ 'node $\Rightarrow$ 'edge list $\Rightarrow$ bool
(- controls_s - via - [51,0,0])

where *dyn-standard-control-dependence-def:n controls_s n' via as $\equiv$*

($\exists a a' as'. (as = a' \# as') \wedge (n' \notin \text{set}(\text{sourcenodes } as)) \wedge (n -as \rightarrow^ n') \wedge$*
(n' postdominates (targetnode a)) $\wedge$
(valid-edge a') $\wedge$ (sourcenode a' = n) $\wedge$
($\neg$ n' postdominates (targetnode a')))

lemma *Exit-not-dyn-standard-control-dependent:*

assumes *control:n controls_s (-Exit-) via as* **shows** *False*

<proof>

lemma *dyn-standard-control-dependence-def-variant:*

n controls_s n' via as = ((n -as $\rightarrow^$ n') $\wedge$ (n $\neq$ n') $\wedge$*

($\neg$ n' postdominates n) $\wedge$ (n' $\notin$ set(sourcenodes as)) $\wedge$

$(\forall n'' \in \text{set}(\text{targetnodes } as). n' \text{ postdominates } n'')$
 $\langle \text{proof} \rangle$

lemma *which-node-dyn-standard-control-dependence-source:*

assumes *path*: $(-\text{Entry}-) -as@a\#as' \rightarrow^* n$
and *Exit-path*: $n -as'' \rightarrow^* (-\text{Exit}-)$ **and** *source*:*sourcenode* $a = n'$
and *source'*:*sourcenode* $a' = n'$
and *no-source*: $n \notin \text{set}(\text{sourcenodes } (a\#as'))$ **and** *valid-edge'*:*valid-edge* a'
and *inner-node*:*inner-node* n **and** *not-pd*: $\neg n \text{ postdominates } (\text{targetnode } a')$
and *last*: $\forall ax \ ax'. ax \in \text{set } as' \wedge \text{sourcenode } ax = \text{sourcenode } ax' \wedge$
 $\text{valid-edge } ax' \longrightarrow n \text{ postdominates } \text{targetnode } ax'$
shows $n' \text{ controls}_s n \text{ via } a\#as'$
 $\langle \text{proof} \rangle$

lemma *inner-node-dyn-standard-control-dependence-predecessor:*

assumes *inner-node*:*inner-node* n
obtains n' *as* **where** $n' \text{ controls}_s n \text{ via } as$
 $\langle \text{proof} \rangle$

end

end

1.8 Dynamic Weak Control Dependence

theory *DynWeakControlDependence* **imports** *Postdomination* **begin**

context *StrongPostdomination* **begin**

definition

dyn-weak-control-dependence :: $'node \Rightarrow 'node \Rightarrow 'edge \text{ list} \Rightarrow \text{bool}$
 $(- \text{ weakly controls } - \text{ via } - [51, 0, 0])$
where *dyn-weak-control-dependence-def*: $n \text{ weakly controls } n' \text{ via } as \equiv$
 $(\exists a \ a' \ as'. (as = a\#as') \wedge (n' \notin \text{set}(\text{sourcenodes } as)) \wedge (n -as \rightarrow^* n') \wedge$
 $(n' \text{ strongly-postdominates } (\text{targetnode } a)) \wedge$
 $(\text{valid-edge } a') \wedge (\text{sourcenode } a' = n) \wedge$
 $(\neg n' \text{ strongly-postdominates } (\text{targetnode } a'))))$

lemma *Exit-not-dyn-weak-control-dependent:*

assumes *control*: $n \text{ weakly controls } (-\text{Exit}-) \text{ via } as$ **shows** *False*
 $\langle \text{proof} \rangle$

end

end

Chapter 2

Dynamic Slicing

2.1 Dynamic Program Dependence Graph

```
theory DynPDG imports
  ../Basic/DynDataDependence
  ../Basic/CFGExit-wf
  ../Basic/DynStandardControlDependence
  ../Basic/DynWeakControlDependence
begin
```

2.1.1 The dynamic PDG

```
locale DynPDG =
  CFGExit-wf sourcenode targetnode kind valid-edge Entry Def Use state-val Exit
  for sourcenode :: 'edge  $\Rightarrow$  'node and targetnode :: 'edge  $\Rightarrow$  'node
  and kind :: 'edge  $\Rightarrow$  'state edge-kind and valid-edge :: 'edge  $\Rightarrow$  bool
  and Entry :: 'node  $\Rightarrow$  ('(-Entry'-)) and Def :: 'node  $\Rightarrow$  'var set
  and Use :: 'node  $\Rightarrow$  'var set and state-val :: 'state  $\Rightarrow$  'var  $\Rightarrow$  'val
  and Exit :: 'node  $\Rightarrow$  ('(-Exit'-)) +
  fixes dyn-control-dependence :: 'node  $\Rightarrow$  'node  $\Rightarrow$  'edge list  $\Rightarrow$  bool
  (- controls - via - [51,0,0])
  assumes Exit-not-dyn-control-dependent: n controls n' via as  $\implies$  n'  $\neq$  (-Exit-)
  assumes dyn-control-dependence-path:
    n controls n' via as  $\implies$  n -as $\rightarrow^*$  n'  $\wedge$  as  $\neq$  []
```

begin

```
inductive cdep-edge :: 'node  $\Rightarrow$  'edge list  $\Rightarrow$  'node  $\Rightarrow$  bool
  (-  $\dashrightarrow_{cd}$  - [51,0,0] 80)
  and ddep-edge :: 'node  $\Rightarrow$  'var  $\Rightarrow$  'edge list  $\Rightarrow$  'node  $\Rightarrow$  bool
  (- -'{-}' $\dashrightarrow_{dd}$  - [51,0,0,0] 80)
  and DynPDG-edge :: 'node  $\Rightarrow$  'var option  $\Rightarrow$  'edge list  $\Rightarrow$  'node  $\Rightarrow$  bool
```

where

— Syntax

$n - as \rightarrow_{cd} n' == \text{DynPDG-edge } n \text{ None } as \ n'$
 $| \ n - \{V\} as \rightarrow_{dd} n' == \text{DynPDG-edge } n \text{ (Some } V) \ as \ n'$

— Rules

$| \text{DynPDG-cdep-edge:}$
 $n \text{ controls } n' \text{ via } as \implies n - as \rightarrow_{cd} n'$

$| \text{DynPDG-ddep-edge:}$
 $n \text{ influences } V \text{ in } n' \text{ via } as \implies n - \{V\} as \rightarrow_{dd} n'$

inductive $\text{DynPDG-path} :: 'node \Rightarrow 'edge \text{ list} \Rightarrow 'node \Rightarrow \text{bool}$
 $(- \dashrightarrow_{d*} - [51, 0, 0] \ 80)$

where DynPDG-path-Nil:

$\text{valid-node } n \implies n - [] \rightarrow_{d*} n$

$| \text{DynPDG-path-Append-cdep:}$
 $\llbracket n - as \rightarrow_{d*} n''; n'' - as' \rightarrow_{cd} n' \rrbracket \implies n - as @ as' \rightarrow_{d*} n'$

$| \text{DynPDG-path-Append-ddep:}$
 $\llbracket n - as \rightarrow_{d*} n''; n'' - \{V\} as' \rightarrow_{dd} n' \rrbracket \implies n - as @ as' \rightarrow_{d*} n'$

lemma $\text{DynPDG-empty-path-eq-nodes: } n - [] \rightarrow_{d*} n' \implies n = n'$
 $\langle \text{proof} \rangle$

lemma $\text{DynPDG-path-cdep: } n - as \rightarrow_{cd} n' \implies n - as \rightarrow_{d*} n'$
 $\langle \text{proof} \rangle$

lemma $\text{DynPDG-path-ddep: } n - \{V\} as \rightarrow_{dd} n' \implies n - as \rightarrow_{d*} n'$
 $\langle \text{proof} \rangle$

lemma $\text{DynPDG-path-Append:}$
 $\llbracket n'' - as' \rightarrow_{d*} n'; n - as \rightarrow_{d*} n' \rrbracket \implies n - as @ as' \rightarrow_{d*} n'$
 $\langle \text{proof} \rangle$

lemma $\text{DynPDG-path-Exit: } \llbracket n - as \rightarrow_{d*} n'; n' = (-\text{Exit-}) \rrbracket \implies n = (-\text{Exit-})$
 $\langle \text{proof} \rangle$

lemma $\text{DynPDG-path-not-inner:}$
 $\llbracket n - as \rightarrow_{d*} n'; \neg \text{inner-node } n' \rrbracket \implies n = n'$
 $\langle \text{proof} \rangle$

lemma $\text{DynPDG-cdep-edge-CFG-path:}$

assumes $n - as \rightarrow_{cd} n'$ **shows** $n - as \rightarrow^* n'$ **and** $as \neq []$
 $\langle proof \rangle$

lemma *DynPDG-ddep-edge-CFG-path*:
assumes $n - \{V\} as \rightarrow_{dd} n'$ **shows** $n - as \rightarrow^* n'$ **and** $as \neq []$
 $\langle proof \rangle$

lemma *DynPDG-path-CFG-path*:
 $n - as \rightarrow_{d^*} n' \implies n - as \rightarrow^* n'$
 $\langle proof \rangle$

lemma *DynPDG-path-split*:
 $n - as \rightarrow_{d^*} n' \implies$
 $(as = [] \wedge n = n') \vee$
 $(\exists n'' asx asx'. (n - asx \rightarrow_{cd} n'') \wedge (n'' - asx' \rightarrow_{d^*} n') \wedge$
 $(as = asx @ asx')) \vee$
 $(\exists n'' V asx asx'. (n - \{V\} asx \rightarrow_{dd} n'') \wedge (n'' - asx' \rightarrow_{d^*} n') \wedge$
 $(as = asx @ asx'))$
 $\langle proof \rangle$

lemma *DynPDG-path-rev-cases* [consumes 1,
case-names DynPDG-path-Nil DynPDG-path-cdep-Append DynPDG-path-ddep-Append]:
 $\llbracket n - as \rightarrow_{d^*} n'; \llbracket as = []; n = n' \rrbracket \implies Q;$
 $\bigwedge n'' asx asx'. \llbracket n - asx \rightarrow_{cd} n''; n'' - asx' \rightarrow_{d^*} n';$
 $as = asx @ asx' \rrbracket \implies Q;$
 $\bigwedge V n'' asx asx'. \llbracket n - \{V\} asx \rightarrow_{dd} n''; n'' - asx' \rightarrow_{d^*} n';$
 $as = asx @ asx' \rrbracket \implies Q \rrbracket$
 $\implies Q$
 $\langle proof \rangle$

lemma *DynPDG-ddep-edge-no-shorter-ddep-edge*:
assumes $ddep:n - \{V\} as \rightarrow_{dd} n'$
shows $\forall as' a as''. tl as = as' @ a \# as'' \longrightarrow \neg \text{sourcenode } a - \{V\} a \# as'' \rightarrow_{dd} n'$
 $\langle proof \rangle$

lemma *no-ddep-same-state*:
assumes $path:n - as \rightarrow^* n'$ **and** $Uses: V \in Use\ n'$ **and** $preds: preds\ (kinds\ as)\ s$
and $no-dep: \forall as' a as''. as = as' @ a \# as'' \longrightarrow \neg \text{sourcenode } a - \{V\} a \# as'' \rightarrow_{dd} n'$
shows $state-val\ (transfers\ (kinds\ as)\ s)\ V = state-val\ s\ V$
 $\langle proof \rangle$

lemma *DynPDG-ddep-edge-only-first-edge:*

$\llbracket n - \{V\} a \# as \rightarrow_{dd} n'; \text{preds } (kinds (a \# as)) s \rrbracket \implies$
 $\text{state-val } (\text{transfers } (kinds (a \# as)) s) V = \text{state-val } (\text{transfer } (kind a) s) V$
 $\langle \text{proof} \rangle$

lemma *Use-value-change-implies-DynPDG-ddep-edge:*

assumes $n - as \rightarrow^* n'$ **and** $V \in \text{Use } n'$ **and** $\text{preds } (kinds as) s$
and $\text{preds } (kinds as) s'$ **and** $\text{state-val } s V = \text{state-val } s' V$
and $\text{state-val } (\text{transfers } (kinds as) s) V \neq$
 $\text{state-val } (\text{transfers } (kinds as) s') V$
obtains $as' a as''$ **where** $as = as' @ a \# as''$
and $\text{sourcenode } a - \{V\} a \# as'' \rightarrow_{dd} n'$
and $\text{state-val } (\text{transfers } (kinds as) s) V =$
 $\text{state-val } (\text{transfers } (kinds (as' @ [a])) s) V$
and $\text{state-val } (\text{transfers } (kinds as) s') V =$
 $\text{state-val } (\text{transfers } (kinds (as' @ [a])) s') V$
 $\langle \text{proof} \rangle$

end

2.1.2 Instantiate dynamic PDG

Standard control dependence

locale *DynStandardControlDependencePDG* =

Postdomination sourcenode targetnode kind valid-edge Entry Exit +
CFGExit-wf sourcenode targetnode kind valid-edge Entry Def Use state-val Exit
for $\text{sourcenode} :: 'edge \Rightarrow 'node$ **and** $\text{targetnode} :: 'edge \Rightarrow 'node$
and $\text{kind} :: 'edge \Rightarrow 'state \text{ edge-kind}$ **and** $\text{valid-edge} :: 'edge \Rightarrow \text{bool}$
and $\text{Entry} :: 'node \Rightarrow ('(-\text{Entry}')')$ **and** $\text{Def} :: 'node \Rightarrow 'var \text{ set}$
and $\text{Use} :: 'node \Rightarrow 'var \text{ set}$ **and** $\text{state-val} :: 'state \Rightarrow 'var \Rightarrow 'val$
and $\text{Exit} :: 'node \Rightarrow ('(-\text{Exit}')')$

begin

lemma *DynPDG-scd:*

DynPDG sourcenode targetnode kind valid-edge (-Entry-)
 $\text{Def Use state-val } (-\text{Exit-}) \text{ dyn-standard-control-dependence}$
 $\langle \text{proof} \rangle$

end

Weak control dependence

locale *DynWeakControlDependencePDG* =

StrongPostdomination sourcenode targetnode kind valid-edge Entry Exit +
CFGExit-wf sourcenode targetnode kind valid-edge Entry Def Use state-val Exit

```

for sourcenode :: 'edge  $\Rightarrow$  'node and targetnode :: 'edge  $\Rightarrow$  'node
and kind :: 'edge  $\Rightarrow$  'state edge-kind and valid-edge :: 'edge  $\Rightarrow$  bool
and Entry :: 'node ('(-Entry'-)) and Def :: 'node  $\Rightarrow$  'var set
and Use :: 'node  $\Rightarrow$  'var set and state-val :: 'state  $\Rightarrow$  'var  $\Rightarrow$  'val
and Exit :: 'node ('(-Exit'-))

```

begin

lemma *DynPDG-wcd*:

```

  DynPDG sourcenode targetnode kind valid-edge (-Entry-)
  Def Use state-val (-Exit-) dyn-weak-control-dependence
  <proof>

```

end

2.1.3 Data slice

definition (in *CFG*) *empty-control-dependence* :: 'node $\Rightarrow$ 'node $\Rightarrow$ 'edge list $\Rightarrow$ bool

where *empty-control-dependence* *n n'* *as* $\equiv$ *False*

lemma (in *CFGExit-wf*) *DynPDG-scd*:

```

  DynPDG sourcenode targetnode kind valid-edge (-Entry-)
  Def Use state-val (-Exit-) empty-control-dependence
  <proof>

```

end

2.2 Dependent Live Variables

theory *DependentLiveVariables* **imports** *DynPDG* **begin**

dependent-live-vars calculates variables which can change the value of the *Use* variables of the target node

context *DynPDG* **begin**

inductive-set

dependent-live-vars :: 'node $\Rightarrow$ ('var $\times$ 'edge list $\times$ 'edge list) set

for *n'* :: 'node

where *dep-vars-Use*:

$V \in \text{Use } n' \implies (V, [], []) \in \text{dependent-live-vars } n'$

| *dep-vars-Cons-cdep*:

$\llbracket V \in \text{Use } (\text{sourcenode } a); \text{sourcenode } a - a \# as' \rightarrow_{cd} n''; n'' - as'' \rightarrow_{d*} n' \rrbracket$
 $\implies (V, [], a \# as' @ as'') \in \text{dependent-live-vars } n'$

| *dep-vars-Cons-ddep*:

$\llbracket (V, as', as) \in \text{dependent-live-vars } n'; V' \in \text{Use } (\text{sourcenode } a);$
 $n' = \text{last}(\text{targetnodes } (a \# as));$
 $\text{sourcenode } a - \{V\} a \# as' \rightarrow_{dd} \text{last}(\text{targetnodes } (a \# as')) \rrbracket$
 $\implies (V', [], a \# as) \in \text{dependent-live-vars } n'$

$| \text{dep-vars-Cons-keep:}$
 $\llbracket (V, as', as) \in \text{dependent-live-vars } n'; n' = \text{last}(\text{targetnodes } (a \# as));$
 $\neg \text{sourcenode } a - \{V\} a \# as' \rightarrow_{dd} \text{last}(\text{targetnodes } (a \# as')) \rrbracket$
 $\implies (V, a \# as', a \# as) \in \text{dependent-live-vars } n'$

lemma *dependent-live-vars-fst-prefix-snd*:
 $(V, as', as) \in \text{dependent-live-vars } n' \implies \exists as''. as' @ as'' = as$
 $\langle \text{proof} \rangle$

lemma *dependent-live-vars-Exit-empty* $[dest]$:
 $(V, as', as) \in \text{dependent-live-vars } (-\text{Exit-}) \implies \text{False}$
 $\langle \text{proof} \rangle$

lemma *dependent-live-vars-lastnode*:
 $\llbracket (V, as', as) \in \text{dependent-live-vars } n'; as \neq [] \rrbracket$
 $\implies n' = \text{last}(\text{targetnodes } as)$
 $\langle \text{proof} \rangle$

lemma *dependent-live-vars-Use-cases*:
 $\llbracket (V, as', as) \in \text{dependent-live-vars } n'; n - as \rightarrow^* n' \rrbracket$
 $\implies \exists nx as''. as = as' @ as'' \wedge n - as' \rightarrow^* nx \wedge nx - as'' \rightarrow_d^* n' \wedge V \in \text{Use } nx$
 $\wedge$
 $(\forall n'' \in \text{set } (\text{sourcenodes } as'). V \notin \text{Def } n'')$
 $\langle \text{proof} \rangle$

lemma *dependent-live-vars-dependent-edge*:
assumes $(V, as', as) \in \text{dependent-live-vars } n'$
and $\text{targetnode } a - as \rightarrow^* n'$
and $V \in \text{Def } (\text{sourcenode } a)$ **and** *valid-edge* a
obtains $nx as''$ **where** $as = as' @ as''$ **and** $\text{sourcenode } a - \{V\} a \# as' \rightarrow_{dd} nx$
and $nx - as'' \rightarrow_d^* n'$
 $\langle \text{proof} \rangle$

lemma *dependent-live-vars-same-pathsI*:
assumes $V \in \text{Use } n'$
shows $\llbracket \forall as' a as''. as = as' @ a \# as'' \longrightarrow \neg \text{sourcenode } a - \{V\} a \# as'' \rightarrow_{dd} n' \rrbracket$

$as \neq [] \longrightarrow n' = \text{last}(\text{targetnodes } as)$
 $\implies (V, as, as) \in \text{dependent-live-vars } n'$
 $\langle \text{proof} \rangle$

lemma *dependent-live-vars-same-pathsD:*

$\llbracket (V, as, as) \in \text{dependent-live-vars } n'; as \neq [] \longrightarrow n' = \text{last}(\text{targetnodes } as) \rrbracket$
 $\implies V \in \text{Use } n' \wedge (\forall as' a as''. as = as' @ a \# as'' \longrightarrow$
 $\quad \neg \text{sourcenode } a - \{V\} a \# as'' \rightarrow_{dd} n')$
 $\langle \text{proof} \rangle$

lemma *dependent-live-vars-same-paths:*

$as \neq [] \longrightarrow n' = \text{last}(\text{targetnodes } as) \implies$
 $(V, as, as) \in \text{dependent-live-vars } n' =$
 $(V \in \text{Use } n' \wedge (\forall as' a as''. as = as' @ a \# as'' \longrightarrow$
 $\quad \neg \text{sourcenode } a - \{V\} a \# as'' \rightarrow_{dd} n'))$
 $\langle \text{proof} \rangle$

lemma *dependent-live-vars-cdep-empty-fst:*

assumes $n'' - as \rightarrow_{cd} n'$ **and** $V' \in \text{Use } n''$
shows $(V', [], as) \in \text{dependent-live-vars } n'$
 $\langle \text{proof} \rangle$

lemma *dependent-live-vars-ddep-empty-fst:*

assumes $n'' - \{V\} as \rightarrow_{dd} n'$ **and** $V' \in \text{Use } n''$
shows $(V', [], as) \in \text{dependent-live-vars } n'$
 $\langle \text{proof} \rangle$

lemma *ddep-dependent-live-vars-keep-notempty:*

assumes $n - \{V\} a \# as \rightarrow_{dd} n''$ **and** $as' \neq []$
and $(V, as'', as') \in \text{dependent-live-vars } n'$
shows $(V, as @ as'', as @ as') \in \text{dependent-live-vars } n'$
 $\langle \text{proof} \rangle$

lemma *dependent-live-vars-cdep-dependent-live-vars:*

assumes $n'' - as'' \rightarrow_{cd} n'$ **and** $(V', as', as) \in \text{dependent-live-vars } n''$
shows $(V', as', as @ as'') \in \text{dependent-live-vars } n'$
 $\langle \text{proof} \rangle$

lemma *dependent-live-vars-ddep-dependent-live-vars:*

assumes $n'' - \{V\} as'' \rightarrow_{dd} n'$ **and** $(V', as', as) \in \text{dependent-live-vars } n''$
shows $(V', as', as @ as'') \in \text{dependent-live-vars } n'$
 $\langle \text{proof} \rangle$

lemma *dependent-live-vars-dep-dependent-live-vars*:
 $\llbracket n'' - as'' \rightarrow_d n'; (V', as', as) \in \text{dependent-live-vars } n'' \rrbracket$
 $\implies (V', as', as @ as'') \in \text{dependent-live-vars } n'$
 $\langle \text{proof} \rangle$

end

end

2.3 Formalization of Bit Vectors

theory *BitVector* **imports** *Main* **begin**

type-synonym *bit-vector* = *bool list*

fun *bv-legs* :: *bit-vector* $\Rightarrow$ *bit-vector* $\Rightarrow$ *bool* ($- \preceq_b -$ 99)
where *bv-Nils*: $\square \preceq_b \square = \text{True}$
 $| \text{bv-Cons}:(x \# xs) \preceq_b (y \# ys) = ((x \longrightarrow y) \wedge xs \preceq_b ys)$
 $| \text{bv-rest}:xs \preceq_b ys = \text{False}$

2.3.1 Some basic properties

lemma *bv-length*: $xs \preceq_b ys \implies \text{length } xs = \text{length } ys$
 $\langle \text{proof} \rangle$

lemma [*dest!*]: $xs \preceq_b \square \implies xs = \square$
 $\langle \text{proof} \rangle$

lemma *bv-legs-AppendI*:
 $\llbracket xs \preceq_b ys; xs' \preceq_b ys' \rrbracket \implies (xs @ xs') \preceq_b (ys @ ys')$
 $\langle \text{proof} \rangle$

lemma *bv-legs-AppendD*:
 $\llbracket (xs @ xs') \preceq_b (ys @ ys'); \text{length } xs = \text{length } ys \rrbracket$
 $\implies xs \preceq_b ys \wedge xs' \preceq_b ys'$
 $\langle \text{proof} \rangle$

lemma *bv-legs-eq*:

$xs \preceq_b ys = ((\forall i < \text{length } xs. xs ! i \longrightarrow ys ! i) \wedge \text{length } xs = \text{length } ys)$
 $\langle \text{proof} \rangle$

2.3.2 $\preceq_b$ is an order on bit vectors with minimal and maximal element

lemma *minimal-element*:
 $\text{replicate } (\text{length } xs) \text{ False } \preceq_b xs$
 $\langle \text{proof} \rangle$

lemma *maximal-element*:
 $xs \preceq_b \text{replicate } (\text{length } xs) \text{ True}$
 $\langle \text{proof} \rangle$

lemma *bv-leqs-refl*: $xs \preceq_b xs$
 $\langle \text{proof} \rangle$

lemma *bv-leqs-trans*: $\llbracket xs \preceq_b ys; ys \preceq_b zs \rrbracket \implies xs \preceq_b zs$
 $\langle \text{proof} \rangle$

lemma *bv-leqs-antisym*: $\llbracket xs \preceq_b ys; ys \preceq_b xs \rrbracket \implies xs = ys$
 $\langle \text{proof} \rangle$

definition *bv-less* :: *bit-vector* $\Rightarrow$ *bit-vector* $\Rightarrow$ *bool* (- $\prec_b$ - 99)
 where $xs \prec_b ys \equiv xs \preceq_b ys \wedge xs \neq ys$

interpretation *order bv-leqs bv-less*
 $\langle \text{proof} \rangle$

end

2.4 Dynamic Backward Slice

theory *DynSlice* **imports** *DependentLiveVariables BitVector ../Basic/SemanticsCFG*
begin

2.4.1 Backward slice of paths

context *DynPDG* **begin**

fun *slice-path* :: *'edge list* $\Rightarrow$ *bit-vector*
 where *slice-path* [] = []
 | *slice-path* (a#as) = (let n' = last(targetnodes (a#as)) in

(*sourcenode* $a - a \# as \rightarrow_a^* n'$) $\#$ *slice-path* as)

lemma *slice-path-length*:
 $length(\text{slice-path } as) = length \text{ } as$
 $\langle proof \rangle$

lemma *slice-path-right-Cons*:
assumes $\text{slice}:\text{slice-path } as = x \# xs$
obtains $a' \text{ } as'$ **where** $as = a' \# as'$ **and** $\text{slice-path } as' = xs$
 $\langle proof \rangle$

2.4.2 The proof of the fundamental property of (dynamic) slicing

fun *select-edge-kinds* :: $'edge \text{ list} \Rightarrow \text{bit-vector} \Rightarrow 'state \text{ edge-kind list}$
where *select-edge-kinds* $\square \square = \square$
 $| \text{select-edge-kinds } (a \# as) (b \# bs) = (\text{if } b \text{ then kind } a$
 $\text{else } (\text{case kind } a \text{ of } \uparrow f \Rightarrow \uparrow id \mid (Q)_{\checkmark} \Rightarrow (\lambda s. \text{True})_{\checkmark})) \# \text{select-edge-kinds } as$
 bs

definition *slice-kinds* :: $'edge \text{ list} \Rightarrow 'state \text{ edge-kind list}$
where $\text{slice-kinds } as = \text{select-edge-kinds } as (\text{slice-path } as)$

lemma *select-edge-kinds-max-bv*:
 $\text{select-edge-kinds } as (\text{replicate } (length \text{ } as) \text{True}) = \text{kinds } as$
 $\langle proof \rangle$

lemma *slice-path-legs-information-same-Uses*:
 $\llbracket n - as \rightarrow^* n'; bs \preceq_b bs'; \text{slice-path } as = bs;$
 $\text{select-edge-kinds } as \text{ } bs = es; \text{select-edge-kinds } as \text{ } bs' = es';$
 $\forall V \text{ } xs. (V, xs, as) \in \text{dependent-live-vars } n' \longrightarrow \text{state-val } s \text{ } V = \text{state-val } s' \text{ } V;$
 $\text{preds } es' \text{ } s' \rrbracket$
 $\implies (\forall V \in \text{Use } n'. \text{state-val } (\text{transfers } es \text{ } s) \text{ } V =$
 $\text{state-val } (\text{transfers } es' \text{ } s') \text{ } V) \wedge \text{preds } es \text{ } s$
 $\langle proof \rangle$

theorem *fundamental-property-of-path-slicing*:
assumes $n - as \rightarrow^* n'$ **and** $\text{preds } (\text{kinds } as) \text{ } s$
shows $(\forall V \in \text{Use } n'. \text{state-val } (\text{transfers } (\text{slice-kinds } as) \text{ } s) \text{ } V =$
 $\text{state-val } (\text{transfers } (\text{kinds } as) \text{ } s) \text{ } V)$
and $\text{preds } (\text{slice-kinds } as) \text{ } s$
 $\langle proof \rangle$

end

2.4.3 The fundamental property of (dynamic) slicing related to the semantics

```

locale BackwardPathSlice-wf =
  DynPDG sourcenode targetnode kind valid-edge Entry Def Use state-val Exit
  dyn-control-dependence +
  CFG-semantics-wf sourcenode targetnode kind valid-edge Entry sem identifies
for sourcenode :: 'edge  $\Rightarrow$  'node and targetnode :: 'edge  $\Rightarrow$  'node
and kind :: 'edge  $\Rightarrow$  'state edge-kind and valid-edge :: 'edge  $\Rightarrow$  bool
and Entry :: 'node ('('Exit'-')) and Def :: 'node  $\Rightarrow$  'var set
and Use :: 'node  $\Rightarrow$  'var set and state-val :: 'state  $\Rightarrow$  'var  $\Rightarrow$  'val
and dyn-control-dependence :: 'node  $\Rightarrow$  'node  $\Rightarrow$  'edge list  $\Rightarrow$  bool
  (- controls - via - [51, 0, 0] 1000)
and Exit :: 'node ('('Exit'-'))
and sem :: 'com  $\Rightarrow$  'state  $\Rightarrow$  'com  $\Rightarrow$  'state  $\Rightarrow$  bool
  (((1<-,->-))  $\Rightarrow$  / (1<-,->-)) [0,0,0,0] 81)
and identifies :: 'node  $\Rightarrow$  'com  $\Rightarrow$  bool (-  $\triangleq$  - [51, 0] 80)

begin

theorem fundamental-property-of-path-slicing-semantically:
  assumes  $n \triangleq c$  and  $\langle c, s \rangle \Rightarrow \langle c', s' \rangle$ 
  obtains  $n'$  as where  $n -as\rightarrow^* n'$  and preds (slice-kinds as) s
  and  $n' \triangleq c'$ 
  and  $\forall V \in Use\ n'.\ state-val\ (transfers\ (slice-kinds\ as)\ s)\ V =$ 
    state-val s' V
  <proof>

end

end

```

2.5 Observable Sets of Nodes

```

theory Observable imports ../Basic/CFG begin

context CFG begin

inductive-set obs :: 'node  $\Rightarrow$  'node set  $\Rightarrow$  'node set
for  $n::'node$  and  $S::'node\ set$ 
where obs-elem:
   $\llbracket n -as\rightarrow^* n'; \forall nx \in set(sourcenodes\ as). nx \notin S; n' \in S \rrbracket \implies n' \in obs\ n\ S$ 

lemma obsE:
  assumes  $n' \in obs\ n\ S$ 
  obtains as where  $n -as\rightarrow^* n'$  and  $\forall nx \in set(sourcenodes\ as). nx \notin S$ 

```

and $n' \in S$
 $\langle proof \rangle$

lemma *n-in-obs*:
 assumes *valid-node* n **and** $n \in S$ **shows** $obs\ n\ S = \{n\}$
 $\langle proof \rangle$

lemma *in-obs-valid*:
 assumes $n' \in obs\ n\ S$ **shows** *valid-node* n **and** *valid-node* n'
 $\langle proof \rangle$

lemma *edge-obs-subset*:
 assumes *valid-edge* a **and** *sourcenode* $a \notin S$
 shows $obs\ (targetnode\ a)\ S \subseteq obs\ (sourcenode\ a)\ S$
 $\langle proof \rangle$

lemma *path-obs-subset*:
 $\llbracket n - as \rightarrow^* n'; \forall n' \in set(sourcenodes\ as). n' \notin S \rrbracket$
 $\implies obs\ n'\ S \subseteq obs\ n\ S$
 $\langle proof \rangle$

lemma *path-ex-obs*:
 assumes $n - as \rightarrow^* n'$ **and** $n' \in S$
 obtains m **where** $m \in obs\ n\ S$
 $\langle proof \rangle$

end

end

Chapter 3

Static Intraprocedural Slicing

Static Slicing analyses a CFG prior to execution. Whereas dynamic slicing can provide better results for certain inputs (i.e. trace and initial state), static slicing is more conservative but provides results independent of inputs.

Correctness for static slicing is defined differently than correctness of dynamic slicing by a weak simulation between nodes and states when traversing the original and the sliced graph. The weak simulation property demands that if a (node,state) tuples (n_1, s_1) simulates (n_2, s_2) and making an observable move in the original graph leads from (n_1, s_1) to (n'_1, s'_1) , this tuple simulates a tuple (n_2, s_2) which is the result of making an observable move in the sliced graph beginning in (n'_2, s'_2) .

We also show how a “dynamic slicing style” correctness criterion for static slicing of a given trace and initial state could look like.

This formalization of static intraprocedural slicing is instantiable with three different kinds of control dependences: standard control, weak control and weak order dependence. The correctness proof for slicing is independent of the control dependence used, it bases only on one property every control dependence definition has to fulfill.

3.1 Distance of Paths

```
theory Distance imports ../Basic/CFG begin
```

```
context CFG begin
```

```
inductive distance :: 'node  $\Rightarrow$  'node  $\Rightarrow$  nat  $\Rightarrow$  bool
```

```
where distanceI:
```

```
   $\llbracket n - as \rightarrow^* n'; \text{length } as = x; \forall as'. n - as' \rightarrow^* n' \longrightarrow x \leq \text{length } as \rrbracket$   
   $\implies \text{distance } n \ n' \ x$ 
```

```
lemma every-path-distance:
```

```
  assumes  $n - as \rightarrow^* n'$ 
```

obtains x **where** $\text{distance } n \ n' \ x$ **and** $x \leq \text{length } as$
 $\langle \text{proof} \rangle$

lemma *distance-det*:
 $\llbracket \text{distance } n \ n' \ x; \text{distance } n \ n' \ x' \rrbracket \implies x = x'$
 $\langle \text{proof} \rangle$

lemma *only-one-SOME-dist-edge*:
assumes $\text{valid}:\text{valid-edge } a$ **and** $\text{dist}:\text{distance } (\text{targetnode } a) \ n' \ x$
shows $\exists! a'. \text{sourcenode } a = \text{sourcenode } a' \wedge \text{distance } (\text{targetnode } a') \ n' \ x \wedge$
 $\text{valid-edge } a' \wedge$
 $\text{targetnode } a' = (\text{SOME } nx. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance } (\text{targetnode } a') \ n' \ x \wedge$
 $\text{valid-edge } a' \wedge \text{targetnode } a' = nx)$
 $\langle \text{proof} \rangle$

lemma *distance-successor-distance*:
assumes $\text{distance } n \ n' \ x$ **and** $x \neq 0$
obtains a **where** $\text{valid-edge } a$ **and** $n = \text{sourcenode } a$
and $\text{distance } (\text{targetnode } a) \ n' \ (x - 1)$
and $\text{targetnode } a = (\text{SOME } nx. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance } (\text{targetnode } a') \ n' \ (x - 1) \wedge$
 $\text{valid-edge } a' \wedge \text{targetnode } a' = nx)$
 $\langle \text{proof} \rangle$

end

end

3.2 Static data dependence

theory *DataDependence* **imports** *../Basic/DynDataDependence* **begin**

context *CFG-wf* **begin**

definition *data-dependence* $:: 'node \Rightarrow 'var \Rightarrow 'node \Rightarrow \text{bool}$
 $(- \text{influences } - \text{ in } - [51, 0])$

where $\text{data-dependences-eq} : n \text{ influences } V \text{ in } n' \equiv \exists as. n \text{ influences } V \text{ in } n' \text{ via } as$

lemma *data-dependence-def*: $n \text{ influences } V \text{ in } n' =$
 $(\exists a' as'. (V \in \text{Def } n) \wedge (V \in \text{Use } n') \wedge$
 $(n - a' \# as' \rightarrow^* n') \wedge (\forall n'' \in \text{set } (\text{sourcenodes } as'). V \notin \text{Def } n''))$

$\langle proof \rangle$

end

end

3.3 Static backward slice

theory *Slice*

imports *Observable Distance DataDependence ../Basic/SemanticsCFG*
begin

locale *BackwardSlice* =

CFG-wf sourcenode targetnode kind valid-edge Entry Def Use state-val
for *sourcenode* :: 'edge $\Rightarrow$ 'node **and** *targetnode* :: 'edge $\Rightarrow$ 'node
and *kind* :: 'edge $\Rightarrow$ 'state edge-kind **and** *valid-edge* :: 'edge $\Rightarrow$ bool
and *Entry* :: 'node ('('Entry'-')) **and** *Def* :: 'node $\Rightarrow$ 'var set
and *Use* :: 'node $\Rightarrow$ 'var set **and** *state-val* :: 'state $\Rightarrow$ 'var $\Rightarrow$ 'val +
fixes *backward-slice* :: 'node set $\Rightarrow$ 'node set
assumes *valid-nodes*: $n \in \text{backward-slice } S \implies \text{valid-node } n$
and *refl*: $\llbracket \text{valid-node } n; n \in S \rrbracket \implies n \in \text{backward-slice } S$
and *dd-closed*: $\llbracket n' \in \text{backward-slice } S; n \text{ influences } V \text{ in } n' \rrbracket$
 $\implies n \in \text{backward-slice } S$
and *obs-finite*: *finite* (*obs* *n* (*backward-slice* *S*))
and *obs-singleton*: *card* (*obs* *n* (*backward-slice* *S*)) ≤ 1

begin

lemma *slice-n-in-obs*:

$n \in \text{backward-slice } S \implies \text{obs } n (\text{backward-slice } S) = \{n\}$

$\langle proof \rangle$

lemma *obs-singleton-disj*:

$(\exists m. \text{obs } n (\text{backward-slice } S) = \{m\}) \vee \text{obs } n (\text{backward-slice } S) = \{\}$

$\langle proof \rangle$

lemma *obs-singleton-element*:

assumes $m \in \text{obs } n (\text{backward-slice } S)$ **shows** $\text{obs } n (\text{backward-slice } S) = \{m\}$

$\langle proof \rangle$

lemma *obs-the-element*:

$m \in \text{obs } n (\text{backward-slice } S) \implies (\text{THE } m. m \in \text{obs } n (\text{backward-slice } S)) = m$

$\langle proof \rangle$

3.3.1 Traversing the sliced graph

slice-kind S a conforms to *kind* a in the sliced graph

definition *slice-kind* $:: 'node\ set \Rightarrow 'edge \Rightarrow 'state\ edge\text{-}kind$

where *slice-kind* S $a = (\text{let } S' = \text{backward-slice } S; n = \text{sourcenode } a \text{ in}$
(if *sourcenode* $a \in S'$ *then* *kind* a
else *(case* *kind* a *of* $\uparrow f \Rightarrow \uparrow id \mid (Q)_{\checkmark} \Rightarrow$
(if *obs* *(sourcenode* $a)$ $S' = \{\}$ *then*
(let $nx = (\text{SOME } n'. \exists a'. n = \text{sourcenode } a' \wedge \text{valid-edge } a' \wedge \text{targetnode } a'$
 $= n')$
in *(if* *(targetnode* $a = nx)$ *then* $(\lambda s. \text{True})_{\checkmark}$ *else* $(\lambda s. \text{False})_{\checkmark})$
else *(let* $m = \text{THE } m. m \in \text{obs } n \ S'$ *in*
(if $(\exists x. \text{distance } (\text{targetnode } a) \ m \ x \wedge \text{distance } n \ m \ (x + 1) \wedge$
 $(\text{targetnode } a = (\text{SOME } nx'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{distance } (\text{targetnode } a') \ m \ x \wedge$
 $\text{valid-edge } a' \wedge \text{targetnode } a' = nx'))$
then $(\lambda s. \text{True})_{\checkmark}$ *else* $(\lambda s. \text{False})_{\checkmark}$
 $)$
 $)$
 $)$
 $)$

definition

slice-kinds $:: 'node\ set \Rightarrow 'edge\ list \Rightarrow 'state\ edge\text{-}kind\ list$

where *slice-kinds* S $as \equiv \text{map } (\text{slice-kind } S) \ as$

lemma *slice-kind-in-slice*:

sourcenode $a \in \text{backward-slice } S \implies \text{slice-kind } S \ a = \text{kind } a$

<proof>

lemma *slice-kind-Upd*:

$\llbracket \text{sourcenode } a \notin \text{backward-slice } S; \text{kind } a = \uparrow f \rrbracket \implies \text{slice-kind } S \ a = \uparrow id$

<proof>

lemma *slice-kind-Pred-empty-obs-SOME*:

$\llbracket \text{sourcenode } a \notin \text{backward-slice } S; \text{kind } a = (Q)_{\checkmark};$

$\text{obs } (\text{sourcenode } a) (\text{backward-slice } S) = \{\};$

$\text{targetnode } a = (\text{SOME } n'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge \text{valid-edge } a'$

$\wedge$

$\text{targetnode } a' = n') \rrbracket$

$\implies \text{slice-kind } S \ a = (\lambda s. \text{True})_{\checkmark}$

<proof>

lemma *slice-kind-Pred-empty-obs-not-SOME*:

$\llbracket \text{sourcenode } a \notin \text{backward-slice } S; \text{kind } a = (Q)_{\checkmark};$

$obs\ (sourcenode\ a)\ (backward-slice\ S) = \{\};$
 $targetnode\ a \neq (SOME\ n'.\ \exists\ a'.\ sourcenode\ a = sourcenode\ a' \wedge valid-edge\ a'$
 $\wedge$
 $targetnode\ a' = n')]$
 $\implies slice-kind\ S\ a = (\lambda s. False)_{\checkmark}$
 $\langle proof \rangle$

lemma *slice-kind-Pred-obs-nearer-SOME*:

assumes $sourcenode\ a \notin backward-slice\ S$ **and** $kind\ a = (Q)_{\checkmark}$
and $m \in obs\ (sourcenode\ a)\ (backward-slice\ S)$
and $distance\ (targetnode\ a)\ m\ x\ distance\ (sourcenode\ a)\ m\ (x + 1)$
and $targetnode\ a = (SOME\ n'.\ \exists\ a'.\ sourcenode\ a = sourcenode\ a' \wedge$
 $distance\ (targetnode\ a')\ m\ x \wedge$
 $valid-edge\ a' \wedge targetnode\ a' = n')$
shows $slice-kind\ S\ a = (\lambda s. True)_{\checkmark}$
 $\langle proof \rangle$

lemma *slice-kind-Pred-obs-nearer-not-SOME*:

assumes $sourcenode\ a \notin backward-slice\ S$ **and** $kind\ a = (Q)_{\checkmark}$
and $m \in obs\ (sourcenode\ a)\ (backward-slice\ S)$
and $distance\ (targetnode\ a)\ m\ x\ distance\ (sourcenode\ a)\ m\ (x + 1)$
and $targetnode\ a \neq (SOME\ nx'.\ \exists\ a'.\ sourcenode\ a = sourcenode\ a' \wedge$
 $distance\ (targetnode\ a')\ m\ x \wedge$
 $valid-edge\ a' \wedge targetnode\ a' = nx')$
shows $slice-kind\ S\ a = (\lambda s. False)_{\checkmark}$
 $\langle proof \rangle$

lemma *slice-kind-Pred-obs-not-nearer*:

assumes $sourcenode\ a \notin backward-slice\ S$ **and** $kind\ a = (Q)_{\checkmark}$
and $in-obs:m \in obs\ (sourcenode\ a)\ (backward-slice\ S)$
and $dist:distance\ (sourcenode\ a)\ m\ (x + 1)$
 $\neg distance\ (targetnode\ a)\ m\ x$
shows $slice-kind\ S\ a = (\lambda s. False)_{\checkmark}$
 $\langle proof \rangle$

lemma *kind-Predicate-notin-slice-slice-kind-Predicate*:

assumes $kind\ a = (Q)_{\checkmark}$ **and** $sourcenode\ a \notin backward-slice\ S$
obtains Q' **where** $slice-kind\ S\ a = (Q')_{\checkmark}$ **and** $Q' = (\lambda s. False) \vee Q' = (\lambda s.$
 $True)$
 $\langle proof \rangle$

lemma *only-one-SOME-edge*:

assumes $valid-edge\ a$
shows $\exists! a'.\ sourcenode\ a = sourcenode\ a' \wedge valid-edge\ a' \wedge$

$\text{targetnode } a' = (\text{SOME } n'. \exists a'. \text{sourcenode } a = \text{sourcenode } a' \wedge$
 $\text{valid-edge } a' \wedge \text{targetnode } a' = n')$

$\langle \text{proof} \rangle$

lemma *slice-kind-only-one-True-edge*:
assumes $\text{sourcenode } a = \text{sourcenode } a'$ **and** $\text{targetnode } a \neq \text{targetnode } a'$
and $\text{valid-edge } a$ **and** $\text{valid-edge } a'$ **and** $\text{slice-kind } S \ a = (\lambda s. \text{True})_{\checkmark}$
shows $\text{slice-kind } S \ a' = (\lambda s. \text{False})_{\checkmark}$
 $\langle \text{proof} \rangle$

lemma *slice-deterministic*:
assumes $\text{valid-edge } a$ **and** $\text{valid-edge } a'$
and $\text{sourcenode } a = \text{sourcenode } a'$ **and** $\text{targetnode } a \neq \text{targetnode } a'$
obtains $Q \ Q'$ **where** $\text{slice-kind } S \ a = (Q)_{\checkmark}$ **and** $\text{slice-kind } S \ a' = (Q')_{\checkmark}$
and $\forall s. (Q \ s \longrightarrow \neg Q' \ s) \wedge (Q' \ s \longrightarrow \neg Q \ s)$
 $\langle \text{proof} \rangle$

3.3.2 Observable and silent moves

inductive *silent-move* ::
 $'\text{node set} \Rightarrow ('edge \Rightarrow 'state \text{ edge-kind}) \Rightarrow 'node \Rightarrow 'state \Rightarrow 'edge \Rightarrow$
 $'node \Rightarrow 'state \Rightarrow \text{bool } (-, - \vdash '(-, -) \dashrightarrow_{\tau} '(-, -)) \ [51, 50, 0, 0, 50, 0, 0] \ 51)$

where *silent-moveI*:
 $\llbracket \text{pred } (f \ a) \ s; \text{transfer } (f \ a) \ s = s'; \text{sourcenode } a \notin \text{backward-slice } S;$
 $\text{valid-edge } a \rrbracket$
 $\implies S, f \vdash (\text{sourcenode } a, s) -a \rightarrow_{\tau} (\text{targetnode } a, s')$

inductive *silent-moves* ::
 $'node \text{ set} \Rightarrow ('edge \Rightarrow 'state \text{ edge-kind}) \Rightarrow 'node \Rightarrow 'state \Rightarrow 'edge \text{ list} \Rightarrow$
 $'node \Rightarrow 'state \Rightarrow \text{bool } (-, - \vdash '(-, -) \dashrightarrow_{\tau} '(-, -)) \ [51, 50, 0, 0, 50, 0, 0] \ 51)$

where *silent-moves-Nil*: $S, f \vdash (n, s) = [] \Rightarrow_{\tau} (n, s)$

| *silent-moves-Cons*:
 $\llbracket S, f \vdash (n, s) -a \rightarrow_{\tau} (n', s'); S, f \vdash (n', s') = as \Rightarrow_{\tau} (n'', s'') \rrbracket$
 $\implies S, f \vdash (n, s) = a \# as \Rightarrow_{\tau} (n'', s'')$

lemma *silent-moves-obs-slice*:
 $\llbracket S, f \vdash (n, s) = as \Rightarrow_{\tau} (n', s'); nx \in \text{obs } n' \ (\text{backward-slice } S) \rrbracket$
 $\implies nx \in \text{obs } n \ (\text{backward-slice } S)$
 $\langle \text{proof} \rangle$

lemma *silent-moves-preds-transfers-path*:

$\llbracket S, f \vdash (n, s) =_{as} \Rightarrow_{\tau} (n', s'); \text{valid-node } n \rrbracket$
 $\implies \text{preds } (\text{map } f \text{ as}) \ s \wedge \text{transfers } (\text{map } f \text{ as}) \ s = s' \wedge n -_{as} \rightarrow^* n'$
 $\langle \text{proof} \rangle$

lemma *obs-silent-moves*:

assumes *obs* n (*backward-slice* S) = $\{n'\}$
obtains *as* **where** $S, \text{slice-kind } S \vdash (n, s) =_{as} \Rightarrow_{\tau} (n', s)$
 $\langle \text{proof} \rangle$

inductive *observable-move* ::

$'node \text{ set} \Rightarrow ('edge \Rightarrow 'state \text{ edge-kind}) \Rightarrow 'node \Rightarrow 'state \Rightarrow 'edge \Rightarrow$
 $'node \Rightarrow 'state \Rightarrow \text{bool } (-, - \vdash '(-, -) \dashrightarrow '(-, -)) \ [51, 50, 0, 0, 50, 0, 0] \ 51)$

where *observable-moveI*:

$\llbracket \text{pred } (f \ a) \ s; \text{transfer } (f \ a) \ s = s'; \text{sourcenode } a \in \text{backward-slice } S;$
 $\text{valid-edge } a \rrbracket$
 $\implies S, f \vdash (\text{sourcenode } a, s) -a \rightarrow (\text{targetnode } a, s')$

inductive *observable-moves* ::

$'node \text{ set} \Rightarrow ('edge \Rightarrow 'state \text{ edge-kind}) \Rightarrow 'node \Rightarrow 'state \Rightarrow 'edge \text{ list} \Rightarrow$
 $'node \Rightarrow 'state \Rightarrow \text{bool } (-, - \vdash '(-, -) \dashrightarrow '(-, -)) \ [51, 50, 0, 0, 50, 0, 0] \ 51)$

where *observable-moves-snoc*:

$\llbracket S, f \vdash (n, s) =_{as} \Rightarrow_{\tau} (n', s'); S, f \vdash (n', s') -a \rightarrow (n'', s'') \rrbracket$
 $\implies S, f \vdash (n, s) =_{as@[a]} \Rightarrow (n'', s'')$

lemma *observable-move-notempty*:

$\llbracket S, f \vdash (n, s) =_{as} \Rightarrow (n', s'); \text{as} = [] \rrbracket \implies \text{False}$
 $\langle \text{proof} \rangle$

lemma *silent-move-observable-moves*:

$\llbracket S, f \vdash (n'', s'') =_{as} \Rightarrow (n', s'); S, f \vdash (n, s) -a \rightarrow_{\tau} (n'', s'') \rrbracket$
 $\implies S, f \vdash (n, s) =_{a\#as} \Rightarrow (n', s')$
 $\langle \text{proof} \rangle$

lemma *observable-moves-preds-transfers-path*:

$S, f \vdash (n, s) =_{as} \Rightarrow (n', s')$
 $\implies \text{preds } (\text{map } f \text{ as}) \ s \wedge \text{transfers } (\text{map } f \text{ as}) \ s = s' \wedge n -_{as} \rightarrow^* n'$
 $\langle \text{proof} \rangle$

3.3.3 Relevant variables

inductive-set *relevant-vars* :: $'node \text{ set} \Rightarrow 'node \Rightarrow 'var \text{ set } (rv \ -)$

for $S :: 'node\ set$ **and** $n :: 'node$

where rvI :

$\llbracket n - as \rightarrow^* n'; n' \in \text{backward-slice } S; V \in \text{Use } n';$
 $\forall nx \in \text{set}(\text{sourcenodes } as). V \notin \text{Def } nx \rrbracket$
 $\implies V \in rv\ S\ n$

lemma rvE :

assumes $rv: V \in rv\ S\ n$
obtains $as\ n'$ **where** $n - as \rightarrow^* n'$ **and** $n' \in \text{backward-slice } S$ **and** $V \in \text{Use } n'$
and $\forall nx \in \text{set}(\text{sourcenodes } as). V \notin \text{Def } nx$
 $\langle \text{proof} \rangle$

lemma $eq\text{-}obs\text{-}in\text{-}rv$:

assumes $obs\text{-}eq: obs\ n\ (\text{backward-slice } S) = obs\ n'\ (\text{backward-slice } S)$
and $x \in rv\ S\ n$ **shows** $x \in rv\ S\ n'$
 $\langle \text{proof} \rangle$

lemma $closed\text{-}eq\text{-}obs\text{-}eq\text{-}rvs$:

fixes $S :: 'node\ set$
assumes $valid\text{-}node\ n$ **and** $valid\text{-}node\ n'$
and $obs\text{-}eq: obs\ n\ (\text{backward-slice } S) = obs\ n'\ (\text{backward-slice } S)$
shows $rv\ S\ n = rv\ S\ n'$
 $\langle \text{proof} \rangle$

lemma $rv\text{-}edge\text{-}slice\text{-}kinds$:

assumes $valid\text{-}edge\ a$ **and** $sourcenode\ a = n$ **and** $targetnode\ a = n''$
and $\forall V \in rv\ S\ n. state\text{-}val\ s\ V = state\text{-}val\ s'\ V$
and $\text{preds } (slice\text{-}kinds\ S\ (a\#as))\ s$ **and** $\text{preds } (slice\text{-}kinds\ S\ (a\#asx))\ s'$
shows $\forall V \in rv\ S\ n''. state\text{-}val\ (\text{transfer } (slice\text{-}kind\ S\ a)\ s)\ V =$
 $state\text{-}val\ (\text{transfer } (slice\text{-}kind\ S\ a)\ s')\ V$
 $\langle \text{proof} \rangle$

lemma $rv\text{-}branching\text{-}edges\text{-}slice\text{-}kinds\text{-}False$:

assumes $valid\text{-}edge\ a$ **and** $valid\text{-}edge\ ax$
and $sourcenode\ a = n$ **and** $sourcenode\ ax = n$
and $targetnode\ a = n''$ **and** $targetnode\ ax \neq n''$
and $\text{preds } (slice\text{-}kinds\ S\ (a\#as))\ s$ **and** $\text{preds } (slice\text{-}kinds\ S\ (ax\#asx))\ s'$
and $\forall V \in rv\ S\ n. state\text{-}val\ s\ V = state\text{-}val\ s'\ V$
shows $False$
 $\langle \text{proof} \rangle$

3.3.4 The set WS

inductive-set $WS :: 'node\ set \Rightarrow (('node \times 'state) \times ('node \times 'state))\ set$
for $S :: 'node\ set$
where $WSI: \llbracket obs\ n\ (backward\ slice\ S) = obs\ n'\ (backward\ slice\ S);$
 $\forall V \in rv\ S\ n.\ state\ val\ s\ V = state\ val\ s'\ V;$
 $valid\ node\ n; valid\ node\ n' \rrbracket$
 $\Rightarrow ((n,s),(n',s')) \in WS\ S$

lemma WSD :

$((n,s),(n',s')) \in WS\ S$
 $\Rightarrow obs\ n\ (backward\ slice\ S) = obs\ n'\ (backward\ slice\ S) \wedge$
 $(\forall V \in rv\ S\ n.\ state\ val\ s\ V = state\ val\ s'\ V) \wedge$
 $valid\ node\ n \wedge valid\ node\ n'$
 $\langle proof \rangle$

lemma $WS\ silent\ move$:

assumes $((n_1,s_1),(n_2,s_2)) \in WS\ S$ **and** $S, kind \vdash (n_1,s_1) -a \rightarrow_\tau (n_1',s_1')$
and $obs\ n_1'\ (backward\ slice\ S) \neq \{\}$ **shows** $((n_1',s_1'),(n_2,s_2)) \in WS\ S$
 $\langle proof \rangle$

lemma $WS\ silent\ moves$:

$\llbracket S, f \vdash (n_1,s_1) = as \Rightarrow_\tau (n_1',s_1'); ((n_1,s_1),(n_2,s_2)) \in WS\ S; f = kind;$
 $obs\ n_1'\ (backward\ slice\ S) \neq \{\} \rrbracket$
 $\Rightarrow ((n_1',s_1'),(n_2,s_2)) \in WS\ S$
 $\langle proof \rangle$

lemma $WS\ observable\ move$:

assumes $((n_1,s_1),(n_2,s_2)) \in WS\ S$ **and** $S, kind \vdash (n_1,s_1) -a \rightarrow (n_1',s_1')$
obtains as **where** $((n_1',s_1'),(n_1',transfer\ (slice\ kind\ S\ a)\ s_2)) \in WS\ S$
and $S, slice\ kind\ S \vdash (n_2,s_2) = as @ [a] \Rightarrow (n_1',transfer\ (slice\ kind\ S\ a)\ s_2)$
 $\langle proof \rangle$

definition $is\ weak\ sim ::$

$(('node \times 'state) \times ('node \times 'state))\ set \Rightarrow 'node\ set \Rightarrow bool$
where $is\ weak\ sim\ R\ S \equiv$
 $\forall n_1\ s_1\ n_2\ s_2\ n_1'\ s_1'\ as.\ ((n_1,s_1),(n_2,s_2)) \in R \wedge S, kind \vdash (n_1,s_1) = as \Rightarrow (n_1',s_1')$
 $\longrightarrow (\exists n_2'\ s_2'\ as'. ((n_1',s_1'),(n_2',s_2')) \in R \wedge$
 $S, slice\ kind\ S \vdash (n_2,s_2) = as' \Rightarrow (n_2',s_2'))$

lemma $WS\ weak\ sim$:

assumes $((n_1,s_1),(n_2,s_2)) \in WS\ S$

and $S, kind \vdash (n_1, s_1) = as \Rightarrow (n_1', s_1')$
shows $((n_1', s_1'), (n_1', transfer (slice-kind S (last as)) s_2)) \in WS S \wedge$
 $(\exists as'. S, slice-kind S \vdash (n_2, s_2) = as' @ [last as] \Rightarrow$
 $(n_1', transfer (slice-kind S (last as)) s_2))$
 $\langle proof \rangle$

The following lemma states the correctness of static intraprocedural slicing:
the simulation $WS S$ is a desired weak simulation

theorem $WS-is-weak-sim:is-weak-sim (WS S) S$
 $\langle proof \rangle$

3.3.5 $n - as \rightarrow^* n'$ and transitive closure of $S, f \vdash (n, s) = as \Rightarrow_\tau (n', s')$

inductive $trans-observable-moves ::$

$'node\ set \Rightarrow ('edge \Rightarrow 'state\ edge-kind) \Rightarrow 'node \Rightarrow 'state \Rightarrow 'edge\ list \Rightarrow$
 $'node \Rightarrow 'state \Rightarrow bool\ (-, - \vdash '(-, -) = \Rightarrow^* '(-, -) [51, 50, 0, 0, 50, 0, 0] 51)$

where $tom-Nil:$

$S, f \vdash (n, s) = [] \Rightarrow^* (n, s)$

| $tom-Cons:$

$\llbracket S, f \vdash (n, s) = as \Rightarrow (n', s'); S, f \vdash (n', s') = as' \Rightarrow^* (n'', s'') \rrbracket$
 $\implies S, f \vdash (n, s) = (last\ as) \# as' \Rightarrow^* (n'', s'')$

definition $slice-edges :: 'node\ set \Rightarrow 'edge\ list \Rightarrow 'edge\ list$

where $slice-edges S as \equiv [a \leftarrow as. sourcenode\ a \in backward-slice\ S]$

lemma $silent-moves-no-slice-edges:$

$S, f \vdash (n, s) = as \Rightarrow_\tau (n', s') \implies slice-edges S as = []$

$\langle proof \rangle$

lemma $observable-moves-last-slice-edges:$

$S, f \vdash (n, s) = as \Rightarrow (n', s') \implies slice-edges S as = [last\ as]$

$\langle proof \rangle$

lemma $slice-edges-no-nodes-in-slice:$

$slice-edges S as = []$

$\implies \forall nx \in set(sourcenodes\ as). nx \notin (backward-slice\ S)$

$\langle proof \rangle$

lemma $sliced-path-determ:$

$\llbracket n - as \rightarrow^* n'; n - as' \rightarrow^* n'; slice-edges S as = slice-edges S as' \rrbracket$

$\text{preds } (\text{slice-kinds } S \text{ as}) \text{ s}; \text{preds } (\text{slice-kinds } S \text{ as}') \text{ s}'; n' \in S;$
 $\forall V \in \text{rv } S \text{ n. state-val } s \text{ V} = \text{state-val } s' \text{ V} \implies \text{as} = \text{as}'$
 $\langle \text{proof} \rangle$

lemma *path-trans-observable-moves*:

assumes $n - \text{as} \rightarrow^* n'$ **and** $\text{preds } (\text{kinds as}) \text{ s}$ **and** $\text{transfers } (\text{kinds as}) \text{ s} = s'$
obtains $n'' \text{ s}'' \text{ as}' \text{ as}''$ **where** $S, \text{kind} \vdash (n, s) = \text{slice-edges } S \text{ as} \Rightarrow^* (n'', s'')$
and $S, \text{kind} \vdash (n'', s'') = \text{as}' \Rightarrow_{\tau} (n', s')$
and $\text{slice-edges } S \text{ as} = \text{slice-edges } S \text{ as}''$ **and** $n - \text{as}'' @ \text{as}' \rightarrow^* n'$
 $\langle \text{proof} \rangle$

lemma *WS-weak-sim-trans*:

assumes $((n_1, s_1), (n_2, s_2)) \in \text{WS } S$
and $S, \text{kind} \vdash (n_1, s_1) = \text{as} \Rightarrow^* (n_1', s_1')$ **and** $\text{as} \neq []$
shows $((n_1', s_1'), (n_1', \text{transfers } (\text{slice-kinds } S \text{ as}) \text{ s}_2)) \in \text{WS } S \wedge$
 $S, \text{slice-kind } S \vdash (n_2, s_2) = \text{as} \Rightarrow^* (n_1', \text{transfers } (\text{slice-kinds } S \text{ as}) \text{ s}_2)$
 $\langle \text{proof} \rangle$

lemma *transfers-slice-kinds-slice-edges*:

$\text{transfers } (\text{slice-kinds } S (\text{slice-edges } S \text{ as})) \text{ s} = \text{transfers } (\text{slice-kinds } S \text{ as}) \text{ s}$
 $\langle \text{proof} \rangle$

lemma *trans-observable-moves-preds*:

assumes $S, f \vdash (n, s) = \text{as} \Rightarrow^* (n', s')$ **and** *valid-node* n
obtains as' **where** $\text{preds } (\text{map } f \text{ as}') \text{ s}$ **and** $\text{slice-edges } S \text{ as}' = \text{as}$
and $n - \text{as}' \rightarrow^* n'$
 $\langle \text{proof} \rangle$

lemma *exists-sliced-path-preds*:

assumes $n - \text{as} \rightarrow^* n'$ **and** $\text{slice-edges } S \text{ as} = []$ **and** $n' \in \text{backward-slice } S$
obtains as' **where** $n - \text{as}' \rightarrow^* n'$ **and** $\text{preds } (\text{slice-kinds } S \text{ as}') \text{ s}$
and $\text{slice-edges } S \text{ as}' = []$
 $\langle \text{proof} \rangle$

theorem *fundamental-property-of-static-slicing*:

assumes $\text{path}: n - \text{as} \rightarrow^* n'$ **and** $\text{preds}: \text{preds } (\text{kinds as}) \text{ s}$ **and** $n' \in S$
obtains as' **where** $\text{preds } (\text{slice-kinds } S \text{ as}') \text{ s}$
and $(\forall V \in \text{Use } n'. \text{state-val } (\text{transfers } (\text{slice-kinds } S \text{ as}') \text{ s}) \text{ V} =$
 $\text{state-val } (\text{transfers } (\text{kinds as}) \text{ s}) \text{ V})$
and $\text{slice-edges } S \text{ as} = \text{slice-edges } S \text{ as}'$ **and** $n - \text{as}' \rightarrow^* n'$
 $\langle \text{proof} \rangle$

end

3.3.6 The fundamental property of (static) slicing related to the semantics

```

locale BackwardSlice-wf =
  BackwardSlice sourcenode targetnode kind valid-edge Entry Def Use state-val
  backward-slice +
  CFG-semantics-wf sourcenode targetnode kind valid-edge Entry sem identifies
  for sourcenode :: 'edge  $\Rightarrow$  'node and targetnode :: 'edge  $\Rightarrow$  'node
  and kind :: 'edge  $\Rightarrow$  'state edge-kind and valid-edge :: 'edge  $\Rightarrow$  bool
  and Entry :: 'node ('('Entry'-')) and Def :: 'node  $\Rightarrow$  'var set
  and Use :: 'node  $\Rightarrow$  'var set and state-val :: 'state  $\Rightarrow$  'var  $\Rightarrow$  'val
  and backward-slice :: 'node set  $\Rightarrow$  'node set
  and sem :: 'com  $\Rightarrow$  'state  $\Rightarrow$  'com  $\Rightarrow$  'state  $\Rightarrow$  bool
    (((1<-,->)  $\Rightarrow$  / (1<-,->)) [0,0,0,0] 81)
  and identifies :: 'node  $\Rightarrow$  'com  $\Rightarrow$  bool (-  $\triangleq$  - [51, 0] 80)

begin

theorem fundamental-property-of-path-slicing-semantically:
  assumes  $n \triangleq c$  and  $\langle c, s \rangle \Rightarrow \langle c', s' \rangle$ 
  obtains  $n'$  as where  $n - as \rightarrow^* n'$  and preds (slice-kinds { $n'$ } as)  $s$  and  $n' \triangleq c'$ 
  and  $\forall V \in Use\ n'.\ state-val\ (transfers\ (slice-kinds\ \{n'\}\ as)\ s)\ V = state-val\ s'\ V$ 
  <proof>

end

end

```

3.4 Static Standard Control Dependence

```

theory StandardControlDependence imports
  ../Basic/Postdomination
  ../Basic/DynStandardControlDependence
begin

```

context Postdomination **begin**

Definition and some lemmas

```

definition standard-control-dependence :: 'node  $\Rightarrow$  'node  $\Rightarrow$  bool

```

(- controls_s - [51,0])
where standard-control-dependences-eq: $n \text{ controls}_s n' \equiv \exists as. n \text{ controls}_s n' \text{ via } as$

lemma standard-control-dependence-def: $n \text{ controls}_s n' =$
 $(\exists a \ a' \ as. (n' \notin \text{set}(\text{sourcenodes } (a \# as))) \wedge (n - a \# as \rightarrow^* n') \wedge$
 $(n' \text{ postdominates } (\text{targetnode } a)) \wedge$
 $(\text{valid-edge } a') \wedge (\text{sourcenode } a' = n) \wedge$
 $(\neg n' \text{ postdominates } (\text{targetnode } a'))))$
 <proof>

lemma Exit-not-standard-control-dependent:
 $n \text{ controls}_s (-\text{Exit-}) \implies \text{False}$
 <proof>

lemma standard-control-dependence-def-variant:
 $n \text{ controls}_s n' = (\exists as. (n - as \rightarrow^* n') \wedge (n \neq n') \wedge$
 $(\neg n' \text{ postdominates } n) \wedge (n' \notin \text{set}(\text{sourcenodes } as))) \wedge$
 $(\forall n'' \in \text{set}(\text{targetnodes } as). n' \text{ postdominates } n''))$
 <proof>

lemma inner-node-standard-control-dependence-predecessor:
assumes inner-node $n (-\text{Entry-}) - as \rightarrow^* n \ n - as' \rightarrow^* (-\text{Exit-})$
obtains n' **where** $n' \text{ controls}_s n$
 <proof>

end

end

3.5 Static Weak Control Dependence

theory WeakControlDependence **imports**

../Basic/Postdomination

../Basic/DynWeakControlDependence

begin

context StrongPostdomination **begin**

definition

weak-control-dependence :: 'node $\Rightarrow$ 'node $\Rightarrow$ bool

(- weakly controls - [51,0])

where weak-control-dependences-eq:

$n \text{ weakly controls } n' \equiv \exists as. n \text{ weakly controls } n' \text{ via } as$

lemma

weak-control-dependence-def: $n \text{ weakly controls } n' =$

$(\exists a \ a' \text{ as. } (n' \notin \text{set}(\text{sourcenodes } (a \# \text{as})))) \wedge (n - a \# \text{as} \rightarrow^* n') \wedge$
 $(n' \text{ strongly-postdominates } (\text{targetnode } a)) \wedge$
 $(\text{valid-edge } a') \wedge (\text{sourcenode } a' = n) \wedge$
 $(\neg n' \text{ strongly-postdominates } (\text{targetnode } a'))$
 <proof>

lemma *Exit-not-weak-control-dependent:*
n weakly controls (-Exit-) $\implies$ False
 <proof>

end

end

3.6 Program Dependence Graph

theory *PDG imports*

DataDependence

StandardControlDependence

WeakControlDependence

../Basic/CFGExit-wf

begin

locale *PDG =*

CFGExit-wf sourcenode targetnode kind valid-edge Entry Def Use state-val Exit

for *sourcenode* :: 'edge $\Rightarrow$ 'node **and** *targetnode* :: 'edge $\Rightarrow$ 'node

and *kind* :: 'edge $\Rightarrow$ 'state edge-kind **and** *valid-edge* :: 'edge $\Rightarrow$ bool

and *Entry* :: 'node ('('Entry'-')) **and** *Def* :: 'node $\Rightarrow$ 'var set

and *Use* :: 'node $\Rightarrow$ 'var set **and** *state-val* :: 'state $\Rightarrow$ 'var $\Rightarrow$ 'val

and *Exit* :: 'node ('('Exit'-')) +

fixes *control-dependence* :: 'node $\Rightarrow$ 'node $\Rightarrow$ bool

(- controls - [51,0])

assumes *Exit-not-control-dependent*: *n controls n' $\implies$ n' $\neq$ (-Exit-)*

assumes *control-dependence-path*:

n controls n'

$\implies \exists \text{ as. } \text{CFG.path } \text{sourcenode } \text{targetnode } \text{valid-edge } n \text{ as } n' \wedge \text{as} \neq []$

begin

inductive *cdep-edge* :: 'node $\Rightarrow$ 'node $\Rightarrow$ bool

(- $\longrightarrow_{cd}$ - [51,0] 80)

and *ddep-edge* :: 'node $\Rightarrow$ 'var $\Rightarrow$ 'node $\Rightarrow$ bool

(- $\dashrightarrow_{dd}$ - [51,0,0] 80)

and *PDG-edge* :: 'node $\Rightarrow$ 'var option $\Rightarrow$ 'node $\Rightarrow$ bool

where

$n \longrightarrow_{cd} n' == PDG\text{-}edge\ n\ None\ n'$
 $| n - V \rightarrow_{dd} n' == PDG\text{-}edge\ n\ (Some\ V)\ n'$

$| PDG\text{-}cdep\text{-}edge:$
 $n\ controls\ n' \implies n \longrightarrow_{cd} n'$

$| PDG\text{-}ddep\text{-}edge:$
 $n\ influences\ V\ in\ n' \implies n - V \rightarrow_{dd} n'$

inductive $PDG\text{-}path :: 'node \Rightarrow 'node \Rightarrow bool$
 $(- \longrightarrow_{d*} - [51,0] 80)$

where $PDG\text{-}path\text{-}Nil:$
 $valid\text{-}node\ n \implies n \longrightarrow_{d*} n$

$| PDG\text{-}path\text{-}Append\text{-}cdep:$
 $\llbracket n \longrightarrow_{d*} n''; n'' \longrightarrow_{cd} n' \rrbracket \implies n \longrightarrow_{d*} n'$

$| PDG\text{-}path\text{-}Append\text{-}ddep:$
 $\llbracket n \longrightarrow_{d*} n''; n'' - V \rightarrow_{dd} n' \rrbracket \implies n \longrightarrow_{d*} n'$

lemma $PDG\text{-}path\text{-}cdep: n \longrightarrow_{cd} n' \implies n \longrightarrow_{d*} n'$
 $\langle proof \rangle$

lemma $PDG\text{-}path\text{-}ddep: n - V \rightarrow_{dd} n' \implies n \longrightarrow_{d*} n'$
 $\langle proof \rangle$

lemma $PDG\text{-}path\text{-}Append:$
 $\llbracket n'' \longrightarrow_{d*} n'; n \longrightarrow_{d*} n'' \rrbracket \implies n \longrightarrow_{d*} n'$
 $\langle proof \rangle$

lemma $PDG\text{-}cdep\text{-}edge\text{-}CFG\text{-}path:$
assumes $n \longrightarrow_{cd} n'$ **obtains** as **where** $n - as \rightarrow^* n'$ **and** $as \neq []$
 $\langle proof \rangle$

lemma $PDG\text{-}ddep\text{-}edge\text{-}CFG\text{-}path:$
assumes $n - V \rightarrow_{dd} n'$ **obtains** as **where** $n - as \rightarrow^* n'$ **and** $as \neq []$
 $\langle proof \rangle$

lemma $PDG\text{-}path\text{-}CFG\text{-}path:$
assumes $n \longrightarrow_{d*} n'$ **obtains** as **where** $n - as \rightarrow^* n'$
 $\langle proof \rangle$

lemma $PDG\text{-}path\text{-}Exit: \llbracket n \longrightarrow_{d*} n'; n' = (-Exit-) \rrbracket \implies n = (-Exit-)$

$\langle proof \rangle$

lemma *PDG-path-not-inner*:

$\llbracket n \longrightarrow_d^* n'; \neg \text{inner-node } n \rrbracket \implies n = n'$
 $\langle proof \rangle$

3.6.1 Definition of the static backward slice

Node: instead of a single node, we calculate the backward slice of a set of nodes.

definition *PDG-BS* :: 'node set $\Rightarrow$ 'node set

where *PDG-BS* *S* $\equiv \{n'. \exists n. n' \longrightarrow_d^* n \wedge n \in S \wedge \text{valid-node } n\}$

lemma *PDG-BS-valid-node*: $n \in \text{PDG-BS } S \implies \text{valid-node } n$

$\langle proof \rangle$

lemma *Exit-PDG-BS*: $n \in \text{PDG-BS } \{(-\text{Exit}-)\} \implies n = (-\text{Exit}-)$

$\langle proof \rangle$

end

3.6.2 Instantiate static PDG

Standard control dependence

locale *StandardControlDependencePDG* =

Postdomination sourcenode targetnode kind valid-edge Entry Exit +
CFGExit-wf sourcenode targetnode kind valid-edge Entry Def Use state-val Exit
for *sourcenode* :: 'edge $\Rightarrow$ 'node **and** *targetnode* :: 'edge $\Rightarrow$ 'node
and *kind* :: 'edge $\Rightarrow$ 'state edge-kind **and** *valid-edge* :: 'edge $\Rightarrow$ bool
and *Entry* :: 'node ('(-Entry'-)) **and** *Def* :: 'node $\Rightarrow$ 'var set
and *Use* :: 'node $\Rightarrow$ 'var set **and** *state-val* :: 'state $\Rightarrow$ 'var $\Rightarrow$ 'val
and *Exit* :: 'node ('(-Exit'-))

begin

lemma *PDG-scd*:

PDG sourcenode targetnode kind valid-edge (-Entry-)
Def Use state-val (-Exit-) standard-control-dependence
 $\langle proof \rangle \langle proof \rangle$
end

Weak control dependence

locale *WeakControlDependencePDG* =

StrongPostdomination sourcenode targetnode kind valid-edge Entry Exit +

```

CFGExit-wf sourcenode targetnode kind valid-edge Entry Def Use state-val Exit
for sourcenode :: 'edge  $\Rightarrow$  'node and targetnode :: 'edge  $\Rightarrow$  'node
and kind :: 'edge  $\Rightarrow$  'state edge-kind and valid-edge :: 'edge  $\Rightarrow$  bool
and Entry :: 'node ('('Entry'-')) and Def :: 'node  $\Rightarrow$  'var set
and Use :: 'node  $\Rightarrow$  'var set and state-val :: 'state  $\Rightarrow$  'var  $\Rightarrow$  'val
and Exit :: 'node ('('Exit'-'))

begin

lemma PDG-wcd:
  PDG sourcenode targetnode kind valid-edge (-Entry-)
  Def Use state-val (-Exit-) weak-control-dependence
  <proof><proof>
end

end

```

3.7 Weak Order Dependence

theory WeakOrderDependence **imports** ../Basic/CFG DataDependence **begin**

Weak order dependence is just defined as a static control dependence

3.7.1 Definition and some lemmas

definition (in CFG) weak-order-dependence :: 'node $\Rightarrow$ 'node $\Rightarrow$ 'node $\Rightarrow$ bool
 ($- \longrightarrow_{wod} -, -$)
where wod-def: $n \longrightarrow_{wod} n_1, n_2 \equiv ((n_1 \neq n_2) \wedge$
 ($\exists as. (n -as \rightarrow^* n_1) \wedge (n_2 \notin \text{set}(\text{sourcenodes } as))$) $\wedge$
 ($\exists as. (n -as \rightarrow^* n_2) \wedge (n_1 \notin \text{set}(\text{sourcenodes } as))$) $\wedge$
 ($\exists a. (\text{valid-edge } a) \wedge (n = \text{sourcenode } a) \wedge$
 ($(\exists as. (\text{targetnode } a -as \rightarrow^* n_1) \wedge$
 ($\forall as'. (\text{targetnode } a -as' \rightarrow^* n_2) \longrightarrow n_1 \in \text{set}(\text{sourcenodes } as'))$) $\vee$
 ($\exists as. (\text{targetnode } a -as \rightarrow^* n_2) \wedge$
 ($\forall as'. (\text{targetnode } a -as' \rightarrow^* n_1) \longrightarrow n_2 \in \text{set}(\text{sourcenodes } as'))$))))

inductive-set (in CFG-wf) wod-backward-slice :: 'node set $\Rightarrow$ 'node set

for $S :: \text{'node set}$

where $\text{refl} : \llbracket \text{valid-node } n; n \in S \rrbracket \Longrightarrow n \in \text{wod-backward-slice } S$

| *cd-closed*:

$\llbracket n' \longrightarrow_{wod} n_1, n_2; n_1 \in \text{wod-backward-slice } S; n_2 \in \text{wod-backward-slice } S \rrbracket$
 $\Longrightarrow n' \in \text{wod-backward-slice } S$

| *dd-closed*: $\llbracket n' \text{ influences } V \text{ in } n''; n'' \in \text{wod-backward-slice } S \rrbracket$

$\Longrightarrow n' \in \text{wod-backward-slice } S$

lemma (in *CFG-wf*)
wod-backward-slice-valid-node: $n \in \text{wod-backward-slice } S \implies \text{valid-node } n$
 ⟨*proof*⟩

end

3.8 Instantiate framework with control dependences

theory *CDepInstantiations* **imports**

Slice

PDG

WeakOrderDependence

begin

3.8.1 Standard control dependence

context *StandardControlDependencePDG* **begin**

lemma *Exit-in-obs-slice-node*: $(\text{-Exit-}) \in \text{obs } n' (PDG\text{-}BS \ S) \implies (\text{-Exit-}) \in S$
 ⟨*proof*⟩

abbreviation *PDG-path'* :: $'node \Rightarrow 'node \Rightarrow \text{bool}$ ($- \longrightarrow_d^* - [51, 0] \ 80$)
where $n \longrightarrow_d^* n' \equiv PDG.PDG\text{-}path \ \text{sourcenode} \ \text{targetnode} \ \text{valid-edge} \ \text{Def} \ \text{Use}$
standard-control-dependence $n \ n'$

lemma *cd-closed*:
 $\llbracket n' \in PDG\text{-}BS \ S; n \ \text{controls}_s \ n' \rrbracket \implies n \in PDG\text{-}BS \ S$
 ⟨*proof*⟩

lemma *obs-postdominate*:
assumes $n \in \text{obs } n' (PDG\text{-}BS \ S)$ **and** $n \neq (\text{-Exit-})$ **shows** $n \ \text{postdominates } n'$
 ⟨*proof*⟩

lemma *obs-singleton*: $(\exists m. \ \text{obs } n (PDG\text{-}BS \ S) = \{m\}) \vee \text{obs } n (PDG\text{-}BS \ S) = \{\}$
 ⟨*proof*⟩

lemma *PDGBackwardSliceCorrect*:
BackwardSlice *sourcenode* *targetnode* *kind* *valid-edge*
 (*-Entry-*) *Def* *Use* *state-val* *PDG-BS*
 ⟨*proof*⟩

end

3.8.2 Weak control dependence

context *WeakControlDependencePDG* **begin**

lemma *Exit-in-obs-slice-node*: $(-Exit-) \in \text{obs } n' (PDG-BS S) \implies (-Exit-) \in S$
<proof>

lemma *cd-closed*:
 $\llbracket n' \in PDG-BS S; n \text{ weakly controls } n' \rrbracket \implies n \in PDG-BS S$
<proof>

lemma *obs-strong-postdominate*:
assumes $n \in \text{obs } n' (PDG-BS S)$ **and** $n \neq (-Exit-)$
shows $n \text{ strongly-postdominates } n'$
<proof>

lemma *obs-singleton*: $(\exists m. \text{obs } n (PDG-BS S) = \{m\}) \vee \text{obs } n (PDG-BS S) = \{\}$
<proof>

lemma *WeakPDGBackwardSliceCorrect*:
BackwardSlice sourcenode targetnode kind valid-edge
(-Entry-) Def Use state-val PDG-BS
<proof>

end

3.8.3 Weak order dependence

context *CFG-wf* **begin**

lemma *obs-singleton*:
shows $(\exists m. \text{obs } n (\text{wod-backward-slice } S) = \{m\}) \vee$
 $\text{obs } n (\text{wod-backward-slice } S) = \{\}$
<proof>

lemma *WODBackwardSliceCorrect*:
BackwardSlice sourcenode targetnode kind valid-edge
(-Entry-) Def Use state-val wod-backward-slice
<proof>

end

end

3.9 Relations between control dependences

theory *ControlDependenceRelations*

imports *WeakOrderDependence StandardControlDependence*

begin

context *StrongPostdomination* **begin**

lemma *standard-control-implies-weak-order:*

assumes $n \text{ controls}_s n'$ **shows** $n \longrightarrow_{wod} n', (-Exit-)$
<proof>

end

end

Chapter 4

Instantiating the Framework with a simple While-Language

4.1 Commands

theory *Com* **imports** *Main* **begin**

4.1.1 Variables and Values

type-synonym *vname* = *string* — names for variables

datatype *val*
 = *Bool bool* — Boolean value
 | *Intg int* — integer value

abbreviation *true* == *Bool True*
abbreviation *false* == *Bool False*

4.1.2 Expressions and Commands

datatype *bop* = *Eq* | *And* | *Less* | *Add* | *Sub* — names of binary operations

datatype *expr*
 = *Val val* — value
 | *Var vname* — local variable
 | *BinOp expr bop expr* (*- <-> - [80,0,81] 80*) — binary operation

fun *binop* :: *bop* $\Rightarrow$ *val* $\Rightarrow$ *val* $\Rightarrow$ *val option*
where *binop Eq* *v₁ v₂* = *Some(Bool(v₁ = v₂))*
 | *binop And* (*Bool b₁*) (*Bool b₂*) = *Some(Bool(b₁ $\wedge$ b₂))*
 | *binop Less* (*Intg i₁*) (*Intg i₂*) = *Some(Bool(i₁ < i₂))*
 | *binop Add* (*Intg i₁*) (*Intg i₂*) = *Some(Intg(i₁ + i₂))*

```

| binop Sub (Intg i1) (Intg i2) = Some(Intg(i1 - i2))
| binop bop v1 v2 = None

```

datatype *cmd*

```

= Skip
| LAss vname expr      (·:=· [70,70] 70) — local assignment
| Seq cmd cmd          (·;;· [61,60] 60)
| Cond expr cmd cmd    (if '(-)' -/ else - [80,79,79] 70)
| While expr cmd       (while '(-)' - [80,79] 70)

```

fun *num-inner-nodes* :: *cmd* ⇒ *nat* (#:-)

```

where #:Skip = 1
| #:(V:=e) = 2
| #:(c1;;c2) = #:c1 + #:c2
| #:(if (b) c1 else c2) = #:c1 + #:c2 + 1
| #:(while (b) c) = #:c + 2

```

lemma *num-inner-nodes-gr-0*:#:*c* > 0
 ⟨*proof*⟩

lemma [*dest*]:#:*c* = 0 ⇒ *False*
 ⟨*proof*⟩

4.1.3 The state

type-synonym *state* = *vname* → *val*

fun *interpret* :: *expr* ⇒ *state* ⇒ *val option*

```

where Val: interpret (Val v) s = Some v
| Var: interpret (Var V) s = s V
| BinOp: interpret (e1⋈bop⋈e2) s =
  (case interpret e1 s of None ⇒ None
   | Some v1 ⇒ (case interpret e2 s of None ⇒ None
                  | Some v2 ⇒ (
    case binop bop v1 v2 of None ⇒ None | Some v ⇒ Some v)))

```

end

4.2 CFG

theory *WCFG* **imports** *Com ../Basic/BasicDefs* **begin**

4.2.1 CFG nodes

datatype *w-node* = *Node nat* ((' - ' -'))
 | *Entry* ((' - *Entry* ' -'))
 | *Exit* ((' - *Exit* ' -'))

fun *label-incr* :: *w-node* $\Rightarrow$ *nat* $\Rightarrow$ *w-node* (- $\oplus$ - 60)
where (- *l* -) $\oplus$ *i* = (- *l* + *i* -)
 | (- *Entry* -) $\oplus$ *i* = (- *Entry* -)
 | (- *Exit* -) $\oplus$ *i* = (- *Exit* -)

lemma *Exit-label-incr* [*dest*]: (- *Exit* -) = *n* $\oplus$ *i* $\Longrightarrow$ *n* = (- *Exit* -)
 <proof>

lemma *label-incr-Exit* [*dest*]: *n* $\oplus$ *i* = (- *Exit* -) $\Longrightarrow$ *n* = (- *Exit* -)
 <proof>

lemma *Entry-label-incr* [*dest*]: (- *Entry* -) = *n* $\oplus$ *i* $\Longrightarrow$ *n* = (- *Entry* -)
 <proof>

lemma *label-incr-Entry* [*dest*]: *n* $\oplus$ *i* = (- *Entry* -) $\Longrightarrow$ *n* = (- *Entry* -)
 <proof>

lemma *label-incr-inj*:
n $\oplus$ *c* = *n'* $\oplus$ *c* $\Longrightarrow$ *n* = *n'*
 <proof>

lemma *label-incr-simp*: *n* $\oplus$ *i* = *m* $\oplus$ (*i* + *j*) $\Longrightarrow$ *n* = *m* $\oplus$ *j*
 <proof>

lemma *label-incr-simp-rev*: *m* $\oplus$ (*j* + *i*) = *n* $\oplus$ *i* $\Longrightarrow$ *m* $\oplus$ *j* = *n*
 <proof>

lemma *label-incr-start-Node-smaller*:
 (- *l* -) = *n* $\oplus$ *i* $\Longrightarrow$ *n* = (- (*l* - *i*) -)
 <proof>

lemma *label-incr-ge*: (- *l* -) = *n* $\oplus$ *i* $\Longrightarrow$ *l* $\geq$ *i*
 <proof>

lemma *label-incr-0* [*dest*]:
 [(-0-) = *n* $\oplus$ *i*; *i* > 0] $\Longrightarrow$ *False*
 <proof>

lemma *label-incr-0-rev* [*dest*]:
 [*n* $\oplus$ *i* = (-0-); *i* > 0] $\Longrightarrow$ *False*
 <proof>

4.2.2 CFG edges

type-synonym $w\text{-edge} = (w\text{-node} \times \text{state edge-kind} \times w\text{-node})$

inductive $\text{While-CFG} :: \text{cmd} \Rightarrow w\text{-node} \Rightarrow \text{state edge-kind} \Rightarrow w\text{-node} \Rightarrow \text{bool}$
 $(- \vdash - \dashrightarrow -)$

where

WCFG-Entry-Exit :

$\text{prog} \vdash (-\text{Entry-}) -(\lambda s. \text{False})_{\checkmark} \rightarrow (-\text{Exit-})$

| WCFG-Entry :

$\text{prog} \vdash (-\text{Entry-}) -(\lambda s. \text{True})_{\checkmark} \rightarrow (-0-)$

| WCFG-Skip :

$\text{Skip} \vdash (-0-) -\uparrow id \rightarrow (-\text{Exit-})$

| WCFG-LAss :

$V := e \vdash (-0-) -\uparrow (\lambda s. s(V := (\text{interpret } e \ s))) \rightarrow (-1-)$

| WCFG-LAssSkip :

$V := e \vdash (-1-) -\uparrow id \rightarrow (-\text{Exit-})$

| WCFG-SeqFirst :

$\llbracket c_1 \vdash n -et \rightarrow n'; n' \neq (-\text{Exit-}) \rrbracket \implies c_1;;c_2 \vdash n -et \rightarrow n'$

| WCFG-SeqConnect :

$\llbracket c_1 \vdash n -et \rightarrow (-\text{Exit-}); n \neq (-\text{Entry-}) \rrbracket \implies c_1;;c_2 \vdash n -et \rightarrow (-0-) \oplus \# : c_1$

| WCFG-SeqSecond :

$\llbracket c_2 \vdash n -et \rightarrow n'; n \neq (-\text{Entry-}) \rrbracket \implies c_1;;c_2 \vdash n \oplus \# : c_1 -et \rightarrow n' \oplus \# : c_1$

| WCFG-CondTrue :

$\text{if } (b) \ c_1 \text{ else } c_2 \vdash (-0-) -(\lambda s. \text{interpret } b \ s = \text{Some true})_{\checkmark} \rightarrow (-0-) \oplus 1$

| WCFG-CondFalse :

$\text{if } (b) \ c_1 \text{ else } c_2 \vdash (-0-) -(\lambda s. \text{interpret } b \ s = \text{Some false})_{\checkmark} \rightarrow (-0-) \oplus (\# : c_1 + 1)$

| WCFG-CondThen :

$\llbracket c_1 \vdash n -et \rightarrow n'; n \neq (-\text{Entry-}) \rrbracket \implies \text{if } (b) \ c_1 \text{ else } c_2 \vdash n \oplus 1 -et \rightarrow n' \oplus 1$

| WCFG-CondElse :

$\llbracket c_2 \vdash n -et \rightarrow n'; n \neq (-\text{Entry-}) \rrbracket$
 $\implies \text{if } (b) \ c_1 \text{ else } c_2 \vdash n \oplus (\# : c_1 + 1) -et \rightarrow n' \oplus (\# : c_1 + 1)$

| WCFG-WhileTrue :

$\text{while } (b) \ c' \vdash (-0-) -(\lambda s. \text{interpret } b \ s = \text{Some true})_{\checkmark} \rightarrow (-0-) \oplus 2$

| WCFG-WhileFalse :

$while\ (b)\ c' \vdash (-0-) \rightarrow (\lambda s. interpret\ b\ s = Some\ false)_{\checkmark} \rightarrow (-1-)$

| *WCFG-WhileFalseSkip*:

$while\ (b)\ c' \vdash (-1-) \rightarrow \uparrow id \rightarrow (-Exit-)$

| *WCFG-WhileBody*:

$\llbracket c' \vdash n \rightarrow et \rightarrow n'; n \neq (-Entry-); n' \neq (-Exit-) \rrbracket$

$\implies while\ (b)\ c' \vdash n \oplus 2 \rightarrow et \rightarrow n' \oplus 2$

| *WCFG-WhileBodyExit*:

$\llbracket c' \vdash n \rightarrow et \rightarrow (-Exit-); n \neq (-Entry-) \rrbracket \implies while\ (b)\ c' \vdash n \oplus 2 \rightarrow et \rightarrow (-0-)$

lemmas *WCFG-intros* = *While-CFG.intros*[*split-format (complete)*]

lemmas *WCFG-elim*s = *While-CFG.cases*[*split-format (complete)*]

lemmas *WCFG-induct* = *While-CFG.induct*[*split-format (complete)*]

4.2.3 Some lemmas about the CFG

lemma *WCFG-Exit-no-sourcenode* [*dest*]:

$prog \vdash (-Exit-) \rightarrow et \rightarrow n' \implies False$

<proof>

lemma *WCFG-Entry-no-targetnode* [*dest*]:

$prog \vdash n \rightarrow et \rightarrow (-Entry-) \implies False$

<proof>

lemma *WCFG-sourcelabel-less-num-nodes*:

$prog \vdash (-l-) \rightarrow et \rightarrow n' \implies l < \# : prog$

<proof>

lemma *WCFG-targetlabel-less-num-nodes*:

$prog \vdash n \rightarrow et \rightarrow (-l-) \implies l < \# : prog$

<proof>

lemma *WCFG-EntryD*:

$prog \vdash (-Entry-) \rightarrow et \rightarrow n'$

$\implies (n' = (-Exit-) \wedge et = (\lambda s. False)_{\checkmark}) \vee (n' = (-0-) \wedge et = (\lambda s. True)_{\checkmark})$

<proof>

lemma *WCFG-edge-det*:

$\llbracket prog \vdash n \rightarrow et \rightarrow n'; prog \vdash n \rightarrow et' \rightarrow n' \rrbracket \implies et = et'$

<proof>

lemma *less-num-nodes-edge-Exit*:

obtains *l et* **where** $l < \# : prog$ **and** $prog \vdash (-l-) \rightarrow et \rightarrow (-Exit-)$

<proof>

lemma *less-num-nodes-edge*:

$l < \# : \text{prog} \implies \exists n \text{ et. } \text{prog} \vdash n - \text{et} \rightarrow (- l -) \vee \text{prog} \vdash (- l -) - \text{et} \rightarrow n$
 $\langle \text{proof} \rangle$

lemma *WCFG-deterministic*:

$\llbracket \text{prog} \vdash n_1 - \text{et}_1 \rightarrow n_1'; \text{prog} \vdash n_2 - \text{et}_2 \rightarrow n_2'; n_1 = n_2; n_1' \neq n_2' \rrbracket$
 $\implies \exists Q Q'. \text{et}_1 = (Q)_{\vee} \wedge \text{et}_2 = (Q')_{\vee} \wedge (\forall s. (Q s \longrightarrow \neg Q' s) \wedge (Q' s \longrightarrow \neg Q s))$
 $\langle \text{proof} \rangle$
end

4.3 Instantiate CFG locale with While CFG

theory *Interpretation* **imports**

WCFG

../Basic/CFGExit

begin

4.3.1 Instatiation of the CFG locale

abbreviation *sourcenode* :: $w\text{-edge} \Rightarrow w\text{-node}$

where *sourcenode* $e \equiv \text{fst } e$

abbreviation *targetnode* :: $w\text{-edge} \Rightarrow w\text{-node}$

where *targetnode* $e \equiv \text{snd}(\text{snd } e)$

abbreviation *kind* :: $w\text{-edge} \Rightarrow \text{state edge-kind}$

where *kind* $e \equiv \text{fst}(\text{snd } e)$

definition *valid-edge* :: $\text{cmd} \Rightarrow w\text{-edge} \Rightarrow \text{bool}$

where *valid-edge* $\text{prog } a \equiv \text{prog} \vdash \text{sourcenode } a - \text{kind } a \rightarrow \text{targetnode } a$

definition *valid-node* :: $\text{cmd} \Rightarrow w\text{-node} \Rightarrow \text{bool}$

where *valid-node* $\text{prog } n \equiv$

$(\exists a. \text{valid-edge } \text{prog } a \wedge (n = \text{sourcenode } a \vee n = \text{targetnode } a))$

lemma *While-CFG-aux*:

CFG sourcenode targetnode (valid-edge prog) Entry

$\langle \text{proof} \rangle$

interpretation *While-CFG*:

CFG sourcenode targetnode kind valid-edge prog Entry

for *prog*

$\langle \text{proof} \rangle$

lemma *While-CFGExit-aux*:
CFGExit sourcenode targetnode kind (valid-edge prog) Entry Exit
 $\langle \text{proof} \rangle$

interpretation *While-CFGExit*:
CFGExit sourcenode targetnode kind valid-edge prog Entry Exit
for *prog*
 $\langle \text{proof} \rangle$

end

4.4 Labels

theory *Labels* **imports** *Com* **begin**

Labels describe a mapping from the inner node label to the matching command

inductive *labels* :: *cmd* $\Rightarrow$ *nat* $\Rightarrow$ *cmd* $\Rightarrow$ *bool*
where

Labels-Base:

labels *c* 0 *c*

| *Labels-LAss*:

labels (*V*:=*e*) 1 *Skip*

| *Labels-Seq1*:

labels *c*₁ *l* *c* $\Longrightarrow$ *labels* (*c*₁;;*c*₂) *l* (*c*;;*c*₂)

| *Labels-Seq2*:

labels *c*₂ *l* *c* $\Longrightarrow$ *labels* (*c*₁;;*c*₂) (*l* + #:*c*₁) *c*

| *Labels-CondTrue*:

labels *c*₁ *l* *c* $\Longrightarrow$ *labels* (if (*b*) *c*₁ else *c*₂) (*l* + 1) *c*

| *Labels-CondFalse*:

labels *c*₂ *l* *c* $\Longrightarrow$ *labels* (if (*b*) *c*₁ else *c*₂) (*l* + #:*c*₁ + 1) *c*

| *Labels-WhileBody*:

labels *c'* *l* *c* $\Longrightarrow$ *labels* (while(*b*) *c'*) (*l* + 2) (*c*;;while(*b*) *c'*)

| *Labels-WhileExit*:

labels (while(*b*) *c'*) 1 *Skip*

lemma *label-less-num-inner-nodes*:

labels *c* *l* *c'* $\Longrightarrow$ *l* < #:*c*

$\langle \text{proof} \rangle$

declare *One-nat-def* [*simp del*]

lemma *less-num-inner-nodes-label*:

$l < \# : c \implies \exists c'. \text{labels } c \text{ l } c'$
 $\langle \text{proof} \rangle$

lemma *labels-det*:

$\text{labels } c \text{ l } c' \implies (\bigwedge c''. \text{labels } c \text{ l } c'' \implies c' = c'')$
 $\langle \text{proof} \rangle$

end

4.5 General well-formedness of While CFG

theory *WellFormed* **imports**

Interpretation

Labels

../Basic/CFGExit-wf

../StaticIntra/CDepInstantiations

begin

4.5.1 Definition of some functions

fun *lhs* :: *cmd* $\Rightarrow$ *vname set*

where

$\text{lhs } \text{Skip} = \{\}$
 $\mid \text{lhs } (V := e) = \{V\}$
 $\mid \text{lhs } (c_1 ;; c_2) = \text{lhs } c_1$
 $\mid \text{lhs } (\text{if } (b) \ c_1 \ \text{else } c_2) = \{\}$
 $\mid \text{lhs } (\text{while } (b) \ c) = \{\}$

fun *rhs-aux* :: *expr* $\Rightarrow$ *vname set*

where

$\text{rhs-aux } (\text{Val } v) = \{\}$
 $\mid \text{rhs-aux } (\text{Var } V) = \{V\}$
 $\mid \text{rhs-aux } (e1 \ll \text{bop} \gg e2) = (\text{rhs-aux } e1 \cup \text{rhs-aux } e2)$

fun *rhs* :: *cmd* $\Rightarrow$ *vname set*

where

$\text{rhs } \text{Skip} = \{\}$
 $\mid \text{rhs } (V := e) = \text{rhs-aux } e$
 $\mid \text{rhs } (c_1 ;; c_2) = \text{rhs } c_1$
 $\mid \text{rhs } (\text{if } (b) \ c_1 \ \text{else } c_2) = \text{rhs-aux } b$
 $\mid \text{rhs } (\text{while } (b) \ c) = \text{rhs-aux } b$

lemma *rhs-interpret-eq*:

$\llbracket \text{interpret } b \ s = \text{Some } v'; \forall V \in \text{rhs-aux } b. \ s \ V = s' \ V \rrbracket$
 $\implies \text{interpret } b \ s' = \text{Some } v'$
 $\langle \text{proof} \rangle$

fun *Defs* :: *cmd* $\Rightarrow$ *w-node* $\Rightarrow$ *vname set*

where *Defs prog n* = $\{ V. \exists l \ c. \ n = (- \ l \ -) \wedge \text{labels } \text{prog } l \ c \wedge V \in \text{lhs } c \}$

fun *Uses* :: *cmd* $\Rightarrow$ *w-node* $\Rightarrow$ *vname set*

where *Uses prog n* = $\{ V. \exists l \ c. \ n = (- \ l \ -) \wedge \text{labels } \text{prog } l \ c \wedge V \in \text{rhs } c \}$

4.5.2 Lemmas about $\text{prog} \vdash n \text{ --et--} \rightarrow n'$ to show well-formed properties

lemma *WCFG-edge-no-Defs-equal*:

$\llbracket \text{prog} \vdash n \text{ --et--} \rightarrow n'; V \notin \text{Defs } \text{prog } n \rrbracket \implies (\text{transfer } \text{et } s) \ V = s \ V$
 $\langle \text{proof} \rangle$

lemma *WCFG-edge-transfer-uses-only-Uses*:

$\llbracket \text{prog} \vdash n \text{ --et--} \rightarrow n'; \forall V \in \text{Uses } \text{prog } n. \ s \ V = s' \ V \rrbracket$
 $\implies \forall V \in \text{Defs } \text{prog } n. (\text{transfer } \text{et } s) \ V = (\text{transfer } \text{et } s') \ V$
 $\langle \text{proof} \rangle$

lemma *WCFG-edge-Uses-pred-eq*:

$\llbracket \text{prog} \vdash n \text{ --et--} \rightarrow n'; \forall V \in \text{Uses } \text{prog } n. \ s \ V = s' \ V; \text{pred } \text{et } s \rrbracket$
 $\implies \text{pred } \text{et } s'$
 $\langle \text{proof} \rangle$

interpretation *While-CFG-wf*: *CFG-wf sourcenode targetnode kind*

valid-edge prog Entry Defs prog Uses prog id

for *prog*

$\langle \text{proof} \rangle$

lemma *While-CFGExit-wf-aux*: *CFGExit-wf sourcenode targetnode kind*

(valid-edge prog) Entry (Defs prog) (Uses prog) id Exit

$\langle \text{proof} \rangle$

interpretation *While-CFGExit-wf*: *CFGExit-wf sourcenode targetnode kind*

valid-edge prog Entry Defs prog Uses prog id Exit

for *prog*

$\langle \text{proof} \rangle$

end

4.6 Lemmas for the control dependences

theory *AdditionalLemmas* **imports** *WellFormed*
begin

4.6.1 Paths to (-Exit-) and from (-Entry-) exist

abbreviation *path* :: *cmd* $\Rightarrow$ *w-node* $\Rightarrow$ *w-edge list* $\Rightarrow$ *w-node* $\Rightarrow$ *bool*
 (- $\vdash$ - $\dashv\vdash$ $\rightarrow^*$ -)

where *prog* $\vdash$ *n* - *as* $\rightarrow^*$ *n'* $\equiv$ *CFG.path* *sourcenode* *targetnode* (*valid-edge prog*)

n as n'

definition *label-incrs* :: *w-edge list* $\Rightarrow$ *nat* $\Rightarrow$ *w-edge list* (- $\oplus$ *s* - 60)
where *as* $\oplus$ *s* $\equiv$ *map* ($\lambda(n, et, n'). (n \oplus i, et, n' \oplus i)$) *as*

lemma *path-SeqFirst*:

prog $\vdash$ *n* - *as* $\rightarrow^*$ (- *l* -) $\implies$ *prog*;; *c*₂ $\vdash$ *n* - *as* $\rightarrow^*$ (- *l* -)
 <proof>

lemma *path-SeqSecond*:

$\llbracket prog \vdash n - as \rightarrow^* n'; n \neq (-Entry-); as \neq [] \rrbracket$
 $\implies c_1;; prog \vdash n \oplus \# : c_1 - as \oplus s \# : c_1 \rightarrow^* n' \oplus \# : c_1$
 <proof>

lemma *path-CondTrue*:

prog $\vdash$ (- *l* -) - *as* $\rightarrow^*$ *n'*
 $\implies$ if (*b*) *prog* else *c*₂ $\vdash$ (- *l* -) $\oplus$ 1 - *as* $\oplus$ *s* 1 $\rightarrow^*$ *n'* $\oplus$ 1
 <proof>

lemma *path-CondFalse*:

prog $\vdash$ (- *l* -) - *as* $\rightarrow^*$ *n'*
 $\implies$ if (*b*) *c*₁ else *prog* $\vdash$ (- *l* -) $\oplus$ ($\# : c_1 + 1$) - *as* $\oplus$ *s* ($\# : c_1 + 1$) $\rightarrow^*$ *n'* $\oplus$ ($\# : c_1 + 1$)
 <proof>

lemma *path-While*:

prog $\vdash$ (- *l* -) - *as* $\rightarrow^*$ (- *l'* -)
 $\implies$ while (*b*) *prog* $\vdash$ (- *l* -) $\oplus$ 2 - *as* $\oplus$ *s* 2 $\rightarrow^*$ (- *l'* -) $\oplus$ 2
 <proof>

lemma *inner-node-Entry-Exit-path*:

l < $\# : prog \implies (\exists as. prog \vdash (- l -) - as \rightarrow^* (-Exit-)) \wedge$
 $(\exists as. prog \vdash (-Entry-) - as \rightarrow^* (- l -))$
 <proof>

lemma *valid-node-Exit-path*:
assumes *valid-node prog n* **shows** $\exists as. prog \vdash n \text{ --} as \rightarrow^* (-Exit-)$
 $\langle proof \rangle$

lemma *valid-node-Entry-path*:
assumes *valid-node prog n* **shows** $\exists as. prog \vdash (-Entry-) \text{ --} as \rightarrow^* n$
 $\langle proof \rangle$

4.6.2 Some finiteness considerations

lemma *finite-labels:finite* $\{l. \exists c. labels\ prog\ l\ c\}$
 $\langle proof \rangle$

lemma *finite-valid-nodes:finite* $\{n. valid-node\ prog\ n\}$
 $\langle proof \rangle$

lemma *finite-successors*:
 $finite\ \{n'. \exists a'. valid-edge\ prog\ a' \wedge sourcenode\ a' = n \wedge$
 $\quad\quad\quad targetnode\ a' = n'\}$
 $\langle proof \rangle$

end

4.7 Interpretations of the various dynamic control dependences

theory *DynamicControlDependences* **imports** *AdditionalLemmas ../Dynamic/DynPDG*
begin

interpretation *WDynStandardControlDependence*:
 $DynStandardControlDependencePDG\ sourcenode\ targetnode\ kind\ valid-edge\ prog$
 $Entry\ Defs\ prog\ Uses\ prog\ id\ Exit$
for *prog*
 $\langle proof \rangle$

interpretation *WDynWeakControlDependence*:
 $DynWeakControlDependencePDG\ sourcenode\ targetnode\ kind\ valid-edge\ prog$
 $Entry\ Defs\ prog\ Uses\ prog\ id\ Exit$
for *prog*
 $\langle proof \rangle$

end

4.8 Semantics

theory *Semantics* **imports** *Labels Com* **begin**

4.8.1 Small Step Semantics

inductive *red* :: *cmd* * *state* $\Rightarrow$ *cmd* * *state* $\Rightarrow$ *bool*

and *red'* :: *cmd* $\Rightarrow$ *state* $\Rightarrow$ *cmd* $\Rightarrow$ *state* $\Rightarrow$ *bool*

((*(1* <-/-) $\rightarrow$ / (*1* <-/-)) [*0,0,0,0*] 81)

where

$\langle c, s \rangle \rightarrow \langle c', s' \rangle == \text{red } (c, s) (c', s')$

| *RedLAss*:

$\langle V := e, s \rangle \rightarrow \langle \text{Skip}, s(V := (\text{interpret } e \ s)) \rangle$

| *SeqRed*:

$\langle c_1, s \rangle \rightarrow \langle c_1', s' \rangle \Longrightarrow \langle c_1;;c_2, s \rangle \rightarrow \langle c_1';c_2, s' \rangle$

| *RedSeq*:

$\langle \text{Skip};;c_2, s \rangle \rightarrow \langle c_2, s \rangle$

| *RedCondTrue*:

$\text{interpret } b \ s = \text{Some true} \Longrightarrow \langle \text{if } (b) \ c_1 \ \text{else } c_2, s \rangle \rightarrow \langle c_1, s \rangle$

| *RedCondFalse*:

$\text{interpret } b \ s = \text{Some false} \Longrightarrow \langle \text{if } (b) \ c_1 \ \text{else } c_2, s \rangle \rightarrow \langle c_2, s \rangle$

| *RedWhileTrue*:

$\text{interpret } b \ s = \text{Some true} \Longrightarrow \langle \text{while } (b) \ c, s \rangle \rightarrow \langle c;;\text{while } (b) \ c, s \rangle$

| *RedWhileFalse*:

$\text{interpret } b \ s = \text{Some false} \Longrightarrow \langle \text{while } (b) \ c, s \rangle \rightarrow \langle \text{Skip}, s \rangle$

lemmas *red-induct* = *red.induct*[*split-format* (*complete*)]

abbreviation *reds* :: *cmd* $\Rightarrow$ *state* $\Rightarrow$ *cmd* $\Rightarrow$ *state* $\Rightarrow$ *bool*

((*(1* <-/-) $\rightarrow$ * / (*1* <-/-)) [*0,0,0,0*] 81) **where**

$\langle c, s \rangle \rightarrow^* \langle c', s' \rangle == \text{red}^{**} (c, s) (c', s')$

4.8.2 Label Semantics

inductive *step* :: *cmd* $\Rightarrow$ *cmd* $\Rightarrow$ *state* $\Rightarrow$ *nat* $\Rightarrow$ *cmd* $\Rightarrow$ *state* $\Rightarrow$ *nat* $\Rightarrow$ *bool*

(($\vdash$ (*1* <-/-, -/-) $\rightsquigarrow$ / (*1* <-/-, -/-)) [*51,0,0,0,0,0,0*] 81)

where

StepLAss:

$V := e \vdash \langle V := e, s, 0 \rangle \rightsquigarrow \langle \text{Skip}, s(V := (\text{interpret } e \ s)), 1 \rangle$

| *StepSeq*:

$\llbracket \text{labels } (c_1;;c_2) \ l \ (\text{Skip};;c_2); \text{labels } (c_1;;c_2) \ \# : c_1 \ c_2; \ l < \# : c_1 \rrbracket$

$\Longrightarrow c_1;;c_2 \vdash \langle \text{Skip};;c_2, s, l \rangle \rightsquigarrow \langle c_2, s, \# : c_1 \rangle$

| *StepSeqWhile*:
labels $(while\ (b)\ c')\ l\ (Skip;;while\ (b)\ c')$
 $\implies while\ (b)\ c' \vdash \langle Skip;;while\ (b)\ c',s,l \rangle \rightsquigarrow \langle while\ (b)\ c',s,0 \rangle$

| *StepCondTrue*:
interpret $b\ s = Some\ true$
 $\implies if\ (b)\ c_1\ else\ c_2 \vdash \langle if\ (b)\ c_1\ else\ c_2,s,0 \rangle \rightsquigarrow \langle c_1,s,1 \rangle$

| *StepCondFalse*:
interpret $b\ s = Some\ false$
 $\implies if\ (b)\ c_1\ else\ c_2 \vdash \langle if\ (b)\ c_1\ else\ c_2,s,0 \rangle \rightsquigarrow \langle c_2,s,\# : c_1 + 1 \rangle$

| *StepWhileTrue*:
interpret $b\ s = Some\ true$
 $\implies while\ (b)\ c \vdash \langle while\ (b)\ c,s,0 \rangle \rightsquigarrow \langle c;;while\ (b)\ c,s,2 \rangle$

| *StepWhileFalse*:
interpret $b\ s = Some\ false \implies while\ (b)\ c \vdash \langle while\ (b)\ c,s,0 \rangle \rightsquigarrow \langle Skip,s,1 \rangle$

| *StepRecSeq1*:
 $prog \vdash \langle c,s,l \rangle \rightsquigarrow \langle c',s',l' \rangle$
 $\implies prog;;c_2 \vdash \langle c;;c_2,s,l \rangle \rightsquigarrow \langle c';;c_2,s',l' \rangle$

| *StepRecSeq2*:
 $prog \vdash \langle c,s,l \rangle \rightsquigarrow \langle c',s',l' \rangle$
 $\implies c_1;;prog \vdash \langle c,s,l + \# : c_1 \rangle \rightsquigarrow \langle c',s',l' + \# : c_1 \rangle$

| *StepRecCond1*:
 $prog \vdash \langle c,s,l \rangle \rightsquigarrow \langle c',s',l' \rangle$
 $\implies if\ (b)\ prog\ else\ c_2 \vdash \langle c,s,l + 1 \rangle \rightsquigarrow \langle c',s',l' + 1 \rangle$

| *StepRecCond2*:
 $prog \vdash \langle c,s,l \rangle \rightsquigarrow \langle c',s',l' \rangle$
 $\implies if\ (b)\ c_1\ else\ prog \vdash \langle c,s,l + \# : c_1 + 1 \rangle \rightsquigarrow \langle c',s',l' + \# : c_1 + 1 \rangle$

| *StepRecWhile*:
 $cx \vdash \langle c,s,l \rangle \rightsquigarrow \langle c',s',l' \rangle$
 $\implies while\ (b)\ cx \vdash \langle c;;while\ (b)\ cx,s,l + 2 \rangle \rightsquigarrow \langle c';;while\ (b)\ cx,s',l' + 2 \rangle$

lemma *step-label-less*:

$prog \vdash \langle c,s,l \rangle \rightsquigarrow \langle c',s',l' \rangle \implies l < \# : prog \wedge l' < \# : prog$
 $\langle proof \rangle$

abbreviation *steps* :: $cmd \Rightarrow cmd \Rightarrow state \Rightarrow nat \Rightarrow cmd \Rightarrow state \Rightarrow nat \Rightarrow bool$
 $((- \vdash (1 \langle -, / -, / - \rangle) \rightsquigarrow * / (1 \langle -, / -, / - \rangle)))\ [51, 0, 0, 0, 0, 0, 0]\ 81$ **where**

$$\begin{aligned} & \text{prog} \vdash \langle c, s, l \rangle \rightsquigarrow^* \langle c', s', l' \rangle == \\ & (\lambda(c, s, l) (c', s', l')). \text{prog} \vdash \langle c, s, l \rangle \rightsquigarrow \langle c', s', l' \rangle^{**} (c, s, l) (c', s', l') \end{aligned}$$

4.8.3 Proof of bisimulation of $\langle c, s \rangle \rightarrow \langle c', s' \rangle$ and $\text{prog} \vdash \langle c, s, l \rangle \rightsquigarrow^* \langle c', s', l' \rangle$ via labels

From $\text{prog} \vdash \langle c, s, l \rangle \rightsquigarrow^* \langle c', s', l' \rangle$ **to** $\langle c, s \rangle \rightarrow \langle c', s' \rangle$

lemma *step-red*:

$$\begin{aligned} & \text{prog} \vdash \langle c, s, l \rangle \rightsquigarrow \langle c', s', l' \rangle \implies \langle c, s \rangle \rightarrow \langle c', s' \rangle \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma *steps-reds*:

$$\begin{aligned} & \text{prog} \vdash \langle c, s, l \rangle \rightsquigarrow^* \langle c', s', l' \rangle \implies \langle c, s \rangle \rightarrow^* \langle c', s' \rangle \\ & \langle \text{proof} \rangle \end{aligned}$$

From $\langle c, s \rangle \rightarrow \langle c', s' \rangle$ **and labels to** $\text{prog} \vdash \langle c, s, l \rangle \rightsquigarrow^* \langle c', s', l' \rangle$

lemma *red-step*:

$$\begin{aligned} & \llbracket \text{labels prog } l \ c; \langle c, s \rangle \rightarrow \langle c', s' \rangle \rrbracket \\ & \implies \exists l'. \text{prog} \vdash \langle c, s, l \rangle \rightsquigarrow \langle c', s', l' \rangle \wedge \text{labels prog } l' \ c' \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma *reds-steps*:

$$\begin{aligned} & \llbracket \langle c, s \rangle \rightarrow^* \langle c', s' \rangle; \text{labels prog } l \ c \rrbracket \\ & \implies \exists l'. \text{prog} \vdash \langle c, s, l \rangle \rightsquigarrow^* \langle c', s', l' \rangle \wedge \text{labels prog } l' \ c' \\ & \langle \text{proof} \rangle \end{aligned}$$

The bisimulation theorem

theorem *reds-steps-bisimulation*:

$$\begin{aligned} & \text{labels prog } l \ c \implies (\langle c, s \rangle \rightarrow^* \langle c', s' \rangle) = \\ & (\exists l'. \text{prog} \vdash \langle c, s, l \rangle \rightsquigarrow^* \langle c', s', l' \rangle \wedge \text{labels prog } l' \ c') \\ & \langle \text{proof} \rangle \end{aligned}$$

end

4.9 Equivalence

theory *WEquivalence* **imports** *Semantics WCFG* **begin**

4.9.1 From $\text{prog} \vdash \langle c, s, l \rangle \rightsquigarrow \langle c', s', l' \rangle$ **to**

$c \vdash (- \ l \ -) \text{--et--} \rightarrow (- \ l' \ -)$ **with transfers and preds**

lemma *Skip-WCFG-edge-Exit*:

$\llbracket \text{labels prog } l \text{ Skip} \rrbracket \implies \text{prog} \vdash (- l -) \dashv\!\!\rightarrow (-\text{Exit-})$
 $\langle \text{proof} \rangle$

lemma *step-WCFG-edge*:

assumes $\text{prog} \vdash \langle c, s, l \rangle \rightsquigarrow \langle c', s', l' \rangle$
obtains *et* **where** $\text{prog} \vdash (- l -) \dashv\!\!\rightarrow (- l' -)$ **and** *transfer et s = s'*
and *pred et s*
 $\langle \text{proof} \rangle$

4.9.2 From $c \vdash (- l -) \dashv\!\!\rightarrow (- l' -)$ **with** *transfers* **and** *preds* **to**
 $\text{prog} \vdash \langle c, s, l \rangle \rightsquigarrow \langle c', s', l' \rangle$

lemma *WCFG-edge-Exit-Skip*:

$\llbracket \text{prog} \vdash n \dashv\!\!\rightarrow (-\text{Exit-}); n \neq (-\text{Entry-}) \rrbracket$
 $\implies \exists l. n = (- l -) \wedge \text{labels prog } l \text{ Skip} \wedge \text{et} = \dashv\!\!\rightarrow$
 $\langle \text{proof} \rangle$

lemma *WCFG-edge-step*:

$\llbracket \text{prog} \vdash (- l -) \dashv\!\!\rightarrow (- l' -); \text{transfer et } s = s'; \text{pred et } s \rrbracket$
 $\implies \exists c c'. \text{prog} \vdash \langle c, s, l \rangle \rightsquigarrow \langle c', s', l' \rangle \wedge \text{labels prog } l \text{ c} \wedge \text{labels prog } l' \text{ c'}$
 $\langle \text{proof} \rangle$

end

4.10 Semantic well-formedness of While CFG

theory *SemanticsWellFormed*

imports *WellFormed WEquivalence ../Basic/SemanticsCFG*
begin

4.10.1 Instatiation of the *CFG-semantics-wf* locale

fun *labels-nodes* :: *cmd* $\Rightarrow$ *w-node* $\Rightarrow$ *cmd* $\Rightarrow$ *bool* **where**
 $\text{labels-nodes prog } (- l -) \text{ c} = \text{labels prog } l \text{ c}$
 $\mid \text{labels-nodes prog } (-\text{Entry-}) \text{ c} = \text{False}$
 $\mid \text{labels-nodes prog } (-\text{Exit-}) \text{ c} = \text{False}$

interpretation *While-semantics-CFG-wf*: *CFG-semantics-wf*

sourcenode targetnode kind valid-edge prog Entry reds labels-nodes prog
for *prog*
 $\langle \text{proof} \rangle$

end

4.11 Interpretations of the various static control dependences

theory *StaticControlDependences* **imports**
AdditionalLemmas
SemanticsWellFormed
begin

lemma *WhilePostdomination-aux*:
Postdomination sourcenode targetnode kind (valid-edge prog) Entry Exit
 ⟨proof⟩

interpretation *WhilePostdomination*:
Postdomination sourcenode targetnode kind valid-edge prog Entry Exit
 ⟨proof⟩

lemma *WhileStrongPostdomination-aux*:
StrongPostdomination sourcenode targetnode kind (valid-edge prog) Entry Exit
 ⟨proof⟩

interpretation *WhileStrongPostdomination*:
StrongPostdomination sourcenode targetnode kind valid-edge prog Entry Exit
 ⟨proof⟩

4.11.1 Standard Control Dependence

lemma *WStandardControlDependence-aux*:
StandardControlDependencePDG sourcenode targetnode kind (valid-edge prog)
Entry (Defs prog) (Uses prog) id Exit
 ⟨proof⟩

interpretation *WStandardControlDependence*:
StandardControlDependencePDG sourcenode targetnode kind valid-edge prog
Entry Defs prog Uses prog id Exit
 ⟨proof⟩

lemma *Fundamental-property-scd-aux*: *BackwardSlice-wf sourcenode targetnode kind*
(valid-edge prog) Entry (Defs prog) (Uses prog) id
(WStandardControlDependence.PDG-BS-s prog) reds (labels-nodes prog)
 ⟨proof⟩

interpretation *Fundamental-property-scd*: *BackwardSlice-wf sourcenode targetnode kind*

valid-edge prog Entry Defs prog Uses prog id
WStandardControlDependence.PDG-BS-s prog reds labels-nodes prog
 ⟨proof⟩

4.11.2 Weak Control Dependence

lemma *WWeakControlDependence-aux:*
WeakControlDependencePDG sourcenode targetnode kind (valid-edge prog)
Entry (Defs prog) (Uses prog) id Exit
 ⟨proof⟩

interpretation *WWeakControlDependence:*
WeakControlDependencePDG sourcenode targetnode kind valid-edge prog
Entry Defs prog Uses prog id Exit
 ⟨proof⟩

lemma *Fundamental-property-wcd-aux: BackwardSlice-wf sourcenode targetnode kind*

(valid-edge prog) Entry (Defs prog) (Uses prog) id
(WWeakControlDependence.PDG-BS-w prog) reds (labels-nodes prog)
 ⟨proof⟩

interpretation *Fundamental-property-wcd: BackwardSlice-wf sourcenode targetnode kind*
valid-edge prog Entry Defs prog Uses prog id
WWeakControlDependence.PDG-BS-w prog reds labels-nodes prog
 ⟨proof⟩

4.11.3 Weak Order Dependence

lemma *Fundamental-property-wod-aux: BackwardSlice-wf sourcenode targetnode kind*

(valid-edge prog) Entry (Defs prog) (Uses prog) id
(While-CFG-wf.wod-backward-slice prog) reds (labels-nodes prog)
 ⟨proof⟩

interpretation *Fundamental-property-wod: BackwardSlice-wf sourcenode targetnode kind*
valid-edge prog Entry Defs prog Uses prog id
While-CFG-wf.wod-backward-slice prog reds labels-nodes prog
 ⟨proof⟩

end

4.12 Slicing guarantees IFC Noninterference

theory *NonInterferenceIntra imports*
../Slicing/StaticIntra/Slice

../Slicing/Basic/CFGExit-wf
begin

4.12.1 Assumptions of this Approach

Classical IFC noninterference, a special case of a noninterference definition using partial equivalence relations (per) [?], partitions the variables (i.e. locations) into security levels. Usually, only levels for secret or high, written H , and public or low, written L , variables are used. Basically, a program that is noninterferent has to fulfil one basic property: executing the program in two different initial states that may differ in the values of their H -variables yields two final states that again only differ in the values of their H -variables; thus the values of the H -variables did not influence those of the L -variables.

Every per-based approach makes certain assumptions: (i) all H -variables are defined at the beginning of the program, (ii) all L -variables are observed (or used in our terms) at the end and (iii) every variable is either H or L . This security label is fixed for a variable and can not be altered during a program run. Thus, we have to extend the prerequisites of the slicing framework in [?] accordingly in a new locale:

```

locale NonInterferenceIntraGraph =
  BackwardSlice sourcenode targetnode kind valid-edge Entry Def Use state-val
  backward-slice +
  CFGExit-wf sourcenode targetnode kind valid-edge Entry Def Use state-val Exit
  for sourcenode :: 'edge  $\Rightarrow$  'node and targetnode :: 'edge  $\Rightarrow$  'node
  and kind :: 'edge  $\Rightarrow$  'state edge-kind and valid-edge :: 'edge  $\Rightarrow$  bool
  and Entry :: 'node ('('Entry'-')) and Def :: 'node  $\Rightarrow$  'var set
  and Use :: 'node  $\Rightarrow$  'var set and state-val :: 'state  $\Rightarrow$  'var  $\Rightarrow$  'val
  and backward-slice :: 'node set  $\Rightarrow$  'node set
  and Exit :: 'node ('('Exit'-')) +
  fixes H :: 'var set
  fixes L :: 'var set
  fixes High :: 'node ('('High'-'))
  fixes Low :: 'node ('('Low'-'))
  assumes Entry-edge-Exit-or-High:
   $\llbracket \text{valid-edge } a; \text{sourcenode } a = (-\text{Entry-}) \rrbracket$ 
     $\implies \text{targetnode } a = (-\text{Exit-}) \vee \text{targetnode } a = (-\text{High-})$ 
  and High-target-Entry-edge:
   $\exists a. \text{valid-edge } a \wedge \text{sourcenode } a = (-\text{Entry-}) \wedge \text{targetnode } a = (-\text{High-}) \wedge$ 
     $\text{kind } a = (\lambda s. \text{True})_{\checkmark}$ 
  and Entry-predecessor-of-High:
   $\llbracket \text{valid-edge } a; \text{targetnode } a = (-\text{High-}) \rrbracket \implies \text{sourcenode } a = (-\text{Entry-})$ 
  and Exit-edge-Entry-or-Low:  $\llbracket \text{valid-edge } a; \text{targetnode } a = (-\text{Exit-}) \rrbracket$ 
     $\implies \text{sourcenode } a = (-\text{Entry-}) \vee \text{sourcenode } a = (-\text{Low-})$ 
  and Low-source-Exit-edge:
   $\exists a. \text{valid-edge } a \wedge \text{sourcenode } a = (-\text{Low-}) \wedge \text{targetnode } a = (-\text{Exit-}) \wedge$ 
     $\text{kind } a = (\lambda s. \text{True})_{\checkmark}$ 
  and Exit-successor-of-Low:

```

$\llbracket \text{valid-edge } a; \text{sourcenode } a = (-\text{Low-}) \rrbracket \implies \text{targetnode } a = (-\text{Exit-})$
and *DefHigh*: $\text{Def } (-\text{High-}) = H$
and *UseHigh*: $\text{Use } (-\text{High-}) = H$
and *UseLow*: $\text{Use } (-\text{Low-}) = L$
and *HighLowDistinct*: $H \cap L = \{\}$
and *HighLowUNIV*: $H \cup L = \text{UNIV}$

begin

lemma *Low-neq-Exit*: **assumes** $L \neq \{\}$ **shows** $(-\text{Low-}) \neq (-\text{Exit-})$
 $\langle \text{proof} \rangle$

lemma *Entry-path-High-path*:
assumes $(-\text{Entry-}) -as \rightarrow^* n$ **and** *inner-node* n
obtains $a' as'$ **where** $as = a' \# as'$ **and** $(-\text{High-}) -as' \rightarrow^* n$
and $\text{kind } a' = (\lambda s. \text{True})_{\checkmark}$
 $\langle \text{proof} \rangle$

lemma *Exit-path-Low-path*:
assumes $n -as \rightarrow^* (-\text{Exit-})$ **and** *inner-node* n
obtains $a' as'$ **where** $as = as' @ [a]$ **and** $n -as' \rightarrow^* (-\text{Low-})$
and $\text{kind } a' = (\lambda s. \text{True})_{\checkmark}$
 $\langle \text{proof} \rangle$

lemma *not-Low-High*: $V \notin L \implies V \in H$
 $\langle \text{proof} \rangle$

lemma *not-High-Low*: $V \notin H \implies V \in L$
 $\langle \text{proof} \rangle$

4.12.2 Low Equivalence

In classical noninterference, an external observer can only see public values, in our case the L -variables. If two states agree in the values of all L -variables, these states are indistinguishable for him. *Low equivalence* groups those states in an equivalence class using the relation $\approx_L$:

definition *lowEquivalence* :: $'state \Rightarrow 'state \Rightarrow \text{bool}$ (**infixl** $\approx_L$ 50)
where $s \approx_L s' \equiv \forall V \in L. \text{state-val } s \ V = \text{state-val } s' \ V$

The following lemmas connect low equivalent states with relevant variables as necessary in the correctness proof for slicing.

lemma *relevant-vars-Entry*:
assumes $V \in \text{rv } S$ $(-\text{Entry-})$ **and** $(-\text{High-}) \notin \text{backward-slice } S$
shows $V \in L$
 $\langle \text{proof} \rangle$

lemma *lowEquivalence-relevant-nodes-Entry*:
assumes $s \approx_L s'$ **and** $(-High-) \notin \text{backward-slice } S$
shows $\forall V \in \text{rv } S \text{ } (-Entry-). \text{state-val } s \ V = \text{state-val } s' \ V$
 $\langle \text{proof} \rangle$

lemma *rv-Low-Use-Low*:
assumes $(-Low-) \in S$
shows $\llbracket n \text{ } -as \rightarrow * \text{ } (-Low-); n \text{ } -as' \rightarrow * \text{ } (-Low-);$
 $\forall V \in \text{rv } S \ n. \text{state-val } s \ V = \text{state-val } s' \ V;$
 $\text{preds } (\text{slice-kinds } S \ as) \ s; \text{preds } (\text{slice-kinds } S \ as') \ s'$
 $\implies \forall V \in \text{Use } (-Low-). \text{state-val } (\text{transfers } (\text{slice-kinds } S \ as) \ s) \ V =$
 $\text{state-val } (\text{transfers } (\text{slice-kinds } S \ as') \ s') \ V$
 $\langle \text{proof} \rangle$

4.12.3 The Correctness Proofs

In the following, we present two correctness proofs that slicing guarantees IFC noninterference. In both theorems, $(-High-) \notin \text{backward-slice } S$, where $(-Low-) \in S$, makes sure that no high variable (which are all defined in $(-High-)$) can influence a low variable (which are all used in $(-Low-)$).

First, a theorem regarding $(-Entry-) \text{ } -as \rightarrow * \text{ } (-Exit-)$ paths in the control flow graph (CFG), which agree to a complete program execution:

lemma *nonInterference-path-to-Low*:
assumes $s \approx_L s'$ **and** $(-High-) \notin \text{backward-slice } S$ **and** $(-Low-) \in S$
and $(-Entry-) \text{ } -as \rightarrow * \text{ } (-Exit-)$ **and** $\text{preds } (\text{kinds } as) \ s$
and $(-Entry-) \text{ } -as' \rightarrow * \text{ } (-Exit-)$ **and** $\text{preds } (\text{kinds } as') \ s'$
shows $\text{transfers } (\text{kinds } as) \ s \approx_L \text{transfers } (\text{kinds } as') \ s'$
 $\langle \text{proof} \rangle$

theorem *nonInterference-path*:
assumes $s \approx_L s'$ **and** $(-High-) \notin \text{backward-slice } S$ **and** $(-Low-) \in S$
and $(-Entry-) \text{ } -as \rightarrow * \text{ } (-Exit-)$ **and** $\text{preds } (\text{kinds } as) \ s$
and $(-Entry-) \text{ } -as' \rightarrow * \text{ } (-Exit-)$ **and** $\text{preds } (\text{kinds } as') \ s'$
shows $\text{transfers } (\text{kinds } as) \ s \approx_L \text{transfers } (\text{kinds } as') \ s'$
 $\langle \text{proof} \rangle$

end

The second theorem assumes that we have a operational semantics, whose evaluations are written $\langle c, s \rangle \Rightarrow \langle c', s' \rangle$ and which conforms to the CFG. The correctness theorem then states that if no high variable influenced a low

variable and the initial states were low equivalent, the resulting states are again low equivalent:

```

locale NonInterferenceIntra =
  NonInterferenceIntraGraph sourcenode targetnode kind valid-edge Entry
  Def Use state-val backward-slice Exit H L High Low +
  BackwardSlice-wf sourcenode targetnode kind valid-edge Entry Def Use state-val
  backward-slice sem identifies
  for sourcenode :: 'edge  $\Rightarrow$  'node and targetnode :: 'edge  $\Rightarrow$  'node
  and kind :: 'edge  $\Rightarrow$  'state edge-kind and valid-edge :: 'edge  $\Rightarrow$  bool
  and Entry :: 'node ('('Entry'-')) and Def :: 'node  $\Rightarrow$  'var set
  and Use :: 'node  $\Rightarrow$  'var set and state-val :: 'state  $\Rightarrow$  'var  $\Rightarrow$  'val
  and backward-slice :: 'node set  $\Rightarrow$  'node set
  and sem :: 'com  $\Rightarrow$  'state  $\Rightarrow$  'com  $\Rightarrow$  'state  $\Rightarrow$  bool
  (((1<-,->)  $\Rightarrow$  / (1<-,->)) [0,0,0,0] 81)
  and identifies :: 'node  $\Rightarrow$  'com  $\Rightarrow$  bool (-  $\triangleq$  - [51, 0] 80)
  and Exit :: 'node ('('Exit'-'))
  and H :: 'var set and L :: 'var set
  and High :: 'node ('('High'-')) and Low :: 'node ('('Low'-')) +
  fixes final :: 'com  $\Rightarrow$  bool
  assumes final-edge-Low:  $\llbracket \text{final } c; n \triangleq c \rrbracket$ 
   $\Rightarrow \exists a. \text{valid-edge } a \wedge \text{sourcenode } a = n \wedge \text{targetnode } a = (-\text{Low-}) \wedge \text{kind } a =$ 
 $\uparrow \text{id}$ 
begin

```

The following theorem needs the explicit edge from $(-\text{High-})$ to n . An approach using a *init* predicate for initial statements, being reachable from $(-\text{High-})$ via a $(\lambda s. \text{True})_{\vee}$ edge, does not work as the same statement could be identified by several nodes, some initial, some not. E.g., in the program `while (True) Skip;;Skip` two nodes identify this initial statement: the initial node and the node within the loop (because of loop unrolling).

```

theorem nonInterference:
  assumes  $s_1 \approx_L s_2$  and  $(-\text{High-}) \notin \text{backward-slice } S$  and  $(-\text{Low-}) \in S$ 
  and  $\text{valid-edge } a$  and  $\text{sourcenode } a = (-\text{High-})$  and  $\text{targetnode } a = n$ 
  and  $\text{kind } a = (\lambda s. \text{True})_{\vee}$  and  $n \triangleq c$  and  $\text{final } c'$ 
  and  $\langle c, s_1 \rangle \Rightarrow \langle c', s_1 \rangle$  and  $\langle c, s_2 \rangle \Rightarrow \langle c', s_2 \rangle$ 
  shows  $s_1' \approx_L s_2'$ 
 $\langle \text{proof} \rangle$ 

```

end

end

4.13 Framework Graph Lifting for Noninterference

theory LiftingIntra

```

imports NonInterferenceIntra ../Slicing/StaticIntra/CDepInstantiations
begin

```

In this section, we show how a valid CFG from the slicing framework in [?] can be lifted to fulfil all properties of the *NonInterferenceIntraGraph* locale. Basically, we redefine the hitherto existing *Entry* and *Exit* nodes as new *High* and *Low* nodes, and introduce two new nodes *NewEntry* and *NewExit*. Then, we have to lift all functions to operate on this new graph.

4.13.1 Liftings

The datatypes

```

datatype 'node LDCFG-node = Node 'node
  | NewEntry
  | NewExit

```

```

type-synonym ('edge,'node,'state) LDCFG-edge =
  'node LDCFG-node × ('state edge-kind) × 'node LDCFG-node

```

Lifting *valid-edge*

```

inductive lift-valid-edge :: ('edge ⇒ bool) ⇒ ('edge ⇒ 'node) ⇒ ('edge ⇒ 'node)
⇒
  ('edge ⇒ 'state edge-kind) ⇒ 'node ⇒ 'node ⇒ ('edge,'node,'state) LDCFG-edge
⇒
  bool
for valid-edge::'edge ⇒ bool and src::'edge ⇒ 'node and trg::'edge ⇒ 'node
and knd::'edge ⇒ 'state edge-kind and E::'node and X::'node

```

where *lve-edge*:

```

  [[valid-edge a; src a ≠ E ∨ trg a ≠ X;
    e = (Node (src a),knd a,Node (trg a))]]
  ⇒ lift-valid-edge valid-edge src trg knd E X e

```

| *lve-Entry-edge*:

```

  e = (NewEntry,(λs. True)√,Node E)
  ⇒ lift-valid-edge valid-edge src trg knd E X e

```

| *lve-Exit-edge*:

```

  e = (Node X,(λs. True)√,NewExit)
  ⇒ lift-valid-edge valid-edge src trg knd E X e

```

| *lve-Entry-Exit-edge*:

```

  e = (NewEntry,(λs. False)√,NewExit)
  ⇒ lift-valid-edge valid-edge src trg knd E X e

```

lemma $[simp]: \neg \text{lift-valid-edge valid-edge src trg kno } E \ X \ (\text{Node } E, et, \text{Node } X)$
 $\langle \text{proof} \rangle$

Lifting Def and Use sets

inductive-set $\text{lift-Def-set} :: ('node \Rightarrow 'var \text{ set}) \Rightarrow 'node \Rightarrow 'node \Rightarrow$
 $'var \text{ set} \Rightarrow 'var \text{ set} \Rightarrow ('node \text{ LDCFG-node} \times 'var) \text{ set}$
for $\text{Def} :: ('node \Rightarrow 'var \text{ set})$ **and** $E :: 'node$ **and** $X :: 'node$
and $H :: 'var \text{ set}$ **and** $L :: 'var \text{ set}$

where $\text{lift-Def-node}:$

$V \in \text{Def } n \implies (\text{Node } n, V) \in \text{lift-Def-set Def } E \ X \ H \ L$

| $\text{lift-Def-High}:$

$V \in H \implies (\text{Node } E, V) \in \text{lift-Def-set Def } E \ X \ H \ L$

abbreviation $\text{lift-Def} :: ('node \Rightarrow 'var \text{ set}) \Rightarrow 'node \Rightarrow 'node \Rightarrow$
 $'var \text{ set} \Rightarrow 'var \text{ set} \Rightarrow 'node \text{ LDCFG-node} \Rightarrow 'var \text{ set}$
where $\text{lift-Def Def } E \ X \ H \ L \ n \equiv \{ V. (n, V) \in \text{lift-Def-set Def } E \ X \ H \ L \}$

inductive-set $\text{lift-Use-set} :: ('node \Rightarrow 'var \text{ set}) \Rightarrow 'node \Rightarrow 'node \Rightarrow$
 $'var \text{ set} \Rightarrow 'var \text{ set} \Rightarrow ('node \text{ LDCFG-node} \times 'var) \text{ set}$
for $\text{Use} :: ('node \Rightarrow 'var \text{ set})$ **and** $E :: 'node$ **and** $X :: 'node$
and $H :: 'var \text{ set}$ **and** $L :: 'var \text{ set}$

where

$\text{lift-Use-node}:$

$V \in \text{Use } n \implies (\text{Node } n, V) \in \text{lift-Use-set Use } E \ X \ H \ L$

| $\text{lift-Use-High}:$

$V \in H \implies (\text{Node } E, V) \in \text{lift-Use-set Use } E \ X \ H \ L$

| $\text{lift-Use-Low}:$

$V \in L \implies (\text{Node } X, V) \in \text{lift-Use-set Use } E \ X \ H \ L$

abbreviation $\text{lift-Use} :: ('node \Rightarrow 'var \text{ set}) \Rightarrow 'node \Rightarrow 'node \Rightarrow$
 $'var \text{ set} \Rightarrow 'var \text{ set} \Rightarrow 'node \text{ LDCFG-node} \Rightarrow 'var \text{ set}$
where $\text{lift-Use Use } E \ X \ H \ L \ n \equiv \{ V. (n, V) \in \text{lift-Use-set Use } E \ X \ H \ L \}$

4.13.2 The lifting lemmas

Lifting the basic locales

abbreviation $\text{src} :: ('edge, 'node, 'state) \text{ LDCFG-edge} \Rightarrow 'node \text{ LDCFG-node}$
where $\text{src } a \equiv \text{fst } a$

abbreviation $\text{trg} :: ('edge, 'node, 'state) \text{ LDCFG-edge} \Rightarrow 'node \text{ LDCFG-node}$
where $\text{trg } a \equiv \text{snd}(\text{snd } a)$

definition $knd :: ('edge, 'node, 'state) \rightarrow LDCFG\text{-}edge \Rightarrow 'state \text{ edge-kind}$
where $knd\ a \equiv fst(snd\ a)$

lemma *lift-CFG*:

assumes $wf:CFGExit\text{-}wf\ sourcenode\ targetnode\ kind\ valid\text{-}edge\ Entry\ Def\ Use$
 $state\text{-}val\ Exit$
shows $CFG\ src\ trg$
 $(lift\text{-}valid\text{-}edge\ valid\text{-}edge\ sourcenode\ targetnode\ kind\ Entry\ Exit)\ NewEntry$
 $\langle proof \rangle$

lemma *lift-CFG-wf*:

assumes $wf:CFGExit\text{-}wf\ sourcenode\ targetnode\ kind\ valid\text{-}edge\ Entry\ Def\ Use$
 $state\text{-}val\ Exit$
shows $CFG\text{-}wf\ src\ trg\ knd$
 $(lift\text{-}valid\text{-}edge\ valid\text{-}edge\ sourcenode\ targetnode\ kind\ Entry\ Exit)\ NewEntry$
 $(lift\text{-}Def\ Def\ Entry\ Exit\ H\ L)\ (lift\text{-}Use\ Use\ Entry\ Exit\ H\ L)\ state\text{-}val$
 $\langle proof \rangle$

lemma *lift-CFGExit*:

assumes $wf:CFGExit\text{-}wf\ sourcenode\ targetnode\ kind\ valid\text{-}edge\ Entry\ Def\ Use$
 $state\text{-}val\ Exit$
shows $CFGExit\ src\ trg\ knd$
 $(lift\text{-}valid\text{-}edge\ valid\text{-}edge\ sourcenode\ targetnode\ kind\ Entry\ Exit)$
 $NewEntry\ NewExit$
 $\langle proof \rangle$

lemma *lift-CFGExit-wf*:

assumes $wf:CFGExit\text{-}wf\ sourcenode\ targetnode\ kind\ valid\text{-}edge\ Entry\ Def\ Use$
 $state\text{-}val\ Exit$
shows $CFGExit\text{-}wf\ src\ trg\ knd$
 $(lift\text{-}valid\text{-}edge\ valid\text{-}edge\ sourcenode\ targetnode\ kind\ Entry\ Exit)\ NewEntry$
 $(lift\text{-}Def\ Def\ Entry\ Exit\ H\ L)\ (lift\text{-}Use\ Use\ Entry\ Exit\ H\ L)\ state\text{-}val\ NewExit$
 $\langle proof \rangle$

Lifting *wod-backward-slice*

lemma *lift-wod-backward-slice*:

fixes $valid\text{-}edge$ **and** $sourcenode$ **and** $targetnode$ **and** $kind$ **and** $Entry$ **and** $Exit$
and Def **and** Use **and** H **and** L
defines $lve:lve \equiv lift\text{-}valid\text{-}edge\ valid\text{-}edge\ sourcenode\ targetnode\ kind\ Entry\ Exit$
and $lDef:lDef \equiv lift\text{-}Def\ Def\ Entry\ Exit\ H\ L$
and $lUse:lUse \equiv lift\text{-}Use\ Use\ Entry\ Exit\ H\ L$
assumes $wf:CFGExit\text{-}wf\ sourcenode\ targetnode\ kind\ valid\text{-}edge\ Entry\ Def\ Use$
 $state\text{-}val\ Exit$

and $H \cap L = \{\}$ **and** $H \cup L = UNIV$
shows *NonInterferenceIntraGraph* *src trg knl lve NewEntry lDef lUse state-val*
(CFG-wf.wod-backward-slice src trg lve lDef lUse)
NewExit H L (Node Entry) (Node Exit)
 <proof>

Lifting PDG-BS with standard-control-dependence

lemma *lift-Postdomination*:
assumes *wf:CFGExit-wf sourcenode targetnode kind valid-edge Entry Def Use*
state-val Exit
and *pd:Postdomination sourcenode targetnode kind valid-edge Entry Exit*
and *inner:CFGExit.inner-node sourcenode targetnode valid-edge Entry Exit nx*
shows *Postdomination src trg knl*
(lift-valid-edge valid-edge sourcenode targetnode kind Entry Exit) NewEntry NewExit
 <proof>

lemma *lift-PDG-scd*:
assumes *PDG:PDG sourcenode targetnode kind valid-edge Entry Def Use state-val*
Exit
(Postdomination.standard-control-dependence sourcenode targetnode valid-edge Exit)
and *pd:Postdomination sourcenode targetnode kind valid-edge Entry Exit*
and *inner:CFGExit.inner-node sourcenode targetnode valid-edge Entry Exit nx*
shows *PDG src trg knl*
(lift-valid-edge valid-edge sourcenode targetnode kind Entry Exit) NewEntry
(lift-Def Def Entry Exit H L) (lift-Use Use Entry Exit H L) state-val NewExit
(Postdomination.standard-control-dependence src trg
(lift-valid-edge valid-edge sourcenode targetnode kind Entry Exit) NewExit)
 <proof>

lemma *lift-PDG-standard-backward-slice*:
fixes *valid-edge* **and** *sourcenode* **and** *targetnode* **and** *kind* **and** *Entry* **and** *Exit*
and *Def* **and** *Use* **and** *H* **and** *L*
defines *lve:lve* $\equiv$ *lift-valid-edge valid-edge sourcenode targetnode kind Entry Exit*
and *lDef:lDef* $\equiv$ *lift-Def Def Entry Exit H L*
and *lUse:lUse* $\equiv$ *lift-Use Use Entry Exit H L*
assumes *PDG:PDG sourcenode targetnode kind valid-edge Entry Def Use state-val*
Exit
(Postdomination.standard-control-dependence sourcenode targetnode valid-edge Exit)
and *pd:Postdomination sourcenode targetnode kind valid-edge Entry Exit*
and *inner:CFGExit.inner-node sourcenode targetnode valid-edge Entry Exit nx*
and $H \cap L = \{\}$ **and** $H \cup L = UNIV$
shows *NonInterferenceIntraGraph src trg knl lve NewEntry lDef lUse state-val*
(PDG.PDG-BS src trg lve lDef lUse
(Postdomination.standard-control-dependence src trg lve NewExit))

NewExit H L (Node Entry) (Node Exit)
 <proof>

Lifting PDG-BS with weak-control-dependence

lemma *lift-StrongPostdomination:*

assumes *wf:CFGExit-wf sourcenode targetnode kind valid-edge Entry Def Use*
state-val Exit
and *spd:StrongPostdomination sourcenode targetnode kind valid-edge Entry Exit*
and *inner:CFGExit.inner-node sourcenode targetnode valid-edge Entry Exit nx*
shows *StrongPostdomination src trg kn*
(lift-valid-edge valid-edge sourcenode targetnode kind Entry Exit) NewEntry NewExit
 <proof>

lemma *lift-PDG-wcd:*

assumes *PDG:PDG sourcenode targetnode kind valid-edge Entry Def Use state-val*
Exit
(StrongPostdomination.weak-control-dependence sourcenode targetnode
valid-edge Exit)
and *spd:StrongPostdomination sourcenode targetnode kind valid-edge Entry Exit*
and *inner:CFGExit.inner-node sourcenode targetnode valid-edge Entry Exit nx*
shows *PDG src trg kn*
(lift-valid-edge valid-edge sourcenode targetnode kind Entry Exit) NewEntry
(lift-Def Def Entry Exit H L) (lift-Use Use Entry Exit H L) state-val NewExit
(StrongPostdomination.weak-control-dependence src trg
(lift-valid-edge valid-edge sourcenode targetnode kind Entry Exit) NewExit)
 <proof>

lemma *lift-PDG-weak-backward-slice:*

fixes *valid-edge and sourcenode and targetnode and kind and Entry and Exit*
and *Def and Use and H and L*
defines *lve:lve ≡ lift-valid-edge valid-edge sourcenode targetnode kind Entry Exit*
and *lDef:lDef ≡ lift-Def Def Entry Exit H L*
and *lUse:lUse ≡ lift-Use Use Entry Exit H L*
assumes *PDG:PDG sourcenode targetnode kind valid-edge Entry Def Use state-val*
Exit
(StrongPostdomination.weak-control-dependence sourcenode targetnode
valid-edge Exit)
and *spd:StrongPostdomination sourcenode targetnode kind valid-edge Entry Exit*
and *inner:CFGExit.inner-node sourcenode targetnode valid-edge Entry Exit nx*
and *H ∩ L = {} and H ∪ L = UNIV*
shows *NonInterferenceIntraGraph src trg kn lve NewEntry lDef lUse state-val*

```

      (PDG.PDG-BS src trg lve lDef lUse
       (StrongPostdomination.weak-control-dependence src trg lve NewExit))
      NewExit H L (Node Entry) (Node Exit)
    <proof>

end

```

4.14 Information Flow for While

```

theory NonInterferenceWhile imports
  SemanticsWellFormed
  StaticControlDependences
  ../../InformationFlowSlicing/LiftingIntra
begin

locale SecurityTypes =
  fixes H :: vname set
  fixes L :: vname set
  assumes HighLowDistinct:  $H \cap L = \{\}$ 
  and HighLowUNIV:  $H \cup L = UNIV$ 
begin

```

4.14.1 Lifting labels-nodes and Defining final

```

fun labels-LDCFG-nodes :: cmd  $\Rightarrow$  w-node LDCFG-node  $\Rightarrow$  cmd  $\Rightarrow$  bool
  where labels-LDCFG-nodes prog (Node n) c = labels-nodes prog n c
  | labels-LDCFG-nodes prog n c = False

```

```

lemmas WCFG-path-induct[consumes 1, case-names empty-path Cons-path]
  = CFG.path.induct[OF While-CFG-aux]

```

```

lemma lift-valid-node:
  assumes CFG.valid-node sourcenode targetnode (valid-edge prog) n
  shows CFG.valid-node src trg
    (lift-valid-edge (valid-edge prog) sourcenode targetnode kind (-Entry-) (-Exit-))
    (Node n)
  <proof>

```

lemma *lifted-CFG-fund-prop*:
assumes *labels-LDCFG-nodes prog n c* **and** $\langle c, s \rangle \rightarrow^* \langle c', s' \rangle$
shows $\exists n' \text{ as. } \text{CFG.path src trg}$
(lift-valid-edge (valid-edge prog) sourcenode targetnode kind (-Entry-) (-Exit-))
n as n' $\wedge$ transfers (CFG.kinds kn d as) s = s' $\wedge$
preds (CFG.kinds kn d as) s $\wedge$ labels-LDCFG-nodes prog n' c'
 $\langle \text{proof} \rangle$

fun *final* :: *cmd* $\Rightarrow$ *bool*
where *final Skip* = *True*
| *final c* = *False*

lemma *final-edge*:
labels-nodes prog n Skip $\implies$ prog $\vdash$ n $\neg \uparrow id \rightarrow$ (-Exit-)
 $\langle \text{proof} \rangle$

4.14.2 Semantic Non-Interference for Weak Order Dependence

lemmas *WODNonInterferenceGraph* =
lift-wod-backward-slice[OF While-CFGExit-wf-aux HighLowDistinct HighLowU-NIV]

lemma *WODNonInterference*:
NonInterferenceIntra src trg kn d
(lift-valid-edge (valid-edge prog) sourcenode targetnode kind
(-Entry-) (-Exit-))
NewEntry (lift-Def (Defs prog) (-Entry-) (-Exit-) H L)
(lift-Use (Uses prog) (-Entry-) (-Exit-) H L) id
(CFG-wf.wod-backward-slice src trg
(lift-valid-edge (valid-edge prog) sourcenode targetnode kind
(-Entry-) (-Exit-))
(lift-Def (Defs prog) (-Entry-) (-Exit-) H L)
(lift-Use (Uses prog) (-Entry-) (-Exit-) H L))
reds (labels-LDCFG-nodes prog)
NewExit H L (LDCFG-node.Node (-Entry-)) (LDCFG-node.Node (-Exit-)) final
 $\langle \text{proof} \rangle$

4.14.3 Semantic Non-Interference for Standard Control Dependence

lemma *inner-node-exists*: $\exists n. \text{CFGExit.inner-node sourcenode targetnode}$
(valid-edge prog) (-Entry-) (-Exit-) n
 $\langle \text{proof} \rangle$

lemmas *SCDNonInterferenceGraph* =
lift-PDG-standard-backward-slice[*OF WStandardControlDependence.PDG-scd*
WhilePostdomination-aux - HighLowDistinct HighLowUNIV]

lemma *SCDNonInterference*:

NonInterferenceIntra src trg knl
(lift-valid-edge (valid-edge prog) sourcenode targetnode kind
(-Entry-) (-Exit-))
NewEntry (lift-Def (Defs prog) (-Entry-) (-Exit-) H L)
(lift-Use (Uses prog) (-Entry-) (-Exit-) H L) id
(PDG.PDG-BS src trg
(lift-valid-edge (valid-edge prog) sourcenode targetnode kind
(-Entry-) (-Exit-))
(lift-Def (Defs prog) (-Entry-) (-Exit-) H L)
(lift-Use (Uses prog) (-Entry-) (-Exit-) H L)
(Postdomination.standard-control-dependence src trg
(lift-valid-edge (valid-edge prog) sourcenode targetnode kind
(-Entry-) (-Exit-)) NewExit))
reds (labels-LDCFG-nodes prog)
NewExit H L (LDCFG-node.Node (-Entry-)) (LDCFG-node.Node (-Exit-)) final
<proof>

4.14.4 Semantic Non-Interference for Weak Control Dependence

lemmas *WCDNonInterferenceGraph* =
lift-PDG-weak-backward-slice[*OF WWeakControlDependence.PDG-wcd*
WhileStrongPostdomination-aux - HighLowDistinct HighLowUNIV]

lemma *WCDNonInterference*:

NonInterferenceIntra src trg knl
(lift-valid-edge (valid-edge prog) sourcenode targetnode kind
(-Entry-) (-Exit-))
NewEntry (lift-Def (Defs prog) (-Entry-) (-Exit-) H L)
(lift-Use (Uses prog) (-Entry-) (-Exit-) H L) id
(PDG.PDG-BS src trg
(lift-valid-edge (valid-edge prog) sourcenode targetnode kind
(-Entry-) (-Exit-))
(lift-Def (Defs prog) (-Entry-) (-Exit-) H L)
(lift-Use (Uses prog) (-Entry-) (-Exit-) H L)
(StrongPostdomination.weak-control-dependence src trg
(lift-valid-edge (valid-edge prog) sourcenode targetnode kind
(-Entry-) (-Exit-)) NewExit))
reds (labels-LDCFG-nodes prog)
NewExit H L (LDCFG-node.Node (-Entry-)) (LDCFG-node.Node (-Exit-)) final
<proof>

end

end

Chapter 5

A Control Flow Graph for Jinja Byte Code

This work was done by Denis Lohner (denis.lohner@kit.edu).

5.1 Formalizing the CFG

theory *JVMCFG* **imports** *../Basic/BasicDefs ../Jinja/BV/BVExample* **begin**

declare *lesub-list-impl-same-size* [*simp del*]
declare *listE-length* [*simp del*]

5.1.1 Type definitions

Wellformed Programs

definition *wf-jvmprog* = $\{(P, \Phi). \text{wf-jvm-prog}_{\Phi} P\}$

typedef *wf-jvmprog* = *wf-jvmprog*
 $\langle \text{proof} \rangle$

hide-const *Phi E*

abbreviation *rep-jvmprog-jvm-prog* :: *wf-jvmprog* $\Rightarrow$ *jvm-prog*
 $(\cdot)_{\text{wf}}$
where $P_{\text{wf}} \equiv \text{fst}(\text{Rep-wf-jvmprog}(P))$

abbreviation *rep-jvmprog-phi* :: *wf-jvmprog* $\Rightarrow$ *ty_P*
 $(\cdot)_{\Phi}$
where $P_{\Phi} \equiv \text{snd}(\text{Rep-wf-jvmprog}(P))$

lemma *wf-jvmprog-is-wf*: *wf-jvm-prog* $_{P_{\Phi}}$ (P_{wf})
 $\langle \text{proof} \rangle$

Basic Types

We consider a program to be a well-formed Jinja program, together with a given base class and a main method

type-synonym $jvmprog = wf-jvmprog \times cname \times mname$

type-synonym $callstack = (cname \times mname \times pc) list$

The state is modeled as $heap \times stack\text{-}variables \times local\text{-}variables$

stack and local variables are modeled as pairs of natural numbers. The first number gives the position in the call stack (i.e. the method in which the variable is used), the second the position in the method's stack or array of local variables resp.

The stack variables are numbered from bottom up (which is the reverse order of the array for the stack in Jinja's state representation), whereas local variables are identified by their position in the array of local variables of Jinja's state representation.

type-synonym $state = heap \times ((nat \times nat) \Rightarrow val) \times ((nat \times nat) \Rightarrow val)$

abbreviation $heap\text{-}of :: state \Rightarrow heap$

where

$heap\text{-}of\ s \equiv fst(s)$

abbreviation $stk\text{-}of :: state \Rightarrow ((nat \times nat) \Rightarrow val)$

where

$stk\text{-}of\ s \equiv fst(snd(s))$

abbreviation $loc\text{-}of :: state \Rightarrow ((nat \times nat) \Rightarrow val)$

where

$loc\text{-}of\ s \equiv snd(snd(s))$

5.1.2 Basic Definitions

State update (instruction execution)

This function models instruction execution for our state representation.

Additional parameters are the call depth of the current program point, the stack length of the current program point, the length of the stack in the underlying call frame (needed for RETURN), and (for INVOKE) the length of the array of local variables of the invoked method.

Exception handling is not covered by this function.

fun $exec\text{-}instr :: instr \Rightarrow wf-jvmprog \Rightarrow state \Rightarrow nat \Rightarrow nat \Rightarrow nat \Rightarrow nat \Rightarrow state$

where

$exec\text{-}instr\text{-}Load:$

$exec\text{-}instr\ (Load\ n)\ P\ s\ calldepth\ stk\text{-}length\ rs\ ill =$

$(let\ (h, stk, loc) = s$

$in\ (h,\ stk((calldepth,stk-length):=loc(calldepth,n)),\ loc))$

$| exec-instr-Store:$
 $exec-instr\ (Store\ n)\ P\ s\ calldepth\ stk-length\ rs\ ill =$
 $(let\ (h,stk,loc) = s$
 $in\ (h,\ stk,\ loc((calldepth,n):=stk(calldepth,stk-length - 1))))$

$| exec-instr-Push:$
 $exec-instr\ (Push\ v)\ P\ s\ calldepth\ stk-length\ rs\ ill =$
 $(let\ (h,stk,loc) = s$
 $in\ (h,\ stk((calldepth,stk-length):=v),\ loc))$

$| exec-instr-New:$
 $exec-instr\ (New\ C)\ P\ s\ calldepth\ stk-length\ rs\ ill =$
 $(let\ (h,stk,loc) = s;$
 $a = the(new-Addr\ h)$
 $in\ (h(a \mapsto (blank\ (P_{wf}\ C)),\ stk((calldepth,stk-length):=Addr\ a),\ loc))$

$| exec-instr-Getfield:$
 $exec-instr\ (Getfield\ F\ C)\ P\ s\ calldepth\ stk-length\ rs\ ill =$
 $(let\ (h,stk,loc) = s;$
 $a = the-Addr\ (stk\ (calldepth,stk-length - 1));$
 $(D,fs) = the(h\ a)$
 $in\ (h,\ stk((calldepth,stk-length - 1) := the(fs(F,C))),\ loc))$

$| exec-instr-Putfield:$
 $exec-instr\ (Putfield\ F\ C)\ P\ s\ calldepth\ stk-length\ rs\ ill =$
 $(let\ (h,stk,loc) = s;$
 $v = stk\ (calldepth,stk-length - 1);$
 $a = the-Addr\ (stk\ (calldepth,stk-length - 2));$
 $(D,fs) = the(h\ a)$
 $in\ (h(a \mapsto (D,fs((F,C) \mapsto v))),\ stk,\ loc))$

$| exec-instr-Checkcast:$
 $exec-instr\ (Checkcast\ C)\ P\ s\ calldepth\ stk-length\ rs\ ill = s$

$| exec-instr-Pop:$
 $exec-instr\ (Pop)\ P\ s\ calldepth\ stk-length\ rs\ ill = s$

$| exec-instr-Add:$
 $exec-instr\ (Add)\ P\ s\ calldepth\ stk-length\ rs\ ill =$
 $(let\ (h,stk,loc) = s;$
 $i_1 = the-Intg\ (stk\ (calldepth,\ stk-length - 1));$
 $i_2 = the-Intg\ (stk\ (calldepth,\ stk-length - 2))$
 $in\ (h,\ stk((calldepth,\ stk-length - 2) := Intg\ (i_1 + i_2)),\ loc))$

$| exec-instr-IfFalse:$
 $exec-instr\ (IfFalse\ b)\ P\ s\ calldepth\ stk-length\ rs\ ill = s$

| *exec-instr-CmpEq*:
exec-instr (CmpEq) P s callddepth stk-length rs ill =
(let (h,stk,loc) = s;
v₁ = stk (callddepth, stk-length - 1);
v₂ = stk (callddepth, stk-length - 2)
in (h, stk((callddepth, stk-length - 2) := Bool (v₁ = v₂)), loc))

| *exec-instr-Goto*:
exec-instr (Goto i) P s callddepth stk-length rs ill = s

| *exec-instr-Throw*:
exec-instr (Throw) P s callddepth stk-length rs ill = s

| *exec-instr-Invoke*:
exec-instr (Invoke M n) P s callddepth stk-length rs invoke-loc-length =
(let (h,stk,loc) = s;
loc' = (λ(a,b). if (a ≠ Suc callddepth ∨ b ≥ invoke-loc-length) then loc(a,b) else
(if (b ≤ n) then stk(callddepth, stk-length - (Suc n - b)) else
arbitrary))
in (h,stk,loc'))

| *exec-instr-Return*:
exec-instr (Return) P s callddepth stk-length ret-stk-length ill =
(if (callddepth = 0)
then s
else
(let (h,stk,loc) = s;
v = stk(callddepth, stk-length - 1)
in (h,stk((callddepth - 1, ret-stk-length - 1) := v),loc))
)

length of stack and local variables

The following terms extract the stack length at a given program point from the well-typing of the given program

abbreviation *stkLength* :: *wf-jvmprog* ⇒ *cname* ⇒ *mname* ⇒ *pc* ⇒ *nat*
where
stkLength P C M pc ≡ *length (fst(the(((P_Φ) C M)!pc)))*

abbreviation *locLength* :: *wf-jvmprog* ⇒ *cname* ⇒ *mname* ⇒ *pc* ⇒ *nat*
where
locLength P C M pc ≡ *length (snd(the(((P_Φ) C M)!pc)))*

Conversion functions

This function takes a natural number *n* and a function *f* with domain *nat* and creates the array [f 0, f 1, f 2, ..., f (n - 1)].

This is used for extracting the array of local variables

abbreviation $locs :: nat \Rightarrow (nat \Rightarrow 'a) \Rightarrow 'a \text{ list}$
where $locs\ n\ loc \equiv map\ loc\ [0..<n]$

This function takes a natural number n and a function f with domain nat and creates the array $[f\ (n - 1), \dots, f\ 1, f\ 0]$.

This is used for extracting the stack as a list

abbreviation $stks :: nat \Rightarrow (nat \Rightarrow 'a) \Rightarrow 'a \text{ list}$
where $stks\ n\ stk \equiv map\ stk\ (rev\ [0..<n])$

This function creates a list of the arrays for local variables from the given state corresponding to the given callstack

fun $locss :: wf\text{-}jvmprog \Rightarrow callstack \Rightarrow ((nat \times nat) \Rightarrow 'a) \Rightarrow 'a \text{ list list}$
where
 $locss\ P\ []\ loc = []$
 $| locss\ P\ ((C,M,pc)\#cs)\ loc =$
 $(locs\ (locLength\ P\ C\ M\ pc)\ (\lambda a. loc\ (length\ cs,\ a)))\#(locss\ P\ cs\ loc)$

This function creates a list of the (methods') stacks from the given state corresponding to the given callstack

fun $stkss :: wf\text{-}jvmprog \Rightarrow callstack \Rightarrow ((nat \times nat) \Rightarrow 'a) \Rightarrow 'a \text{ list list}$
where
 $stkss\ P\ []\ stk = []$
 $| stkss\ P\ ((C,M,pc)\#cs)\ stk =$
 $(stks\ (stkLength\ P\ C\ M\ pc)\ (\lambda a. stk\ (length\ cs,\ a)))\#(stkss\ P\ cs\ stk)$

Given a callstack and a state, this abbreviation converts the state to Jinja's state representation

abbreviation $state\text{-}to\text{-}jvm\text{-}state :: wf\text{-}jvmprog \Rightarrow callstack \Rightarrow state \Rightarrow jvm\text{-}state$
where $state\text{-}to\text{-}jvm\text{-}state\ P\ cs\ s \equiv$
 $(None,\ heap\text{-}of\ s,\ zip\ (stkss\ P\ cs\ (stk\text{-}of\ s))\ (zip\ (locss\ P\ cs\ (loc\text{-}of\ s))\ cs))$

This function extracts the call stack from a given frame stack (as it is given by Jinja's state representation)

definition $framestack\text{-}to\text{-}callstack :: frame\ list \Rightarrow callstack$
where $framestack\text{-}to\text{-}callstack\ frs \equiv map\ snd\ (map\ snd\ frs)$

State Conformance

Now we lift byte code verifier conformance to our state representation

definition $bv\text{-}conform :: wf\text{-}jvmprog \Rightarrow callstack \Rightarrow state \Rightarrow bool$
 $(\cdot, \cdot \vdash_{BV} \cdot \checkmark)$
where $P, cs \vdash_{BV} s \checkmark \equiv correct\text{-}state\ (P_{wf})\ (P_{\Phi})\ (state\text{-}to\text{-}jvm\text{-}state\ P\ cs\ s)$

Statically determine catch-block

This function is equivalent to Jinja's *find-handler* function

fun *find-handler-for* :: *wf-jvmprog* $\Rightarrow$ *cname* $\Rightarrow$ *callstack* $\Rightarrow$ *callstack*
where
find-handler-for *P C* [] = []
| *find-handler-for* *P C* (*c#cs*) = (let (*C'*,*M'*,*pc'*) = *c* in
(case *match-ex-table* (*P_{wf}*) *C pc'* (*ex-table-of* (*P_{wf}*) *C' M'*) of
None $\Rightarrow$ *find-handler-for* *P C cs*
| Some *pc-d* $\Rightarrow$ (*C'*, *M'*, *fst pc-d*)#*cs*))

5.1.3 Simplification lemmas

lemma *find-handler-decr* [*simp*]: *find-handler-for* *P Exc cs* $\neq$ *c#cs*
 $\langle$ *proof* $\rangle$

lemma *stkss-length* [*simp*]: *length* (*stkss P cs stk*) = *length cs*
 $\langle$ *proof* $\rangle$

lemma *locss-length* [*simp*]: *length* (*locss P cs loc*) = *length cs*
 $\langle$ *proof* $\rangle$

lemma *nth-stkss*:
 $\llbracket a < \text{length } cs; b < \text{length } (\text{stkss } P \text{ cs } stk ! (\text{length } cs - \text{Suc } a)) \rrbracket$
 $\implies \text{stkss } P \text{ cs } stk ! (\text{length } cs - \text{Suc } a) !$
 $(\text{length } (\text{stkss } P \text{ cs } stk ! (\text{length } cs - \text{Suc } a)) - \text{Suc } b) = \text{stk } (a,b)$
 $\langle$ *proof* $\rangle$

lemma *nth-locss*:
 $\llbracket a < \text{length } cs; b < \text{length } (\text{locss } P \text{ cs } loc ! (\text{length } cs - \text{Suc } a)) \rrbracket$
 $\implies \text{locss } P \text{ cs } loc ! (\text{length } cs - \text{Suc } a) ! b = \text{loc } (a,b)$
 $\langle$ *proof* $\rangle$

lemma *hd-stks* [*simp*]: $n \neq 0 \implies \text{hd } (\text{stks } n \text{ stk}) = \text{stk}(n - 1)$
 $\langle$ *proof* $\rangle$

lemma *hd-tl-stks*: $n > 1 \implies \text{hd } (\text{tl } (\text{stks } n \text{ stk})) = \text{stk}(n - 2)$
 $\langle$ *proof* $\rangle$

lemma *stkss-purge*:
 $\text{length } cs \leq a \implies \text{stkss } P \text{ cs } (\text{stk}((a,b) := c)) = \text{stkss } P \text{ cs } stk$
 $\langle$ *proof* $\rangle$

lemma *stkss-purge'*:

$length\ cs \leq a \implies stks\ P\ cs\ (\lambda s. \text{if } s = (a, b) \text{ then } c \text{ else } stk\ s) = stks\ P\ cs\ stk$
 $\langle proof \rangle$

lemma *locss-purge*:

$length\ cs \leq a \implies locss\ P\ cs\ (loc((a, b) := c)) = locss\ P\ cs\ loc$
 $\langle proof \rangle$

lemma *locss-purge'*:

$length\ cs \leq a \implies locss\ P\ cs\ (\lambda s. \text{if } s = (a, b) \text{ then } c \text{ else } loc\ s) = locss\ P\ cs\ loc$
 $\langle proof \rangle$

lemma *locs-pullout* [simp]:

$locs\ b\ (loc(n := e)) = locs\ b\ loc\ [n := e]$
 $\langle proof \rangle$

lemma *locs-pullout'* [simp]:

$locs\ b\ (\lambda a. \text{if } a = n \text{ then } e \text{ else } loc\ (c, a)) = locs\ b\ (\lambda a. loc\ (c, a))\ [n := e]$
 $\langle proof \rangle$

lemma *stks-pullout*:

$n < b \implies stks\ b\ (stk(n := e)) = stks\ b\ stk\ [b - Suc\ n := e]$
 $\langle proof \rangle$

lemma *nth-tl* : $xs \neq [] \implies tl\ xs\ !\ n = xs\ !\ (Suc\ n)$

$\langle proof \rangle$

lemma *f2c-Nil* [simp]: *framestack-to-callstack* $[] = []$

$\langle proof \rangle$

lemma *f2c-Cons* [simp]:

framestack-to-callstack $((stk, loc, C, M, pc) \# frs) = (C, M, pc) \# (\text{framestack-to-callstack}\ frs)$
 $\langle proof \rangle$

lemma *f2c-length* [simp]:

$length\ (\text{framestack-to-callstack}\ frs) = length\ frs$
 $\langle proof \rangle$

lemma *f2c-s2jvm-id* [simp]:

framestack-to-callstack
 $(snd(snd(state-to-jvm-state\ P\ cs\ s))) =$
 cs
 $\langle proof \rangle$

lemma *f2c-s2jvm-id'* [simp]:

framestack-to-callstack
 $(zip\ (stks\ P\ cs\ stk)\ (zip\ (locss\ P\ cs\ loc)\ cs)) = cs$

$\langle proof \rangle$

lemma *f2c-append* [*simp*]:
 $framestack\text{-}to\text{-}callstack\ (frs\ @\ frs') =$
 $(framestack\text{-}to\text{-}callstack\ frs)\ @\ (framestack\text{-}to\text{-}callstack\ frs')$
 $\langle proof \rangle$

5.1.4 CFG construction

5.1.5 Datatypes

Nodes are labeled with a callstack and an optional tuple (consisting of a callstack and a flag).

The first callstack determines the current program point (i.e. the next statement to execute). If the second parameter is not None, we are at an intermediate state, where the target of the instruction is determined (the second callstack) and the flag is set to whether an exception is thrown or not.

datatype *j-node* =
 $Entry\ ('(-Entry-'))$
 $| Node\ callstack\ (callstack \times bool)\ option\ ('(-,-\ -'))$

The empty callstack indicates the exit node

abbreviation *j-node-Exit* :: *j-node* ('(-Exit-'))
where *j-node-Exit* $\equiv (- [], None -)$

An edge is a triple, consisting of two nodes and the edge kind

type-synonym *j-edge* = (*j-node* $\times$ *state edge-kind* $\times$ *j-node*)

5.1.6 CFG

The CFG is constructed by a case analysis on the instructions and their different behavior in different states. E.g. the exceptional behavior of NEW, if there is no more space in the heap, vs. the normal behavior.

Note: The set of edges defined by this predicate is a first approximation to the real set of edges in the CFG. We later (theory JVMInterpretation) add some well-formedness requirements to the nodes.

inductive *JVM-CFG* :: *jvmprog* $\Rightarrow$ *j-node* $\Rightarrow$ *state edge-kind* $\Rightarrow$ *j-node* $\Rightarrow$ *bool*
 $(- \vdash - \dashrightarrow -)$

where

JCFG-EntryExit:
 $prog \vdash (-Entry-) -(\lambda s. False)_{\sqrt{\rightarrow}} (-Exit-)$

$|$ *JCFG-EntryStart*:
 $prog = (P, C0, Main) \implies prog \vdash (-Entry-) -(\lambda s. True)_{\sqrt{\rightarrow}} (-[(C0, Main, 0)], None -)$

| *JCFG-ReturnExit*:

$$\begin{aligned} & \llbracket \text{prog} = (P, C0, \text{Main}); \\ & \quad (\text{instrs-of } (P_{wf}) \ C \ M) ! \text{pc} = \text{Return} \rrbracket \\ & \implies \text{prog} \vdash (- [(C, M, \text{pc})], \text{None} \ -) \dashv\!\!\dashv\!\! id \rightarrow (-\text{Exit}-) \end{aligned}$$

| *JCFG-Straight-NoExc*:

$$\begin{aligned} & \llbracket \text{prog} = (P, C0, \text{Main}); \\ & \quad \text{instrs-of } (P_{wf}) \ C \ M ! \text{pc} \in \{\text{Load idx}, \text{Store idx}, \text{Push val}, \text{Pop}, \text{IAdd}, \text{CmpEq}\}; \\ & \quad \text{ek} = \uparrow(\lambda s. \text{exec-instr } ((\text{instrs-of } (P_{wf}) \ C \ M) ! \text{pc}) \ P \ s \\ & \quad \quad (\text{length } cs) \ (\text{stkLength } P \ C \ M \ \text{pc}) \ \text{arbitrary arbitrary}) \rrbracket \\ & \implies \text{prog} \vdash (- (C, M, \text{pc}) \# cs, \text{None} \ -) \dashv\!\!\dashv\!\! ek \rightarrow (- (C, M, \text{Suc } \text{pc}) \# cs, \text{None} \ -) \end{aligned}$$

| *JCFG-New-Normal-Pred*:

$$\begin{aligned} & \llbracket \text{prog} = (P, C0, \text{Main}); \\ & \quad (\text{instrs-of } (P_{wf}) \ C \ M) ! \text{pc} = (\text{New } Cl); \\ & \quad \text{ek} = (\lambda(h, \text{stk}, \text{loc}). \text{new-Addr } h \neq \text{None})_{\checkmark} \rrbracket \\ & \implies \text{prog} \vdash (- (C, M, \text{pc}) \# cs, \text{None} \ -) \dashv\!\!\dashv\!\! ek \rightarrow (- (C, M, \text{pc}) \# cs, [(C, M, \text{Suc } \text{pc}) \# cs, \text{False}]) \ -) \end{aligned}$$

| *JCFG-New-Normal-Update*:

$$\begin{aligned} & \llbracket \text{prog} = (P, C0, \text{Main}); \\ & \quad (\text{instrs-of } (P_{wf}) \ C \ M) ! \text{pc} = (\text{New } Cl); \\ & \quad \text{ek} = \uparrow(\lambda s. \text{exec-instr } (\text{New } Cl) \ P \ s \ (\text{length } cs) \ (\text{stkLength } P \ C \ M \ \text{pc}) \ \text{arbitrary arbitrary}) \rrbracket \\ & \implies \text{prog} \vdash (- (C, M, \text{pc}) \# cs, [(C, M, \text{Suc } \text{pc}) \# cs, \text{False}]) \ -) \dashv\!\!\dashv\!\! ek \rightarrow (- (C, M, \text{Suc } \text{pc}) \# cs, \text{None} \ -) \end{aligned}$$

| *JCFG-New-Exc-Pred*:

$$\begin{aligned} & \llbracket \text{prog} = (P, C0, \text{Main}); \\ & \quad (\text{instrs-of } (P_{wf}) \ C \ M) ! \text{pc} = (\text{New } Cl); \\ & \quad \text{find-handler-for } P \ \text{OutOfMemory} \ ((C, M, \text{pc}) \# cs) = cs'; \\ & \quad \text{ek} = (\lambda(h, \text{stk}, \text{loc}). \text{new-Addr } h = \text{None})_{\checkmark} \rrbracket \\ & \implies \text{prog} \vdash (- (C, M, \text{pc}) \# cs, \text{None} \ -) \dashv\!\!\dashv\!\! ek \rightarrow (- (C, M, \text{pc}) \# cs, [(cs', \text{True}]) \ -) \end{aligned}$$

| *JCFG-New-Exc-Update*:

$$\begin{aligned} & \llbracket \text{prog} = (P, C0, \text{Main}); \\ & \quad (\text{instrs-of } (P_{wf}) \ C \ M) ! \text{pc} = (\text{New } Cl); \\ & \quad \text{find-handler-for } P \ \text{OutOfMemory} \ ((C, M, \text{pc}) \# cs) = (C', M', \text{pc}') \# cs'; \\ & \quad \text{ek} = \uparrow(\lambda(h, \text{stk}, \text{loc}). \\ & \quad \quad (h, \\ & \quad \quad \quad \text{stk}((\text{length } cs', (\text{stkLength } P \ C' \ M' \ \text{pc}') - 1) := \text{Addr } (\text{addr-of-sys-xcpt } \\ & \quad \quad \quad \text{OutOfMemory})), \\ & \quad \quad \quad \text{loc}) \\ & \quad \quad) \rrbracket \\ & \implies \text{prog} \vdash (- (C, M, \text{pc}) \# cs, [(C', M', \text{pc}') \# cs', \text{True}]) \ -) \dashv\!\!\dashv\!\! ek \rightarrow (- (C', M', \text{pc}') \# cs', \text{None} \ -) \end{aligned}$$

| *JCFG-New-Exc-Exit*:

$$\llbracket \text{prog} = (P, C0, \text{Main});$$

$(instrs\text{-}of (P_{wf}) C M) ! pc = (New Cl);$
 $find\text{-}handler\text{-}for P OutOfMemory ((C, M, pc)\#cs) = []$
 $\implies prog \vdash (- (C, M, pc)\#cs, [([], True)] -) \dashv id \rightarrow (-Exit-)$

| JCFG-Getfield-Normal-Pred:

$\llbracket prog = (P, C0, Main);$
 $(instrs\text{-}of (P_{wf}) C M) ! pc = (Getfield Fd Cl);$
 $ek = (\lambda(h, stk, loc). \text{stk}(\text{length } cs, \text{stkLength } P C M pc - 1) \neq Null)_{\checkmark}$
 $\implies prog \vdash (- (C, M, pc)\#cs, None -) \dashv ek \rightarrow (- (C, M, pc)\#cs, [((C, M, Suc pc)\#cs, False)] -)$

| JCFG-Getfield-Normal-Update:

$\llbracket prog = (P, C0, Main);$
 $(instrs\text{-}of (P_{wf}) C M) ! pc = (Getfield Fd Cl);$
 $ek = \uparrow(\lambda s. \text{exec-instr } (Getfield Fd Cl) P s (\text{length } cs) (\text{stkLength } P C M pc)$
 $\quad \text{arbitrary arbitrary}) \rrbracket$
 $\implies prog \vdash (- (C, M, pc)\#cs, [((C, M, Suc pc)\#cs, False)] -) \dashv ek \rightarrow (- (C, M, Suc pc)\#cs, None -)$

| JCFG-Getfield-Exc-Pred:

$\llbracket prog = (P, C0, Main);$
 $(instrs\text{-}of (P_{wf}) C M) ! pc = (Getfield Fd Cl);$
 $find\text{-}handler\text{-}for P NullPointer ((C, M, pc)\#cs) = cs';$
 $ek = (\lambda(h, stk, loc). \text{stk}(\text{length } cs, \text{stkLength } P C M pc - 1) = Null)_{\checkmark}$
 $\implies prog \vdash (- (C, M, pc)\#cs, None -) \dashv ek \rightarrow (- (C, M, pc)\#cs, [(cs', True)] -)$

| JCFG-Getfield-Exc-Update:

$\llbracket prog = (P, C0, Main);$
 $(instrs\text{-}of (P_{wf}) C M) ! pc = (Getfield Fd Cl);$
 $find\text{-}handler\text{-}for P NullPointer ((C, M, pc)\#cs) = (C', M', pc')\#cs';$
 $ek = \uparrow(\lambda(h, stk, loc).$
 $\quad (h,$
 $\quad \text{stk}((\text{length } cs', (\text{stkLength } P C' M' pc') - 1) := Addr (\text{addr-of-sys-xcpt}$
 $\quad \text{NullPointer})),$
 $\quad loc)$
 $\quad) \rrbracket$
 $\implies prog \vdash (- (C, M, pc)\#cs, [((C', M', pc')\#cs', True)] -) \dashv ek \rightarrow (- (C', M', pc')\#cs', None -)$

| JCFG-Getfield-Exc-Exit:

$\llbracket prog = (P, C0, Main);$
 $(instrs\text{-}of (P_{wf}) C M) ! pc = (Getfield Fd Cl);$
 $find\text{-}handler\text{-}for P NullPointer ((C, M, pc)\#cs) = []$
 $\implies prog \vdash (- (C, M, pc)\#cs, [([], True)] -) \dashv id \rightarrow (-Exit-)$

| JCFG-Putfield-Normal-Pred:

$\llbracket prog = (P, C0, Main);$
 $(instrs\text{-}of (P_{wf}) C M) ! pc = (Putfield Fd Cl);$
 $ek = (\lambda(h, stk, loc). \text{stk}(\text{length } cs, \text{stkLength } P C M pc - 2) \neq Null)_{\checkmark}$

$\implies \text{prog} \vdash (- (C, M, pc) \# cs, \text{None} -) -ek \rightarrow (- (C, M, pc) \# cs, [((C, M, \text{Suc } pc) \# cs, \text{False})] -)$

| *JCFG-Putfield-Normal-Update*:

$\llbracket \text{prog} = (P, C0, \text{Main});$
 $(\text{instrs-of } (P_{wf}) C M) ! pc = (\text{Putfield Fd Cl});$
 $ek = \uparrow(\lambda s. \text{exec-instr } (\text{Putfield Fd Cl}) P s (\text{length } cs) (\text{stkLength } P C M pc)$
 $\text{arbitrary arbitrary}) \rrbracket$
 $\implies \text{prog} \vdash (- (C, M, pc) \# cs, [((C, M, \text{Suc } pc) \# cs, \text{False})] -) -ek \rightarrow (- (C,$
 $M, \text{Suc } pc) \# cs, \text{None} -)$

| *JCFG-Putfield-Exc-Pred*:

$\llbracket \text{prog} = (P, C0, \text{Main});$
 $(\text{instrs-of } (P_{wf}) C M) ! pc = (\text{Putfield Fd Cl});$
 $\text{find-handler-for } P \text{ NullPointer } ((C, M, pc) \# cs) = cs';$
 $ek = (\lambda(h, \text{stk}, \text{loc}). \text{stk}(\text{length } cs, \text{stkLength } P C M pc - 2) = \text{Null})_{\checkmark} \rrbracket$
 $\implies \text{prog} \vdash (- (C, M, pc) \# cs, \text{None} -) -ek \rightarrow (- (C, M, pc) \# cs, [(cs', \text{True})] -)$

| *JCFG-Putfield-Exc-Update*:

$\llbracket \text{prog} = (P, C0, \text{Main});$
 $(\text{instrs-of } (P_{wf}) C M) ! pc = (\text{Putfield Fd Cl});$
 $\text{find-handler-for } P \text{ NullPointer } ((C, M, pc) \# cs) = (C', M', pc') \# cs';$
 $ek = \uparrow(\lambda(h, \text{stk}, \text{loc}).$
 $(h,$
 $\text{stk}((\text{length } cs', (\text{stkLength } P C' M' pc') - 1) := \text{Addr } (\text{addr-of-sys-xcpt}$
 $\text{NullPointer})),$
 $\text{loc})$
 $) \rrbracket$
 $\implies \text{prog} \vdash (- (C, M, pc) \# cs, [(C', M', pc') \# cs', \text{True}] -) -ek \rightarrow (- (C', M',$
 $pc') \# cs', \text{None} -)$

| *JCFG-Putfield-Exc-Exit*:

$\llbracket \text{prog} = (P, C0, \text{Main});$
 $(\text{instrs-of } (P_{wf}) C M) ! pc = (\text{Putfield Fd Cl});$
 $\text{find-handler-for } P \text{ NullPointer } ((C, M, pc) \# cs) = [] \rrbracket$
 $\implies \text{prog} \vdash (- (C, M, pc) \# cs, [([], \text{True})] -) -\uparrow id \rightarrow (-\text{Exit}-)$

| *JCFG-Checkcast-Normal-Pred*:

$\llbracket \text{prog} = (P, C0, \text{Main});$
 $(\text{instrs-of } (P_{wf}) C M) ! pc = (\text{Checkcast Cl});$
 $ek = (\lambda(h, \text{stk}, \text{loc}). \text{cast-ok } (P_{wf}) Cl h (\text{stk}(\text{length } cs, \text{stkLength } P C M pc -$
 $\text{Suc } 0)))_{\checkmark} \rrbracket$
 $\implies \text{prog} \vdash (- (C, M, pc) \# cs, \text{None} -) -ek \rightarrow (- (C, M, \text{Suc } pc) \# cs, \text{None} -)$

| *JCFG-Checkcast-Exc-Pred*:

$\llbracket \text{prog} = (P, C0, \text{Main});$
 $(\text{instrs-of } (P_{wf}) C M) ! pc = (\text{Checkcast Cl});$
 $\text{find-handler-for } P \text{ ClassCast } ((C, M, pc) \# cs) = cs';$
 $ek = (\lambda(h, \text{stk}, \text{loc}). \neg \text{cast-ok } (P_{wf}) Cl h (\text{stk}(\text{length } cs, \text{stkLength } P C M pc -$

$$\begin{aligned}
& Suc\ 0)))_{\checkmark} \mathbb{I} \\
& \implies prog \vdash (- (C, M, pc) \# cs, None -) - ek \rightarrow (- (C, M, pc) \# cs, [(cs', True)] -)
\end{aligned}$$

$$\begin{aligned}
& | \text{JCFG-Checkcast-Exc-Update:} \\
& \mathbb{I} \text{ } prog = (P, C0, Main); \\
& \quad (instrs\text{-of } (P_{wf})\ C\ M) ! pc = (Checkcast\ Cl); \\
& \quad find\text{-handler-for } P\ ClassCast\ ((C, M, pc) \# cs) = (C', M', pc') \# cs'; \\
& \quad ek = \uparrow(\lambda(h, stk, loc). \\
& \quad \quad (h, \\
& \quad \quad \quad stk((length\ cs', (stkLength\ P\ C'\ M'\ pc') - 1) := Addr\ (addr\text{-of-sys-xcpt} \\
& \quad \quad \quad ClassCast)), \\
& \quad \quad \quad loc) \\
& \quad \quad) \mathbb{I} \\
& \implies prog \vdash (- (C, M, pc) \# cs, [(C', M', pc') \# cs', True]) - ek \rightarrow (- (C', M', \\
& \quad pc') \# cs', None -)
\end{aligned}$$

$$\begin{aligned}
& | \text{JCFG-Checkcast-Exc-Exit:} \\
& \mathbb{I} \text{ } prog = (P, C0, Main); \\
& \quad (instrs\text{-of } (P_{wf})\ C\ M) ! pc = (Checkcast\ Cl); \\
& \quad find\text{-handler-for } P\ ClassCast\ ((C, M, pc) \# cs) = [] \mathbb{I} \\
& \implies prog \vdash (- (C, M, pc) \# cs, [([], True)] -) - \uparrow id \rightarrow (-Exit-)
\end{aligned}$$

$$\begin{aligned}
& | \text{JCFG-Invoke-Normal-Pred:} \\
& \mathbb{I} \text{ } prog = (P, C0, Main); \\
& \quad (instrs\text{-of } (P_{wf})\ C\ M) ! pc = (Invoke\ M2\ n); \\
& \quad cd = length\ cs; \\
& \quad stk\text{-length} = stkLength\ P\ C\ M\ pc; \\
& \quad ek = (\lambda(h, stk, loc). \\
& \quad \quad stk(cd, stk\text{-length} - Suc\ n) \neq Null \wedge \\
& \quad \quad fst(method\ (P_{wf})\ (cname\text{-of } h\ (the\ Addr(stk(cd, stk\text{-length} - Suc\ n))))\ M2) \\
& \quad \quad = D \\
& \quad \quad)_{\checkmark} \mathbb{I} \\
& \implies \\
& \quad prog \vdash (- (C, M, pc) \# cs, None -) - ek \rightarrow (- (C, M, pc) \# cs, [(D, M2, 0) \# (C, \\
& \quad M, pc) \# cs, False]) -)
\end{aligned}$$

$$\begin{aligned}
& | \text{JCFG-Invoke-Normal-Update:} \\
& \mathbb{I} \text{ } prog = (P, C0, Main); \\
& \quad (instrs\text{-of } (P_{wf})\ C\ M) ! pc = (Invoke\ M2\ n); \\
& \quad stk\text{-length} = stkLength\ P\ C\ M\ pc; \\
& \quad loc\text{-length} = locLength\ P\ D\ M2\ 0; \\
& \quad ek = \uparrow(\lambda s. exec\text{-instr } (Invoke\ M2\ n)\ P\ s\ (length\ cs)\ stk\text{-length}\ arbitrary \\
& \quad \quad loc\text{-length}) \\
& \quad \mathbb{I} \\
& \implies prog \vdash (- (C, M, pc) \# cs, [(D, M2, 0) \# (C, M, pc) \# cs, False]) - ek \rightarrow \\
& \quad (- (D, M2, 0) \# (C, M, pc) \# cs, None -)
\end{aligned}$$

$$\begin{aligned}
& | \text{JCFG-Invoke-Exc-Pred:} \\
& \mathbb{I} \text{ } prog = (P, C0, Main);
\end{aligned}$$

$(instrs\text{-}of\ (P_{wf})\ C\ M) !\ pc = (Invoke\ m2\ n);$
 $find\text{-}handler\text{-}for\ P\ NullPointer\ ((C, M, pc)\#cs) = cs';$
 $ek = (\lambda(h,stk,loc).\ stk(length\ cs,\ stkLength\ P\ C\ M\ pc - Suc\ n) = Null)_{\checkmark} \llbracket$
 $\implies prog \vdash (-\ (C, M, pc)\#cs, None\ -) -ek \rightarrow (-\ (C, M, pc)\#cs, [(cs', True)]\ -)$

| *JCFG-Invoke-Exc-Update:*

$\llbracket prog = (P, C0, Main);$
 $(instrs\text{-}of\ (P_{wf})\ C\ M) !\ pc = (Invoke\ M2\ n);$
 $find\text{-}handler\text{-}for\ P\ NullPointer\ ((C, M, pc)\#cs) = (C', M', pc')\#cs';$
 $ek = \uparrow(\lambda(h,stk,loc).$
 $\quad (h,$
 $\quad\quad stk((length\ cs', (stkLength\ P\ C'\ M'\ pc') - 1) := Addr\ (addr\text{-}of\ sys\text{-}xcpt$
 $\quad\quad NullPointer)),$
 $\quad\quad loc)$
 $\quad\quad)$
 $\quad\quad \rrbracket$
 $\implies prog \vdash (-\ (C, M, pc)\#cs, [(C', M', pc')\#cs', True])\ -) -ek \rightarrow (-\ (C', M',$
 $pc')\#cs', None\ -)$

| *JCFG-Invoke-Exc-Exit:*

$\llbracket prog = (P, C0, Main);$
 $(instrs\text{-}of\ (P_{wf})\ C\ M) !\ pc = (Invoke\ M2\ n);$
 $find\text{-}handler\text{-}for\ P\ NullPointer\ ((C, M, pc)\#cs) = [] \rrbracket$
 $\implies prog \vdash (-\ (C, M, pc)\#cs, [([], True)]\ -) -\uparrow id \rightarrow (-Exit-)$

| *JCFG-Return-Update:*

$\llbracket prog = (P, C0, Main);$
 $(instrs\text{-}of\ (P_{wf})\ C\ M) !\ pc = Return;$
 $stk\text{-}length = stkLength\ P\ C\ M\ pc;$
 $r\text{-}stk\text{-}length = stkLength\ P\ C'\ M'\ (Suc\ pc');$
 $ek = \uparrow(\lambda s. exec\text{-}instr\ Return\ P\ s\ (Suc\ (length\ cs))\ stk\text{-}length\ r\text{-}stk\text{-}length\ arbitrary) \rrbracket$
 $\implies prog \vdash (-\ (C, M, pc)\#(C', M', pc')\#cs, None\ -) -ek \rightarrow (-\ (C', M', Suc$
 $pc')\#cs, None\ -)$

| *JCFG-Goto-Update:*

$\llbracket prog = (P, C0, Main);$
 $(instrs\text{-}of\ (P_{wf})\ C\ M) !\ pc = Goto\ idx \rrbracket$
 $\implies prog \vdash (-\ (C, M, pc)\#cs, None\ -) -\uparrow id \rightarrow (-\ (C, M, nat\ (int\ pc +$
 $idx))\#cs, None\ -)$

| *JCFG-IfFalse-False:*

$\llbracket prog = (P, C0, Main);$
 $(instrs\text{-}of\ (P_{wf})\ C\ M) !\ pc = (IfFalse\ b);$
 $b \neq 1;$
 $ek = (\lambda(h,stk,loc).\ stk(length\ cs,\ stkLength\ P\ C\ M\ pc - 1) = Bool\ False)_{\checkmark} \llbracket$
 $\implies prog \vdash (-\ (C, M, pc)\#cs, None\ -) -ek \rightarrow (-\ (C, M, nat\ (int\ pc + b))\#cs, None$
 $-)$

| *JCFG-IfFalse-Next*:
 $\llbracket \text{prog} = (P, C0, \text{Main});$
 $(\text{instrs-of } (P_{wf}) C M) ! pc = (\text{IfFalse } b);$
 $ek = (\lambda(h, stk, loc). \text{stk}(\text{length } cs, \text{stkLength } P C M pc - 1) \neq \text{Bool False} \vee b$
 $= 1)_{\vee} \rrbracket$
 $\implies \text{prog} \vdash (- (C, M, pc) \# cs, \text{None } -) -ek \rightarrow (- (C, M, \text{Suc } pc) \# cs, \text{None } -)$

| *JCFG-Throw-Pred*:
 $\llbracket \text{prog} = (P, C0, \text{Main});$
 $(\text{instrs-of } (P_{wf}) C M) ! pc = \text{Throw};$
 $cd = \text{length } cs;$
 $\text{stk-length} = \text{stkLength } P C M pc;$
 $\exists \text{Exc. find-handler-for } P \text{ Exc } ((C, M, pc) \# cs) = cs';$
 $ek = (\lambda(h, stk, loc).$
 $(\text{stk}(\text{length } cs, \text{stkLength } P C M pc - 1) = \text{Null} \wedge$
 $\text{find-handler-for } P \text{ NullPointer } ((C, M, pc) \# cs) = cs') \vee$
 $(\text{stk}(\text{length } cs, \text{stkLength } P C M pc - 1) \neq \text{Null} \wedge$
 $\text{find-handler-for } P (\text{cname-of } h (\text{the-Addr}(\text{stk}(cd, \text{stk-length} - 1)))) ((C,$
 $M, pc) \# cs) = cs')$
 $\rrbracket_{\vee}$
 $\implies \text{prog} \vdash (- (C, M, pc) \# cs, \text{None } -) -ek \rightarrow (- (C, M, pc) \# cs, [(cs', \text{True})] -)$

| *JCFG-Throw-Update*:
 $\llbracket \text{prog} = (P, C0, \text{Main});$
 $(\text{instrs-of } (P_{wf}) C M) ! pc = \text{Throw};$
 $ek = \uparrow(\lambda(h, stk, loc).$
 $(h,$
 $\text{stk}((\text{length } cs', (\text{stkLength } P C' M' pc') - 1) :=$
 $\text{if } (\text{stk}(\text{length } cs, \text{stkLength } P C M pc - 1) = \text{Null}) \text{ then}$
 $\text{Addr } (\text{addr-of-sys-xcpt NullPointer})$
 $\text{else } (\text{stk}(\text{length } cs, \text{stkLength } P C M pc - 1))),$
 $loc)$
 $\rrbracket$
 $\implies \text{prog} \vdash (- (C, M, pc) \# cs, [(C', M', pc') \# cs', \text{True}] -) -ek \rightarrow (- (C', M',$
 $pc') \# cs', \text{None } -)$

| *JCFG-Throw-Exit*:
 $\llbracket \text{prog} = (P, C0, \text{Main});$
 $(\text{instrs-of } (P_{wf}) C M) ! pc = \text{Throw} \rrbracket$
 $\implies \text{prog} \vdash (- (C, M, pc) \# cs, [\llbracket, \text{True} \rrbracket] -) -\uparrow id \rightarrow (-\text{Exit-})$

5.1.7 CFG properties

lemma *JVMCFG-Exit-no-sourcenode* [*dest*]:
assumes $\text{edge:prog} \vdash (-\text{Exit-}) -et \rightarrow n'$
shows *False*
 $\langle \text{proof} \rangle$

lemma *JVMCFG-Entry-no-targetnode* [*dest*]:

assumes $edge:prog \vdash n -et \rightarrow (-Entry-)$
shows $False$
 $\langle proof \rangle$

lemma *JVMCFG-EntryD*:
 $\llbracket (P, C, M) \vdash n -et \rightarrow n'; n = (-Entry-) \rrbracket$
 $\implies (n' = (-Exit-) \wedge et = (\lambda s. False)_{\checkmark}) \vee (n' = (-[(C, M, 0)], None -) \wedge et = (\lambda s. True)_{\checkmark})$
 $\langle proof \rangle$

declare *split-def* [*simp add*]
declare *find-handler-for.simps* [*simp del*]

lemma *JVMCFG-edge-det*:
 $\llbracket prog \vdash n -et \rightarrow n'; prog \vdash n -et' \rightarrow n' \rrbracket \implies et = et'$
 $\langle proof \rangle$

declare *split-def* [*simp del*]
declare *find-handler-for.simps* [*simp add*]

end

theory *JVMInterpretation* **imports** *JVMCFG ../Basic/CFGExit* **begin**

5.2 Instatiation of the *CFG* locale

abbreviation *sourcenode* :: *j-edge* $\Rightarrow$ *j-node*
where *sourcenode* $e \equiv fst\ e$

abbreviation *targetnode* :: *j-edge* $\Rightarrow$ *j-node*
where *targetnode* $e \equiv snd(snd\ e)$

abbreviation *kind* :: *j-edge* $\Rightarrow$ *state edge-kind*
where *kind* $e \equiv fst(snd\ e)$

The following predicates define the aforementioned well-formedness requirements for nodes. Later, *valid-callstack* will be implied by Jinja's state conformance.

fun *valid-callstack* :: *jvmprog* $\Rightarrow$ *callstack* $\Rightarrow$ *bool*
where
 $valid_callstack\ prog\ [] = True$
 $| valid_callstack\ (P, C0, Main)\ [(C, M, pc)] \longleftrightarrow$
 $C = C0 \wedge M = Main \wedge$
 $(P_{\Phi})\ C\ M\ !\ pc \neq None \wedge$
 $(\exists T\ Ts\ mxs\ mxl\ is\ xt. (P_{wf}) \vdash C\ sees\ M:Ts \rightarrow T = (mxs, mxl, is, xt)\ in\ C \wedge pc < length\ is)$

$\mid \text{valid-callstack } (P, C0, \text{Main}) ((C, M, pc) \# (C', M', pc') \# cs) \longleftrightarrow$
 $\text{instrs-of } (P_{wf}) C' M' ! pc' =$
 $\text{Invoke } M (\text{locLength } P C M 0 - \text{Suc } (\text{fst}(\text{snd}(\text{snd}(\text{snd}(\text{method } (P_{wf}) C$
 $M))))))) \wedge$
 $(P_{\Phi}) C M ! pc \neq \text{None} \wedge$
 $(\exists T Ts m\!xs m\!xl \text{ is } xt. (P_{wf}) \vdash C \text{ sees } M:Ts \rightarrow T = (m\!xs, m\!xl, \text{is}, xt) \text{ in } C \wedge pc$
 $< \text{length is}) \wedge$
 $\text{valid-callstack } (P, C0, \text{Main}) ((C', M', pc') \# cs)$

fun *valid-node* :: *jvmprog* $\Rightarrow$ *j-node* $\Rightarrow$ *bool*

where

valid-node prog (-Entry-) = *True*

$\mid \text{valid-node prog } (- \text{cs}, \text{None } -) \longleftrightarrow \text{valid-callstack prog cs}$
 $\mid \text{valid-node prog } (- \text{cs}, \lfloor (cs', xf) \rfloor -) \longleftrightarrow$
 $\text{valid-callstack prog cs} \wedge \text{valid-callstack prog cs}' \wedge$
 $(\exists Q. \text{prog} \vdash (- \text{cs}, \text{None } -) - (Q)_{\checkmark} \rightarrow (- \text{cs}, \lfloor (cs', xf) \rfloor -)) \wedge$
 $(\exists f. \text{prog} \vdash (- \text{cs}, \lfloor (cs', xf) \rfloor -) - \uparrow f \rightarrow (- \text{cs}', \text{None } -))$

fun *valid-edge* :: *jvmprog* $\Rightarrow$ *j-edge* $\Rightarrow$ *bool*

where

$\text{valid-edge prog } a \longleftrightarrow$
 $(\text{prog} \vdash (\text{sourcenode } a) - (\text{kind } a) \rightarrow (\text{targetnode } a))$
 $\wedge (\text{valid-node prog } (\text{sourcenode } a))$
 $\wedge (\text{valid-node prog } (\text{targetnode } a))$

interpretation *JVM-CFG-Interpret*:

CFG sourcenode targetnode kind valid-edge prog Entry

for *prog*

$\langle \text{proof} \rangle$

interpretation *JVM-CFGExit-Interpret*:

CFGExit sourcenode targetnode kind valid-edge prog Entry (-Exit-)

for *prog*

$\langle \text{proof} \rangle$

end

Chapter 6

Standard and Weak Control Dependence

6.1 A type for well-formed programs

theory *JVMPostdomination* **imports** *JVMInterpretation* *../Basic/Postdomination*
begin

For instantiating *Postdomination* every node in the CFG of a program must be reachable from the (-Entry-) node and there must be a path to the (-Exit-) node from each node.

Therefore, we restrict the set of allowed programs to those, where the CFG fulfills these requirements. This is done by defining a new type for well-formed programs. The universe of every type in Isabelle must be non-empty. That's why we first define an example program *EP* and its typing *Phi-EP*, which is a member of the carrier set of the later defined type.

Restricting the set of allowed programs in this way is reasonable, as Jinja's compiler only produces byte code programs, that are members of this type (A proof for this is current work).

definition *EP* :: *jvm-prog*
 where *EP* = ("*C*", *Object*, [], [("*M*", [], *Void*, 1::nat, 0::nat, [*Push Unit*, *Return*], [])]) #
 SystemClasses

definition *Phi-EP* :: *typ*
 where *Phi-EP C M* = (if *C* = "*C*" ∧ *M* = "*M*" then [[([], [OK (*Class* "*C*")])], [([*Void*], [OK (*Class* "*C*")])]] else [])

Now we show, that *EP* is indeed a well-formed program in the sense of Jinja's byte code verifier

lemma *distinct-classes*':
 "*C*" ≠ *Object*
 "*C*" ≠ *NullPointer*

$"C" \neq \text{OutOfMemory}$
 $"C" \neq \text{ClassCast}$
 $\langle \text{proof} \rangle$

lemmas *distinct-classes* =
distinct-classes distinct-classes'' distinct-classes'' [symmetric]

declare *distinct-classes* [simp add]

lemma *i-max-2D*: $i < \text{Suc } (\text{Suc } 0) \implies i = 0 \vee i = 1$
 $\langle \text{proof} \rangle$

lemma *EP-wf*: $\text{wf-jvm-prog } \text{Phi-EP } EP$
 $\langle \text{proof} \rangle$

lemma [simp]: $\text{Abs-wf-jvmprog } (EP, \text{Phi-EP})_{\text{wf}} = EP$
 $\langle \text{proof} \rangle$

lemma [simp]: $\text{Abs-wf-jvmprog } (EP, \text{Phi-EP})_{\Phi} = \text{Phi-EP}$
 $\langle \text{proof} \rangle$

lemma *method-in-EP-is-M*:
 $EP \vdash C \text{ sees } M: Ts \rightarrow T = (mxs, mxl, is, xt) \text{ in } D$
 $\implies C = "C" \wedge$
 $M = "M" \wedge$
 $Ts = [] \wedge$
 $T = \text{Void} \wedge$
 $mxs = 1 \wedge$
 $mxl = 0 \wedge$
 $is = [\text{Push Unit}, \text{Return}] \wedge$
 $xt = [] \wedge$
 $D = "C"$
 $\langle \text{proof} \rangle$

lemma [simp]:
 $\exists T Ts mxs mxl is. (\exists xt. EP \vdash "C" \text{ sees } "M": Ts \rightarrow T = (mxs, mxl, is, xt) \text{ in } "C") \wedge is \neq []$
 $\langle \text{proof} \rangle$

lemma [simp]:
 $\exists T Ts mxs mxl is. (\exists xt. EP \vdash "C" \text{ sees } "M": Ts \rightarrow T = (mxs, mxl, is, xt) \text{ in } "C") \wedge$
 $\text{Suc } 0 < \text{length } is$
 $\langle \text{proof} \rangle$

lemma *C-sees-M-in-EP* [simp]:
 $EP \vdash "C" \text{ sees } "M": [] \rightarrow \text{Void} = (1, 0, [\text{Push Unit}, \text{Return}], []) \text{ in } "C"$

$\langle \text{proof} \rangle$

lemma *instrs-of-EP-C-M* [simp]:
instrs-of EP "C" "M" = [Push Unit, Return]
 $\langle \text{proof} \rangle$

lemma *valid-node-in-EP-D*:
valid-node (Abs-wf-jvmprog (EP, Phi-EP), "C", "M") n
 $\implies n \in \{(-\text{Entry-}), (- [(\text{"C"}, \text{"M"}, 0)], \text{None } -), (- [(\text{"C"}, \text{"M"}, 1)], \text{None } -), (-\text{Exit-})\}$
 $\langle \text{proof} \rangle$

lemma *EP-C-M-0-valid* [simp]:
JVM-CFG-Interpret.valid-node (Abs-wf-jvmprog (EP, Phi-EP), "C", "M")
(- [(\text{"C"}, \text{"M"}, 0)], \text{None } -)
 $\langle \text{proof} \rangle$

lemma *EP-C-M-Suc-0-valid* [simp]:
JVM-CFG-Interpret.valid-node (Abs-wf-jvmprog (EP, Phi-EP), "C", "M")
(- [(\text{"C"}, \text{"M"}, Suc 0)], \text{None } -)
 $\langle \text{proof} \rangle$

definition
cfg-wf-prog =
 $\{P. (\forall n. \text{valid-node } P \ n \longrightarrow (\exists \text{ as. JVM-CFG-Interpret.path } P \ (-\text{Entry-}) \text{ as } n) \wedge (\exists \text{ as. JVM-CFG-Interpret.path } P \ n \text{ as } (-\text{Exit-}))))\}$

typedef *cfg-wf-prog = cfg-wf-prog*
 $\langle \text{proof} \rangle$

abbreviation *lift-to-cfg-wf-prog* :: (*jvmprog* $\Rightarrow$ 'a) $\Rightarrow$ (*cfg-wf-prog* $\Rightarrow$ 'a)
 $(\neg_{CFG})$
where $f_{CFG} \equiv (\lambda P. f \ (\text{Rep-cfg-wf-prog } P))$

6.2 Interpretation of the *Postdomination* locale

interpretation *JVM-CFG-Postdomination*:
Postdomination sourcenode targetnode kind valid-edge CFG prog Entry (-Exit-)
for *prog*
 $\langle \text{proof} \rangle$

6.3 Interpretation of the *StrongPostdomination* locale

6.3.1 Some helpfull lemmas

lemma *find-handler-for-tl-eq*:

find-handler-for P *Exc* $cs = (C, M, pc) \# cs' \implies \exists cs'' pc. cs = cs'' @ [(C, M, pc)] @ cs'$
 $\langle proof \rangle$

lemma *valid-callstack-tl*:

valid-callstack prog $((C, M, pc) \# cs) \implies \text{valid-callstack prog } cs$
 $\langle proof \rangle$

lemma *find-handler-Throw-Invoke-pc-in-range*:

$\llbracket cs = (C', M', pc') \# cs'; \text{valid-callstack } (P, C0, \text{Main}) \text{ } cs; \text{instrs-of } (P_{wf}) \text{ } C' M' ! pc' = \text{Throw} \vee (\exists M'' n''. \text{instrs-of } (P_{wf}) \text{ } C' M' ! pc' = \text{Invoke } M'' n''); \text{find-handler-for } P \text{ } Exc \text{ } cs = (C, M, pc) \# cs'' \rrbracket \implies pc < \text{length } (\text{instrs-of } (P_{wf}) \text{ } C M)$
 $\langle proof \rangle$

6.3.2 Every node has only finitely many successors

lemma *successor-set-finite*:

JVM-CFG-Interpret.valid-node prog n
 $\implies \text{finite } \{n'. \exists a'. \text{valid-edge prog } a' \wedge \text{sourcenode } a' = n \wedge \text{targetnode } a' = n'\}$
 $\langle proof \rangle$

6.3.3 Interpretation of the locale

interpretation *JVM-CFG-StrongPostdomination*:

StrongPostdomination *sourcenode targetnode kind valid-edge* CFG *prog Entry (-Exit-)*
for *prog*
 $\langle proof \rangle$

end

theory *JVMCFG-wf* **imports** *JVMInterpretation ../Basic/CFGExit-wf* **begin**

6.4 Instantiation of the *CFG-wf* locale

6.4.1 Variables and Values

datatype *jinja-var* = *HeapVar addr* | *Stk nat nat* | *Loc nat nat*
datatype *jinja-val* = *Object obj option* | *Primitive val*

fun *state-val* :: *state* $\Rightarrow$ *jinja-var* $\Rightarrow$ *jinja-val*
where
 state-val (*h*, *stk*, *loc*) (*HeapVar a*) = *Object* (*h a*)
 | *state-val* (*h*, *stk*, *loc*) (*Stk cd idx*) = *Primitive* (*stk* (*cd*, *idx*))
 | *state-val* (*h*, *stk*, *loc*) (*Loc cd idx*) = *Primitive* (*loc* (*cd*, *idx*))

6.4.2 The *Def* and *Use* sets

inductive-set *Def* :: *wf-jvmprog* $\Rightarrow$ *j-node* $\Rightarrow$ *jinja-var* *set*

for *P* :: *wf-jvmprog*

and *n* :: *j-node*

where

Def-Load:

$\llbracket n = (- (C, M, pc) \# cs, None) - \rrbracket$;
 instrs-of (*P_{wf}*) *C M* ! *pc* = *Load idx*;
 cd = *length cs*;
 i = *stkLength P C M pc* $\llbracket$
 $\Rightarrow$ *Stk cd i* $\in$ *Def P n*

 | *Def-Store*:

$\llbracket n = (- (C, M, pc) \# cs, None) - \rrbracket$;
 instrs-of (*P_{wf}*) *C M* ! *pc* = *Store idx*;
 cd = *length cs* $\llbracket$
 $\Rightarrow$ *Loc cd idx* $\in$ *Def P n*

 | *Def-Push*:

$\llbracket n = (- (C, M, pc) \# cs, None) - \rrbracket$;
 instrs-of (*P_{wf}*) *C M* ! *pc* = *Push v*;
 cd = *length cs*;
 i = *stkLength P C M pc* $\llbracket$
 $\Rightarrow$ *Stk cd i* $\in$ *Def P n*

 | *Def-New-Normal-Stk*:

$\llbracket n = (- (C, M, pc) \# cs, [(cs', False)] - \rrbracket$;
 instrs-of (*P_{wf}*) *C M* ! *pc* = *New Cl*;
 cd = *length cs*;
 i = *stkLength P C M pc* $\llbracket$
 $\Rightarrow$ *Stk cd i* $\in$ *Def P n*

 | *Def-New-Normal-Heap*:

$\llbracket n = (- (C, M, pc) \# cs, [(cs', False)] - \rrbracket$;
 instrs-of (*P_{wf}*) *C M* ! *pc* = *New Cl* $\llbracket$
 $\Rightarrow$ *HeapVar a* $\in$ *Def P n*

 | *Def-Exc-Stk*:

$\llbracket n = (- (C, M, pc) \# cs, [(cs', True)] - \rrbracket$;
 cs' $\neq []$;
 cd = *length cs' - 1*;

$(C', M', pc') = \text{hd } cs'$;
 $i = \text{stkLength } P \ C' \ M' \ pc' - 1$]
 $\implies \text{Stk } cd \ i \in \text{Def } P \ n$

| *Def-Getfield-Stk*:
 $\llbracket n = (- (C, M, pc) \# cs, [(cs', False)] -);$
 $\text{instrs-of } (P_{wf}) \ C \ M \ ! \ pc = \text{Getfield } Fd \ Cl;$
 $cd = \text{length } cs;$
 $i = \text{stkLength } P \ C \ M \ pc - 1$]
 $\implies \text{Stk } cd \ i \in \text{Def } P \ n$

| *Def-Putfield-Heap*:
 $\llbracket n = (- (C, M, pc) \# cs, [(cs', False)] -);$
 $\text{instrs-of } (P_{wf}) \ C \ M \ ! \ pc = \text{Putfield } Fd \ Cl$]
 $\implies \text{HeapVar } a \in \text{Def } P \ n$

| *Def-Invoke-Loc*:
 $\llbracket n = (- (C, M, pc) \# cs, [(cs', False)] -);$
 $\text{instrs-of } (P_{wf}) \ C \ M \ ! \ pc = \text{Invoke } M' \ n';$
 $cs' \neq [];$
 $\text{hd } cs' = (C', M', 0);$
 $i < \text{locLength } P \ C' \ M' \ 0;$
 $cd = \text{Suc } (\text{length } cs)$]
 $\implies \text{Loc } cd \ i \in \text{Def } P \ n$

| *Def-Return-Stk*:
 $\llbracket n = (- (C, M, pc) \# (D, M', pc') \# cs, None -);$
 $\text{instrs-of } (P_{wf}) \ C \ M \ ! \ pc = \text{Return};$
 $cd = \text{length } cs;$
 $i = \text{stkLength } P \ D \ M' \ (\text{Suc } pc') - 1$]
 $\implies \text{Stk } cd \ i \in \text{Def } P \ n$

| *Def-IAdd-Stk*:
 $\llbracket n = (- (C, M, pc) \# cs, None -);$
 $\text{instrs-of } (P_{wf}) \ C \ M \ ! \ pc = \text{IAdd};$
 $cd = \text{length } cs;$
 $i = \text{stkLength } P \ C \ M \ pc - 2$]
 $\implies \text{Stk } cd \ i \in \text{Def } P \ n$

| *Def-CmpEq-Stk*:
 $\llbracket n = (- (C, M, pc) \# cs, None -);$
 $\text{instrs-of } (P_{wf}) \ C \ M \ ! \ pc = \text{CmpEq};$
 $cd = \text{length } cs;$
 $i = \text{stkLength } P \ C \ M \ pc - 2$]
 $\implies \text{Stk } cd \ i \in \text{Def } P \ n$

inductive-set *Use* :: *wf-jvmprog* $\Rightarrow$ *j-node* $\Rightarrow$ *jinja-var set*
for *P* :: *wf-jvmprog*
and *n* :: *j-node*

where

Use-Load:

$\llbracket n = (- (C, M, pc) \# cs, None \ -);$
 $instrs\text{-}of \ (P_{wf}) \ C \ M \ ! \ pc = Load \ idx;$
 $cd = length \ cs \ \rrbracket$
 $\implies (Loc \ cd \ idx) \in Use \ P \ n$

| *Use-Store:*

$\llbracket n = (- (C, M, pc) \# cs, None \ -);$
 $instrs\text{-}of \ (P_{wf}) \ C \ M \ ! \ pc = Store \ idx;$
 $cd = length \ cs;$
 $Suc \ i = (stkLength \ P \ C \ M \ pc) \ \rrbracket$
 $\implies (Stk \ cd \ i) \in Use \ P \ n$

| *Use-New:*

$\llbracket n = (- (C, M, pc) \# cs, x \ -);$
 $x = None \vee x = \lfloor (cs', False) \rfloor;$
 $instrs\text{-}of \ (P_{wf}) \ C \ M \ ! \ pc = New \ Cl \ \rrbracket$
 $\implies HeapVar \ a \in Use \ P \ n$

| *Use-Getfield-Stk:*

$\llbracket n = (- (C, M, pc) \# cs, x \ -);$
 $x = None \vee x = \lfloor (cs', False) \rfloor;$
 $instrs\text{-}of \ (P_{wf}) \ C \ M \ ! \ pc = Getfield \ Fd \ Cl;$
 $cd = length \ cs;$
 $Suc \ i = stkLength \ P \ C \ M \ pc \ \rrbracket$
 $\implies Stk \ cd \ i \in Use \ P \ n$

| *Use-Getfield-Heap:*

$\llbracket n = (- (C, M, pc) \# cs, \lfloor (cs', False) \rfloor \ -);$
 $instrs\text{-}of \ (P_{wf}) \ C \ M \ ! \ pc = Getfield \ Fd \ Cl \ \rrbracket$
 $\implies HeapVar \ a \in Use \ P \ n$

| *Use-Putfield-Stk-Pred:*

$\llbracket n = (- (C, M, pc) \# cs, None \ -);$
 $instrs\text{-}of \ (P_{wf}) \ C \ M \ ! \ pc = Putfield \ Fd \ Cl;$
 $cd = length \ cs;$
 $i = stkLength \ P \ C \ M \ pc - 2 \ \rrbracket$
 $\implies Stk \ cd \ i \in Use \ P \ n$

| *Use-Putfield-Stk-Update:*

$\llbracket n = (- (C, M, pc) \# cs, \lfloor (cs', False) \rfloor \ -);$
 $instrs\text{-}of \ (P_{wf}) \ C \ M \ ! \ pc = Putfield \ Fd \ Cl;$
 $cd = length \ cs;$
 $i = stkLength \ P \ C \ M \ pc - 2 \vee i = stkLength \ P \ C \ M \ pc - 1 \ \rrbracket$
 $\implies Stk \ cd \ i \in Use \ P \ n$

| *Use-Putfield-Heap:*

$\llbracket n = (- (C, M, pc) \# cs, \lfloor (cs', False) \rfloor \ -);$

$\text{instrs-of } (P_{wf}) \ C \ M \ ! \ pc = \text{Putfield Fd Cl} \]$
 $\implies \text{HeapVar } a \in \text{Use } P \ n$

| *Use-Checkcast-Stk*:
 $\llbracket n = (- (C, M, pc) \# cs, x -);$
 $x = \text{None} \vee x = \lfloor (cs', \text{False}) \rfloor;$
 $\text{instrs-of } (P_{wf}) \ C \ M \ ! \ pc = \text{Checkcast Cl};$
 $cd = \text{length } cs;$
 $i = \text{stkLength } P \ C \ M \ pc - \text{Suc } 0 \]$
 $\implies \text{Stk } cd \ i \in \text{Use } P \ n$

| *Use-Checkcast-Heap*:
 $\llbracket n = (- (C, M, pc) \# cs, \text{None} -);$
 $\text{instrs-of } (P_{wf}) \ C \ M \ ! \ pc = \text{Checkcast Cl} \]$
 $\implies \text{HeapVar } a \in \text{Use } P \ n$

| *Use-Invoke-Stk-Pred*:
 $\llbracket n = (- (C, M, pc) \# cs, \text{None} -);$
 $\text{instrs-of } (P_{wf}) \ C \ M \ ! \ pc = \text{Invoke } M' \ n';$
 $cd = \text{length } cs;$
 $i = \text{stkLength } P \ C \ M \ pc - \text{Suc } n' \]$
 $\implies \text{Stk } cd \ i \in \text{Use } P \ n$

| *Use-Invoke-Heap-Pred*:
 $\llbracket n = (- (C, M, pc) \# cs, \text{None} -);$
 $\text{instrs-of } (P_{wf}) \ C \ M \ ! \ pc = \text{Invoke } M' \ n' \]$
 $\implies \text{HeapVar } a \in \text{Use } P \ n$

| *Use-Invoke-Stk-Update*:
 $\llbracket n = (- (C, M, pc) \# cs, \lfloor (cs', \text{False}) \rfloor -);$
 $\text{instrs-of } (P_{wf}) \ C \ M \ ! \ pc = \text{Invoke } M' \ n';$
 $cd = \text{length } cs;$
 $i < \text{stkLength } P \ C \ M \ pc;$
 $i \geq \text{stkLength } P \ C \ M \ pc - \text{Suc } n' \]$
 $\implies \text{Stk } cd \ i \in \text{Use } P \ n$

| *Use-Return-Stk*:
 $\llbracket n = (- (C, M, pc) \# (D, M', pc') \# cs, \text{None} -);$
 $\text{instrs-of } (P_{wf}) \ C \ M \ ! \ pc = \text{Return};$
 $cd = \text{Suc } (\text{length } cs);$
 $i = \text{stkLength } P \ C \ M \ pc - 1 \]$
 $\implies \text{Stk } cd \ i \in \text{Use } P \ n$

| *Use-IAdd-Stk*:
 $\llbracket n = (- (C, M, pc) \# cs, \text{None} -);$
 $\text{instrs-of } (P_{wf}) \ C \ M \ ! \ pc = \text{IAdd};$
 $cd = \text{length } cs;$
 $i = \text{stkLength } P \ C \ M \ pc - 1 \vee i = \text{stkLength } P \ C \ M \ pc - 2 \]$
 $\implies \text{Stk } cd \ i \in \text{Use } P \ n$

| *Use-IfFalse-Stk*:
 $\llbracket n = (- (C, M, pc) \# cs, None -);$
instrs-of (P_{wf}) $C M ! pc = (IfFalse\ b);$
 $cd = length\ cs;$
 $i = stkLength\ P\ C\ M\ pc - 1 \rrbracket$
 $\implies Stk\ cd\ i \in Use\ P\ n$

| *Use-CmpEq-Stk*:
 $\llbracket n = (- (C, M, pc) \# cs, None -);$
instrs-of (P_{wf}) $C M ! pc = CmpEq;$
 $cd = length\ cs;$
 $i = stkLength\ P\ C\ M\ pc - 1 \vee i = stkLength\ P\ C\ M\ pc - 2 \rrbracket$
 $\implies Stk\ cd\ i \in Use\ P\ n$

| *Use-Throw-Stk*:
 $\llbracket n = (- (C, M, pc) \# cs, x -);$
 $x = None \vee x = \lfloor (cs', True) \rfloor;$
instrs-of (P_{wf}) $C M ! pc = Throw;$
 $cd = length\ cs;$
 $i = stkLength\ P\ C\ M\ pc - 1 \rrbracket$
 $\implies Stk\ cd\ i \in Use\ P\ n$

| *Use-Throw-Heap*:
 $\llbracket n = (- (C, M, pc) \# cs, x -);$
 $x = None \vee x = \lfloor (cs', True) \rfloor;$
instrs-of (P_{wf}) $C M ! pc = Throw \rrbracket$
 $\implies HeapVar\ a \in Use\ P\ n$

declare *correct-state-def* [*simp del*]

lemma *edge-transfer-uses-only-Use*:

$\llbracket valid-edge\ (P, C0, Main)\ a; \forall V \in Use\ P\ (sourcenode\ a). state-val\ s\ V = state-val\ s'\ V \rrbracket$
 $\implies \forall V \in Def\ P\ (sourcenode\ a). state-val\ (BasicDefs.transfer\ (kind\ a)\ s)\ V = state-val\ (BasicDefs.transfer\ (kind\ a)\ s')\ V$

<proof>

lemma *CFG-edge-Uses-pred-equal*:

$\llbracket valid-edge\ (P, C0, Main)\ a;$
 $pred\ (kind\ a)\ s;$
 $\forall V \in Use\ P\ (sourcenode\ a). state-val\ s\ V = state-val\ s'\ V \rrbracket$
 $\implies pred\ (kind\ a)\ s'$

<proof>

lemma *edge-no-Def-equal*:

$\llbracket valid-edge\ (P, C0, Main)\ a;$
 $V \notin Def\ P\ (sourcenode\ a) \rrbracket$

$\Rightarrow$ *state-val (transfer (kind a) s) V = state-val s V*
 <proof>

interpretation *JVM-CFG-wf: CFG-wf*
sourcenode targetnode kind valid-edge prog (-Entry-)
Def (fst prog) Use (fst prog) state-val
for *prog*
 <proof>

interpretation *JVM-CFGExit-wf: CFGExit-wf*
sourcenode targetnode kind valid-edge prog (-Entry-)
Def (fst prog) Use (fst prog) state-val (-Exit-)
 <proof>

end

6.5 Instantiating the control dependences

theory *JVMControlDependences imports*
JVMPostdomination
JVMCFG-wf
../Dynamic/DynPDG
../StaticIntra/CDepInstantiations
begin

6.5.1 Dynamic dependences

interpretation *JVMDynStandardControlDependence:*
DynStandardControlDependencePDG sourcenode targetnode kind
valid-edge_CFG prog (-Entry-) Def (fst_CFG prog) Use (fst_CFG prog)
state-val (-Exit-) <proof>

interpretation *JVMDynWeakControlDependence:*
DynWeakControlDependencePDG sourcenode targetnode kind
valid-edge_CFG prog (-Entry-) Def (fst_CFG prog) Use (fst_CFG prog)
state-val (-Exit-) <proof>

6.5.2 Static dependences

interpretation *JVMStandardControlDependence:*
StandardControlDependencePDG sourcenode targetnode kind
valid-edge_CFG prog (-Entry-) Def (fst_CFG prog) Use (fst_CFG prog)
state-val (-Exit-) <proof>

interpretation *JVMWeakControlDependence:*
WeakControlDependencePDG sourcenode targetnode kind
valid-edge_CFG prog (-Entry-) Def (fst_CFG prog) Use (fst_CFG prog)

```
state-val (-Exit-) ⟨proof⟩  
end
```

Chapter 7

Equivalence of the CFG and Jinja

```
theory SemanticsWF imports JVMInterpretation ../Basic/SemanticsCFG begin  
  
declare rev-nth [simp add]
```

7.1 State updates

The following abbreviations update the stack and the local variables (in the representation as used in the CFG) according to a *frame list* as it is used in Jinja's state representation.

abbreviation *update-stk* :: $((nat \times nat) \Rightarrow val) \Rightarrow (frame\ list) \Rightarrow ((nat \times nat) \Rightarrow val)$

where

update-stk stk frs $\equiv (\lambda(a, b).$
 if *length frs* $\leq a$ *then* *stk* (*a*, *b*)
 else let *xs* = *fst* (*frs* ! (*length frs* - *Suc a*))
 in if *length xs* $\leq b$ *then* *stk* (*a*, *b*) *else* *xs* ! (*length xs* - *Suc b*)

abbreviation *update-loc* :: $((nat \times nat) \Rightarrow val) \Rightarrow (frame\ list) \Rightarrow ((nat \times nat) \Rightarrow val)$

where

update-loc loc frs $\equiv (\lambda(a, b).$
 if *length frs* $\leq a$ *then* *loc* (*a*, *b*)
 else let *xs* = *fst* (*snd* (*frs* ! (*length frs* - *Suc a*)))
 in if *length xs* $\leq b$ *then* *loc* (*a*, *b*) *else* *xs* ! *b*

7.1.1 Some simplification lemmas

lemma *update-loc-s2jvm* [*simp*]:

update-loc loc (snd(snd(state-to-jvm-state P cs (h,stk,loc)))) = loc
<proof>

lemma *update-stk-s2jvm* [simp]:
 $\text{update-stk } stk \ (\text{snd}(\text{snd}(\text{state-to-jvm-state } P \ cs \ (h,stk,loc)))) = stk$
 ⟨proof⟩

lemma *update-loc-s2jvm'* [simp]:
 $\text{update-loc } loc \ (\text{zip } (stkss \ P \ cs \ stk) \ (\text{zip } (locss \ P \ cs \ loc) \ cs)) = loc$
 ⟨proof⟩

lemma *update-stk-s2jvm'* [simp]:
 $\text{update-stk } stk \ (\text{zip } (stkss \ P \ cs \ stk) \ (\text{zip } (locss \ P \ cs \ loc) \ cs)) = stk$
 ⟨proof⟩

lemma *find-handler-find-handler-forD*:
 $\text{find-handler } (P_{wf}) \ a \ h \ frs = (xp', h', frs')$
 $\implies \text{find-handler-for } P \ (\text{cname-of } h \ a) \ (\text{framestack-to-callstack } frs) =$
 $\text{framestack-to-callstack } frs'$
 ⟨proof⟩

lemma *find-handler-nonempty-frs* [simp]:
 $(\text{find-handler } P \ a \ h \ frs \neq (\text{None}, h', []))$
 ⟨proof⟩

lemma *find-handler-heap-eqD*:
 $\text{find-handler } P \ a \ h \ frs = (xp, h', frs') \implies h' = h$
 ⟨proof⟩

lemma *find-handler-frs-decrD*:
 $\text{find-handler } P \ a \ h \ frs = (xp, h', frs') \implies \text{length } frs' \leq \text{length } frs$
 ⟨proof⟩

lemma *find-handler-decrD* [dest]:
 $\text{find-handler } P \ a \ h \ frs = (xp, h', f \# frs) \implies \text{False}$
 ⟨proof⟩

lemma *find-handler-decrD'* [dest]:
 $\llbracket \text{find-handler } P \ a \ h \ frs = (xp, h', f \# frs'); \text{length } frs = \text{length } frs' \rrbracket \implies \text{False}$
 ⟨proof⟩

lemma *Suc-minus-Suc-Suc* [simp]:
 $b < n - 1 \implies \text{Suc } (n - \text{Suc } (\text{Suc } b)) = n - \text{Suc } b$
 ⟨proof⟩

lemma *find-handler-loc-fun-eq'*:
 $\text{find-handler } (P_{wf}) \ a \ h$
 $(\text{zip } (stkss \ P \ cs \ stk) \ (\text{zip } (locss \ P \ cs \ loc) \ cs)) =$
 (xf, h', frs)
 $\implies \text{update-loc } loc \ frs = loc$
 ⟨proof⟩

lemma *find-handler-loc-fun-eq*:

$find_handler\ (P_{wf})\ a\ h\ (snd(snd(state_to_jvm_state\ P\ cs\ (h,stk,loc)))) = (xf,h',frs)$
 $\implies update_loc\ loc\ frs = loc$
 $\langle proof \rangle$

lemma *find-handler-stk-fun-eq'*:

$\llbracket find_handler\ (P_{wf})\ a\ h$
 $(zip\ (stkss\ P\ cs\ stk)\ (zip\ (locss\ P\ cs\ loc)\ cs)) =$
 $(None,\ h',\ frs);$
 $cd = length\ frs - 1;$
 $i = length\ (fst(hd(frs))) - 1 \rrbracket$
 $\implies update_stk\ stk\ frs = stk((cd,\ i) := Addr\ a)$
 $\langle proof \rangle$

lemma *find-handler-stk-fun-eq*:

$find_handler\ (P_{wf})\ a\ h\ (snd(snd(state_to_jvm_state\ P\ cs\ (h,stk,loc)))) = (None,h',frs)$
 $\implies update_stk\ stk\ frs = stk((length\ frs - 1,\ length\ (fst(hd(frs))) - 1) := Addr\ a)$
 $\langle proof \rangle$

lemma *f2c-emptyD [dest]*:

$framestack_to_callstack\ frs = [] \implies frs = []$
 $\langle proof \rangle$

lemma *f2c-emptyD' [dest]*:

$[] = framestack_to_callstack\ frs \implies frs = []$
 $\langle proof \rangle$

lemma *correct-state-imp-valid-callstack*:

$\llbracket P,cs \vdash_{BV} s\ \checkmark; fst\ (last\ cs) = C0; fst(snd\ (last\ cs)) = Main \rrbracket$
 $\implies valid_callstack\ (P,C0,Main)\ cs$
 $\langle proof \rangle$

declare *correct-state-def [simp del]*

lemma *bool-sym*: $Bool\ (a = b) = Bool\ (b = a)$

$\langle proof \rangle$

lemma *find-handler-exec-correct*:

$\llbracket (P_{wf}), (P_{\Phi}) \vdash state_to_jvm_state\ P\ cs\ (h,stk,loc)\ \checkmark;$
 $(P_{wf}), (P_{\Phi}) \vdash find_handler\ (P_{wf})\ a\ h$
 $(zip\ (stkss\ P\ cs\ stk)\ (zip\ (locss\ P\ cs\ loc)\ cs))\ \checkmark;$
 $find_handler_for\ P\ (cname_of\ h\ a)\ cs = (C',\ M',\ pc') \# cs'$
 $\rrbracket \implies$
 $(P_{wf}), (P_{\Phi}) \vdash (None,\ h,$
 $(stks\ (stkLength\ P\ C'\ M'\ pc')$
 $(\lambda a'. (stk((length\ cs',\ stkLength\ P\ C'\ M'\ pc' - Suc\ 0) := Addr\ a))\ (length$
 $cs',\ a'))),$

$$\text{locs } (\text{locLength } P \ C' \ M' \ pc') \ (\lambda a. \text{loc } (\text{length } cs', a)), \ C', \ M', \ pc') \ \#$$

$$\text{zip } (\text{stkss } P \ cs' \ stk) \ (\text{zip } (\text{locss } P \ cs' \ loc) \ cs')) \ \surd$$

$$\langle \text{proof} \rangle$$

lemma *locs-rev-stks*:

$$x \geq z \implies$$

$$\text{locs } z$$

$$(\lambda b.$$

$$\text{if } z < b \text{ then } \text{loc } (\text{Suc } y, b)$$

$$\text{else if } b \leq z$$

$$\text{then } \text{stk } (y, x + b - \text{Suc } z)$$

$$\text{else arbitrary})$$

$$@ [\text{stk } (y, x - \text{Suc } 0)]$$

$$=$$

$$\text{stk } (y, x - \text{Suc } (z))$$

$$\# \text{rev } (\text{take } z \ (\text{stks } x \ (\lambda a. \text{stk}(y, a))))$$

$$\langle \text{proof} \rangle$$

lemma *locs-invoke-purge*:

$$(z::\text{nat}) > c \implies$$

$$\text{locs } l$$

$$(\lambda b. \text{if } z = c \longrightarrow Q \ b \text{ then } \text{loc } (c, b) \text{ else } u \ b) =$$

$$\text{locs } l \ (\lambda a. \text{loc } (c, a))$$

$$\langle \text{proof} \rangle$$

lemma *nth-rev-equalityI*:

$$\llbracket \text{length } xs = \text{length } ys; \forall i < \text{length } xs. \ xs \ ! \ (\text{length } xs - \text{Suc } i) = ys \ ! \ (\text{length } ys$$

$$- \text{Suc } i) \rrbracket$$

$$\implies xs = ys$$

$$\langle \text{proof} \rangle$$

lemma *length-locss*:

$$i < \text{length } cs$$

$$\implies \text{length } (\text{locss } P \ cs \ loc \ ! \ (\text{length } cs - \text{Suc } i)) =$$

$$\text{locLength } P \ (\text{fst}(cs \ ! \ (\text{length } cs - \text{Suc } i)))$$

$$(\text{fst}(\text{snd}(cs \ ! \ (\text{length } cs - \text{Suc } i))))$$

$$(\text{snd}(\text{snd}(cs \ ! \ (\text{length } cs - \text{Suc } i))))$$

$$\langle \text{proof} \rangle$$

lemma *locss-invoke-purge*:

$$z > \text{length } cs \implies$$

$$\text{locss } P \ cs$$

$$(\lambda(a, b). \text{if } (a = z \longrightarrow Q \ b)$$

$$\text{then } \text{loc } (a, b)$$

$$\text{else } u \ b)$$

$$= \text{locss } P \ cs \ loc$$

$$\langle \text{proof} \rangle$$

lemma *stks-purge'*:

$d \geq b \implies \text{stks } b \ (\lambda x. \text{ if } x = d \text{ then } e \text{ else } \text{stk } x) = \text{stks } b \ \text{stk}$
 $\langle \text{proof} \rangle$

7.1.2 Byte code verifier conformance

Here we prove state conformance invariant under *transfer* for our CFG. Therefore, we must assume, that the predicate of a potential preceding predicate-edge holds for every update-edge.

theorem *bv-invariant*:

$\llbracket \text{valid-edge } (P, C0, \text{Main}) \ a;$
 $\text{sourcenode } a = (- (C, M, pc) \# cs, x -);$
 $\text{targetnode } a = (- (C', M', pc') \# cs', x' -);$
 $\text{pred } (\text{kind } a) \ s;$
 $x \neq \text{None} \implies (\exists a\text{-pred.}$
 $\text{sourcenode } a\text{-pred} = (- (C, M, pc) \# cs, \text{None} -) \wedge$
 $\text{targetnode } a\text{-pred} = \text{sourcenode } a \wedge$
 $\text{valid-edge } (P, C0, \text{Main}) \ a\text{-pred} \wedge$
 $\text{pred } (\text{kind } a\text{-pred}) \ s$
 $\rangle;$
 $P, ((C, M, pc) \# cs) \vdash_{BV} s \ \checkmark \rrbracket$
 $\implies P, ((C', M', pc') \# cs') \vdash_{BV} \text{transfer } (\text{kind } a) \ s \ \checkmark$
 $\langle \text{proof} \rangle$

7.2 CFG simulates Jinja's semantics

7.2.1 Definitions

The following predicate defines the semantics of Jinja lifted to our state representation. Thereby, we require the state to be byte code verifier conform; otherwise the step in the semantics is undefined.

The predicate *valid-callstack* is actually an implication of the byte code verifier conformance. But we list it explicitly for convenience.

inductive *sem* :: *jvmprog* $\Rightarrow$ *callstack* $\Rightarrow$ *state* $\Rightarrow$ *callstack* $\Rightarrow$ *state* $\Rightarrow$ *bool*

$(- \vdash \langle -, - \rangle \Rightarrow \langle -, - \rangle)$

where *Step*:

$\llbracket \text{prog} = (P, C0, \text{Main});$
 $P, cs \vdash_{BV} s \ \checkmark;$
 $\text{valid-callstack } \text{prog } cs;$
 $\text{JVMEExec.exec } ((P_{wf}), \text{state-to-jvm-state } P \ cs \ s) = \lfloor (\text{None}, h', frs') \rfloor;$
 $cs' = \text{framestack-to-callstack } frs';$
 $s = (h, \text{stk}, \text{loc});$
 $s' = (h', \text{update-stk } \text{stk } frs', \text{update-loc } \text{loc } frs') \rrbracket$
 $\implies \text{prog} \vdash \langle cs, s \rangle \Rightarrow \langle cs', s' \rangle$

abbreviation *identifies* :: *j-node* $\Rightarrow$ *callstack* $\Rightarrow$ *bool*
where *identifies* *n cs* $\equiv$ (*n* = (- *cs*, *None* -))

7.2.2 Some more simplification lemmas

lemma *valid-callstack-tl*:

valid-callstack prog ((C,M,pc)#cs) $\implies$ valid-callstack prog cs
<proof>

lemma *stkss-cong [cong]*:

$\llbracket P = P';$
 $cs = cs';$
 $\bigwedge a b. \llbracket a < \text{length } cs;$
 $b < \text{stkLength } P (\text{fst}(cs ! (\text{length } cs - \text{Suc } a)))$
 $(\text{fst}(\text{snd}(cs ! (\text{length } cs - \text{Suc } a))))$
 $(\text{snd}(\text{snd}(cs ! (\text{length } cs - \text{Suc } a)))) \rrbracket$
 $\implies \text{stk } (a, b) = \text{stk}' (a, b) \rrbracket$
 $\implies \text{stkss } P \text{ cs } \text{stk} = \text{stkss } P' \text{ cs}' \text{stk}'$
<proof>

lemma *locss-cong [cong]*:

$\llbracket P = P';$
 $cs = cs';$
 $\bigwedge a b. \llbracket a < \text{length } cs;$
 $b < \text{locLength } P (\text{fst}(cs ! (\text{length } cs - \text{Suc } a)))$
 $(\text{fst}(\text{snd}(cs ! (\text{length } cs - \text{Suc } a))))$
 $(\text{snd}(\text{snd}(cs ! (\text{length } cs - \text{Suc } a)))) \rrbracket$
 $\implies \text{loc } (a, b) = \text{loc}' (a, b) \rrbracket$
 $\implies \text{locss } P \text{ cs } \text{loc} = \text{locss } P' \text{ cs}' \text{loc}'$
<proof>

lemma *hd-tl-equalityI*:

$\llbracket \text{length } xs = \text{length } ys; \text{hd } xs = \text{hd } ys; \text{tl } xs = \text{tl } ys \rrbracket \implies xs = ys$
<proof>

lemma *stkLength-is-length-stk*:

$P_{wf}, P_{\Phi} \vdash (\text{None}, h, (\text{stk}, \text{loc}, C, M, pc) \# \text{frs}') \checkmark \implies \text{stkLength } P \text{ C } M \text{ pc} =$
 $\text{length } \text{stk}$
<proof>

lemma *locLength-is-length-loc*:

$P_{wf}, P_{\Phi} \vdash (\text{None}, h, (\text{stk}, \text{loc}, C, M, pc) \# \text{frs}') \checkmark \implies \text{locLength } P \text{ C } M \text{ pc} =$
 $\text{length } \text{loc}$
<proof>

lemma *correct-state-frs-tlD*:

$(P_{wf}), (P_{\Phi}) \vdash (\text{None}, h, a \# \text{frs}') \checkmark \implies (P_{wf}), (P_{\Phi}) \vdash (\text{None}, h, \text{frs}') \checkmark$
<proof>

lemma *update-stk-Cons* [simp]:

$stkss\ P\ (framestack\text{-}to\text{-}callstack\ frs')\ (update\text{-}stk\ stk\ ((stk',\ loc',\ C',\ M',\ pc')\ \#$
 $frs')) =$
 $stkss\ P\ (framestack\text{-}to\text{-}callstack\ frs')\ (update\text{-}stk\ stk\ frs')$
 $\langle proof \rangle$

lemma *update-loc-Cons* [simp]:

$locss\ P\ (framestack\text{-}to\text{-}callstack\ frs')\ (update\text{-}loc\ loc\ ((stk',\ loc',\ C',\ M',\ pc')\ \#$
 $frs')) =$
 $locss\ P\ (framestack\text{-}to\text{-}callstack\ frs')\ (update\text{-}loc\ loc\ frs')$
 $\langle proof \rangle$

lemma *s2j-id*:

$(P_{wf}), (P_{\Phi}) \vdash (None, h', frs') \checkmark$
 $\implies state\text{-}to\text{-}jvm\text{-}state\ P\ (framestack\text{-}to\text{-}callstack\ frs')$
 $(h, update\text{-}stk\ stk\ frs', update\text{-}loc\ loc\ frs') = (None, h, frs')$
 $\langle proof \rangle$

lemma *find-handler-last-cs-eqD*:

$\llbracket find\text{-}handler\ P_{wf}\ a\ h\ frs = (None, h', frs') \rrbracket$;
 $last\ frs = (stk, loc, C, M, pc)$;
 $last\ frs' = (stk', loc', C', M', pc')$ $\rrbracket$
 $\implies C = C' \wedge M = M'$
 $\langle proof \rangle$

lemma *exec-last-frs-eq-class*:

$\llbracket JVMExec.exec\ (P_{wf},\ None, h, frs) = \lfloor (None, h', frs') \rrbracket$;
 $last\ frs = (stk, loc, C, M, pc)$;
 $last\ frs' = (stk', loc', C', M', pc')$;
 $frs \neq []$;
 $frs' \neq []$ $\rrbracket$
 $\implies C = C'$
 $\langle proof \rangle$

lemma *exec-last-frs-eq-method*:

$\llbracket JVMExec.exec\ (P_{wf},\ None, h, frs) = \lfloor (None, h', frs') \rrbracket$;
 $last\ frs = (stk, loc, C, M, pc)$;
 $last\ frs' = (stk', loc', C', M', pc')$;
 $frs \neq []$;
 $frs' \neq []$ $\rrbracket$
 $\implies M = M'$
 $\langle proof \rangle$

lemma *valid-callstack-append-last-class*:

$valid\text{-}callstack\ (P, C0, Main)\ (cs@[C, M, pc]) \implies C = C0$
 $\langle proof \rangle$

lemma *valid-callstack-append-last-method*:

valid-callstack ($P, C0, Main$) ($cs @ [(C, M, pc)]$) $\implies M = Main$
 ⟨proof⟩

lemma *zip-stkss-locss-append-single* [simp]:

zip (*stkss* P ($cs @ [(C, M, pc)]$) *stk*)
 (*zip* (*locss* P ($cs @ [(C, M, pc)]$) *loc*) ($cs @ [(C, M, pc)]$)
 = (*zip* (*stkss* P ($cs @ [(C, M, pc)]$) *stk*) (*zip* (*locss* P ($cs @ [(C, M, pc)]$) *loc*)
cs))
 @ [(*stks* (*stkLength* $P C M pc$) ($\lambda a. stk (0, a)$),
locs (*locLength* $P C M pc$) ($\lambda a. loc (0, a)$), C, M, pc)]
 ⟨proof⟩

7.2.3 Interpretation of the *CFG-semantics-wf* locale

interpretation *JVM-semantics-CFG-wf*:

CFG-semantics-wf *sourcenode* *targetnode* *kind* *valid-edge* *prog* (-Entry-)
sem *prog* *identifies*
for *prog*
 ⟨proof⟩

end

theory *Slicing*

imports

Basic/Postdomination
Basic/CFGExit-wf
Basic/SemanticsCFG
Dynamic/DynSlice
StaticIntra/CDepInstantiations
StaticIntra/ControlDependenceRelations
While/DynamicControlDependences
While/NonInterferenceWhile
JinjaVM/JVMControlDependences
JinjaVM/SemanticsWF

begin

end

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