

An Isabelle Correctness Proof for the Volpano/Smith Security Typing System

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March 17, 2025

Abstract

The Volpano/Smith/Irvine security type systems [2] requires that variables are annotated as high (secret) or low (public), and provides typing rules which guarantee that secret values cannot leak to public output ports. This property of a program is called confidentiality.

For a simple while-language without threads, our proof shows that typeability in the Volpano/Smith system guarantees noninterference. Noninterference means that if two initial states for program execution are low-equivalent, then the final states are low-equivalent as well. This indeed implies that secret values cannot leak to public ports. For more details on noninterference and security typing systems, see [1].

The proof defines an abstract syntax and operational semantics for programs, formalizes noninterference, and then proceeds by rule induction on the operational semantics. The mathematically most intricate part is the treatment of implicit flows. Note that the Volpano/Smith system is not flow-sensitive and thus quite unprecise, resulting in false alarms. However, due to the correctness property, all potential breaks of confidentiality are discovered.

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```

theory Semantics
imports Main
begin

```

1 The Language

1.1 Variables and Values

type-synonym $vname = string$ — names for variables

datatype val
 $= Bool \text{ bool}$ — Boolean value
 $| Intg \text{ int}$ — integer value

abbreviation $true == Bool True$
abbreviation $false == Bool False$

1.2 Expressions and Commands

datatype $bop = Eq | And | Less | Add | Sub$ — names of binary operations

datatype $expr$
 $= Val \text{ val}$ — value
 $| Var \text{ vname}$ — local variable
 $| BinOp \text{ expr } bop \text{ expr} \quad (\langle\!\langle \cdot \rangle\!\rangle \rightarrow [80,0,81] 80)$ — binary operation

Note: we assume that only type correct expressions are regarded as later proofs fail if expressions evaluate to None due to type errors. However there is [yet] no typing system

```

fun binop :: bop ⇒ val ⇒ val ⇒ val option
where binop Eq v1 v2 = Some(Bool(v1 = v2))
| binop And (Bool b1) (Bool b2) = Some(Bool(b1 ∧ b2))
| binop Less (Intg i1) (Intg i2) = Some(Bool(i1 < i2))
| binop Add (Intg i1) (Intg i2) = Some(Intg(i1 + i2))
| binop Sub (Intg i1) (Intg i2) = Some(Intg(i1 - i2))

| binop bop v1 v2 = Some(Intg(0))

```

datatype com
 $= Skip$
 $| LAss \text{ vname } expr \quad (\langle\!\langle := \rangle\!\rangle [70,70] 70)$ — local assignment
 $| Seq \text{ com com} \quad (\langle\!\langle ; \rangle\!\rangle [61,60] 60)$
 $| Cond \text{ expr com com} \quad (\langle\!\langle if '(-) / else \rangle\!\rangle [80,79,79] 70)$
 $| While \text{ expr com} \quad (\langle\!\langle while '(-) \rangle\!\rangle [80,79] 70)$

fun $fv :: expr \Rightarrow vname \text{ set}$ — free variables in an expression
where

```

FVc: fv (Val V) = {}
| FVv: fv (Var V) = {V}
| FVe: fv (e1 «bop» e2) = fv e1 ∪ fv e2

```

1.3 State

type-synonym $state = vname \rightharpoonup val$

interpret silently assumes type correct expressions, i.e. no expression evaluates to None

```

fun interpret :: expr ⇒ state ⇒ val option (⟨[ ]-⟩)
where Val: [Val v] s = Some v
  | Var: [Var V] s = s V
  | BinOp: [e1«bop»e2] s = (case [e1] s of None ⇒ None
    | Some v1 ⇒ (case [e2] s of None ⇒ None
      | Some v2 ⇒ binop bop v1 v2))

```

1.4 Small Step Semantics

```

inductive red :: com * state ⇒ com * state ⇒ bool
and red' :: com ⇒ state ⇒ com ⇒ state ⇒ bool
  (⟨((1⟨-,/-⟩) → / (1⟨-,/-⟩))⟩ [0,0,0,0] 81)
where
  ⟨c1,s1⟩ → ⟨c2,s2⟩ == red (c1,s1) (c2,s2) |
  RedLAss:
  ⟨V:=e,s⟩ → ⟨Skip,s(V:=(⟨e⟩ s))⟩

  | SeqRed:
  ⟨c1,s⟩ → ⟨c1',s'⟩ ⇒ ⟨c1;;c2,s⟩ → ⟨c1';c2,s'⟩

  | RedSeq:
  ⟨Skip;;c2,s⟩ → ⟨c2,s⟩

  | RedCondTrue:
  [b] s = Some true ⇒ ⟨if (b) c1 else c2,s⟩ → ⟨c1,s⟩

  | RedCondFalse:
  [b] s = Some false ⇒ ⟨if (b) c1 else c2,s⟩ → ⟨c2,s⟩

  | RedWhileTrue:
  [b] s = Some true ⇒ ⟨while (b) c,s⟩ → ⟨c;;while (b) c,s⟩

  | RedWhileFalse:
  [b] s = Some false ⇒ ⟨while (b) c,s⟩ → ⟨Skip,s⟩

```

lemmas $red\text{-induct} = red\text{.induct}[split\text{-format (complete)}]$

```

abbreviation reds :: com ⇒ state ⇒ com ⇒ state ⇒ bool
  (⟨((1⟨-,/-⟩) → */ (1⟨-,/-⟩))⟩ [0,0,0,0] 81) where
  ⟨c,s⟩ → * ⟨c',s'⟩ == red** (c,s) (c',s')

```

lemma *Skip-reds*:
 $\langle \text{Skip}, s \rangle \rightarrow^* \langle c', s' \rangle \implies s = s' \wedge c' = \text{Skip}$
 $\langle \text{proof} \rangle$

lemma *LAss-reds*:
 $\langle V := e, s \rangle \rightarrow^* \langle \text{Skip}, s' \rangle \implies s' = s(V := \llbracket e \rrbracket s)$
 $\langle \text{proof} \rangle$

lemma *Seq2-reds*:
 $\langle \text{Skip}; c_2, s \rangle \rightarrow^* \langle \text{Skip}, s' \rangle \implies \langle c_2, s \rangle \rightarrow^* \langle \text{Skip}, s' \rangle$
 $\langle \text{proof} \rangle$

lemma *Seq-reds*:
assumes $\langle c_1;; c_2, s \rangle \rightarrow^* \langle \text{Skip}, s' \rangle$
obtains s'' **where** $\langle c_1, s \rangle \rightarrow^* \langle \text{Skip}, s'' \rangle$ **and** $\langle c_2, s'' \rangle \rightarrow^* \langle \text{Skip}, s' \rangle$
 $\langle \text{proof} \rangle$

lemma *Cond-True-or-False*:
 $\langle \text{if } (b) \ c_1 \ \text{else } c_2, s \rangle \rightarrow^* \langle \text{Skip}, s' \rangle \implies \llbracket b \rrbracket s = \text{Some true} \vee \llbracket b \rrbracket s = \text{Some false}$
 $\langle \text{proof} \rangle$

lemma *CondTrue-reds*:
 $\langle \text{if } (b) \ c_1 \ \text{else } c_2, s \rangle \rightarrow^* \langle \text{Skip}, s' \rangle \implies \llbracket b \rrbracket s = \text{Some true} \implies \langle c_1, s \rangle \rightarrow^* \langle \text{Skip}, s' \rangle$
 $\langle \text{proof} \rangle$

lemma *CondFalse-reds*:
 $\langle \text{if } (b) \ c_1 \ \text{else } c_2, s \rangle \rightarrow^* \langle \text{Skip}, s' \rangle \implies \llbracket b \rrbracket s = \text{Some false} \implies \langle c_2, s \rangle \rightarrow^* \langle \text{Skip}, s' \rangle$
 $\langle \text{proof} \rangle$

lemma *WhileFalse-reds*:
 $\langle \text{while } (b) \ cx, s \rangle \rightarrow^* \langle \text{Skip}, s' \rangle \implies \llbracket b \rrbracket s = \text{Some false} \implies s = s'$
 $\langle \text{proof} \rangle$

lemma *WhileTrue-reds*:
 $\langle \text{while } (b) \ cx, s \rangle \rightarrow^* \langle \text{Skip}, s' \rangle \implies \llbracket b \rrbracket s = \text{Some true}$
 $\implies \exists sx. \langle cx, s \rangle \rightarrow^* \langle \text{Skip}, sx \rangle \wedge \langle \text{while } (b) \ cx, sx \rangle \rightarrow^* \langle \text{Skip}, s' \rangle$
 $\langle \text{proof} \rangle$

lemma *While-True-or-False*:
 $\langle \text{while } (b) \ com, s \rangle \rightarrow^* \langle \text{Skip}, s' \rangle \implies \llbracket b \rrbracket s = \text{Some true} \vee \llbracket b \rrbracket s = \text{Some false}$
 $\langle \text{proof} \rangle$

inductive *red-n* :: $com \Rightarrow state \Rightarrow nat \Rightarrow com \Rightarrow state \Rightarrow bool$
 $\langle \langle ((1 \langle -, / \rangle) \rightarrow^* (1 \langle -, / \rangle)) \rangle [0, 0, 0, 0, 0] \ 81 \rangle$
where *red-n-Base*: $\langle c, s \rangle \rightarrow^0 \langle c, s \rangle$
 $| \ i \text{ red-n-Rec}: \llbracket \langle c, s \rangle \rightarrow \langle c'', s'' \rangle; \langle c'', s'' \rangle \rightarrow^n \langle c', s' \rangle \rrbracket \implies \langle c, s \rangle \rightarrow^{Suc \ n} \langle c', s' \rangle$

lemma *Seq-red-nE*: **assumes** $\langle c_1;;c_2,s \rangle \rightarrow^n \langle Skip,s' \rangle$
obtains $i j s''$ **where** $\langle c_1,s \rangle \rightarrow^i \langle Skip,s'' \rangle$ **and** $\langle c_2,s'' \rangle \rightarrow^j \langle Skip,s' \rangle$
and $n = i + j + 1$
 $\langle proof \rangle$

lemma *while-red-nE*:
 $\langle while(b) cx,s \rangle \rightarrow^n \langle Skip,s' \rangle$
 $\implies (\llbracket b \rrbracket s = Some\ false \wedge s = s' \wedge n = 1) \vee$
 $(\exists i j s''. \llbracket b \rrbracket s = Some\ true \wedge \langle cx,s \rangle \rightarrow^i \langle Skip,s'' \rangle \wedge$
 $\langle while(b) cx,s'' \rangle \rightarrow^j \langle Skip,s' \rangle \wedge n = i + j + 2)$
 $\langle proof \rangle$

lemma *while-red-n-induct* [*consumes 1, case-names false true*]:
assumes major: $\langle while(b) cx,s \rangle \rightarrow^n \langle Skip,s' \rangle$
and IHfalse: $\bigwedge s. \llbracket b \rrbracket s = Some\ false \implies P s s$
and IHtrue: $\bigwedge s i j s''. \llbracket \llbracket b \rrbracket s = Some\ true; \langle cx,s \rangle \rightarrow^i \langle Skip,s'' \rangle;$
 $\langle while(b) cx,s'' \rangle \rightarrow^j \langle Skip,s' \rangle; P s'' s \rrbracket \implies P s s'$
shows $P s s'$
 $\langle proof \rangle$

lemma *reds-to-red-n*: $\langle c,s \rangle \rightarrow^* \langle c',s' \rangle \implies \exists n. \langle c,s \rangle \rightarrow^n \langle c',s' \rangle$
 $\langle proof \rangle$

lemma *red-n-to-reds*: $\langle c,s \rangle \rightarrow^n \langle c',s' \rangle \implies \langle c,s \rangle \rightarrow^* \langle c',s' \rangle$
 $\langle proof \rangle$

lemma *while-reds-induct* [*consumes 1, case-names false true*]:
 $\llbracket \langle while(b) cx,s \rangle \rightarrow^* \langle Skip,s' \rangle; \bigwedge s. \llbracket b \rrbracket s = Some\ false \implies P s s;$
 $\bigwedge s s''. \llbracket \llbracket b \rrbracket s = Some\ true; \langle cx,s \rangle \rightarrow^* \langle Skip,s'' \rangle;$
 $\langle while(b) cx,s'' \rangle \rightarrow^* \langle Skip,s' \rangle; P s'' s \rrbracket \implies P s s'$
 $\implies P s s'$
 $\langle proof \rangle$

lemma *red-det*:
 $\llbracket \langle c,s \rangle \rightarrow \langle c_1,s_1 \rangle; \langle c,s \rangle \rightarrow \langle c_2,s_2 \rangle \rrbracket \implies c_1 = c_2 \wedge s_1 = s_2$
 $\langle proof \rangle$

theorem *reds-det*:
 $\llbracket \langle c,s \rangle \rightarrow^* \langle Skip,s_1 \rangle; \langle c,s \rangle \rightarrow^* \langle Skip,s_2 \rangle \rrbracket \implies s_1 = s_2$
 $\langle proof \rangle$

```

end
theory secTypes
imports Semantics
begin

```

2 Security types

2.1 Security definitions

datatype $secLevel = Low \mid High$

type-synonym $typeEnv = vname \rightsquigarrow secLevel$

inductive $secExprTyping :: typeEnv \Rightarrow expr \Rightarrow secLevel \Rightarrow bool$ ($\langle \cdot, \cdot \vdash \cdot : \cdot \rangle$)
where $typeVal: \Gamma \vdash Val V : lev$

- | $typeVar: \Gamma Vn = Some lev \implies \Gamma \vdash Var Vn : lev$
- | $typeBinOp1: [\Gamma \vdash e1 : Low; \Gamma \vdash e2 : Low] \implies \Gamma \vdash e1 \llbracket bop \rrbracket e2 : Low$
- | $typeBinOp2: [\Gamma \vdash e1 : High; \Gamma \vdash e2 : lev] \implies \Gamma \vdash e1 \llbracket bop \rrbracket e2 : High$
- | $typeBinOp3: [\Gamma \vdash e1 : lev; \Gamma \vdash e2 : High] \implies \Gamma \vdash e1 \llbracket bop \rrbracket e2 : High$

inductive $secComTyping :: typeEnv \Rightarrow secLevel \Rightarrow com \Rightarrow bool$ ($\langle \cdot, \cdot \vdash \cdot \rangle$)
where $typeSkip: \Gamma, T \vdash Skip$

- | $typeAssH: \Gamma V = Some High \implies \Gamma, T \vdash V := e$
- | $typeAssL: [\Gamma \vdash e : Low; \Gamma V = Some Low] \implies \Gamma, Low \vdash V := e$
- | $typeSeq: [\Gamma, T \vdash c1; \Gamma, T \vdash c2] \implies \Gamma, T \vdash c1;; c2$
- | $typeWhile: [\Gamma \vdash b : T; \Gamma, T \vdash c] \implies \Gamma, T \vdash while (b) c$
- | $typeIf: [\Gamma \vdash b : T; \Gamma, T \vdash c1; \Gamma, T \vdash c2] \implies \Gamma, T \vdash if (b) c1 else c2$
- | $typeConvert: \Gamma, High \vdash c \implies \Gamma, Low \vdash c$

2.2 Lemmas concerning expressions

lemma $exprTypeable:$

assumes $fv e \subseteq dom \Gamma$ **obtains** T **where** $\Gamma \vdash e : T$
 $\langle proof \rangle$

```

lemma exprBinopTypeable:
  assumes  $\Gamma \vdash e1 \text{ ``bop'' } e2 : T$ 
  shows  $(\exists T1. \Gamma \vdash e1 : T1) \wedge (\exists T2. \Gamma \vdash e2 : T2)$ 
  {proof}

```

```

lemma exprTypingHigh:
  assumes  $\Gamma \vdash e : T$  and  $x \in fv e$  and  $\Gamma x = Some\ High$ 
  shows  $\Gamma \vdash e : High$ 
  {proof}

```

```

lemma exprTypingLow:
  assumes  $\Gamma \vdash e : Low$  and  $x \in fv e$  shows  $\Gamma x = Some\ Low$ 
  {proof}

```

```

lemma typeableFreevars:
  assumes  $\Gamma \vdash e : T$  shows  $fv e \subseteq dom \Gamma$ 
  {proof}

```

```

lemma exprNotNone:
  assumes  $\Gamma \vdash e : T$  and  $fv e \subseteq dom s$ 
  shows  $\llbracket e \rrbracket s \neq None$ 
  {proof}

```

2.3 Noninterference definitions

2.3.1 Low Equivalence

Low Equivalence is reflexive even if the involved states are undefined. But in non-reflexive situations low variables must be initialized (i.e. $\in dom state$), otherwise the proof will not work. This is not a restriction, but a natural requirement, and could be formalized as part of a standard type system.

Low equivalence is also symmetric and transitive (see lemmas) hence an equivalence relation.

```

definition lowEquiv :: typeEnv  $\Rightarrow$  state  $\Rightarrow$  state  $\Rightarrow$  bool ( $\cdot \vdash \cdot \approx_L \cdot$ )
where  $\Gamma \vdash s1 \approx_L s2 \equiv \forall v \in dom \Gamma. \Gamma v = Some\ Low \longrightarrow (s1 v = s2 v)$ 

```

```

lemma lowEquivReflexive:  $\Gamma \vdash s1 \approx_L s1$ 
{proof}

```

```

lemma lowEquivSymmetric:
 $\Gamma \vdash s1 \approx_L s2 \implies \Gamma \vdash s2 \approx_L s1$ 

```

$\langle proof \rangle$

lemma *lowEquivTransitive*:

$$[\Gamma \vdash s1 \approx_L s2; \Gamma \vdash s2 \approx_L s3] \implies \Gamma \vdash s1 \approx_L s3$$

$\langle proof \rangle$

2.3.2 Non Interference

definition *nonInterference* :: *typeEnv* \Rightarrow *com* \Rightarrow *bool*

where *nonInterference* $\Gamma c \equiv$

$$(\forall s1 s2 s1' s2'. (\Gamma \vdash s1 \approx_L s2 \wedge \langle c, s1 \rangle \rightarrow^* \langle Skip, s1 \rangle \wedge \langle c, s2 \rangle \rightarrow^* \langle Skip, s2 \rangle) \\ \longrightarrow \Gamma \vdash s1' \approx_L s2')$$

lemma *nonInterferenceI*:

$$[\bigwedge s1 s2 s1' s2'. [\Gamma \vdash s1 \approx_L s2; \langle c, s1 \rangle \rightarrow^* \langle Skip, s1 \rangle; \langle c, s2 \rangle \rightarrow^* \langle Skip, s2 \rangle]]$$

$$\implies \Gamma \vdash s1' \approx_L s2]$$

$\langle proof \rangle$

lemma *interpretLow*:

assumes $\Gamma \vdash s1 \approx_L s2$ **and** $\text{all:} \forall V \in fv e. \Gamma V = \text{Some Low}$

shows $\llbracket e \rrbracket s1 = \llbracket e \rrbracket s2$

$\langle proof \rangle$

lemma *interpretLow2*:

assumes $\Gamma \vdash e : \text{Low}$ **and** $\Gamma \vdash s1 \approx_L s2$ **shows** $\llbracket e \rrbracket s1 = \llbracket e \rrbracket s2$

$\langle proof \rangle$

lemma *assignNIhighlemma*:

assumes $\Gamma \vdash s1 \approx_L s2$ **and** $\Gamma V = \text{Some High}$ **and** $s1' = s1(V := \llbracket e \rrbracket s1)$

and $s2' = s2(V := \llbracket e \rrbracket s2)$

shows $\Gamma \vdash s1' \approx_L s2'$

$\langle proof \rangle$

lemma *assignNIlowlemma*:

assumes $\Gamma \vdash s1 \approx_L s2$ **and** $\Gamma V = \text{Some Low}$ **and** $\Gamma \vdash e : \text{Low}$

and $s1' = s1(V := \llbracket e \rrbracket s1)$ **and** $s2' = s2(V := \llbracket e \rrbracket s2)$

shows $\Gamma \vdash s1' \approx_L s2'$

$\langle proof \rangle$

Sequential Compositionality is given the status of a theorem because compositionality is no longer valid in case of concurrency

theorem *SqCompositionality*:

assumes *nonInterference* $\Gamma c1$ **and** *nonInterference* $\Gamma c2$

shows *nonInterference* $\Gamma (c1;;c2)$

$\langle proof \rangle$

lemma *WhileStepInduct*:

assumes *while*: $\langle \text{while } (b) c, s1 \rangle \rightarrow^* \langle \text{Skip}, s2 \rangle$
and *body*: $\bigwedge s1 s2. \langle c, s1 \rangle \rightarrow^* \langle \text{Skip}, s2 \rangle \implies \Gamma \vdash s1 \approx_L s2 \text{ and } \Gamma, \text{High} \vdash c$
shows $\Gamma \vdash s1 \approx_L s2$

$\langle proof \rangle$

In case control conditions from if/while are high, the body of an if/while must not change low variables in order to prevent implicit flow. That is, start and end state of any if/while body must be low equivalent.

theorem *highBodies*:

assumes $\Gamma, \text{High} \vdash c$ **and** $\langle c, s1 \rangle \rightarrow^* \langle \text{Skip}, s2 \rangle$
shows $\Gamma \vdash s1 \approx_L s2$

— all intermediate states must be well formed, otherwise the proof does not work for uninitialized variables. Thus it is propagated through the theorem conclusion

$\langle proof \rangle$

lemma *CondHighCompositionality*:

assumes $\Gamma, \text{High} \vdash \text{if } (b) c1 \text{ else } c2$
shows *nonInterference* $\Gamma (\text{if } (b) c1 \text{ else } c2)$

$\langle proof \rangle$

lemma *CondLowCompositionality*:

assumes *nonInterference* $\Gamma c1$ **and** *nonInterference* $\Gamma c2$ **and** $\Gamma \vdash b : \text{Low}$
shows *nonInterference* $\Gamma (\text{if } (b) c1 \text{ else } c2)$

$\langle proof \rangle$

lemma *WhileHighCompositionality*:

assumes $\Gamma, \text{High} \vdash \text{while } (b) c'$
shows *nonInterference* $\Gamma (\text{while } (b) c')$

$\langle proof \rangle$

lemma *WhileLowStepInduct*:

assumes *while1*: $\langle \text{while } (b) c', s1 \rangle \rightarrow^* \langle \text{Skip}, s1' \rangle$
and *while2*: $\langle \text{while } (b) c', s2 \rangle \rightarrow^* \langle \text{Skip}, s2' \rangle$
and $\Gamma \vdash b : \text{Low}$
and $\text{body}: \bigwedge s1 s1' s2 s2'. \llbracket \langle c', s1 \rangle \rightarrow^* \langle \text{Skip}, s1' \rangle; \langle c', s2 \rangle \rightarrow^* \langle \text{Skip}, s2' \rangle; \Gamma \vdash s1 \approx_L s2 \rrbracket \implies \Gamma \vdash s1' \approx_L s2'$
and $le: \Gamma \vdash s1 \approx_L s2$

```

shows    $\Gamma \vdash s1' \approx_L s2'$ 
 $\langle proof \rangle$ 

```

```

lemma WhileLowCompositionality:
assumes nonInterference  $\Gamma c'$  and  $\Gamma \vdash b : Low$  and  $\Gamma, Low \vdash c'$ 
shows nonInterference  $\Gamma (\text{while } (b) c')$ 
 $\langle proof \rangle$ 

```

and now: the main theorem:

```

theorem secTypeImpliesNonInterference:
 $\Gamma, T \vdash c \implies \text{nonInterference } \Gamma c$ 
 $\langle proof \rangle$ 

```

end

```

theory Execute
imports secTypes
begin

```

3 Executing the small step semantics

```

code-pred (modes:  $i \Rightarrow o \Rightarrow \text{bool}$  as exec-red,  $i \Rightarrow i * o \Rightarrow \text{bool}$ ,  $i \Rightarrow o * i \Rightarrow \text{bool}$ ,  $i \Rightarrow i \Rightarrow \text{bool}$ ) red  $\langle proof \rangle$ 
thm red.equation

```

```

definition [code]: one-step  $x = \text{Predicate.the } (\text{exec-red } x)$ 

```

```

lemmas [code-pred-intro] = typeVal[where lev = Low] typeVal[where lev = High]
typeVar
typeBinOp1 typeBinOp2[where lev = Low] typeBinOp2[where lev = High] type-
BinOp3[where lev = Low]

```

```

code-pred (modes:  $i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$  as compute-secExprTyping,
 $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$  as check-secExprTyping) secExprTyping
 $\langle proof \rangle$ 

```

```

lemmas [code-pred-intro] = typeSkip[where T=Low] typeSkip[where T=High]
typeAssH[where T = Low] typeAssH[where T=High]
typeAssL typeSeq typeWhile typeIf typeConvert

```

```

code-pred (modes:  $i \Rightarrow o \Rightarrow i \Rightarrow \text{bool}$  as compute-secComTyping,
 $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$  as check-secComTyping) secComTyping
 $\langle proof \rangle$ 

```

```

thm secComTyping.equation

```

3.1 An example taken from Volpano, Smith, Irvine

```
definition com = if (Var "x" «Eq» Val (Intg 1)) ("y" := Val (Intg 1)) else
("y" := Val (Intg 0))
definition Env = map-of [("x", High), ("y", High)]

values {T. Env ⊢ (Var "x" «Eq» Val (Intg 1)): T}
value Env, High ⊢ com
value Env, Low ⊢ com
values 1 {T. Env, T ⊢ com}

definition Env' = map-of [("x", Low), ("y", High)]

value Env', Low ⊢ com
value Env', High ⊢ com
values 1 {T. Env, T ⊢ com}

end
```

References

- [1] Andrei Sabelfeld and Andrew C. Myers. Language-based information-flow security. *IEEE Journal on Selected Areas in Communications*, 21(1):5–19, 2003.
- [2] Dennis Volpano, Cynthia Irvine, and Geoffrey Smith. A sound type system for secure flow analysis. *Journal of Computer Security*, 4(2-3):167–187, 1996.