

Jinja with Threads

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March 17, 2025

Abstract. We extend the Jinja source code semantics by Klein and Nipkow with Java-style arrays and threads. Concurrency is captured in a generic framework semantics for adding concurrency through interleaving to a sequential semantics, which features dynamic thread creation, inter-thread communication via shared memory, lock synchronisation and joins. Also, threads can suspend themselves and be notified by others. We instantiate the framework with the adapted versions of both Jinja source and byte code and show type safety for the multithreaded case. Equally, the compiler from source to byte code is extended, for which we prove weak bisimilarity between the source code small step semantics and the defensive Jinja virtual machine. On top of this, we formalise the JMM and show the DRF guarantee and consistency.

For description of the different parts, see [1, 2, 4, 3].

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Chapter 1

The generic multithreaded semantics

1.1 State of the multithreaded semantics

```
theory FWState
imports
  ../Basic/Auxiliary
begin

datatype lock-action =
  Lock
  | Unlock
  | UnlockFail
  | ReleaseAcquire

datatype ('t,'x,'m) new-thread-action =
  NewThread 't 'x 'm
  | ThreadExists 't bool

datatype 't conditional-action =
  Join 't
  | Yield

datatype ('t, 'w) wait-set-action =
  Suspend 'w
  | Notify 'w
  | NotifyAll 'w
  | WakeUp 't
  | Notified
  | WokenUp

datatype 't interrupt-action
= IsInterrupted 't bool
  | Interrupt 't
  | ClearInterrupt 't

type-synonym 'l lock-actions = 'l =>f lock-action list
```

translations

(type) 'l lock-actions <= (type) 'l =>f lock-action list

type-synonym

('l,'t,'x,'m,'w,'o) thread-action =
 'l lock-actions × ('t,'x,'m) new-thread-action list ×
 't conditional-action list × ('t, 'w) wait-set-action list ×
 't interrupt-action list × 'o list

<ML>

typ ('l,'t,'x,'m,'w,'o) thread-action

definition locks-a :: ('l,'t,'x,'m,'w,'o) thread-action => 'l lock-actions (<f-⟩_l [0] 1000) **where**
 locks-a ≡ fst

definition thr-a :: ('l,'t,'x,'m,'w,'o) thread-action => ('t,'x,'m) new-thread-action list (<f-⟩_t [0] 1000)
where
 thr-a ≡ fst o snd

definition cond-a :: ('l,'t,'x,'m,'w,'o) thread-action => 't conditional-action list (<f-⟩_c [0] 1000)
where
 cond-a = fst o snd o snd

definition wset-a :: ('l,'t,'x,'m,'w,'o) thread-action => ('t, 'w) wait-set-action list (<f-⟩_w [0] 1000)
where
 wset-a = fst o snd o snd o snd

definition interrupt-a :: ('l,'t,'x,'m,'w,'o) thread-action => 't interrupt-action list (<f-⟩_i [0] 1000)
where
 interrupt-a = fst o snd o snd o snd o snd

definition obs-a :: ('l,'t,'x,'m,'w,'o) thread-action => 'o list (<f-⟩_o [0] 1000) **where**
 obs-a ≡ snd o snd o snd o snd o snd

lemma locks-a-conv [simp]: locks-a (ls, ntsjswss) = ls
 <proof>

lemma thr-a-conv [simp]: thr-a (ls, nts, jswss) = nts
 <proof>

lemma cond-a-conv [simp]: cond-a (ls, nts, js, wws) = js
 <proof>

lemma wset-a-conv [simp]: wset-a (ls, nts, js, wss, isobs) = wss
 <proof>

lemma interrupt-a-conv [simp]: interrupt-a (ls, nts, js, ws, is, obs) = is
 <proof>

lemma obs-a-conv [simp]: obs-a (ls, nts, js, wss, is, obs) = obs
 <proof>

fun ta-update-locks :: ('l,'t,'x,'m,'w,'o) thread-action => lock-action => 'l => ('l,'t,'x,'m,'w,'o) thread-action
where

ta-update-locks (*ls*, *nts*, *js*, *wss*, *obs*) *lta* *l* = (*ls*(*l* \$:= *ls* \$ *l* @ [*lta*]), *nts*, *js*, *wss*, *obs*)

fun *ta-update-NewThread* :: ('*l*, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action* ⇒ ('*t*, '*x*, '*m*) *new-thread-action* ⇒ ('*l*, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action* **where**

ta-update-NewThread (*ls*, *nts*, *js*, *wss*, *is*, *obs*) *nt* = (*ls*, *nts* @ [*nt*], *js*, *wss*, *is*, *obs*)

fun *ta-update-Conditional* :: ('*l*, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action* ⇒ '*t* *conditional-action* ⇒ ('*l*, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action*

where

ta-update-Conditional (*ls*, *nts*, *js*, *wss*, *is*, *obs*) *j* = (*ls*, *nts*, *js* @ [*j*], *wss*, *is*, *obs*)

fun *ta-update-wait-set* :: ('*l*, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action* ⇒ ('*t*, '*w*) *wait-set-action* ⇒ ('*l*, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action* **where**

ta-update-wait-set (*ls*, *nts*, *js*, *wss*, *is*, *obs*) *ws* = (*ls*, *nts*, *js*, *wss* @ [*ws*], *is*, *obs*)

fun *ta-update-interrupt* :: ('*l*, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action* ⇒ '*t* *interrupt-action* ⇒ ('*l*, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action*

where

ta-update-interrupt (*ls*, *nts*, *js*, *wss*, *is*, *obs*) *i* = (*ls*, *nts*, *js*, *wss*, *is* @ [*i*], *obs*)

fun *ta-update-obs* :: ('*l*, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action* ⇒ '*o* ⇒ ('*l*, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action*

where

ta-update-obs (*ls*, *nts*, *js*, *wss*, *is*, *obs*) *ob* = (*ls*, *nts*, *js*, *wss*, *is*, *obs* @ [*ob*])

abbreviation *empty-ta* :: ('*l*, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action* **where**

empty-ta ≡ (*K*\$ [], [], [], [], [], [])

notation (*input*) *empty-ta* (⟨*ε*⟩)

Pretty syntax for specifying thread actions: Write { *Lock*→*l*, *Unlock*→*l*, *Suspend* *w*, *Interrupt* *t*} instead of ((*K*\$ [])(*l* \$:= [*Lock*, *Unlock*]), [], [*Suspend* *w*], [*Interrupt* *t*], []).

thread-action' is a type that contains of all basic thread actions. Automatically coerce basic thread actions into that type and then dispatch to the right update function by pattern matching. For coercion, adhoc overloading replaces the generic injection *inject-thread-action* by the specific ones, i.e. constructors. To avoid ambiguities with observable actions, the observable actions must be of sort *obs-action*, which the basic thread action types are not.

class *obs-action*

datatype ('*l*, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action'*
 = *LockAction* *lock-action* × '*l*
 | *NewThreadAction* ('*t*, '*x*, '*m*) *new-thread-action*
 | *ConditionalAction* '*t* *conditional-action*
 | *WaitSetAction* ('*t*, '*w*) *wait-set-action*
 | *InterruptAction* '*t* *interrupt-action*
 | *ObsAction* '*o*

⟨*ML*⟩

fun *thread-action'-to-thread-action* ::

('*l*, '*t*, '*x*, '*m*, '*w*, '*o* :: *obs-action*) *thread-action'* ⇒ ('*l*, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action* ⇒ ('*l*, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action*

where

thread-action'-to-thread-action (*LockAction* (*la*, *l*)) *ta* = *ta-update-locks* *ta* *la* *l*

| *thread-action'-to-thread-action* (*NewThreadAction* *nt*) *ta* = *ta-update-NewThread* *ta* *nt*
| *thread-action'-to-thread-action* (*ConditionalAction* *ca*) *ta* = *ta-update-Conditional* *ta* *ca*
| *thread-action'-to-thread-action* (*WaitSetAction* *wa*) *ta* = *ta-update-wait-set* *ta* *wa*
| *thread-action'-to-thread-action* (*InterruptAction* *ia*) *ta* = *ta-update-interrupt* *ta* *ia*
| *thread-action'-to-thread-action* (*ObsAction* *ob*) *ta* = *ta-update-obs* *ta* *ob*

consts *inject-thread-action* :: 'a ⇒ ('l,'t,'x,'m,'w,'o) *thread-action'*

nonterminal *ta-let* and *ta-lets*

syntax

-*ta-snoc* :: *ta-lets* ⇒ *ta-let* ⇒ *ta-lets* (⟨-,/ -⟩)
-*ta-block* :: *ta-lets* ⇒ 'a (⟨{-}⟩ [0] 1000)
-*ta-empty* :: *ta-lets* (⟨⟩)
-*ta-single* :: *ta-let* ⇒ *ta-lets* (⟨-⟩)
-*ta-inject* :: *logic* ⇒ *ta-let* (⟨(-)⟩)
-*ta-lock* :: *logic* ⇒ *logic* ⇒ *ta-let* (⟨-→-⟩)

translations

-*ta-block* -*ta-empty* == *CONST* *empty-ta*
-*ta-block* (-*ta-single* *bta*) == -*ta-block* (-*ta-snoc* -*ta-empty* *bta*)
-*ta-inject* *bta* == *CONST* *inject-thread-action* *bta*
-*ta-lock* *la* *l* == *CONST* *inject-thread-action* (*CONST* *Pair* *la* *l*)
-*ta-block* (-*ta-snoc* *btas* *bta*) == *CONST* *thread-action'-to-thread-action* *bta* (-*ta-block* *btas*)

ad hoc-overloading

inject-thread-action ⇔ *NewThreadAction* *ConditionalAction* *WaitSetAction* *InterruptAction* *ObsAction* *LockAction*

lemma *ta-upd-proj-simps* [*simp*]:

shows *ta-obs-proj-simps*:

$\{\{ta\text{-update-obs } ta \text{ obs}\}_l = \{\{ta\}_l \{\{ta\text{-update-obs } ta \text{ obs}\}_t = \{\{ta\}_t \{\{ta\text{-update-obs } ta \text{ obs}\}_w = \{\{ta\}_w$
 $\{\{ta\text{-update-obs } ta \text{ obs}\}_c = \{\{ta\}_c \{\{ta\text{-update-obs } ta \text{ obs}\}_i = \{\{ta\}_i \{\{ta\text{-update-obs } ta \text{ obs}\}_o = \{\{ta\}_o @$
[*obs*]

and *ta-lock-proj-simps*:

$\{\{ta\text{-update-locks } ta \ x \ l\}_l = (let \ ls = \{\{ta\}_l \ in \ ls(l \ \$:= \ ls \ \$ \ l \ @ \ [x]))$
 $\{\{ta\text{-update-locks } ta \ x \ l\}_t = \{\{ta\}_t \{\{ta\text{-update-locks } ta \ x \ l\}_w = \{\{ta\}_w \{\{ta\text{-update-locks } ta \ x \ l\}_c = \{\{ta\}_c$

$\{\{ta\text{-update-locks } ta \ x \ l\}_i = \{\{ta\}_i \{\{ta\text{-update-locks } ta \ x \ l\}_o = \{\{ta\}_o$

and *ta-thread-proj-simps*:

$\{\{ta\text{-update-NewThread } ta \ t\}_l = \{\{ta\}_l \{\{ta\text{-update-NewThread } ta \ t\}_t = \{\{ta\}_t @ [t] \{\{ta\text{-update-NewThread}$
 $ta \ t\}_w = \{\{ta\}_w$

$\{\{ta\text{-update-NewThread } ta \ t\}_c = \{\{ta\}_c \{\{ta\text{-update-NewThread } ta \ t\}_i = \{\{ta\}_i \{\{ta\text{-update-NewThread}$
 $ta \ t\}_o = \{\{ta\}_o$

and *ta-wset-proj-simps*:

$\{\{ta\text{-update-wait-set } ta \ w\}_l = \{\{ta\}_l \{\{ta\text{-update-wait-set } ta \ w\}_t = \{\{ta\}_t \{\{ta\text{-update-wait-set } ta \ w\}_w =$
 $\{\{ta\}_w @ [w]$

$\{\{ta\text{-update-wait-set } ta \ w\}_c = \{\{ta\}_c \{\{ta\text{-update-wait-set } ta \ w\}_i = \{\{ta\}_i \{\{ta\text{-update-wait-set } ta \ w\}_o =$
 $\{\{ta\}_o$

and *ta-cond-proj-simps*:

$\{\{ta\text{-update-Conditional } ta \ c\}_l = \{\{ta\}_l \{\{ta\text{-update-Conditional } ta \ c\}_t = \{\{ta\}_t \{\{ta\text{-update-Conditional}$
 $ta \ c\}_w = \{\{ta\}_w$

$\{\{ta\text{-update-Conditional } ta \ c\}_c = \{\{ta\}_c @ [c] \{\{ta\text{-update-Conditional } ta \ c\}_i = \{\{ta\}_i \{\{ta\text{-update-Conditional}$
 $ta \ c\}_o = \{\{ta\}_o$

and *ta-interrupt-proj-simps*:

$$\{\{ta\text{-update-interrupt } ta \ i\}_l = \{\{ta\}_l \ \{\{ta\text{-update-interrupt } ta \ i\}_t = \{\{ta\}_t \ \{\{ta\text{-update-interrupt } ta \ i\}_c = \{\{ta\}_c$$

$$\{\{ta\text{-update-interrupt } ta \ i\}_w = \{\{ta\}_w \ \{\{ta\text{-update-interrupt } ta \ i\}_i = \{\{ta\}_i \ @ \ [i] \ \{\{ta\text{-update-interrupt } ta \ i\}_o = \{\{ta\}_o$$

$\langle proof \rangle$

lemma *thread-action'-to-thread-action-proj-simps* [simp]:

shows *thread-action'-to-thread-action-proj-locks-simps*:

$$\{\{thread\text{-action}'\text{-to-thread-action } (LockAction \ (la, \ l)) \ ta\}_l = \{\{ta\text{-update-locks } ta \ la \ l\}_l$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (NewThreadAction \ nt) \ ta\}_l = \{\{ta\text{-update-NewThread } ta \ nt\}_l$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (ConditionalAction \ ca) \ ta\}_l = \{\{ta\text{-update-Conditional } ta \ ca\}_l$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (WaitSetAction \ wa) \ ta\}_l = \{\{ta\text{-update-wait-set } ta \ wa\}_l$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (InterruptAction \ ia) \ ta\}_l = \{\{ta\text{-update-interrupt } ta \ ia\}_l$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (ObsAction \ ob) \ ta\}_l = \{\{ta\text{-update-obs } ta \ ob\}_l$$

and *thread-action'-to-thread-action-proj-nt-simps*:

$$\{\{thread\text{-action}'\text{-to-thread-action } (LockAction \ (la, \ l)) \ ta\}_t = \{\{ta\text{-update-locks } ta \ la \ l\}_t$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (NewThreadAction \ nt) \ ta\}_t = \{\{ta\text{-update-NewThread } ta \ nt\}_t$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (ConditionalAction \ ca) \ ta\}_t = \{\{ta\text{-update-Conditional } ta \ ca\}_t$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (WaitSetAction \ wa) \ ta\}_t = \{\{ta\text{-update-wait-set } ta \ wa\}_t$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (InterruptAction \ ia) \ ta\}_t = \{\{ta\text{-update-interrupt } ta \ ia\}_t$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (ObsAction \ ob) \ ta\}_t = \{\{ta\text{-update-obs } ta \ ob\}_t$$

and *thread-action'-to-thread-action-proj-cond-simps*:

$$\{\{thread\text{-action}'\text{-to-thread-action } (LockAction \ (la, \ l)) \ ta\}_c = \{\{ta\text{-update-locks } ta \ la \ l\}_c$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (NewThreadAction \ nt) \ ta\}_c = \{\{ta\text{-update-NewThread } ta \ nt\}_c$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (ConditionalAction \ ca) \ ta\}_c = \{\{ta\text{-update-Conditional } ta \ ca\}_c$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (WaitSetAction \ wa) \ ta\}_c = \{\{ta\text{-update-wait-set } ta \ wa\}_c$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (InterruptAction \ ia) \ ta\}_c = \{\{ta\text{-update-interrupt } ta \ ia\}_c$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (ObsAction \ ob) \ ta\}_c = \{\{ta\text{-update-obs } ta \ ob\}_c$$

and *thread-action'-to-thread-action-proj-wset-simps*:

$$\{\{thread\text{-action}'\text{-to-thread-action } (LockAction \ (la, \ l)) \ ta\}_w = \{\{ta\text{-update-locks } ta \ la \ l\}_w$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (NewThreadAction \ nt) \ ta\}_w = \{\{ta\text{-update-NewThread } ta \ nt\}_w$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (ConditionalAction \ ca) \ ta\}_w = \{\{ta\text{-update-Conditional } ta \ ca\}_w$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (WaitSetAction \ wa) \ ta\}_w = \{\{ta\text{-update-wait-set } ta \ wa\}_w$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (InterruptAction \ ia) \ ta\}_w = \{\{ta\text{-update-interrupt } ta \ ia\}_w$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (ObsAction \ ob) \ ta\}_w = \{\{ta\text{-update-obs } ta \ ob\}_w$$

and *thread-action'-to-thread-action-proj-interrupt-simps*:

$$\{\{thread\text{-action}'\text{-to-thread-action } (LockAction \ (la, \ l)) \ ta\}_i = \{\{ta\text{-update-locks } ta \ la \ l\}_i$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (NewThreadAction \ nt) \ ta\}_i = \{\{ta\text{-update-NewThread } ta \ nt\}_i$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (ConditionalAction \ ca) \ ta\}_i = \{\{ta\text{-update-Conditional } ta \ ca\}_i$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (WaitSetAction \ wa) \ ta\}_i = \{\{ta\text{-update-wait-set } ta \ wa\}_i$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (InterruptAction \ ia) \ ta\}_i = \{\{ta\text{-update-interrupt } ta \ ia\}_i$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (ObsAction \ ob) \ ta\}_i = \{\{ta\text{-update-obs } ta \ ob\}_i$$

and *thread-action'-to-thread-action-proj-obs-simps*:

$$\{\{thread\text{-action}'\text{-to-thread-action } (LockAction \ (la, \ l)) \ ta\}_o = \{\{ta\text{-update-locks } ta \ la \ l\}_o$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (NewThreadAction \ nt) \ ta\}_o = \{\{ta\text{-update-NewThread } ta \ nt\}_o$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (ConditionalAction \ ca) \ ta\}_o = \{\{ta\text{-update-Conditional } ta \ ca\}_o$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (WaitSetAction \ wa) \ ta\}_o = \{\{ta\text{-update-wait-set } ta \ wa\}_o$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (InterruptAction \ ia) \ ta\}_o = \{\{ta\text{-update-interrupt } ta \ ia\}_o$$

$$\{\{thread\text{-action}'\text{-to-thread-action } (ObsAction \ ob) \ ta\}_o = \{\{ta\text{-update-obs } ta \ ob\}_o$$

$\langle proof \rangle$

lemmas *ta-upd-simps* =

ta-update-locks.simps ta-update-NewThread.simps ta-update-Conditional.simps

ta-update-wait-set.simps ta-update-interrupt.simps ta-update-obs.simps
thread-action'-to-thread-action.simps

declare *ta-upd-simps* [*simp del*]

hide-const (**open**)

LockAction NewThreadAction ConditionalAction WaitSetAction InterruptAction ObsAction
thread-action'-to-thread-action

hide-type (**open**) *thread-action'*

datatype *wake-up-status* =

WSNotified
| *WSWokenUp*

datatype *'w wait-set-status* =

InWS 'w
| *PostWS wake-up-status*

type-synonym *'t lock* = (*'t × nat*) *option*

type-synonym (*'l, 't*) *locks* = *'l ⇒f 't lock*

type-synonym *'l released-locks* = *'l ⇒f nat*

type-synonym (*'l, 't, 'x*) *thread-info* = *'t → ('x × 'l released-locks)*

type-synonym (*'w, 't*) *wait-sets* = *'t → 'w wait-set-status*

type-synonym *'t interrupts* = *'t set*

type-synonym (*'l, 't, 'x, 'm, 'w*) *state* = (*'l, 't*) *locks* × ((*'l, 't, 'x*) *thread-info* × *'m*) × (*'w, 't*) *wait-sets*
× *'t interrupts*

translations

(*type*) (*'l, 't*) *locks* <= (*type*) *'l ⇒f ('t × nat) option*

(*type*) (*'l, 't, 'x*) *thread-info* <= (*type*) *'t → ('x × ('l ⇒f nat))*

⟨*ML*⟩

typ (*'l, 't, 'x, 'm, 'w*) *state*

abbreviation *no-wait-locks* :: *'l ⇒f nat*

where *no-wait-locks* ≡ (*K \$ 0*)

lemma *neg-no-wait-locks-conv*:

$\bigwedge ln. ln \neq no-wait-locks \longleftrightarrow (\exists l. ln \$ l > 0)$

⟨*proof*⟩

lemma *neg-no-wait-locksE*:

fixes *ln* **assumes** *ln ≠ no-wait-locks* **obtains** *l* **where** *ln \$ l > 0*

⟨*proof*⟩

Use type variables for components instead of (*'l, 't, 'x, 'm, 'w*) *state* in types for state projections to allow to reuse them for refined state implementations for code generation.

definition *locks* :: (*'locks* × (*'thread-info* × *'m*) × *'wsets* × *'interrupts*) ⇒ *'locks* **where**

locks lstsmws ≡ *fst lstsmws*

definition *thr* :: (*'locks* × (*'thread-info* × *'m*) × *'wsets* × *'interrupts*) ⇒ *'thread-info* **where**

thr lstsmws ≡ *fst (fst (snd lstsmws))*

definition $shr :: ('locks \times ('thread-info \times 'm) \times 'wsets \times 'interrupts) \Rightarrow 'm$ **where**
 $shr\ ltsmws \equiv snd\ (fst\ (snd\ ltsmws))$

definition $wset :: ('locks \times ('thread-info \times 'm) \times 'wsets \times 'interrupts) \Rightarrow 'wsets$ **where**
 $wset\ ltsmws \equiv fst\ (snd\ (snd\ ltsmws))$

definition $interrupts :: ('locks \times ('thread-info \times 'm) \times 'wsets \times 'interrupts) \Rightarrow 'interrupts$ **where**
 $interrupts\ ltsmws \equiv snd\ (snd\ (snd\ ltsmws))$

lemma $locks-conv\ [simp]:\ locks\ (ls,\ tsmws) = ls$
 $\langle proof \rangle$

lemma $thr-conv\ [simp]:\ thr\ (ls,\ (ts,\ m),\ ws) = ts$
 $\langle proof \rangle$

lemma $shr-conv\ [simp]:\ shr\ (ls,\ (ts,\ m),\ ws,\ is) = m$
 $\langle proof \rangle$

lemma $wset-conv\ [simp]:\ wset\ (ls,\ (ts,\ m),\ ws,\ is) = ws$
 $\langle proof \rangle$

lemma $interrupts-conv\ [simp]:\ interrupts\ (ls,\ (ts,\ m),\ ws,\ is) = is$
 $\langle proof \rangle$

primrec $convert-new-thread-action :: ('x \Rightarrow 'x') \Rightarrow ('t, 'x, 'm) new-thread-action \Rightarrow ('t, 'x', 'm) new-thread-action$
where
 $convert-new-thread-action\ f\ (NewThread\ t\ x\ m) = NewThread\ t\ (f\ x)\ m$
 $| convert-new-thread-action\ f\ (ThreadExists\ t\ b) = ThreadExists\ t\ b$

lemma $convert-new-thread-action-inv\ [simp]:$
 $NewThread\ t\ x\ h = convert-new-thread-action\ f\ nta \longleftrightarrow (\exists x'. nta = NewThread\ t\ x' h \wedge x = f\ x')$
 $ThreadExists\ t\ b = convert-new-thread-action\ f\ nta \longleftrightarrow nta = ThreadExists\ t\ b$
 $convert-new-thread-action\ f\ nta = NewThread\ t\ x\ h \longleftrightarrow (\exists x'. nta = NewThread\ t\ x' h \wedge x = f\ x')$
 $convert-new-thread-action\ f\ nta = ThreadExists\ t\ b \longleftrightarrow nta = ThreadExists\ t\ b$
 $\langle proof \rangle$

lemma $convert-new-thread-action-eqI:$
 $\llbracket \bigwedge t\ x\ m.\ nta = NewThread\ t\ x\ m \implies nta' = NewThread\ t\ (f\ x)\ m;$
 $\bigwedge t\ b.\ nta = ThreadExists\ t\ b \implies nta' = ThreadExists\ t\ b \rrbracket$
 $\implies convert-new-thread-action\ f\ nta = nta'$
 $\langle proof \rangle$

lemma $convert-new-thread-action-compose\ [simp]:$
 $convert-new-thread-action\ f\ (convert-new-thread-action\ g\ ta) = convert-new-thread-action\ (f\ o\ g)\ ta$
 $\langle proof \rangle$

lemma $inj-convert-new-thread-action\ [simp]:$
 $inj\ (convert-new-thread-action\ f) = inj\ f$
 $\langle proof \rangle$

lemma $convert-new-thread-action-id:$
 $convert-new-thread-action\ id = (id :: ('t, 'x, 'm) new-thread-action \Rightarrow ('t, 'x, 'm) new-thread-action)$
(is ?thesis1)

$convert_new_thread_action (\lambda x. x) = (id :: ('t, 'x, 'm) new_thread_action \Rightarrow ('t, 'x, 'm) new_thread_action)$
(is ?thesis2)
 <proof>

definition $convert_extTA :: ('x \Rightarrow 'x') \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) thread_action \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) thread_action$
where $convert_extTA f ta = (\{ta\}_l, map (convert_new_thread_action f) \{ta\}_t, snd (snd ta))$

lemma $convert_extTA_simps [simp]$:

$convert_extTA f \varepsilon = \varepsilon$
 $\{convert_extTA f ta\}_l = \{ta\}_l$
 $\{convert_extTA f ta\}_t = map (convert_new_thread_action f) \{ta\}_t$
 $\{convert_extTA f ta\}_c = \{ta\}_c$
 $\{convert_extTA f ta\}_w = \{ta\}_w$
 $\{convert_extTA f ta\}_i = \{ta\}_i$
 $convert_extTA f (las, tas, was, cas, is, obs) = (las, map (convert_new_thread_action f) tas, was, cas, is, obs)$
 <proof>

lemma $convert_extTA_eq_conv$:

$convert_extTA f ta = ta' \iff$
 $\{ta\}_l = \{ta'\}_l \wedge \{ta\}_c = \{ta'\}_c \wedge \{ta\}_w = \{ta'\}_w \wedge \{ta\}_o = \{ta'\}_o \wedge \{ta\}_i = \{ta'\}_i \wedge length$
 $\{ta\}_t = length \{ta'\}_t \wedge$
 $(\forall n < length \{ta\}_t. convert_new_thread_action f (\{ta\}_t ! n) = \{ta'\}_t ! n)$
 <proof>

lemma $convert_extTA_compose [simp]$:

$convert_extTA f (convert_extTA g ta) = convert_extTA (f o g) ta$
 <proof>

lemma $obs_a_convert_extTA [simp]$: $obs_a (convert_extTA f ta) = obs_a ta$

<proof>

Actions for thread start/finish

datatype $'o action =$

$NormalAction 'o$
 $| InitialThreadAction$
 $| ThreadFinishAction$

instance $action :: (type) obs_action$

<proof>

definition $convert_obs_initial :: ('l, 't, 'x, 'm, 'w, 'o) thread_action \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) action thread_action$

where

$convert_obs_initial ta = (\{ta\}_l, \{ta\}_t, \{ta\}_c, \{ta\}_w, \{ta\}_i, map NormalAction \{ta\}_o)$

lemma $inj_NormalAction [simp]$: $inj NormalAction$

<proof>

lemma $convert_obs_initial_inject [simp]$:

$convert_obs_initial ta = convert_obs_initial ta' \iff ta = ta'$
 <proof>

lemma $convert_obs_initial_empty_TA [simp]$:

$convert_obs_initial \varepsilon = \varepsilon$

$\langle proof \rangle$

lemma *convert-obs-initial-eq-empty-TA* [simp]:

$convert-obs-initial\ ta = \varepsilon \longleftrightarrow ta = \varepsilon$

$\varepsilon = convert-obs-initial\ ta \longleftrightarrow ta = \varepsilon$

$\langle proof \rangle$

lemma *convert-obs-initial-simps* [simp]:

$\{\{convert-obs-initial\ ta\}\}_o = map\ NormalAction\ \{\{ta\}\}_o$

$\{\{convert-obs-initial\ ta\}\}_l = \{\{ta\}\}_l$

$\{\{convert-obs-initial\ ta\}\}_t = \{\{ta\}\}_t$

$\{\{convert-obs-initial\ ta\}\}_c = \{\{ta\}\}_c$

$\{\{convert-obs-initial\ ta\}\}_w = \{\{ta\}\}_w$

$\{\{convert-obs-initial\ ta\}\}_i = \{\{ta\}\}_i$

$\langle proof \rangle$

type-synonym

$(l, 't, 'x, 'm, 'w, 'o)\ semantics =$

$'t \Rightarrow 'x \times 'm \Rightarrow (l, 't, 'x, 'm, 'w, 'o)\ thread-action \Rightarrow 'x \times 'm \Rightarrow bool$

$\langle ML \rangle$

typ $(l, 't, 'x, 'm, 'w, 'o)\ semantics$

end

1.2 All about a managing a single lock

theory *FWLock*

imports

FWState

begin

fun *has-locks* :: $'t\ lock \Rightarrow 't \Rightarrow nat$ **where**

has-locks *None* $t = 0$

| *has-locks* $\lfloor (t', n) \rfloor\ t = (if\ t = t'\ then\ Suc\ n\ else\ 0)$

lemma *has-locks-iff*:

has-locks $l\ t = n \longleftrightarrow$

$(l = None \wedge n = 0) \vee$

$(\exists n'. l = \lfloor (t, n') \rfloor \wedge Suc\ n' = n) \vee (\exists t'\ n'. l = \lfloor (t', n') \rfloor \wedge t' \neq t \wedge n = 0)$

$\langle proof \rangle$

lemma *has-locksE*:

$\llbracket has-locks\ l\ t = n;$

$\llbracket l = None; n = 0 \rrbracket \Longrightarrow P;$

$\bigwedge n'. \llbracket l = \lfloor (t, n') \rfloor; Suc\ n' = n \rrbracket \Longrightarrow P;$

$\bigwedge t'\ n'. \llbracket l = \lfloor (t', n') \rfloor; t' \neq t; n = 0 \rrbracket \Longrightarrow P \rrbracket$

$\Longrightarrow P$

$\langle proof \rangle$

inductive *may-lock* :: $'t\ lock \Rightarrow 't \Rightarrow bool$ **where**

may-lock *None* *t*
| *may-lock* [(*t*, *n*)] *t*

lemma *may-lock-iff* [*code*]:

may-lock *l* *t* = (*case* *l* of *None* \Rightarrow *True* | [(*t'*, *n*)] \Rightarrow *t* = *t'*)
⟨*proof*⟩

lemma *may-lockI*:

l = *None* \vee (\exists *n*. *l* = [(*t*, *n*)]]) \Longrightarrow *may-lock* *l* *t*
⟨*proof*⟩

lemma *may-lockE* [*consumes* 1, *case-names* *None* *Locked*]:

\llbracket *may-lock* *l* *t*; *l* = *None* \Longrightarrow *P*; \bigwedge *n*. *l* = [(*t*, *n*)] \Longrightarrow *P* $\rrbracket \Longrightarrow$ *P*
⟨*proof*⟩

lemma *may-lock-may-lock-t-eq*:

\llbracket *may-lock* *l* *t*; *may-lock* *l* *t'* $\rrbracket \Longrightarrow$ (*l* = *None*) \vee (*t* = *t'*)
⟨*proof*⟩

abbreviation *has-lock* :: '*t* lock \Rightarrow '*t* \Rightarrow *bool*

where *has-lock* *l* *t* \equiv 0 < *has-locks* *l* *t*

lemma *has-locks-Suc-has-lock*:

has-locks *l* *t* = *Suc* *n* \Longrightarrow *has-lock* *l* *t*
⟨*proof*⟩

lemmas *has-lock-has-locks-Suc* = *gr0-implies-Suc*[**where** *n* = *has-locks* *l* *t*] **for** *l* *t*

lemma *has-lock-has-locks-conv*:

has-lock *l* *t* \longleftrightarrow (\exists *n*. *has-locks* *l* *t* = (*Suc* *n*))
⟨*proof*⟩

lemma *has-lock-may-lock*:

has-lock *l* *t* \Longrightarrow *may-lock* *l* *t*
⟨*proof*⟩

lemma *has-lock-may-lock-t-eq*:

\llbracket *has-lock* *l* *t*; *may-lock* *l* *t'* $\rrbracket \Longrightarrow$ *t* = *t'*
⟨*proof*⟩

lemma *has-locks-has-locks-t-eq*:

\llbracket *has-locks* *l* *t* = *Suc* *n*; *has-locks* *l* *t'* = *Suc* *n* $\rrbracket \Longrightarrow$ *t* = *t'*
⟨*proof*⟩

lemma *has-lock-has-lock-t-eq*:

\llbracket *has-lock* *l* *t*; *has-lock* *l* *t'* $\rrbracket \Longrightarrow$ *t* = *t'*
⟨*proof*⟩

lemma *not-may-lock-conv*:

\neg *may-lock* *l* *t* \longleftrightarrow (\exists *t'*. *t'* \neq *t* \wedge *has-lock* *l* *t'*)
⟨*proof*⟩

fun *lock-lock* :: 't lock \Rightarrow 't \Rightarrow 't lock **where**

lock-lock None t = [(t, 0)]
| *lock-lock* [(t', n)] t = [(t', Suc n)]

fun *unlock-lock* :: 't lock \Rightarrow 't lock **where**

unlock-lock None = None
| *unlock-lock* [(t, n)] = (case n of 0 \Rightarrow None | Suc n' \Rightarrow [(t, n')])

fun *release-all* :: 't lock \Rightarrow 't \Rightarrow 't lock **where**

release-all None t = None
| *release-all* [(t', n)] t = (if t = t' then None else [(t', n)])

fun *acquire-locks* :: 't lock \Rightarrow 't \Rightarrow nat \Rightarrow 't lock **where**

acquire-locks L t 0 = L
| *acquire-locks* L t (Suc m) = *acquire-locks* (*lock-lock* L t) t m

lemma *acquire-locks-conv*:

acquire-locks L t n = (case L of None \Rightarrow (case n of 0 \Rightarrow None | Suc m \Rightarrow [(t, m)]) | [(t', m)] \Rightarrow [(t', n + m)])
<proof>

lemma *lock-lock-ls-Some*:

$\exists t' n. \text{lock-lock } l t = [(t', n)]$
<proof>

lemma *unlock-lock-SomeD*:

unlock-lock l = [(t', n)] \Longrightarrow l = [(t', Suc n)]
<proof>

lemma *has-locks-Suc-lock-lock-has-locks-Suc-Suc*:

has-locks l t = Suc n \Longrightarrow *has-locks* (*lock-lock* l t) t = Suc (Suc n)
<proof>

lemma *has-locks-lock-lock-conv* [*simp*]:

may-lock l t \Longrightarrow *has-locks* (*lock-lock* l t) t = Suc (*has-locks* l t)
<proof>

lemma *has-locks-release-all-conv* [*simp*]:

has-locks (*release-all* l t) t = 0
<proof>

lemma *may-lock-lock-lock-conv* [*simp*]: *may-lock* (*lock-lock* l t) t = *may-lock* l t

<proof>

lemma *has-locks-acquire-locks-conv* [*simp*]:

may-lock l t \Longrightarrow *has-locks* (*acquire-locks* l t n) t = *has-locks* l t + n
<proof>

lemma *may-lock-unlock-lock-conv* [*simp*]:

has-lock l t \Longrightarrow *may-lock* (*unlock-lock* l) t = *may-lock* l t
<proof>

lemma *may-lock-release-all-conv* [simp]:
 $may\text{-}lock\ (release\text{-}all\ l\ t)\ t = may\text{-}lock\ l\ t$
 ⟨proof⟩

lemma *may-lock-t-may-lock-unlock-lock-t*:
 $may\text{-}lock\ l\ t \implies may\text{-}lock\ (unlock\text{-}lock\ l)\ t$
 ⟨proof⟩

lemma *may-lock-has-locks-lock-lock-0*:
 $\llbracket may\text{-}lock\ l\ t';\ t \neq t' \rrbracket \implies has\text{-}locks\ (lock\text{-}lock\ l\ t')\ t = 0$
 ⟨proof⟩

lemma *has-locks-unlock-lock-conv* [simp]:
 $has\text{-}lock\ l\ t \implies has\text{-}locks\ (unlock\text{-}lock\ l)\ t = has\text{-}locks\ l\ t - 1$
 ⟨proof⟩

lemma *has-lock-lock-lock-unlock-lock-id* [simp]:
 $has\text{-}lock\ l\ t \implies lock\text{-}lock\ (unlock\text{-}lock\ l)\ t = l$
 ⟨proof⟩

lemma *may-lock-unlock-lock-lock-lock-id* [simp]:
 $may\text{-}lock\ l\ t \implies unlock\text{-}lock\ (lock\text{-}lock\ l\ t) = l$
 ⟨proof⟩

lemma *may-lock-has-locks-0*:
 $\llbracket may\text{-}lock\ l\ t;\ t \neq t' \rrbracket \implies has\text{-}locks\ l\ t' = 0$
 ⟨proof⟩

fun *upd-lock* :: 't lock \Rightarrow 't \Rightarrow lock-action \Rightarrow 't lock
where
 $upd\text{-}lock\ l\ t\ Lock = lock\text{-}lock\ l\ t$
 $|\ upd\text{-}lock\ l\ t\ Unlock = unlock\text{-}lock\ l$
 $|\ upd\text{-}lock\ l\ t\ UnlockFail = l$
 $|\ upd\text{-}lock\ l\ t\ ReleaseAcquire = release\text{-}all\ l\ t$

fun *upd-locks* :: 't lock \Rightarrow 't \Rightarrow lock-action list \Rightarrow 't lock
where
 $upd\text{-}locks\ l\ t\ [] = l$
 $|\ upd\text{-}locks\ l\ t\ (L\ \#\ Ls) = upd\text{-}locks\ (upd\text{-}lock\ l\ t\ L)\ t\ Ls$

lemma *upd-locks-append* [simp]:
 $upd\text{-}locks\ l\ t\ (Ls\ @\ Ls') = upd\text{-}locks\ (upd\text{-}locks\ l\ t\ Ls)\ t\ Ls'$
 ⟨proof⟩

lemma *upd-lock-Some-thread-idD*:
assumes $ul: upd\text{-}lock\ l\ t\ L = \llbracket (t', n) \rrbracket$
and $tt': t \neq t'$
shows $\exists n. l = \llbracket (t', n) \rrbracket$
 ⟨proof⟩

lemma *has-lock-upd-lock-implies-has-lock*:
 $\llbracket \text{has-lock } (\text{upd-lock } l \ t \ L) \ t'; \ t \neq \ t' \rrbracket \implies \text{has-lock } l \ t'$
 $\langle \text{proof} \rangle$

lemma *has-lock-upd-locks-implies-has-lock*:
 $\llbracket \text{has-lock } (\text{upd-locks } l \ t \ Ls) \ t'; \ t \neq \ t' \rrbracket \implies \text{has-lock } l \ t'$
 $\langle \text{proof} \rangle$

fun *lock-action-ok* :: 't lock \Rightarrow 't \Rightarrow lock-action \Rightarrow bool **where**
lock-action-ok l t Lock = may-lock l t
| *lock-action-ok* l t Unlock = has-lock l t
| *lock-action-ok* l t UnlockFail = (\neg has-lock l t)
| *lock-action-ok* l t ReleaseAcquire = True

fun *lock-actions-ok* :: 't lock \Rightarrow 't \Rightarrow lock-action list \Rightarrow bool **where**
lock-actions-ok l t [] = True
| *lock-actions-ok* l t (L # Ls) = (*lock-action-ok* l t L \wedge *lock-actions-ok* (upd-lock l t L) t Ls)

lemma *lock-actions-ok-append* [simp]:
lock-actions-ok l t (Ls @ Ls') \longleftrightarrow *lock-actions-ok* l t Ls \wedge *lock-actions-ok* (upd-locks l t Ls) t Ls'
 $\langle \text{proof} \rangle$

lemma *not-lock-action-okE* [consumes 1, case-names Lock Unlock UnlockFail]:
 $\llbracket \neg \text{lock-action-ok } l \ t \ L; \rrbracket$
 $\llbracket L = \text{Lock}; \neg \text{may-lock } l \ t \rrbracket \implies Q;$
 $\llbracket L = \text{Unlock}; \neg \text{has-lock } l \ t \rrbracket \implies Q;$
 $\llbracket L = \text{UnlockFail}; \text{has-lock } l \ t \rrbracket \implies Q$
 $\implies Q$
 $\langle \text{proof} \rangle$

lemma *may-lock-upd-lock-conv* [simp]:
lock-action-ok l t L \implies may-lock (upd-lock l t L) t = may-lock l t
 $\langle \text{proof} \rangle$

lemma *may-lock-upd-locks-conv* [simp]:
lock-actions-ok l t Ls \implies may-lock (upd-locks l t Ls) t = may-lock l t
 $\langle \text{proof} \rangle$

lemma *lock-actions-ok-Lock-may-lock*:
 $\llbracket \text{lock-actions-ok } l \ t \ Ls; \text{Lock} \in \text{set } Ls \rrbracket \implies \text{may-lock } l \ t$
 $\langle \text{proof} \rangle$

lemma *has-locks-lock-lock-conv'* [simp]:
 $\llbracket \text{may-lock } l \ t'; \ t \neq \ t' \rrbracket \implies \text{has-locks } (\text{lock-lock } l \ t') \ t = \text{has-locks } l \ t$
 $\langle \text{proof} \rangle$

lemma *has-locks-unlock-lock-conv'* [simp]:
 $\llbracket \text{has-lock } l \ t'; \ t \neq \ t' \rrbracket \implies \text{has-locks } (\text{unlock-lock } l) \ t = \text{has-locks } l \ t$
 $\langle \text{proof} \rangle$

lemma *has-locks-release-all-conv'* [simp]:
 $t \neq \ t' \implies \text{has-locks } (\text{release-all } l \ t') \ t = \text{has-locks } l \ t$

<proof>

lemma *has-locks-acquire-locks-conv'* [simp]:

$\llbracket \text{may-lock } l \ t; t \neq t' \rrbracket \implies \text{has-locks } (\text{acquire-locks } l \ t \ n) \ t' = \text{has-locks } l \ t'$
<proof>

lemma *lock-action-ok-has-locks-upd-lock-eq-has-locks* [simp]:

$\llbracket \text{lock-action-ok } l \ t' \ L; t \neq t' \rrbracket \implies \text{has-locks } (\text{upd-lock } l \ t' \ L) \ t = \text{has-locks } l \ t$
<proof>

lemma *lock-actions-ok-has-locks-upd-locks-eq-has-locks* [simp]:

$\llbracket \text{lock-actions-ok } l \ t' \ Ls; t \neq t' \rrbracket \implies \text{has-locks } (\text{upd-locks } l \ t' \ Ls) \ t = \text{has-locks } l \ t$
<proof>

lemma *has-lock-acquire-locks-implies-has-lock*:

$\llbracket \text{has-lock } (\text{acquire-locks } l \ t \ n) \ t'; t \neq t' \rrbracket \implies \text{has-lock } l \ t'$
<proof>

lemma *has-lock-has-lock-acquire-locks*:

$\text{has-lock } l \ T \implies \text{has-lock } (\text{acquire-locks } l \ t \ n) \ T$
<proof>

fun *lock-actions-ok'* :: 't lock \Rightarrow 't \Rightarrow lock-action list \Rightarrow bool **where**

$\text{lock-actions-ok}' \ l \ t \ [] = \text{True}$
 $\mid \text{lock-actions-ok}' \ l \ t \ (L\#Ls) = ((L = \text{Lock} \wedge \neg \text{may-lock } l \ t) \vee$
 $\text{lock-action-ok } l \ t \ L \wedge \text{lock-actions-ok}' \ (\text{upd-lock } l \ t \ L) \ t \ Ls)$

lemma *lock-actions-ok'-iff*:

$\text{lock-actions-ok}' \ l \ t \ las \longleftrightarrow$
 $\text{lock-actions-ok } l \ t \ las \vee (\exists xs \ ys. las = xs @ \text{Lock} \# ys \wedge \text{lock-actions-ok } l \ t \ xs \wedge \neg \text{may-lock}$
 $(\text{upd-locks } l \ t \ xs) \ t)$
<proof>

lemma *lock-actions-ok'E*[consumes 1, case-names ok Lock]:

$\llbracket \text{lock-actions-ok}' \ l \ t \ las;$
 $\text{lock-actions-ok } l \ t \ las \implies P;$
 $\bigwedge xs \ ys. \llbracket las = xs @ \text{Lock} \# ys; \text{lock-actions-ok } l \ t \ xs; \neg \text{may-lock } (\text{upd-locks } l \ t \ xs) \ t \rrbracket \implies P \rrbracket$
 $\implies P$
<proof>

end

1.3 Semantics of the thread actions for locking

theory *FWLocking*

imports

FWLock

begin

definition *redT-updLs* :: ('l,'t) locks \Rightarrow 't \Rightarrow 'l lock-actions \Rightarrow ('l,'t) locks **where**

$\text{redT-updLs } ls \ t \ las \equiv (\lambda(l, la). \text{upd-locks } l \ t \ la) \circ\$ ((\$ls, las\$))$

lemma *redT-updLs-iff* [simp]: $\text{redT-updLs } ls \ t \ las \ \$ \ l = \text{upd-locks } (ls \ \$ \ l) \ t \ (las \ \$ \ l)$
 ⟨proof⟩

lemma *upd-locks-empty-conv* [simp]: $(\lambda(l, las). \text{upd-locks } l \ t \ las) \circ \$ \ (\$ls, K\$ \ []) = ls$
 ⟨proof⟩

lemma *redT-updLs-Some-thread-idD*:

$\llbracket \text{has-lock } (\text{redT-updLs } ls \ t \ las \ \$ \ l) \ t'; \ t \neq t' \rrbracket \implies \text{has-lock } (ls \ \$ \ l) \ t'$
 ⟨proof⟩

definition *acquire-all* :: $(l, t) \text{ locks} \Rightarrow t \Rightarrow (l \Rightarrow f \text{ nat}) \Rightarrow (l, t) \text{ locks}$
where $\bigwedge ln. \text{acquire-all } ls \ t \ ln \equiv (\lambda(l, la). \text{acquire-locks } l \ t \ la) \circ \$ \ ((\$ls, ln\$))$

lemma *acquire-all-iff* [simp]:

$\bigwedge ln. \text{acquire-all } ls \ t \ ln \ \$ \ l = \text{acquire-locks } (ls \ \$ \ l) \ t \ (ln \ \$ \ l)$
 ⟨proof⟩

definition *lock-ok-las* :: $(l, t) \text{ locks} \Rightarrow t \Rightarrow l \text{ lock-actions} \Rightarrow \text{bool}$ **where**
 $\text{lock-ok-las } ls \ t \ las \equiv \forall l. \text{lock-actions-ok } (ls \ \$ \ l) \ t \ (las \ \$ \ l)$

lemma *lock-ok-lasI* [intro]:

$(\bigwedge l. \text{lock-actions-ok } (ls \ \$ \ l) \ t \ (las \ \$ \ l)) \implies \text{lock-ok-las } ls \ t \ las$
 ⟨proof⟩

lemma *lock-ok-lasE*:

$\llbracket \text{lock-ok-las } ls \ t \ las; (\bigwedge l. \text{lock-actions-ok } (ls \ \$ \ l) \ t \ (las \ \$ \ l)) \implies Q \rrbracket \implies Q$
 ⟨proof⟩

lemma *lock-ok-lasD*:

$\text{lock-ok-las } ls \ t \ las \implies \text{lock-actions-ok } (ls \ \$ \ l) \ t \ (las \ \$ \ l)$
 ⟨proof⟩

lemma *lock-ok-las-code* [code]:

$\text{lock-ok-las } ls \ t \ las = \text{finfun-All } ((\lambda(l, la). \text{lock-actions-ok } l \ t \ la) \circ \$ \ (\$ls, las\$))$
 ⟨proof⟩

lemma *lock-ok-las-may-lock*:

$\llbracket \text{lock-ok-las } ls \ t \ las; \text{Lock} \in \text{set } (las \ \$ \ l) \rrbracket \implies \text{may-lock } (ls \ \$ \ l) \ t$
 ⟨proof⟩

lemma *redT-updLs-may-lock* [simp]:

$\text{lock-ok-las } ls \ t \ las \implies \text{may-lock } (\text{redT-updLs } ls \ t \ las \ \$ \ l) \ t = \text{may-lock } (ls \ \$ \ l) \ t$
 ⟨proof⟩

lemma *redT-updLs-has-locks* [simp]:

$\llbracket \text{lock-ok-las } ls \ t' \ las; \ t \neq t' \rrbracket \implies \text{has-locks } (\text{redT-updLs } ls \ t' \ las \ \$ \ l) \ t = \text{has-locks } (ls \ \$ \ l) \ t$
 ⟨proof⟩

definition *may-acquire-all* :: $(l, t) \text{ locks} \Rightarrow t \Rightarrow (l \Rightarrow f \text{ nat}) \Rightarrow \text{bool}$

where $\bigwedge ln. \text{may-acquire-all } ls \ t \ ln \equiv \forall l. ln \ \$ \ l > 0 \longrightarrow \text{may-lock } (ls \ \$ \ l) \ t$

lemma *may-acquire-allI* [intro]:

$\bigwedge ln. (\bigwedge l. ln \$ l > 0 \implies \text{may-lock } (ls \$ l) t) \implies \text{may-acquire-all } ls t ln$
 $\langle \text{proof} \rangle$

lemma *may-acquire-allE*:

$\bigwedge ln. [\text{may-acquire-all } ls t ln; \forall l. ln \$ l > 0 \implies \text{may-lock } (ls \$ l) t \implies P] \implies P$
 $\langle \text{proof} \rangle$

lemma *may-acquire-allD* [*dest*]:

$\bigwedge ln. [\text{may-acquire-all } ls t ln; ln \$ l > 0] \implies \text{may-lock } (ls \$ l) t$
 $\langle \text{proof} \rangle$

lemma *may-acquire-all-has-locks-acquire-locks* [*simp*]:

fixes *ln*
shows $[\text{may-acquire-all } ls t ln; t \neq t'] \implies \text{has-locks } (\text{acquire-locks } (ls \$ l) t (ln \$ l)) t' = \text{has-locks } (ls \$ l) t'$
 $\langle \text{proof} \rangle$

lemma *may-acquire-all-code* [*code*]:

$\bigwedge ln. \text{may-acquire-all } ls t ln \iff \text{finfun-All } ((\lambda(\text{lock}, n). n > 0 \implies \text{may-lock } \text{lock } t) \circ \$ (\$ls, ln\$))$
 $\langle \text{proof} \rangle$

definition *collect-locks* :: *'l lock-actions* \Rightarrow *'l set* **where**

collect-locks las = $\{l. \text{Lock} \in \text{set } (las \$ l)\}$

lemma *collect-locksI*:

$\text{Lock} \in \text{set } (las \$ l) \implies l \in \text{collect-locks } las$
 $\langle \text{proof} \rangle$

lemma *collect-locksE*:

$[\text{collect-locks } las; \text{Lock} \in \text{set } (las \$ l) \implies P] \implies P$
 $\langle \text{proof} \rangle$

lemma *collect-locksD*:

$l \in \text{collect-locks } las \implies \text{Lock} \in \text{set } (las \$ l)$
 $\langle \text{proof} \rangle$

fun *must-acquire-lock* :: *lock-action list* \Rightarrow *bool* **where**

must-acquire-lock [] = *False*
| *must-acquire-lock* (*Lock* # *las*) = *True*
| *must-acquire-lock* (*Unlock* # *las*) = *False*
| *must-acquire-lock* (- # *las*) = *must-acquire-lock las*

lemma *must-acquire-lock-append*:

$\text{must-acquire-lock } (xs @ ys) \iff (\text{if } \text{Lock} \in \text{set } xs \vee \text{Unlock} \in \text{set } xs \text{ then } \text{must-acquire-lock } xs \text{ else } \text{must-acquire-lock } ys)$
 $\langle \text{proof} \rangle$

lemma *must-acquire-lock-contains-lock*:

$\text{must-acquire-lock } las \implies \text{Lock} \in \text{set } las$
 $\langle \text{proof} \rangle$

lemma *must-acquire-lock-conv*:

$\text{must-acquire-lock } las = (\text{case } (\text{filter } (\lambda L. L = \text{Lock} \vee L = \text{Unlock}) las) \text{ of } [] \Rightarrow \text{False} \mid L \# Ls \Rightarrow L$

= Lock)
 ⟨proof⟩

definition *collect-locks'* :: 'l lock-actions ⇒ 'l set **where**
collect-locks' las ≡ {l. *must-acquire-lock* (las \$ l)}

lemma *collect-locks'I*:
must-acquire-lock (las \$ l) ⇒ l ∈ *collect-locks' las*
 ⟨proof⟩

lemma *collect-locks'E*:
 [l ∈ *collect-locks' las*; *must-acquire-lock* (las \$ l) ⇒ P] ⇒ P
 ⟨proof⟩

lemma *collect-locks'-subset-collect-locks*:
collect-locks' las ⊆ *collect-locks las*
 ⟨proof⟩

definition *lock-ok-las'* :: ('l, 't) locks ⇒ 't ⇒ 'l lock-actions ⇒ bool **where**
lock-ok-las' ls t las ≡ ∀l. *lock-actions-ok'* (ls \$ l) t (las \$ l)

lemma *lock-ok-las'I*: (∧l. *lock-actions-ok'* (ls \$ l) t (las \$ l)) ⇒ *lock-ok-las' ls t las*
 ⟨proof⟩

lemma *lock-ok-las'D*: *lock-ok-las' ls t las* ⇒ *lock-actions-ok'* (ls \$ l) t (las \$ l)
 ⟨proof⟩

lemma *not-lock-ok-las'-conv*:
 ¬ *lock-ok-las' ls t las* ↔ (∃l. ¬ *lock-actions-ok'* (ls \$ l) t (las \$ l))
 ⟨proof⟩

lemma *lock-ok-las'-code*:
lock-ok-las' ls t las = *finfun-All* ((λ(l, la). *lock-actions-ok'* l t la) ∘\$ (\$ls, las\$))
 ⟨proof⟩

lemma *lock-ok-las'-collect-locks'-may-lock*:
assumes *lot'*: *lock-ok-las' ls t las*
and *mayl*: ∀l ∈ *collect-locks' las*. *may-lock* (ls \$ l) t
and *l*: l ∈ *collect-locks las*
shows *may-lock* (ls \$ l) t
 ⟨proof⟩

lemma *lock-actions-ok'-must-acquire-lock-lock-actions-ok*:
 [*lock-actions-ok'* l t Ls; *must-acquire-lock* Ls → *may-lock* l t] ⇒ *lock-actions-ok* l t Ls
 ⟨proof⟩

lemma *lock-ok-las'-collect-locks-lock-ok-las*:
assumes *lol'*: *lock-ok-las' ls t las*
and *clml*: ∧l. l ∈ *collect-locks las* ⇒ *may-lock* (ls \$ l) t
shows *lock-ok-las ls t las*
 ⟨proof⟩

lemma *lock-ok-las'-into-lock-on-las*:

$\llbracket \text{lock-ok-las}' \text{ } ls \text{ } t \text{ } las; \bigwedge l. l \in \text{collect-locks}' \text{ } las \implies \text{may-lock} (ls \ \$ \ l) \ t \rrbracket \implies \text{lock-ok-las } ls \ t \text{ } las$
 $\langle \text{proof} \rangle$

end

1.4 Semantics of the thread actions for thread creation

theory *FWThread*

imports

FWState

begin

Abstractions for thread ids

context

notes $\llbracket \text{inductive-internals} \rrbracket$

begin

inductive *free-thread-id* :: $(l, t, x) \text{ thread-info} \Rightarrow t \Rightarrow \text{bool}$

for $ts :: (l, t, x) \text{ thread-info}$ **and** $t :: t$

where $ts \ t = \text{None} \implies \text{free-thread-id } ts \ t$

declare *free-thread-id.cases* $[\text{elim}]$

end

lemma *free-thread-id-iff*: $\text{free-thread-id } ts \ t = (ts \ t = \text{None})$

$\langle \text{proof} \rangle$

Update functions for the multithreaded state

fun *redT-updT* :: $(l, t, x) \text{ thread-info} \Rightarrow (t, x, m) \text{ new-thread-action} \Rightarrow (l, t, x) \text{ thread-info}$

where

$\text{redT-updT } ts \ (\text{NewThread } t' \ x \ m) = ts(t' \mapsto (x, \text{no-wait-locks}))$

$|\ \text{redT-updT } ts \ - = ts$

fun *redT-updTs* :: $(l, t, x) \text{ thread-info} \Rightarrow (t, x, m) \text{ new-thread-action list} \Rightarrow (l, t, x) \text{ thread-info}$

where

$\text{redT-updTs } ts \ [] = ts$

$|\ \text{redT-updTs } ts \ (ta \ \# \ tas) = \text{redT-updTs } (\text{redT-updT } ts \ ta) \ tas$

lemma *redT-updTs-append* $[\text{simp}]$:

$\text{redT-updTs } ts \ (tas \ @ \ tas') = \text{redT-updTs } (\text{redT-updTs } ts \ tas) \ tas'$

$\langle \text{proof} \rangle$

lemma *redT-updT-None*:

$\text{redT-updT } ts \ ta \ t = \text{None} \implies ts \ t = \text{None}$

$\langle \text{proof} \rangle$

lemma *redT-updTs-None*: $\text{redT-updTs } ts \ tas \ t = \text{None} \implies ts \ t = \text{None}$

$\langle \text{proof} \rangle$

lemma *redT-updT-Some1*:

$ts \ t = \lfloor xw \rfloor \implies \exists xw. \text{redT-updT } ts \ ta \ t = \lfloor xw \rfloor$

$\langle \text{proof} \rangle$

lemma *redT-updTs-Some1*:

$ts\ t = [xw] \implies \exists xw. \text{redT-updTs}\ ts\ tas\ t = [xw]$
 ⟨proof⟩

lemma *redT-updT-finite-dom-inv*:

$\text{finite}\ (\text{dom}\ (\text{redT-updT}\ ts\ ta)) = \text{finite}\ (\text{dom}\ ts)$
 ⟨proof⟩

lemma *redT-updTs-finite-dom-inv*:

$\text{finite}\ (\text{dom}\ (\text{redT-updTs}\ ts\ tas)) = \text{finite}\ (\text{dom}\ ts)$
 ⟨proof⟩

Preconditions for thread creation actions

These primed versions are for checking preconditions only. They allow the thread actions to have a type for thread-local information that is different than the thread info state itself.

fun *redT-updT'* :: $(l, 't, 'x)\ \text{thread-info} \Rightarrow ('t, 'x', 'm)\ \text{new-thread-action} \Rightarrow (l, 't, 'x)\ \text{thread-info}$

where

$\text{redT-updT}'\ ts\ (\text{NewThread}\ t'\ x\ m) = ts(t' \mapsto (\text{undefined}, \text{no-wait-locks}))$
 | $\text{redT-updT}'\ ts\ - = ts$

fun *redT-updTs'* :: $(l, 't, 'x)\ \text{thread-info} \Rightarrow ('t, 'x', 'm)\ \text{new-thread-action list} \Rightarrow (l, 't, 'x)\ \text{thread-info}$

where

$\text{redT-updTs}'\ ts\ [] = ts$
 | $\text{redT-updTs}'\ ts\ (ta\ \#\ tas) = \text{redT-updTs}'\ (\text{redT-updT}'\ ts\ ta)\ tas$

lemma *redT-updT'-None*:

$\text{redT-updT}'\ ts\ ta\ t = \text{None} \implies ts\ t = \text{None}$
 ⟨proof⟩

primrec *thread-ok* :: $(l, 't, 'x)\ \text{thread-info} \Rightarrow ('t, 'x', 'm)\ \text{new-thread-action} \Rightarrow \text{bool}$

where

$\text{thread-ok}\ ts\ (\text{NewThread}\ t\ x\ m) = \text{free-thread-id}\ ts\ t$
 | $\text{thread-ok}\ ts\ (\text{ThreadExists}\ t\ b) = (b \neq \text{free-thread-id}\ ts\ t)$

fun *thread-oks* :: $(l, 't, 'x)\ \text{thread-info} \Rightarrow ('t, 'x', 'm)\ \text{new-thread-action list} \Rightarrow \text{bool}$

where

$\text{thread-oks}\ ts\ [] = \text{True}$
 | $\text{thread-oks}\ ts\ (ta\ \#\ tas) = (\text{thread-ok}\ ts\ ta \wedge \text{thread-oks}\ (\text{redT-updT}'\ ts\ ta)\ tas)$

lemma *thread-ok-ts-change*:

$(\bigwedge t. ts\ t = \text{None} \longleftrightarrow ts'\ t = \text{None}) \implies \text{thread-ok}\ ts\ ta \longleftrightarrow \text{thread-ok}\ ts'\ ta$
 ⟨proof⟩

lemma *thread-oks-ts-change*:

$(\bigwedge t. ts\ t = \text{None} \longleftrightarrow ts'\ t = \text{None}) \implies \text{thread-oks}\ ts\ tas \longleftrightarrow \text{thread-oks}\ ts'\ tas$
 ⟨proof⟩

lemma *redT-updT'-eq-None-conv*:

$(\bigwedge t. ts\ t = \text{None} \longleftrightarrow ts'\ t = \text{None}) \implies \text{redT-updT}'\ ts\ ta\ t = \text{None} \longleftrightarrow \text{redT-updT}\ ts'\ ta\ t = \text{None}$
 ⟨proof⟩

lemma *redT-updTs'-eq-None-conv*:

$(\bigwedge t. ts\ t = \text{None} \longleftrightarrow ts'\ t = \text{None}) \implies \text{redT-updTs}'\ ts\ tas\ t = \text{None} \longleftrightarrow \text{redT-updTs}\ ts'\ tas\ t = \text{None}$
 ⟨proof⟩

lemma *thread-oks-redT-updT-conv* [simp]:
 $\text{thread-oks} (\text{redT-updT}'\ ts\ ta)\ tas = \text{thread-oks} (\text{redT-updT}\ ts\ ta)\ tas$
 ⟨proof⟩

lemma *thread-oks-append* [simp]:
 $\text{thread-oks}\ ts\ (tas\ @\ tas') = (\text{thread-oks}\ ts\ tas \wedge \text{thread-oks} (\text{redT-updT}'\ ts\ tas)\ tas')$
 ⟨proof⟩

lemma *thread-oks-redT-updT'-conv* [simp]:
 $\text{thread-oks} (\text{redT-updT}'\ ts\ ta)\ tas = \text{thread-oks} (\text{redT-updT}\ ts\ ta)\ tas$
 ⟨proof⟩

lemma *redT-updT-Some*:
 $\llbracket ts\ t = [xw]; \text{thread-ok}\ ts\ ta \rrbracket \implies \text{redT-updT}\ ts\ ta\ t = [xw]$
 ⟨proof⟩

lemma *redT-updT'-Some*:
 $\llbracket ts\ t = [xw]; \text{thread-oks}\ ts\ tas \rrbracket \implies \text{redT-updT}'\ ts\ tas\ t = [xw]$
 ⟨proof⟩

lemma *redT-updT'-Some*:
 $\llbracket ts\ t = [xw]; \text{thread-ok}\ ts\ ta \rrbracket \implies \text{redT-updT}'\ ts\ ta\ t = [xw]$
 ⟨proof⟩

lemma *redT-updT'-Some*:
 $\llbracket ts\ t = [xw]; \text{thread-oks}\ ts\ tas \rrbracket \implies \text{redT-updT}'\ ts\ tas\ t = [xw]$
 ⟨proof⟩

lemma *thread-ok-new-thread*:
 $\text{thread-ok}\ ts\ (\text{NewThread}\ t\ m'\ x) \implies ts\ t = \text{None}$
 ⟨proof⟩

lemma *thread-oks-new-thread*:
 $\llbracket \text{thread-oks}\ ts\ tas; \text{NewThread}\ t\ x\ m \in \text{set}\ tas \rrbracket \implies ts\ t = \text{None}$
 ⟨proof⟩

lemma *redT-updT-new-thread-ts*:
 $\text{thread-ok}\ ts\ (\text{NewThread}\ t\ x\ m) \implies \text{redT-updT}\ ts\ (\text{NewThread}\ t\ x\ m)\ t = [(x, \text{no-wait-locks})]$
 ⟨proof⟩

lemma *redT-updT'-new-thread-ts*:
 $\llbracket \text{thread-oks}\ ts\ tas; \text{NewThread}\ t\ x\ m \in \text{set}\ tas \rrbracket \implies \text{redT-updT}'\ ts\ tas\ t = [(x, \text{no-wait-locks})]$
 ⟨proof⟩

lemma *redT-updT-new-thread*:
 $\llbracket \text{redT-updT}\ ts\ ta\ t = [(x, w)]; \text{thread-ok}\ ts\ ta; ts\ t = \text{None} \rrbracket \implies \exists m. ta = \text{NewThread}\ t\ x\ m \wedge w$

= *no-wait-locks*
 ⟨*proof*⟩

lemma *redT-updTs-new-thread*:

[[*redT-updTs ts tas t = [(x, w)]; thread-oks ts tas; ts t = None*]]
 ⇒ ∃ *m* . *NewThread t x m ∈ set tas* ∧ *w = no-wait-locks*
 ⟨*proof*⟩

lemma *redT-updT-upd*:

[[*ts t = [xw]; thread-ok ts ta*]] ⇒ (*redT-updT ts ta*)(*t ↦ xw'*) = *redT-updT (ts(t ↦ xw')) ta*
 ⟨*proof*⟩

lemma *redT-updTs-upd*:

[[*ts t = [xw]; thread-oks ts tas*]] ⇒ (*redT-updTs ts tas*)(*t ↦ xw'*) = *redT-updTs (ts(t ↦ xw')) tas*
 ⟨*proof*⟩

lemma *thread-ok-upd*:

ts t = [xln] ⇒ *thread-ok (ts(t ↦ xln')) ta = thread-ok ts ta*
 ⟨*proof*⟩

lemma *thread-oks-upd*:

ts t = [xln] ⇒ *thread-oks (ts(t ↦ xln')) tas = thread-oks ts tas*
 ⟨*proof*⟩

lemma *thread-ok-convert-new-thread-action [simp]*:

thread-ok ts (convert-new-thread-action f ta) = thread-ok ts ta
 ⟨*proof*⟩

lemma *redT-updT'-convert-new-thread-action-eq-None*:

redT-updT' ts (convert-new-thread-action f ta) t = None ⇔ *redT-updT' ts ta t = None*
 ⟨*proof*⟩

lemma *thread-oks-convert-new-thread-action [simp]*:

thread-oks ts (map (convert-new-thread-action f) tas) = thread-oks ts tas
 ⟨*proof*⟩

lemma *map-redT-updT*:

map-option (map-prod f id) (redT-updT ts ta t) =
redT-updT (λt. map-option (map-prod f id) (ts t)) (convert-new-thread-action f ta) t
 ⟨*proof*⟩

lemma *map-redT-updTs*:

map-option (map-prod f id) (redT-updTs ts tas t) =
redT-updTs (λt. map-option (map-prod f id) (ts t)) (map (convert-new-thread-action f) tas) t
 ⟨*proof*⟩

end

1.5 Semantics of the thread actions for wait, notify and interrupt

theory *FWWait*
 imports

FWState

begin

Update functions for the wait sets in the multithreaded state

inductive *redT-updW* :: 't ⇒ ('w, 't) wait-sets ⇒ ('t, 'w) wait-set-action ⇒ ('w, 't) wait-sets ⇒ bool
for t :: 't **and** ws :: ('w, 't) wait-sets

where

ws t' = [InWS w] ⇒ *redT-updW* t ws (Notify w) (ws(t' ↦ PostWS WSNotified))
| (∧ t'. ws t' ≠ [InWS w]) ⇒ *redT-updW* t ws (Notify w) ws
| *redT-updW* t ws (NotifyAll w) (λt. if ws t = [InWS w] then [PostWS WSNotified] else ws t)
| *redT-updW* t ws (Suspend w) (ws(t ↦ InWS w))
| ws t' = [InWS w] ⇒ *redT-updW* t ws (WakeUp t') (ws(t' ↦ PostWS WSInterrupted))
| (∧ w. ws t' ≠ [InWS w]) ⇒ *redT-updW* t ws (WakeUp t') ws
| *redT-updW* t ws Notified (ws(t := None))
| *redT-updW* t ws WokenUp (ws(t := None))

definition *redT-updWs* :: 't ⇒ ('w, 't) wait-sets ⇒ ('t, 'w) wait-set-action list ⇒ ('w, 't) wait-sets ⇒ bool

where *redT-updWs* t = *rtrancl3p* (*redT-updW* t)

inductive-simps *redT-updW-simps* [simp]:

redT-updW t ws (Notify w) ws'
redT-updW t ws (NotifyAll w) ws'
redT-updW t ws (Suspend w) ws'
redT-updW t ws (WakeUp t') ws'
redT-updW t ws WokenUp ws'
redT-updW t ws Notified ws'

lemma *redT-updW-total*: ∃ ws'. *redT-updW* t ws wa ws'

⟨proof⟩

lemma *redT-updWs-total*: ∃ ws'. *redT-updWs* t ws was ws'

⟨proof⟩

lemma *redT-updWs-trans*: [*redT-updWs* t ws was ws'; *redT-updWs* t ws' was' ws''] ⇒ *redT-updWs* t ws (was @ was') ws''

⟨proof⟩

lemma *redT-updW-None-implies-None*:

[*redT-updW* t' ws wa ws'; ws t = None; t ≠ t'] ⇒ ws' t = None

⟨proof⟩

lemma *redT-updWs-None-implies-None*:

assumes *redT-updWs* t' ws was ws'

and t ≠ t' **and** ws t = None

shows ws' t = None

⟨proof⟩

lemma *redT-updW-PostWS-imp-PostWS*:

[*redT-updW* t ws wa ws'; ws t'' = [PostWS w]; t'' ≠ t] ⇒ ws' t'' = [PostWS w]

⟨proof⟩

lemma *redT-updWs-PostWS-imp-PostWS*:

[*redT-updWs* t ws was ws'; t'' ≠ t; ws t'' = [PostWS w]] ⇒ ws' t'' = [PostWS w]

$\langle \text{proof} \rangle$

lemma *redT-updW-Some-otherD:*

$$\begin{aligned} & \llbracket \text{redT-updW } t' \text{ ws wa ws'; ws' } t = \lfloor w \rfloor; t \neq t' \rrbracket \\ & \implies (\text{case } w \text{ of } \text{InWS } w' \Rightarrow \text{ws } t = \lfloor \text{InWS } w' \rfloor \mid - \Rightarrow \text{ws } t = \lfloor w \rfloor \vee (\exists w'. \text{ws } t = \lfloor \text{InWS } w' \rfloor)) \end{aligned}$$

$\langle \text{proof} \rangle$

lemma *redT-updWs-Some-otherD:*

$$\begin{aligned} & \llbracket \text{redT-updWs } t' \text{ ws was ws'; ws' } t = \lfloor w \rfloor; t \neq t' \rrbracket \\ & \implies (\text{case } w \text{ of } \text{InWS } w' \Rightarrow \text{ws } t = \lfloor \text{InWS } w' \rfloor \mid - \Rightarrow \text{ws } t = \lfloor w \rfloor \vee (\exists w'. \text{ws } t = \lfloor \text{InWS } w' \rfloor)) \end{aligned}$$

$\langle \text{proof} \rangle$

lemma *redT-updW-None-SomeD:*

$$\llbracket \text{redT-updW } t \text{ ws wa ws'; ws' } t' = \lfloor w \rfloor; \text{ws } t' = \text{None} \rrbracket \implies t = t' \wedge (\exists w'. w = \text{InWS } w' \wedge \text{wa} = \text{Suspend } w')$$

$\langle \text{proof} \rangle$

lemma *redT-updWs-None-SomeD:*

$$\llbracket \text{redT-updWs } t \text{ ws was ws'; ws' } t' = \lfloor w \rfloor; \text{ws } t' = \text{None} \rrbracket \implies t = t' \wedge (\exists w'. \text{Suspend } w' \in \text{set was})$$

$\langle \text{proof} \rangle$

lemma *redT-updW-neq-Some-SomeD:*

$$\llbracket \text{redT-updW } t' \text{ ws wa ws'; ws' } t = \lfloor \text{InWS } w \rfloor; \text{ws } t \neq \lfloor \text{InWS } w \rfloor \rrbracket \implies t = t' \wedge \text{wa} = \text{Suspend } w$$

$\langle \text{proof} \rangle$

lemma *redT-updWs-neq-Some-SomeD:*

$$\llbracket \text{redT-updWs } t \text{ ws was ws'; ws' } t' = \lfloor \text{InWS } w \rfloor; \text{ws } t' \neq \lfloor \text{InWS } w \rfloor \rrbracket \implies t = t' \wedge \text{Suspend } w \in \text{set was}$$

$\langle \text{proof} \rangle$

lemma *redT-updW-not-Suspend-Some:*

$$\begin{aligned} & \llbracket \text{redT-updW } t \text{ ws wa ws'; ws' } t = \lfloor w' \rfloor; \text{ws } t = \lfloor w \rfloor; \bigwedge w. \text{wa} \neq \text{Suspend } w \rrbracket \\ & \implies w' = w \vee (\exists w'' w'''. w = \text{InWS } w'' \wedge w' = \text{PostWS } w''') \end{aligned}$$

$\langle \text{proof} \rangle$

lemma *redT-updWs-not-Suspend-Some:*

$$\begin{aligned} & \llbracket \text{redT-updWs } t \text{ ws was ws'; ws' } t = \lfloor w' \rfloor; \text{ws } t = \lfloor w \rfloor; \bigwedge w. \text{Suspend } w \notin \text{set was} \rrbracket \\ & \implies w' = w \vee (\exists w'' w'''. w = \text{InWS } w'' \wedge w' = \text{PostWS } w''') \end{aligned}$$

$\langle \text{proof} \rangle$

lemma *redT-updWs-WokenUp-SuspendD:*

$$\llbracket \text{redT-updWs } t \text{ ws was ws'; } \text{Notified} \in \text{set was} \vee \text{WokenUp} \in \text{set was}; \text{ws' } t = \lfloor w \rfloor \rrbracket \implies \exists w. \text{Suspend } w \in \text{set was}$$

$\langle \text{proof} \rangle$

lemma *redT-updW-Woken-Up-same-no-Notified-Interrupted:*

$$\begin{aligned} & \llbracket \text{redT-updW } t \text{ ws wa ws'; ws' } t = \lfloor \text{PostWS } w \rfloor; \text{ws } t = \lfloor \text{PostWS } w \rfloor; \bigwedge w. \text{wa} \neq \text{Suspend } w \rrbracket \\ & \implies \text{wa} \neq \text{Notified} \wedge \text{wa} \neq \text{WokenUp} \end{aligned}$$

$\langle \text{proof} \rangle$

lemma *redT-updWs-Woken-Up-same-no-Notified-Interrupted:*

$$\begin{aligned} & \llbracket \text{redT-updWs } t \text{ ws was ws'; ws' } t = \lfloor \text{PostWS } w \rfloor; \text{ws } t = \lfloor \text{PostWS } w \rfloor; \bigwedge w. \text{Suspend } w \notin \text{set was} \rrbracket \\ & \implies \text{Notified} \notin \text{set was} \wedge \text{WokenUp} \notin \text{set was} \end{aligned}$$

$\langle \text{proof} \rangle$

Preconditions for wait set actions

definition *wset-actions-ok* :: ('w,'t) wait-sets ⇒ 't ⇒ ('t,'w) wait-set-action list ⇒ bool

where

wset-actions-ok ws t was \longleftrightarrow
 (if *Notified* ∈ set was then ws t = [PostWS WSNotified]
 else if *WokenUp* ∈ set was then ws t = [PostWS WSWokenUp]
 else ws t = None)

lemma *wset-actions-ok-Nil* [simp]:

wset-actions-ok ws t [] \longleftrightarrow ws t = None
 ⟨proof⟩

definition *waiting* :: 'w wait-set-status option ⇒ bool

where *waiting* w \longleftrightarrow (∃ w'. w = [InWS w'])

lemma *not-waiting-iff*:

\neg *waiting* w \longleftrightarrow w = None ∨ (∃ w'. w = [PostWS w'])
 ⟨proof⟩

lemma *waiting-code* [code]:

waiting None = False
 $\bigwedge w.$ *waiting* [PostWS w] = False
 $\bigwedge w.$ *waiting* [InWS w] = True
 ⟨proof⟩

end

1.6 Semantics of the thread actions for purely conditional purpose such as Join

theory *FWCondAction*

imports

FWState

begin

locale *final-thread* =

fixes *final* :: 'x ⇒ bool

begin

primrec *cond-action-ok* :: ('l,'t,'x,'m,'w) state ⇒ 't ⇒ 't conditional-action ⇒ bool **where**

$\bigwedge ln.$ *cond-action-ok* s t (Join T) =
 (case thr s T of None ⇒ True | [(x, ln)] ⇒ t ≠ T ∧ *final* x ∧ ln = no-wait-locks ∧ wset s T = None)
 | *cond-action-ok* s t Yield = True

primrec *cond-action-oks* :: ('l,'t,'x,'m,'w) state ⇒ 't ⇒ 't conditional-action list ⇒ bool **where**

cond-action-oks s t [] = True
 | *cond-action-oks* s t (ct#cts) = (*cond-action-ok* s t ct ∧ *cond-action-oks* s t cts)

lemma *cond-action-oks-append* [simp]:

cond-action-oks s t (cts @ cts') \longleftrightarrow *cond-action-oks* s t cts ∧ *cond-action-oks* s t cts'
 ⟨proof⟩

lemma *cond-action-oks-conv-set*:

$cond\text{-}action\text{-}oks\ s\ t\ cts \longleftrightarrow (\forall ct \in set\ cts. cond\text{-}action\text{-}ok\ s\ t\ ct)$
 ⟨proof⟩

lemma *cond-action-ok-Join*:

$\bigwedge ln. \llbracket cond\text{-}action\text{-}ok\ s\ t\ (Join\ T); thr\ s\ T = \llbracket (x, ln) \rrbracket \rrbracket \implies final\ x \wedge ln = no\text{-}wait\text{-}locks \wedge wset\ s\ T = None$
 ⟨proof⟩

lemma *cond-action-oks-Join*:

$\bigwedge ln. \llbracket cond\text{-}action\text{-}oks\ s\ t\ cas; Join\ T \in set\ cas; thr\ s\ T = \llbracket (x, ln) \rrbracket \rrbracket \implies final\ x \wedge ln = no\text{-}wait\text{-}locks \wedge wset\ s\ T = None \wedge t \neq T$
 ⟨proof⟩

lemma *cond-action-oks-upd*:

assumes *tst*: $thr\ s\ t = \llbracket xln \rrbracket$
shows $cond\text{-}action\text{-}oks\ (locks\ s, ((thr\ s)(t \mapsto xln'), shr\ s), wset\ s, interrupts\ s)\ t\ cas = cond\text{-}action\text{-}oks\ s\ t\ cas$
 ⟨proof⟩

lemma *cond-action-ok-shr-change*:

$cond\text{-}action\text{-}ok\ (ls, (ts, m), ws, is)\ t\ ct \implies cond\text{-}action\text{-}ok\ (ls, (ts, m'), ws, is)\ t\ ct$
 ⟨proof⟩

lemma *cond-action-oks-shr-change*:

$cond\text{-}action\text{-}oks\ (ls, (ts, m), ws, is)\ t\ cts \implies cond\text{-}action\text{-}oks\ (ls, (ts, m'), ws, is)\ t\ cts$
 ⟨proof⟩

primrec *cond-action-ok'* :: $(l, 't, 'x, 'm, 'w)\ state \Rightarrow 't \Rightarrow 't\ conditional\text{-}action \Rightarrow bool$

where

$cond\text{-}action\text{-}ok'\ - - (Join\ t) = True$
 $| cond\text{-}action\text{-}ok'\ - - Yield = True$

primrec *cond-action-oks'* :: $(l, 't, 'x, 'm, 'w)\ state \Rightarrow 't \Rightarrow 't\ conditional\text{-}action\ list \Rightarrow bool$ **where**

$cond\text{-}action\text{-}oks'\ s\ t\ [] = True$
 $| cond\text{-}action\text{-}oks'\ s\ t\ (ct\#\ cts) = (cond\text{-}action\text{-}ok'\ s\ t\ ct \wedge cond\text{-}action\text{-}oks'\ s\ t\ cts)$

lemma *cond-action-oks'-append* [simp]:

$cond\text{-}action\text{-}oks'\ s\ t\ (cts\ @\ cts') \longleftrightarrow cond\text{-}action\text{-}oks'\ s\ t\ cts \wedge cond\text{-}action\text{-}oks'\ s\ t\ cts'$
 ⟨proof⟩

lemma *cond-action-oks'-subset-Join*:

$set\ cts \subseteq insert\ Yield\ (range\ Join) \implies cond\text{-}action\text{-}oks'\ s\ t\ cts$
 ⟨proof⟩

end

definition *collect-cond-actions* :: $'t\ conditional\text{-}action\ list \Rightarrow 't\ set$ **where**

$collect\text{-}cond\text{-}actions\ cts = \{t. Join\ t \in set\ cts\}$

declare *collect-cond-actions-def* [simp]

lemma *cond-action-ok-final-change*:

$\llbracket final\text{-}thread.cond\text{-}action\text{-}ok\ final1\ s1\ t\ ca;$

$\bigwedge t. \text{thr } s1 \ t = \text{None} \longleftrightarrow \text{thr } s2 \ t = \text{None};$
 $\bigwedge t \ x1. \llbracket \text{thr } s1 \ t = \lfloor (x1, \text{no-wait-locks}) \rfloor; \text{final1 } x1; \text{wset } s1 \ t = \text{None} \rrbracket$
 $\implies \exists x2. \text{thr } s2 \ t = \lfloor (x2, \text{no-wait-locks}) \rfloor \wedge \text{final2 } x2 \wedge \text{ln2} = \text{no-wait-locks} \wedge \text{wset } s2 \ t = \text{None} \rrbracket$
 $\implies \text{final-thread.cond-action-ok final2 } s2 \ t \ \text{ca}$
 <proof>

lemma *cond-action-oks-final-change*:

assumes *major*: *final-thread.cond-action-oks final1 s1 t cas*

and *minor*: $\bigwedge t. \text{thr } s1 \ t = \text{None} \longleftrightarrow \text{thr } s2 \ t = \text{None}$

$\bigwedge t \ x1. \llbracket \text{thr } s1 \ t = \lfloor (x1, \text{no-wait-locks}) \rfloor; \text{final1 } x1; \text{wset } s1 \ t = \text{None} \rrbracket$

$\implies \exists x2. \text{thr } s2 \ t = \lfloor (x2, \text{no-wait-locks}) \rfloor \wedge \text{final2 } x2 \wedge \text{ln2} = \text{no-wait-locks} \wedge \text{wset } s2 \ t = \text{None}$

shows *final-thread.cond-action-oks final2 s2 t cas*

<proof>

end

1.7 Wellformedness conditions for the multithreaded state

theory *FWWellform*

imports

FWLocking

FWThread

FWWait

FWCondAction

begin

Well-formedness property: Locks are held by real threads

definition

lock-thread-ok :: $(l, t) \text{ locks} \Rightarrow (l, t, x) \text{ thread-info} \Rightarrow \text{bool}$

where [*code del*]:

lock-thread-ok *ls ts* $\equiv \forall l \ t. \text{has-lock } (ls \ \$ \ l) \ t \longrightarrow (\exists xw. ts \ t = \lfloor xw \rfloor)$

lemma *lock-thread-ok-code* [*code*]:

lock-thread-ok *ls ts* = *finfun-All* $((\lambda l. \text{case } l \ \text{of } \text{None} \Rightarrow \text{True} \mid \lfloor (t, n) \rfloor \Rightarrow (ts \ t \neq \text{None})) \circ \$ \ ls)$

<proof>

lemma *lock-thread-okI*:

$(\bigwedge l \ t. \text{has-lock } (ls \ \$ \ l) \ t \implies \exists xw. ts \ t = \lfloor xw \rfloor) \implies \text{lock-thread-ok } ls \ ts$

<proof>

lemma *lock-thread-okD*:

$\llbracket \text{lock-thread-ok } ls \ ts; \text{has-lock } (ls \ \$ \ l) \ t \rrbracket \implies \exists xw. ts \ t = \lfloor xw \rfloor$

<proof>

lemma *lock-thread-okD'*:

$\llbracket \text{lock-thread-ok } ls \ ts; \text{has-locks } (ls \ \$ \ l) \ t = \text{Suc } n \rrbracket \implies \exists xw. ts \ t = \lfloor xw \rfloor$

<proof>

lemma *lock-thread-okE*:

$\llbracket \text{lock-thread-ok } ls \ ts; \forall l \ t. \text{has-lock } (ls \ \$ \ l) \ t \longrightarrow (\exists xw. ts \ t = \lfloor xw \rfloor) \implies P \rrbracket \implies P$

<proof>

lemma *lock-thread-ok-upd*:

lock-thread-ok *ls ts* $\implies \text{lock-thread-ok } ls \ (ts(t \mapsto xw))$

$\langle proof \rangle$

lemma *lock-thread-ok-has-lockE*:
assumes *lock-thread-ok* *ls ts*
and *has-lock* (*ls* \$ *l*) *t*
obtains *x ln'* **where** *ts t* = $\lfloor (x, ln') \rfloor$
 $\langle proof \rangle$

lemma *redT-updLs-preserves-lock-thread-ok*:
assumes *lto*: *lock-thread-ok* *ls ts*
and *tst*: *ts t* = $\lfloor xw \rfloor$
shows *lock-thread-ok* (*redT-updLs* *ls t las*) *ts*
 $\langle proof \rangle$

lemma *redT-updTs-preserves-lock-thread-ok*:
assumes *lto*: *lock-thread-ok* *ls ts*
shows *lock-thread-ok* *ls* (*redT-updTs* *ts nts*)
 $\langle proof \rangle$

lemma *lock-thread-ok-has-lock*:
assumes *lock-thread-ok* *ls ts*
and *has-lock* (*ls* \$ *l*) *t*
obtains *xw* **where** *ts t* = $\lfloor xw \rfloor$
 $\langle proof \rangle$

lemma *lock-thread-ok-None-has-locks-0*:
 $\llbracket \text{lock-thread-ok } ls \text{ } ts; ts \text{ } t = \text{None} \rrbracket \implies \text{has-locks } (ls \text{ } \$ \text{ } l) \text{ } t = 0$
 $\langle proof \rangle$

lemma *redT-upds-preserves-lock-thread-ok*:
 $\llbracket \text{lock-thread-ok } ls \text{ } ts; ts \text{ } t = \lfloor xw \rfloor; \text{thread-oks } ts \text{ } tas \rrbracket$
 $\implies \text{lock-thread-ok } (\text{redT-updLs } ls \text{ } t \text{ } las) ((\text{redT-updTs } ts \text{ } tas)(t \mapsto xw'))$
 $\langle proof \rangle$

lemma *acquire-all-preserves-lock-thread-ok*:
fixes *ln*
shows $\llbracket \text{lock-thread-ok } ls \text{ } ts; ts \text{ } t = \lfloor (x, ln) \rfloor \rrbracket \implies \text{lock-thread-ok } (\text{acquire-all } ls \text{ } t \text{ } ln) (ts(t \mapsto xw))$
 $\langle proof \rangle$

Well-formedness condition: Wait sets contain only real threads

definition *wset-thread-ok* :: (*'w*, *'t*) *wait-sets* \Rightarrow (*'l*, *'t*, *'x*) *thread-info* \Rightarrow *bool*
where *wset-thread-ok* *ws ts* $\equiv \forall t. ts \text{ } t = \text{None} \longrightarrow ws \text{ } t = \text{None}$

lemma *wset-thread-okI*:
 $(\bigwedge t. ts \text{ } t = \text{None} \implies ws \text{ } t = \text{None}) \implies \text{wset-thread-ok } ws \text{ } ts$
 $\langle proof \rangle$

lemma *wset-thread-okD*:
 $\llbracket \text{wset-thread-ok } ws \text{ } ts; ts \text{ } t = \text{None} \rrbracket \implies ws \text{ } t = \text{None}$
 $\langle proof \rangle$

lemma *wset-thread-ok-conv-dom*:
 $\text{wset-thread-ok } ws \text{ } ts \iff \text{dom } ws \subseteq \text{dom } ts$
 $\langle proof \rangle$

lemma *wset-thread-ok-upd*:

$wset\text{-thread-ok } ls \ ts \Longrightarrow wset\text{-thread-ok } ls \ (ts(t \mapsto xw))$
 $\langle proof \rangle$

lemma *wset-thread-ok-upd-None*:

$wset\text{-thread-ok } ws \ ts \Longrightarrow wset\text{-thread-ok } (ws(t := None)) \ (ts(t := None))$
 $\langle proof \rangle$

lemma *wset-thread-ok-upd-Some*:

$wset\text{-thread-ok } ws \ ts \Longrightarrow wset\text{-thread-ok } (ws(t := wo)) \ (ts(t \mapsto xln))$
 $\langle proof \rangle$

lemma *wset-thread-ok-upd-ws*:

$\llbracket wset\text{-thread-ok } ws \ ts; \ ts \ t = [xln] \rrbracket \Longrightarrow wset\text{-thread-ok } (ws(t := w)) \ ts$
 $\langle proof \rangle$

lemma *wset-thread-ok-NotifyAll*:

$wset\text{-thread-ok } ws \ ts \Longrightarrow wset\text{-thread-ok } (\lambda t. \text{if } ws \ t = [w \ t] \text{ then } [w' \ t] \text{ else } ws \ t) \ ts$
 $\langle proof \rangle$

lemma *redT-updTs-preserves-wset-thread-ok*:

assumes *wto*: $wset\text{-thread-ok } ws \ ts$
shows $wset\text{-thread-ok } ws \ (redT\text{-updTs } ts \ nts)$
 $\langle proof \rangle$

lemma *redT-updW-preserve-wset-thread-ok*:

$\llbracket wset\text{-thread-ok } ws \ ts; \ redT\text{-updW } t \ ws \ wa \ ws'; \ ts \ t = [xln] \rrbracket \Longrightarrow wset\text{-thread-ok } ws' \ ts$
 $\langle proof \rangle$

lemma *redT-updWs-preserve-wset-thread-ok*:

$\llbracket wset\text{-thread-ok } ws \ ts; \ redT\text{-updWs } t \ ws \ was \ ws'; \ ts \ t = [xln] \rrbracket \Longrightarrow wset\text{-thread-ok } ws' \ ts$
 $\langle proof \rangle$

Well-formedness condition: Wait sets contain only non-final threads

context *final-thread* **begin**

definition *wset-final-ok* :: $('w, 't) \text{ wait-sets} \Rightarrow ('l, 't, 'x) \text{ thread-info} \Rightarrow \text{bool}$

where $wset\text{-final-ok } ws \ ts \longleftrightarrow (\forall t \in \text{dom } ws. \exists x \ ln. \ ts \ t = [(x, ln)] \wedge \neg \text{final } x)$

lemma *wset-final-okI*:

$(\bigwedge t \ w. \ ws \ t = [w]) \Longrightarrow \exists x \ ln. \ ts \ t = [(x, ln)] \wedge \neg \text{final } x \Longrightarrow wset\text{-final-ok } ws \ ts$
 $\langle proof \rangle$

lemma *wset-final-okD*:

$\llbracket wset\text{-final-ok } ws \ ts; \ ws \ t = [w] \rrbracket \Longrightarrow \exists x \ ln. \ ts \ t = [(x, ln)] \wedge \neg \text{final } x$
 $\langle proof \rangle$

lemma *wset-final-okE*:

assumes $wset\text{-final-ok } ws \ ts \ ws \ t = [w]$
and $\bigwedge x \ ln. \ ts \ t = [(x, ln)] \Longrightarrow \neg \text{final } x \Longrightarrow \text{thesis}$
shows *thesis*
 $\langle proof \rangle$

lemma *wset-final-ok-imp-wset-thread-ok*:
 $wset\text{-final-ok } ws \ ts \implies wset\text{-thread-ok } ws \ ts$
 ⟨proof⟩

end

end

1.8 Semantics of the thread action ReleaseAcquire for the thread state

theory *FWLockingThread*

imports

FWLocking

begin

fun *upd-threadR* :: $nat \Rightarrow 't \text{ lock} \Rightarrow 't \Rightarrow \text{lock-action} \Rightarrow nat$

where

$upd\text{-threadR } n \ l \ t \ \text{ReleaseAcquire} = n + \text{has-locks } l \ t$

| $upd\text{-threadR } n \ l \ t \ - = n$

primrec *upd-threadRs* :: $nat \Rightarrow 't \text{ lock} \Rightarrow 't \Rightarrow \text{lock-action list} \Rightarrow nat$

where

$upd\text{-threadRs } n \ l \ t \ [] = n$

| $upd\text{-threadRs } n \ l \ t \ (la \ \# \ las) = upd\text{-threadRs } (upd\text{-threadR } n \ l \ t \ la) \ (upd\text{-lock } l \ t \ la) \ t \ las$

lemma *upd-threadRs-append* [*simp*]:

$upd\text{-threadRs } n \ l \ t \ (las \ @ \ las') = upd\text{-threadRs } (upd\text{-threadRs } n \ l \ t \ las) \ (upd\text{-locks } l \ t \ las) \ t \ las'$

⟨proof⟩

definition *redT-updLns* :: $('l, 't) \text{ locks} \Rightarrow 't \Rightarrow ('l \Rightarrow f \text{ nat}) \Rightarrow 'l \text{ lock-actions} \Rightarrow ('l \Rightarrow f \text{ nat})$

where $\bigwedge ln. \text{redT-updLns } ls \ t \ ln \ las = (\lambda(l, n, la). upd\text{-threadRs } n \ l \ t \ la) \circ\$ (\$ls, (\$ln, las)\$)$

lemma *redT-updLns-iff* [*simp*]:

$\bigwedge ln. \text{redT-updLns } ls \ t \ ln \ las \ \$ \ l = upd\text{-threadRs } (ln \ \$ \ l) \ (ls \ \$ \ l) \ t \ (las \ \$ \ l)$

⟨proof⟩

lemma *upd-threadRs-comp-empty* [*simp*]: $(\lambda(l, n, las). upd\text{-threadRs } n \ l \ t \ las) \circ\$ (\$ls, (\$lns, K\$ [])\$)$

$= lns$

⟨proof⟩

lemma *redT-updLs-empty* [*simp*]: $\text{redT-updLs } ls \ t \ (K\$ []) = ls$

⟨proof⟩

end

1.9 Semantics of the thread actions for interruption

theory *FWInterrupt*

imports

FWState

begin

primrec $\text{redT-updI} :: 't \text{ interrupts} \Rightarrow 't \text{ interrupt-action} \Rightarrow 't \text{ interrupts}$
where

$\text{redT-updI } is \text{ (Interrupt } t) = \text{insert } t \text{ } is$
 $|\text{redT-updI } is \text{ (ClearInterrupt } t) = is - \{t\}$
 $|\text{redT-updI } is \text{ (IsInterrupted } t \text{ } b) = is$

fun $\text{redT-updIs} :: 't \text{ interrupts} \Rightarrow 't \text{ interrupt-action list} \Rightarrow 't \text{ interrupts}$
where

$\text{redT-updIs } is \ [] = is$
 $|\text{redT-updIs } is \ (ia \# \ ias) = \text{redT-updIs } (\text{redT-updI } is \ ia) \ ias$

primrec $\text{interrupt-action-ok} :: 't \text{ interrupts} \Rightarrow 't \text{ interrupt-action} \Rightarrow \text{bool}$
where

$\text{interrupt-action-ok } is \text{ (Interrupt } t) = \text{True}$
 $|\text{interrupt-action-ok } is \text{ (ClearInterrupt } t) = \text{True}$
 $|\text{interrupt-action-ok } is \text{ (IsInterrupted } t \text{ } b) = (b = (t \in is))$

fun $\text{interrupt-actions-ok} :: 't \text{ interrupts} \Rightarrow 't \text{ interrupt-action list} \Rightarrow \text{bool}$
where

$\text{interrupt-actions-ok } is \ [] = \text{True}$
 $|\text{interrupt-actions-ok } is \ (ia \# \ ias) \longleftrightarrow \text{interrupt-action-ok } is \ ia \wedge \text{interrupt-actions-ok } (\text{redT-updI } is \ ia) \ ias$

primrec $\text{interrupt-action-ok}' :: 't \text{ interrupts} \Rightarrow 't \text{ interrupt-action} \Rightarrow \text{bool}$
where

$\text{interrupt-action-ok}' is \text{ (Interrupt } t) = \text{True}$
 $|\text{interrupt-action-ok}' is \text{ (ClearInterrupt } t) = \text{True}$
 $|\text{interrupt-action-ok}' is \text{ (IsInterrupted } t \text{ } b) = (b \vee t \notin is)$

fun $\text{interrupt-actions-ok}' :: 't \text{ interrupts} \Rightarrow 't \text{ interrupt-action list} \Rightarrow \text{bool}$
where

$\text{interrupt-actions-ok}' is \ [] = \text{True}$
 $|\text{interrupt-actions-ok}' is \ (ia \# \ ias) \longleftrightarrow \text{interrupt-action-ok}' is \ ia \wedge \text{interrupt-actions-ok}' (\text{redT-updI } is \ ia) \ ias$

fun $\text{collect-interrupt} :: 't \text{ interrupt-action} \Rightarrow 't \text{ set} \Rightarrow 't \text{ set}$
where

$\text{collect-interrupt } (IsInterrupted \ t \ \text{True}) \ Ts = \text{insert } t \ Ts$
 $|\text{collect-interrupt } (Interrupt \ t) \ Ts = Ts - \{t\}$
 $|\text{collect-interrupt } - \ Ts = Ts$

definition $\text{collect-interrupts} :: 't \text{ interrupt-action list} \Rightarrow 't \text{ set}$
where $\text{collect-interrupts } ias = \text{foldr } \text{collect-interrupt } ias \ \{\}$

lemma $\text{collect-interrupts-interrupted}$:

$\llbracket \text{interrupt-actions-ok } is \ ias; t' \in \text{collect-interrupts } ias \rrbracket \Longrightarrow t' \in is$
 $\langle \text{proof} \rangle$

lemma $\text{interrupt-actions-ok-append}$ [simp]:

$\text{interrupt-actions-ok } is \ (ias \ @ \ ias') \longleftrightarrow \text{interrupt-actions-ok } is \ ias \wedge \text{interrupt-actions-ok } (\text{redT-updIs } is \ ias) \ ias'$
 $\langle \text{proof} \rangle$

lemma *collect-interrupt-subset*: $Ts \subseteq Ts' \implies \text{collect-interrupt } ia \ Ts \subseteq \text{collect-interrupt } ia \ Ts'$
 ⟨proof⟩

lemma *foldr-collect-interrupt-subset*:
 $Ts \subseteq Ts' \implies \text{foldr } \text{collect-interrupt } ias \ Ts \subseteq \text{foldr } \text{collect-interrupt } ias \ Ts'$
 ⟨proof⟩

lemma *interrupt-actions-ok-all-nthI*:
assumes $\bigwedge n. n < \text{length } ias \implies \text{interrupt-action-ok } (\text{redT-updIs } is \ (\text{take } n \ ias)) \ (ias \ ! \ n)$
shows *interrupt-actions-ok is ias*
 ⟨proof⟩

lemma *interrupt-actions-ok-nthD*:
assumes *interrupt-actions-ok is ias*
and $n < \text{length } ias$
shows $\text{interrupt-action-ok } (\text{redT-updIs } is \ (\text{take } n \ ias)) \ (ias \ ! \ n)$
 ⟨proof⟩

lemma *interrupt-actions-ok'-all-nthI*:
assumes $\bigwedge n. n < \text{length } ias \implies \text{interrupt-action-ok}' \ (\text{redT-updIs } is \ (\text{take } n \ ias)) \ (ias \ ! \ n)$
shows *interrupt-actions-ok' is ias*
 ⟨proof⟩

lemma *interrupt-actions-ok'-nthD*:
assumes *interrupt-actions-ok' is ias*
and $n < \text{length } ias$
shows $\text{interrupt-action-ok}' \ (\text{redT-updIs } is \ (\text{take } n \ ias)) \ (ias \ ! \ n)$
 ⟨proof⟩

lemma *interrupt-action-ok-imp-interrupt-action-ok'* [simp]:
 $\text{interrupt-action-ok } is \ ia \implies \text{interrupt-action-ok}' \ is \ ia$
 ⟨proof⟩

lemma *interrupt-actions-ok-imp-interrupt-actions-ok'* [simp]:
 $\text{interrupt-actions-ok } is \ ias \implies \text{interrupt-actions-ok}' \ is \ ias$
 ⟨proof⟩

lemma *collect-interruptsE*:
assumes $t' \in \text{collect-interrupts } ias'$
obtains n' **where** $n' < \text{length } ias' \ ias' \ ! \ n' = \text{IsInterrupted } t' \ \text{True}$
and $\text{Interrupt } t' \notin \text{set } (\text{take } n' \ ias')$
 ⟨proof⟩

lemma *collect-interrupts-prefix*:
 $\text{collect-interrupts } ias \subseteq \text{collect-interrupts } (ias \ @ \ ias')$
 ⟨proof⟩

lemma *redT-updI-insert-Interrupt*:
 $\llbracket t \in \text{redT-updI } is \ ia; t \notin is \rrbracket \implies ia = \text{Interrupt } t$
 ⟨proof⟩

lemma *redT-updIs-insert-Interrupt*:
 $\llbracket t \in \text{redT-updIs } is \ ias; t \notin is \rrbracket \implies \text{Interrupt } t \in \text{set } ias$
 ⟨proof⟩

lemma *interrupt-actions-ok-takeI*:

interrupt-actions-ok is ias \implies interrupt-actions-ok is (take n ias)

<proof>

lemma *interrupt-actions-ok'-collect-interrupts-imp-interrupt-actions-ok*:

assumes *int*: *interrupt-actions-ok' is ias*

and *ci*: *collect-interrupts ias \subseteq is*

and *int'*: *interrupt-actions-ok is' ias*

shows *interrupt-actions-ok is ias*

<proof>

end

1.10 The multithreaded semantics

theory *FWSemantics*

imports

FWWellform

FWLockingThread

FWCondAction

FWInterrupt

begin

inductive *redT-upd* :: (l, t, x, m, w) state \Rightarrow $t \Rightarrow (l, t, x, m, w, o)$ thread-action \Rightarrow $x \Rightarrow m \Rightarrow (l, t, x, m, w)$ state \Rightarrow bool

for s t ta x' m'

where

redT-updWs t (*wset* s) $\{ta\}_w$ ws'

\implies *redT-upd* s t ta x' m' (*redT-updLs* (*locks* s) t $\{ta\}_l$, (*redT-updTs* (*thr* s) $\{ta\}_t$)($t \mapsto (x',$
redT-updLns (*locks* s) t (*snd* (*the* (*thr* s t))) $\{ta\}_l$), m'), ws' , *redT-updIs* (*interrupts* s) $\{ta\}_i$)

inductive-simps *redT-upd-simps* [*simp*]:

redT-upd s t ta x' m' s'

definition *redT-acq* :: (l, t, x, m, w) state \Rightarrow $t \Rightarrow (l \Rightarrow f$ nat) $\Rightarrow (l, t, x, m, w)$ state

where

$\bigwedge ln.$ *redT-acq* s t $ln =$ (*acquire-all* (*locks* s) t ln , (*thr* s)($t \mapsto$ (*fst* (*the* (*thr* s t))), *no-wait-locks*)), *shr* s), *wset* s , *interrupts* s)

context *final-thread* **begin**

inductive *actions-ok* :: (l, t, x, m, w) state \Rightarrow $t \Rightarrow (l, t, x, m, w, o)$ thread-action \Rightarrow bool

for s :: (l, t, x, m, w) state **and** t :: t **and** ta :: (l, t, x, m, w, o) thread-action

where

\llbracket *lock-ok-las* (*locks* s) t $\{ta\}_l$; *thread-oks* (*thr* s) $\{ta\}_t$; *cond-action-oks* s t $\{ta\}_c$;

wset-actions-ok (*wset* s) t $\{ta\}_w$; *interrupt-actions-ok* (*interrupts* s) $\{ta\}_i$ \rrbracket

\implies *actions-ok* s t ta

declare *actions-ok.intros* [*intro!*]

declare *actions-ok.cases* [*elim!*]

lemma *actions-ok-iff* [*simp*]:

$actions-ok\ s\ t\ ta \longleftrightarrow$
 $lock-ok-las\ (locks\ s)\ t\ \{\{ta\}\}_l \wedge thread-oks\ (thr\ s)\ \{\{ta\}\}_t \wedge cond-action-oks\ s\ t\ \{\{ta\}\}_c \wedge$
 $wset-actions-ok\ (wset\ s)\ t\ \{\{ta\}\}_w \wedge interrupt-actions-ok\ (interrupts\ s)\ \{\{ta\}\}_i$
 <proof>

lemma *actions-ok-thread-oksD*:
 $actions-ok\ s\ t\ ta \implies thread-oks\ (thr\ s)\ \{\{ta\}\}_t$
 <proof>

inductive *actions-ok'* :: ('l,'t,'x,'m,'w) state \Rightarrow 't \Rightarrow ('l,'t,'x','m','w,'o) thread-action \Rightarrow bool **where**
 $\llbracket lock-ok-las'\ (locks\ s)\ t\ \{\{ta\}\}_l; thread-oks\ (thr\ s)\ \{\{ta\}\}_t; cond-action-oks'\ s\ t\ \{\{ta\}\}_c;$
 $wset-actions-ok\ (wset\ s)\ t\ \{\{ta\}\}_w; interrupt-actions-ok'\ (interrupts\ s)\ \{\{ta\}\}_i \rrbracket$
 $\implies actions-ok'\ s\ t\ ta$

declare *actions-ok'.intros* [intro!]
declare *actions-ok'.cases* [elim!]

lemma *actions-ok'-iff*:
 $actions-ok'\ s\ t\ ta \longleftrightarrow$
 $lock-ok-las'\ (locks\ s)\ t\ \{\{ta\}\}_l \wedge thread-oks\ (thr\ s)\ \{\{ta\}\}_t \wedge cond-action-oks'\ s\ t\ \{\{ta\}\}_c \wedge$
 $wset-actions-ok\ (wset\ s)\ t\ \{\{ta\}\}_w \wedge interrupt-actions-ok'\ (interrupts\ s)\ \{\{ta\}\}_i$
 <proof>

lemma *actions-ok'-ta-upd-obs*:
 $actions-ok'\ s\ t\ (ta-update-obs\ ta\ obs) \longleftrightarrow actions-ok'\ s\ t\ ta$
 <proof>

lemma *actions-ok'-empty*: $actions-ok'\ s\ t\ \varepsilon \longleftrightarrow wset\ s\ t = None$
 <proof>

lemma *actions-ok'-convert-extTA*:
 $actions-ok'\ s\ t\ (convert-extTA\ f\ ta) = actions-ok'\ s\ t\ ta$
 <proof>

inductive *actions-subset* :: ('l,'t,'x,'m,'w,'o) thread-action \Rightarrow ('l,'t,'x','m','w,'o) thread-action \Rightarrow bool
where
 $\llbracket collect-locks'\ \{\{ta'\}\}_l \subseteq collect-locks\ \{\{ta\}\}_l;$
 $collect-cond-actions\ \{\{ta'\}\}_c \subseteq collect-cond-actions\ \{\{ta\}\}_c;$
 $collect-interrupts\ \{\{ta'\}\}_i \subseteq collect-interrupts\ \{\{ta\}\}_i \rrbracket$
 $\implies actions-subset\ ta'\ ta$

declare *actions-subset.intros* [intro!]
declare *actions-subset.cases* [elim!]

lemma *actions-subset-iff*:
 $actions-subset\ ta'\ ta \longleftrightarrow$
 $collect-locks'\ \{\{ta'\}\}_l \subseteq collect-locks\ \{\{ta\}\}_l \wedge$
 $collect-cond-actions\ \{\{ta'\}\}_c \subseteq collect-cond-actions\ \{\{ta\}\}_c \wedge$
 $collect-interrupts\ \{\{ta'\}\}_i \subseteq collect-interrupts\ \{\{ta\}\}_i$
 <proof>

lemma *actions-subset-refl* [intro]:
 $actions-subset\ ta\ ta$
 <proof>

definition *final-thread* :: ('l,'t,'x,'m,'w) state \Rightarrow 't \Rightarrow bool **where**

\bigwedge ln. *final-thread* s t \equiv (case thr s t of None \Rightarrow False | [(x, ln)] \Rightarrow final x \wedge ln = no-wait-locks \wedge wset s t = None)

definition *final-threads* :: ('l,'t,'x,'m,'w) state \Rightarrow 't set

where *final-threads* s \equiv {t. *final-thread* s t}

lemma [iff]: t \in *final-threads* s = *final-thread* s t

\langle proof \rangle

lemma [pred-set-conv]: *final-thread* s = (λ t. t \in *final-threads* s)

\langle proof \rangle

definition *mfinal* :: ('l,'t,'x,'m,'w) state \Rightarrow bool

where *mfinal* s \longleftrightarrow (\forall t x ln. thr s t = [(x, ln)] \longrightarrow final x \wedge ln = no-wait-locks \wedge wset s t = None)

lemma *final-threadI*:

\llbracket thr s t = [(x, no-wait-locks)]; final x; wset s t = None $\rrbracket \Longrightarrow$ *final-thread* s t

\langle proof \rangle

lemma *final-threadE*:

assumes *final-thread* s t

obtains x **where** thr s t = [(x, no-wait-locks)] final x wset s t = None

\langle proof \rangle

lemma *mfinalI*:

(\bigwedge t x ln. thr s t = [(x, ln)] \Longrightarrow final x \wedge ln = no-wait-locks \wedge wset s t = None) \Longrightarrow *mfinal* s

\langle proof \rangle

lemma *mfinalD*:

fixes ln

assumes *mfinal* s thr s t = [(x, ln)]

shows final x ln = no-wait-locks wset s t = None

\langle proof \rangle

lemma *mfinalE*:

fixes ln

assumes *mfinal* s thr s t = [(x, ln)]

obtains final x ln = no-wait-locks wset s t = None

\langle proof \rangle

lemma *mfinal-def2*: *mfinal* s \longleftrightarrow dom (thr s) \subseteq *final-threads* s

\langle proof \rangle

end

locale *multithreaded-base* = *final-thread* +

constrains *final* :: 'x \Rightarrow bool

fixes r :: ('l,'t,'x,'m,'w,'o) semantics (\vdash - \dashrightarrow -) [50,0,0,50] 80)

and *convert-RA* :: 'l released-locks \Rightarrow 'o list

begin

abbreviation

$r\text{-syntax} :: 't \Rightarrow 'x \Rightarrow 'm \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow 'x \Rightarrow 'm \Rightarrow \text{bool}$
 $(\langle \cdot \vdash \langle \cdot, \cdot \rangle \dashrightarrow \langle \cdot, \cdot \rangle \rangle [50, 0, 0, 0, 0, 0] 80)$

where

$t \vdash \langle x, m \rangle \dashrightarrow \langle x', m' \rangle \equiv t \vdash (x, m) \dashrightarrow (x', m')$

inductive

$\text{redT} :: ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow 't \times ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow \text{bool}$

and

$\text{redT-syntax1} :: ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow \text{bool}$
 $(\langle \cdot \dashrightarrow \dashrightarrow \cdot \rangle [50, 0, 0, 50] 80)$

where

$s \dashrightarrow t \dashrightarrow s' \equiv \text{redT } s (t, ta) s'$

| redT-normal :

$\llbracket t \vdash \langle x, \text{shr } s \rangle \dashrightarrow \langle x', m' \rangle;$
 $\text{thr } s t = \llbracket (x, \text{no-wait-locks});$
 $\text{actions-ok } s t ta;$
 $\text{redT-upd } s t ta x' m' s' \rrbracket$
 $\implies s \dashrightarrow t \dashrightarrow s'$

| redT-acquire :

$\bigwedge ln. \llbracket \text{thr } s t = \llbracket (x, ln); \neg \text{waiting } (wset s t);$
 $\text{may-acquire-all } (locks s) t ln; ln \$ n > 0;$
 $s' = (\text{acquire-all } (locks s) t ln, ((\text{thr } s)(t \mapsto (x, \text{no-wait-locks})), \text{shr } s), wset s, \text{interrupts } s) \rrbracket$
 $\implies s \dashrightarrow t \dashrightarrow (K\$ \llbracket \cdot \rrbracket, \llbracket \cdot \rrbracket, \llbracket \cdot \rrbracket, \llbracket \cdot \rrbracket, \llbracket \cdot \rrbracket, \text{convert-RA } ln) \dashrightarrow s'$

abbreviation

$\text{redT-syntax2} :: ('l, 't) \text{ locks} \Rightarrow ('l, 't, 'x) \text{ thread-info} \times 'm \Rightarrow ('w, 't) \text{ wait-sets} \Rightarrow 't \text{ interrupts}$
 $\Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action}$
 $\Rightarrow ('l, 't) \text{ locks} \Rightarrow ('l, 't, 'x) \text{ thread-info} \times 'm \Rightarrow ('w, 't) \text{ wait-sets} \Rightarrow 't \text{ interrupts} \Rightarrow \text{bool}$
 $(\langle \langle \cdot, \cdot, \cdot, \cdot \rangle \dashrightarrow \langle \cdot, \cdot, \cdot, \cdot \rangle \rangle [0, 0, 0, 0, 0, 0, 0, 0, 0] 80)$

where

$\langle ls, tsm, ws, is \rangle \dashrightarrow \langle ls', tsm', ws', is' \rangle \equiv (ls, tsm, ws, is) \dashrightarrow (ls', tsm', ws', is')$

lemma redT-elim [*consumes 1, case-names normal acquire*]:

assumes red : $s \dashrightarrow t \dashrightarrow s'$

and normal : $\bigwedge x x' m' ws'$.

$\llbracket t \vdash \langle x, \text{shr } s \rangle \dashrightarrow \langle x', m' \rangle;$
 $\text{thr } s t = \llbracket (x, \text{no-wait-locks});$
 $\text{lock-ok-las } (locks s) t \llbracket ta \rrbracket_t;$
 $\text{thread-oks } (\text{thr } s) \llbracket ta \rrbracket_t;$
 $\text{cond-action-oks } s t \llbracket ta \rrbracket_c;$
 $\text{wset-actions-ok } (wset s) t \llbracket ta \rrbracket_w;$
 $\text{interrupt-actions-ok } (\text{interrupts } s) \llbracket ta \rrbracket_i;$
 $\text{redT-updWs } t (wset s) \llbracket ta \rrbracket_w ws';$
 $s' = (\text{redT-updLs } (locks s) t \llbracket ta \rrbracket_l, ((\text{redT-updTs } (\text{thr } s) \llbracket ta \rrbracket_t)(t \mapsto (x', \text{redT-updLns } (locks s) t$
 $\text{no-wait-locks } \llbracket ta \rrbracket_l)), m'), ws', \text{redT-updIs } (\text{interrupts } s) \llbracket ta \rrbracket_i) \rrbracket$
 $\implies \text{thesis}$

and acquire : $\bigwedge x ln n$.

$\llbracket \text{thr } s t = \llbracket (x, ln);$
 $ta = (K\$ \llbracket \cdot \rrbracket, \llbracket \cdot \rrbracket, \llbracket \cdot \rrbracket, \llbracket \cdot \rrbracket, \llbracket \cdot \rrbracket, \text{convert-RA } ln);$
 $\neg \text{waiting } (wset s t);$
 $\text{may-acquire-all } (locks s) t ln; 0 < ln \$ n;$

$s' = (\text{acquire-all } (\text{locks } s) \ t \ \text{ln}, ((\text{thr } s)(t \mapsto (x, \text{no-wait-locks})), \text{shr } s), \text{wset } s, \text{interrupts } s) \]$
 $\implies \text{thesis}$
shows *thesis*
 $\langle \text{proof} \rangle$

definition

$\text{RedT} :: ('l, 't, 'x, 'm, 'w) \text{state} \Rightarrow ('t \times ('l, 't, 'x, 'm, 'w, 'o) \text{thread-action}) \text{list} \Rightarrow ('l, 't, 'x, 'm, 'w) \text{state} \Rightarrow \text{bool}$
 $(\langle \cdot \rightarrow \rightarrow * \rightarrow \rangle [50, 0, 50] \ 80)$

where

$\text{RedT} \equiv \text{rtrancl3p } \text{redT}$

lemma *RedTI*:

$\text{rtrancl3p } \text{redT } s \ \text{ttas } s' \implies \text{RedT } s \ \text{ttas } s'$
 $\langle \text{proof} \rangle$

lemma *RedTE*:

$\llbracket \text{RedT } s \ \text{ttas } s'; \text{rtrancl3p } \text{redT } s \ \text{ttas } s' \implies P \rrbracket \implies P$
 $\langle \text{proof} \rangle$

lemma *RedTD*:

$\text{RedT } s \ \text{ttas } s' \implies \text{rtrancl3p } \text{redT } s \ \text{ttas } s'$
 $\langle \text{proof} \rangle$

lemma *RedT-induct* [consumes 1, case-names refl step]:

$\llbracket s \rightarrow \text{ttas} \rightarrow * \ s';$
 $\bigwedge s. P \ s \rrbracket s;$
 $\bigwedge s \ \text{ttas } s' \ t \ \text{ta} \ s''. \llbracket s \rightarrow \text{ttas} \rightarrow * \ s'; P \ s \ \text{ttas } s'; s' \rightarrow \text{ta} \rightarrow s'' \rrbracket \implies P \ s \ (\text{ttas} \ @ \ [(t, \text{ta})]) \ s''$
 $\implies P \ s \ \text{ttas } s'$
 $\langle \text{proof} \rangle$

lemma *RedT-induct'* [consumes 1, case-names refl step]:

$\llbracket s \rightarrow \text{ttas} \rightarrow * \ s';$
 $P \ s \rrbracket s;$
 $\bigwedge \text{ttas } s' \ t \ \text{ta} \ s''. \llbracket s \rightarrow \text{ttas} \rightarrow * \ s'; P \ s \ \text{ttas } s'; s' \rightarrow \text{ta} \rightarrow s'' \rrbracket \implies P \ s \ (\text{ttas} \ @ \ [(t, \text{ta})]) \ s''$
 $\implies P \ s \ \text{ttas } s'$
 $\langle \text{proof} \rangle$

lemma *RedT-lift-preserveD*:

assumes *Red*: $s \rightarrow \text{ttas} \rightarrow * \ s'$
and *P*: $P \ s$
and *preserve*: $\bigwedge s \ t \ \text{tas} \ s'. \llbracket s \rightarrow \text{tas} \rightarrow s'; P \ s \rrbracket \implies P \ s'$
shows $P \ s'$
 $\langle \text{proof} \rangle$

lemma *RedT-refl* [intro, simp]:

$s \rightarrow \llbracket \cdot \rrbracket \rightarrow * \ s$
 $\langle \text{proof} \rangle$

lemma *redT-has-locks-inv*:

$\llbracket \langle ls, (ts, m), ws, is \rangle \rightarrow \text{ta} \rightarrow \langle ls', (ts', m'), ws', is' \rangle; t \neq t' \rrbracket \implies$
 $\text{has-locks } (ls \ \$ \ l) \ t' = \text{has-locks } (ls' \ \$ \ l) \ t'$
 $\langle \text{proof} \rangle$

lemma *redT-has-lock-inv*:

$$\llbracket \langle ls, (ts, m), ws, is \rangle -t \triangleright ta \rightarrow \langle ls', (ts', m'), ws', is' \rangle; t \neq t' \rrbracket \\ \implies \text{has-lock } (ls' \$ l) t' = \text{has-lock } (ls \$ l) t'$$

<proof>

lemma *redT-ts-Some-inv*:

$$\llbracket \langle ls, (ts, m), ws, is \rangle -t \triangleright ta \rightarrow \langle ls', (ts', m'), ws', is' \rangle; t \neq t'; ts t' = \lfloor x \rfloor \rrbracket \implies ts' t' = \lfloor x \rfloor$$

<proof>

lemma *redT-thread-not-disappear*:

$$\llbracket s -t \triangleright ta \rightarrow s'; \text{thr } s' t' = \text{None} \rrbracket \implies \text{thr } s t' = \text{None}$$

<proof>

lemma *RedT-thread-not-disappear*:

$$\llbracket s -\triangleright ttas \rightarrow * s'; \text{thr } s' t' = \text{None} \rrbracket \implies \text{thr } s t' = \text{None}$$

<proof>

lemma *redT-preserves-wset-thread-ok*:

$$\llbracket s -t \triangleright ta \rightarrow s'; \text{wset-thread-ok } (wset s) (\text{thr } s) \rrbracket \implies \text{wset-thread-ok } (wset s') (\text{thr } s')$$

<proof>

lemma *RedT-preserves-wset-thread-ok*:

$$\llbracket s -\triangleright ttas \rightarrow * s'; \text{wset-thread-ok } (wset s) (\text{thr } s) \rrbracket \implies \text{wset-thread-ok } (wset s') (\text{thr } s')$$

<proof>

lemma *redT-new-thread-ts-Some*:

$$\llbracket s -t \triangleright ta \rightarrow s'; \text{NewThread } t' x m'' \in \text{set } \{\{ta\}_t\}; \text{wset-thread-ok } (wset s) (\text{thr } s) \rrbracket \\ \implies \text{thr } s' t' = \lfloor (x, \text{no-wait-locks}) \rfloor$$

<proof>

lemma *RedT-new-thread-ts-not-None*:

$$\llbracket s -\triangleright ttas \rightarrow * s'; \text{NewThread } t x m'' \in \text{set } (\text{concat } (\text{map } (\text{thr-a} \circ \text{snd}) \text{ttas})); \text{wset-thread-ok } (wset s) (\text{thr } s) \rrbracket$$

$$\implies \text{thr } s' t \neq \text{None}$$

<proof>

lemma *redT-preserves-lock-thread-ok*:

$$\llbracket s -t \triangleright ta \rightarrow s'; \text{lock-thread-ok } (\text{locks } s) (\text{thr } s) \rrbracket \implies \text{lock-thread-ok } (\text{locks } s') (\text{thr } s')$$

<proof>

lemma *RedT-preserves-lock-thread-ok*:

$$\llbracket s -\triangleright ttas \rightarrow * s'; \text{lock-thread-ok } (\text{locks } s) (\text{thr } s) \rrbracket \implies \text{lock-thread-ok } (\text{locks } s') (\text{thr } s')$$

<proof>

lemma *redT-ex-new-thread*:

assumes $s -t \triangleright ta \rightarrow s'$ *wset-thread-ok* $(wset s) (\text{thr } s)$ $\text{thr } s' t = \lfloor (x, w) \rfloor$ $\text{thr } s t = \text{None}$

shows $\exists m. \text{NewThread } t x m \in \text{set } \{\{ta\}_t\} \wedge w = \text{no-wait-locks}$

<proof>

lemma *redT-ex-new-thread'*:

assumes $s -t \triangleright ta \rightarrow s'$ $\text{thr } s' t = \lfloor (x, w) \rfloor$ $\text{thr } s t = \text{None}$

shows $\exists m x. \text{NewThread } t x m \in \text{set } \{\{ta\}_t\}$

<proof>

definition *deterministic* :: ($'l, 't, 'x, 'm, 'w$) state set \Rightarrow bool

where

$$\begin{aligned} & \text{deterministic } I \iff \\ & (\forall s \ t \ x \ ta' \ x' \ m' \ ta'' \ x'' \ m''. \\ & \quad s \in I \\ & \quad \longrightarrow \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor \\ & \quad \longrightarrow t \vdash \langle x, \text{shr } s \rangle -ta' \rightarrow \langle x', m' \rangle \\ & \quad \longrightarrow t \vdash \langle x, \text{shr } s \rangle -ta'' \rightarrow \langle x'', m'' \rangle \\ & \quad \longrightarrow \text{actions-ok } s \ t \ ta' \longrightarrow \text{actions-ok } s \ t \ ta'' \\ & \quad \longrightarrow ta' = ta'' \wedge x' = x'' \wedge m' = m'' \wedge \text{invariant3p redT } I \end{aligned}$$

lemma *deterministicI*:

$$\begin{aligned} & \llbracket \bigwedge s \ t \ x \ ta' \ x' \ m' \ ta'' \ x'' \ m''. \\ & \quad \llbracket s \in I; \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor; \\ & \quad \quad t \vdash \langle x, \text{shr } s \rangle -ta' \rightarrow \langle x', m' \rangle; t \vdash \langle x, \text{shr } s \rangle -ta'' \rightarrow \langle x'', m'' \rangle; \\ & \quad \quad \text{actions-ok } s \ t \ ta'; \text{actions-ok } s \ t \ ta'' \rrbracket \\ & \quad \implies ta' = ta'' \wedge x' = x'' \wedge m' = m''; \\ & \quad \text{invariant3p redT } I \rrbracket \\ & \implies \text{deterministic } I \end{aligned}$$

\langle proof \rangle

lemma *deterministicD*:

$$\begin{aligned} & \llbracket \text{deterministic } I; \\ & \quad t \vdash \langle x, \text{shr } s \rangle -ta' \rightarrow \langle x', m' \rangle; t \vdash \langle x, \text{shr } s \rangle -ta'' \rightarrow \langle x'', m'' \rangle; \\ & \quad \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor; \text{actions-ok } s \ t \ ta'; \text{actions-ok } s \ t \ ta''; s \in I \rrbracket \\ & \implies ta' = ta'' \wedge x' = x'' \wedge m' = m'' \end{aligned}$$

\langle proof \rangle

lemma *deterministic-invariant3p*:

$$\text{deterministic } I \implies \text{invariant3p redT } I$$

\langle proof \rangle

lemma *deterministic-THE*:

$$\llbracket \text{deterministic } I; \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor; t \vdash \langle x, \text{shr } s \rangle -ta \rightarrow \langle x', m' \rangle; \text{actions-ok } s \ t \ ta; s \in I \rrbracket$$

$$\implies (\text{THE } (ta, x', m'). t \vdash \langle x, \text{shr } s \rangle -ta \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s \ t \ ta) = (ta, x', m')$$

\langle proof \rangle

end

locale *multithreaded* = *multithreaded-base* +

constrains *final* :: $'x \Rightarrow$ bool

and $r :: ('l, 't, 'x, 'm, 'w, 'o)$ semantics

and *convert-RA* :: $'l$ released-locks \Rightarrow $'o$ list

assumes *new-thread-memory*: $\llbracket t \vdash s -ta \rightarrow s'; \text{NewThread } t' \ x \ m \in \text{set } \{ta\}_t \rrbracket \implies m = \text{snd } s'$

and *final-no-red*: $\llbracket t \vdash (x, m) -ta \rightarrow (x', m'); \text{final } x \rrbracket \implies \text{False}$

begin

lemma *redT-new-thread-common*:

$$\llbracket s -t \triangleright ta \rightarrow s'; \text{NewThread } t' \ x \ m'' \in \text{set } \{ta\}_t; \{ta\}_w = [] \rrbracket \implies m'' = \text{shr } s'$$

\langle proof \rangle

lemma *redT-new-thread*:

assumes $s -t \triangleright ta \rightarrow s'$ *thr* $s' \ t = \lfloor (x, w) \rfloor$ *thr* $s \ t = \text{None } \{ta\}_w = []$

shows *NewThread* $t\ x\ (shr\ s') \in set\ \{\{ta\}_t \wedge w = no\text{-}wait\text{-}locks$
 ⟨proof⟩

lemma *final-no-redT*:

$\llbracket s -t>ta \rightarrow s'; thr\ s\ t = \llbracket (x, no\text{-}wait\text{-}locks) \rrbracket \rrbracket \implies \neg\ final\ x$
 ⟨proof⟩

lemma *mfinal-no-redT*:

assumes *redT*: $s -t>ta \rightarrow s'$ **and** *mfinal*: *mfinal* s
shows *False*
 ⟨proof⟩

end

end

1.11 Auxiliary definitions for the progress theorem for the multithreaded semantics

theory *FWProgressAux*

imports

FWSemantics

begin

abbreviation *collect-waits* $:: (l, 't, 'x, 'm, 'w, 'o)\ thread\ action \Rightarrow (l + 't + 't)\ set$

where *collect-waits* $ta \equiv collect\ locks\ \{\{ta\}_l\} <+> collect\ cond\ actions\ \{\{ta\}_c\} <+> collect\ interrupts\ \{\{ta\}_i\}$

lemma *collect-waits-unfold*:

collect-waits $ta = \{l.\ Lock \in set\ (\{\{ta\}_l\} \$ l)\} <+> \{t.\ Join\ t \in set\ \{\{ta\}_c\}\} <+> collect\ interrupts\ \{\{ta\}_i\}$
 ⟨proof⟩

context *multithreaded-base* **begin**

definition *must-sync* $:: 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool\ (\langle - \vdash \langle -, / - \rangle / \rangle \wr [50, 0, 0]\ 81)$ **where**

$t \vdash \langle x, m \rangle \wr \longleftrightarrow (\exists ta\ x'\ m'\ s.\ t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle \wedge shr\ s = m \wedge actions\text{-}ok\ s\ t\ ta)$

lemma *must-sync-def2*:

$t \vdash \langle x, m \rangle \wr \longleftrightarrow (\exists ta\ x'\ m'\ s.\ t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle \wedge actions\text{-}ok\ s\ t\ ta)$
 ⟨proof⟩

lemma *must-syncI*:

$\exists ta\ x'\ m'\ s.\ t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle \wedge actions\text{-}ok\ s\ t\ ta \implies t \vdash \langle x, m \rangle \wr$
 ⟨proof⟩

lemma *must-syncE*:

$\llbracket t \vdash \langle x, m \rangle \wr; \bigwedge ta\ x'\ m'\ s.\ \llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; actions\text{-}ok\ s\ t\ ta; m = shr\ s \rrbracket \implies thesis \rrbracket$
 $\implies thesis$
 ⟨proof⟩

definition *can-sync* $:: 't \Rightarrow 'x \Rightarrow 'm \Rightarrow (l + 't + 't)\ set \Rightarrow bool\ (\langle - \vdash \langle -, / - \rangle / - / \rangle \wr [50, 0, 0, 0]\ 81)$
where

$$t \vdash \langle x, m \rangle LT \wr \equiv \exists ta \ x' \ m'. t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle \wedge (LT = \text{collect-waits } ta)$$

lemma *can-syncI*:

$$\llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; \\ LT = \text{collect-waits } ta \rrbracket$$

$$\implies t \vdash \langle x, m \rangle LT \wr$$

<proof>

lemma *can-syncE*:

assumes $t \vdash \langle x, m \rangle LT \wr$

obtains $ta \ x' \ m'$

where $t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle$

and $LT = \text{collect-waits } ta$

<proof>

inductive-set *active-threads* :: (l, t, x, m, w) state \Rightarrow 't set

for $s :: (l, t, x, m, w)$ state

where

normal:

$$\bigwedge ln. \llbracket thr \ s \ t = \text{Some } (x, ln);$$

$$ln = \text{no-wait-locks};$$

$$t \vdash (x, shr \ s) -ta \rightarrow x' \ m';$$

$$\text{actions-ok } s \ t \ ta \rrbracket$$

$$\implies t \in \text{active-threads } s$$

| *acquire*:

$$\bigwedge ln. \llbracket thr \ s \ t = \text{Some } (x, ln);$$

$$ln \neq \text{no-wait-locks};$$

$$\neg \text{waiting } (wset \ s \ t);$$

$$\text{may-acquire-all } (locks \ s) \ t \ ln \rrbracket$$

$$\implies t \in \text{active-threads } s$$

lemma *active-threads-iff*:

active-threads $s =$

$$\{t. \exists x \ ln. thr \ s \ t = \text{Some } (x, ln) \wedge$$

$$(\text{if } ln = \text{no-wait-locks}$$

$$\text{then } \exists ta \ x' \ m'. t \vdash (x, shr \ s) -ta \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s \ t \ ta$$

$$\text{else } \neg \text{waiting } (wset \ s \ t) \wedge \text{may-acquire-all } (locks \ s) \ t \ ln\}$$

<proof>

lemma *active-thread-ex-red*:

assumes $t \in \text{active-threads } s$

shows $\exists ta \ s'. s -t \triangleright ta \rightarrow s'$

<proof>

end

Well-formedness conditions for final

context *final-thread* **begin**

inductive *not-final-thread* :: (l, t, x, m, w) state \Rightarrow 't \Rightarrow bool

for $s :: (l, t, x, m, w)$ state **and** $t ::$ 't **where**

$$\text{not-final-thread-final: } \bigwedge ln. \llbracket thr \ s \ t = \lfloor (x, ln) \rfloor; \neg \text{final } x \rrbracket \implies \text{not-final-thread } s \ t$$

| *not-final-thread-wait-locks*: $\bigwedge ln. \llbracket thr \ s \ t = \lfloor (x, ln) \rfloor; ln \neq \text{no-wait-locks} \rrbracket \implies \text{not-final-thread } s \ t$

| *not-final-thread-wait-set*: $\bigwedge ln. \llbracket thr \ s \ t = \lfloor (x, ln) \rfloor; wset \ s \ t = \lfloor w \rfloor \rrbracket \implies \text{not-final-thread } s \ t$

declare *not-final-thread.cases* [*elim*]

lemmas *not-final-thread-cases* = *not-final-thread.cases* [*consumes 1, case-names final wait-locks wait-set*]

lemma *not-final-thread-cases2* [*consumes 2, case-names final wait-locks wait-set*]:

$\bigwedge ln. \llbracket \text{not-final-thread } s \ t; \text{thr } s \ t = \lfloor (x, ln) \rfloor;$
 $\neg \text{final } x \implies \text{thesis}; ln \neq \text{no-wait-locks} \implies \text{thesis}; \bigwedge w. \text{wset } s \ t = \lfloor w \rfloor \implies \text{thesis} \rrbracket$
 $\implies \text{thesis}$
 ⟨*proof*⟩

lemma *not-final-thread-iff*:

$\text{not-final-thread } s \ t \iff (\exists x \ ln. \text{thr } s \ t = \lfloor (x, ln) \rfloor \wedge (\neg \text{final } x \vee ln \neq \text{no-wait-locks} \vee (\exists w. \text{wset } s \ t = \lfloor w \rfloor)))$
 ⟨*proof*⟩

lemma *not-final-thread-conv*:

$\text{not-final-thread } s \ t \iff \text{thr } s \ t \neq \text{None} \wedge \neg \text{final-thread } s \ t$
 ⟨*proof*⟩

lemma *not-final-thread-existsE*:

assumes *not-final-thread* $s \ t$
and $\bigwedge x \ ln. \text{thr } s \ t = \lfloor (x, ln) \rfloor \implies \text{thesis}$
shows *thesis*
 ⟨*proof*⟩

lemma *not-final-thread-final-thread-conv*:

$\text{thr } s \ t \neq \text{None} \implies \neg \text{final-thread } s \ t \iff \text{not-final-thread } s \ t$
 ⟨*proof*⟩

lemma *may-join-cond-action-oks*:

assumes $\bigwedge t'. \text{Join } t' \in \text{set } \text{cas} \implies \neg \text{not-final-thread } s \ t' \wedge t \neq t'$
shows *cond-action-oks* $s \ t \ \text{cas}$
 ⟨*proof*⟩

end

context *multithreaded* **begin**

lemma *red-not-final-thread*:

$s -t \triangleright ta \rightarrow s' \implies \text{not-final-thread } s \ t$
 ⟨*proof*⟩

lemma *redT-preserves-final-thread*:

$\llbracket s -t \triangleright ta \rightarrow s'; \text{final-thread } s \ t \rrbracket \implies \text{final-thread } s' \ t$
 ⟨*proof*⟩

end

context *multithreaded-base* **begin**

definition *wset-Suspend-ok* :: (l, t, x, m, w) *state set* \Rightarrow (l, t, x, m, w) *state set*
where

$wset\text{-}Suspend\text{-}ok\ I =$
 $\{s. s \in I \wedge$
 $(\forall t \in dom\ (wset\ s). \exists s0 \in I. \exists s1 \in I. \exists ttas\ x\ x0\ ta\ w'\ ln'\ ln''. s0 \text{-}t \triangleright ta \rightarrow s1 \wedge s1 \text{-}\triangleright ttas \rightarrow * s \wedge$
 $thr\ s0\ t = [(x0, no\text{-}wait\text{-}locks)] \wedge t \vdash \langle x0, shr\ s0 \rangle \text{-}ta \rightarrow \langle x, shr\ s1 \rangle \wedge Suspend\ w' \in set$
 $\{ta\}_w \wedge$
 $actions\text{-}ok\ s0\ t\ ta \wedge thr\ s1\ t = [(x, ln')] \wedge thr\ s\ t = [(x, ln'')]\}$

lemma *wset-Suspend-okI*:

$\llbracket s \in I;$
 $\bigwedge t\ w. wset\ s\ t = [w] \implies \exists s0 \in I. \exists s1 \in I. \exists ttas\ x\ x0\ ta\ w'\ ln'\ ln''. s0 \text{-}t \triangleright ta \rightarrow s1 \wedge s1 \text{-}\triangleright ttas \rightarrow * s \wedge$
 $s \wedge$
 $thr\ s0\ t = [(x0, no\text{-}wait\text{-}locks)] \wedge t \vdash \langle x0, shr\ s0 \rangle \text{-}ta \rightarrow \langle x, shr\ s1 \rangle \wedge Suspend\ w' \in set$
 $\{ta\}_w \wedge$
 $actions\text{-}ok\ s0\ t\ ta \wedge thr\ s1\ t = [(x, ln')] \wedge thr\ s\ t = [(x, ln'')]\ \rrbracket$
 $\implies s \in wset\text{-}Suspend\text{-}ok\ I$
 $\langle proof \rangle$

lemma *wset-Suspend-okD1*:

$s \in wset\text{-}Suspend\text{-}ok\ I \implies s \in I$
 $\langle proof \rangle$

lemma *wset-Suspend-okD2*:

$\llbracket s \in wset\text{-}Suspend\text{-}ok\ I; wset\ s\ t = [w] \rrbracket$
 $\implies \exists s0 \in I. \exists s1 \in I. \exists ttas\ x\ x0\ ta\ w'\ ln'\ ln''. s0 \text{-}t \triangleright ta \rightarrow s1 \wedge s1 \text{-}\triangleright ttas \rightarrow * s \wedge$
 $thr\ s0\ t = [(x0, no\text{-}wait\text{-}locks)] \wedge t \vdash \langle x0, shr\ s0 \rangle \text{-}ta \rightarrow \langle x, shr\ s1 \rangle \wedge Suspend\ w' \in set$
 $\{ta\}_w \wedge$
 $actions\text{-}ok\ s0\ t\ ta \wedge thr\ s1\ t = [(x, ln')] \wedge thr\ s\ t = [(x, ln'')]\ \rrbracket$
 $\langle proof \rangle$

lemma *wset-Suspend-ok-imp-wset-thread-ok*:

$s \in wset\text{-}Suspend\text{-}ok\ I \implies wset\text{-}thread\text{-}ok\ (wset\ s)\ (thr\ s)$
 $\langle proof \rangle$

lemma *invariant3p-wset-Suspend-ok*:

assumes $I: invariant3p\ redT\ I$
shows $invariant3p\ redT\ (wset\text{-}Suspend\text{-}ok\ I)$
 $\langle proof \rangle$

end

end

1.12 Deadlock formalisation

theory *FWDeadlock*

imports

FWProgressAux

begin

context *final-thread* **begin**

definition *all-final-except* :: (l, t, x, m, w) state \Rightarrow $t\ set \Rightarrow$ bool **where**
 $all\text{-}final\text{-}except\ s\ Ts \equiv \forall t. not\text{-}final\text{-}thread\ s\ t \longrightarrow t \in Ts$

lemma *all-final-except-mono* [*mono*]:

$(\bigwedge x. x \in A \longrightarrow x \in B) \Longrightarrow \text{all-final-except } ts \ A \longrightarrow \text{all-final-except } ts \ B$
 $\langle \text{proof} \rangle$

lemma *all-final-except-mono'*:

$\llbracket \text{all-final-except } ts \ A; \bigwedge x. x \in A \Longrightarrow x \in B \rrbracket \Longrightarrow \text{all-final-except } ts \ B$
 $\langle \text{proof} \rangle$

lemma *all-final-exceptI*:

$(\bigwedge t. \text{not-final-thread } s \ t \Longrightarrow t \in Ts) \Longrightarrow \text{all-final-except } s \ Ts$
 $\langle \text{proof} \rangle$

lemma *all-final-exceptD*:

$\llbracket \text{all-final-except } s \ Ts; \text{not-final-thread } s \ t \rrbracket \Longrightarrow t \in Ts$
 $\langle \text{proof} \rangle$

inductive *must-wait* :: (l, t, x, m, w) state $\Rightarrow t \Rightarrow (l + t + w) \Rightarrow t \text{ set} \Rightarrow \text{bool}$

for $s :: (l, t, x, m, w)$ state **and** $t :: t$ **where**

— Lock l

$\llbracket \text{has-lock } (\text{locks } s \ \$ \ l) \ t'; t' \neq t; t' \in Ts \rrbracket \Longrightarrow \text{must-wait } s \ t \ (\text{Inl } l) \ Ts$

| — Join t'

$\llbracket \text{not-final-thread } s \ t'; t' \in Ts \rrbracket \Longrightarrow \text{must-wait } s \ t \ (\text{Inr } (\text{Inl } t')) \ Ts$

| — IsInterrupted t' True

$\llbracket \text{all-final-except } s \ Ts; t' \notin \text{interrupts } s \rrbracket \Longrightarrow \text{must-wait } s \ t \ (\text{Inr } (\text{Inr } t')) \ Ts$

declare *must-wait.cases* [*elim*]

declare *must-wait.intros* [*intro*]

lemma *must-wait-elim1* [*consumes 1, case-names lock join interrupt, cases pred*]:

assumes $\text{must-wait } s \ t \ lt \ Ts$

obtains $l \ t'$ **where** $lt = \text{Inl } l \ \text{has-lock } (\text{locks } s \ \$ \ l) \ t' \ t' \neq t \ t' \in Ts$

| t' **where** $lt = \text{Inr } (\text{Inl } t') \ \text{not-final-thread } s \ t' \ t' \in Ts$

| t' **where** $lt = \text{Inr } (\text{Inr } t') \ \text{all-final-except } s \ Ts \ t' \notin \text{interrupts } s$

$\langle \text{proof} \rangle$

inductive-cases *must-wait-elim2* [*elim!*]:

$\text{must-wait } s \ t \ (\text{Inl } l) \ Ts$

$\text{must-wait } s \ t \ (\text{Inr } (\text{Inl } t')) \ Ts$

$\text{must-wait } s \ t \ (\text{Inr } (\text{Inr } t')) \ Ts$

lemma *must-wait-iff*:

$\text{must-wait } s \ t \ lt \ Ts \longleftrightarrow$

(*case* lt of $\text{Inl } l \Rightarrow \exists t' \in Ts. t \neq t' \wedge \text{has-lock } (\text{locks } s \ \$ \ l) \ t'$

| $\text{Inr } (\text{Inl } t') \Rightarrow \text{not-final-thread } s \ t' \wedge t' \in Ts$

| $\text{Inr } (\text{Inr } t') \Rightarrow \text{all-final-except } s \ Ts \wedge t' \notin \text{interrupts } s$)

$\langle \text{proof} \rangle$

end

Deadlock as a system-wide property

context *multithreaded-base* **begin**

definition

$deadlock :: ('l, 't, 'x, 'm, 'w) state \Rightarrow bool$

where

$deadlock\ s$

$$\begin{aligned} &\equiv (\forall t\ x.\ thr\ s\ t = [(x, no\ wait\ locks)] \wedge \neg final\ x \wedge wset\ s\ t = None \\ &\quad \longrightarrow t \vdash \langle x, shr\ s \rangle \wr \wedge (\forall LT.\ t \vdash \langle x, shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT.\ must\ wait\ s\ t\ lt\ (dom\ (thr\ s)))) \\ &\quad \wedge (\forall t\ x\ ln.\ thr\ s\ t = [(x, ln)] \wedge (\exists l.\ ln\ \$\ l > 0) \wedge \neg waiting\ (wset\ s\ t) \\ &\quad \longrightarrow (\exists l\ t'. ln\ \$\ l > 0 \wedge t \neq t' \wedge thr\ s\ t' \neq None \wedge has\ lock\ (locks\ s\ \$\ l)\ t')) \\ &\quad \wedge (\forall t\ x\ w.\ thr\ s\ t = [(x, no\ wait\ locks)] \longrightarrow wset\ s\ t \neq [PostWS\ w]) \end{aligned}$$
lemma deadlockI:

$$\begin{aligned} &[[\wedge t\ x.\ [thr\ s\ t = [(x, no\ wait\ locks)]; \neg final\ x; wset\ s\ t = None] \\ &\quad \Longrightarrow t \vdash \langle x, shr\ s \rangle \wr \wedge (\forall LT.\ t \vdash \langle x, shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT.\ must\ wait\ s\ t\ lt\ (dom\ (thr\ s))))); \\ &\quad \wedge t\ x\ ln\ l.\ [thr\ s\ t = [(x, ln)]; ln\ \$\ l > 0; \neg waiting\ (wset\ s\ t)] \\ &\quad \Longrightarrow \exists l\ t'. ln\ \$\ l > 0 \wedge t \neq t' \wedge thr\ s\ t' \neq None \wedge has\ lock\ (locks\ s\ \$\ l)\ t'; \\ &\quad \wedge t\ x\ w.\ thr\ s\ t = [(x, no\ wait\ locks)] \Longrightarrow wset\ s\ t \neq [PostWS\ w]] \\ &\Longrightarrow deadlock\ s \end{aligned}$$

$\langle proof \rangle$

lemma deadlockE:

assumes $deadlock\ s$

obtains $\forall t\ x.\ thr\ s\ t = [(x, no\ wait\ locks)] \wedge \neg final\ x \wedge wset\ s\ t = None$

$\longrightarrow t \vdash \langle x, shr\ s \rangle \wr \wedge (\forall LT.\ t \vdash \langle x, shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT.\ must\ wait\ s\ t\ lt\ (dom\ (thr\ s))))$

and $\forall t\ x\ ln.\ thr\ s\ t = [(x, ln)] \wedge (\exists l.\ ln\ \$\ l > 0) \wedge \neg waiting\ (wset\ s\ t)$

$\longrightarrow (\exists l\ t'. ln\ \$\ l > 0 \wedge t \neq t' \wedge thr\ s\ t' \neq None \wedge has\ lock\ (locks\ s\ \$\ l)\ t')$

and $\forall t\ x\ w.\ thr\ s\ t = [(x, no\ wait\ locks)] \longrightarrow wset\ s\ t \neq [PostWS\ w]$

$\langle proof \rangle$

lemma deadlockD1:

assumes $deadlock\ s$

and $thr\ s\ t = [(x, no\ wait\ locks)]$

and $\neg final\ x$

and $wset\ s\ t = None$

obtains $t \vdash \langle x, shr\ s \rangle \wr$

and $\forall LT.\ t \vdash \langle x, shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT.\ must\ wait\ s\ t\ lt\ (dom\ (thr\ s)))$

$\langle proof \rangle$

lemma deadlockD2:

fixes ln

assumes $deadlock\ s$

and $thr\ s\ t = [(x, ln)]$

and $ln\ \$\ l > 0$

and $\neg waiting\ (wset\ s\ t)$

obtains $l'\ t'$ **where** $ln\ \$\ l' > 0 \wedge t \neq t' \wedge thr\ s\ t' \neq None \wedge has\ lock\ (locks\ s\ \$\ l')\ t'$

$\langle proof \rangle$

lemma deadlockD3:

assumes $deadlock\ s$

and $thr\ s\ t = [(x, no\ wait\ locks)]$

shows $\forall w.\ wset\ s\ t \neq [PostWS\ w]$

$\langle proof \rangle$

lemma deadlock-def2:

$deadlock\ s \longleftrightarrow$

$(\forall t x. thr\ s\ t = \llbracket(x, no\text{-}wait\text{-}locks)\rrbracket \wedge \neg final\ x \wedge wset\ s\ t = None$
 $\longrightarrow t \vdash \langle x, shr\ s \rangle \wr \wedge (\forall LT. t \vdash \langle x, shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT. must\text{-}wait\ s\ t\ lt\ (dom\ (thr\ s))))$
 $\wedge (\forall t x\ ln. thr\ s\ t = \llbracket(x, ln)\rrbracket \wedge ln \neq no\text{-}wait\text{-}locks \wedge \neg waiting\ (wset\ s\ t)$
 $\longrightarrow (\exists l. ln\ \$\ l > 0 \wedge must\text{-}wait\ s\ t\ (Inl\ l)\ (dom\ (thr\ s))))$
 $\wedge (\forall t x\ w. thr\ s\ t = \llbracket(x, no\text{-}wait\text{-}locks)\rrbracket \longrightarrow wset\ s\ t \neq \llbracket PostWS\ WSNotified\rrbracket \wedge wset\ s\ t \neq \llbracket PostWS$
 $WSWokenUp\rrbracket)$
 $\langle proof \rangle$

lemma *all-waiting-implies-deadlock*:

assumes *lock-thread-ok* (*locks s*) (*thr s*)

and normal: $\bigwedge t x. \llbracket thr\ s\ t = \llbracket(x, no\text{-}wait\text{-}locks)\rrbracket; \neg final\ x; wset\ s\ t = None \rrbracket$

$\implies t \vdash \langle x, shr\ s \rangle \wr \wedge (\forall LT. t \vdash \langle x, shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT. must\text{-}wait\ s\ t\ lt\ (dom\ (thr\ s))))$

and acquire: $\bigwedge t x\ ln\ l. \llbracket thr\ s\ t = \llbracket(x, ln)\rrbracket; \neg waiting\ (wset\ s\ t); ln\ \$\ l > 0 \rrbracket$

$\implies \exists l'. ln\ \$\ l' > 0 \wedge \neg may\text{-}lock\ (locks\ s\ \$\ l')\ t$

and wakeup: $\bigwedge t x\ w. thr\ s\ t = \llbracket(x, no\text{-}wait\text{-}locks)\rrbracket \implies wset\ s\ t \neq \llbracket PostWS\ w\rrbracket$

shows *deadlock s*

$\langle proof \rangle$

lemma *mfinal-deadlock*:

mfinal s \implies *deadlock s*

$\langle proof \rangle$

Now deadlock for single threads

lemma *must-wait-mono*:

$(\bigwedge x. x \in A \longrightarrow x \in B) \implies must\text{-}wait\ s\ t\ lt\ A \longrightarrow must\text{-}wait\ s\ t\ lt\ B$

$\langle proof \rangle$

lemma *must-wait-mono'*:

$\llbracket must\text{-}wait\ s\ t\ lt\ A; A \subseteq B \rrbracket \implies must\text{-}wait\ s\ t\ lt\ B$

$\langle proof \rangle$

end

lemma *UN-mono*: $\llbracket x \in A \longrightarrow x \in A'; x \in B \longrightarrow x \in B' \rrbracket \implies x \in A \cup B \longrightarrow x \in A' \cup B'$

$\langle proof \rangle$

lemma *Collect-mono-conv* [*mono*]: $x \in \{x. P\ x\} \longleftrightarrow P\ x$

$\langle proof \rangle$

context *multithreaded-base* **begin**

coinductive-set *deadlocked* :: (*l, 't, 'x, 'm, 'w*) *state* \Rightarrow *'t set*

for *s* :: (*l, 't, 'x, 'm, 'w*) *state* **where**

deadlockedLock:

$\llbracket thr\ s\ t = \llbracket(x, no\text{-}wait\text{-}locks)\rrbracket; t \vdash \langle x, shr\ s \rangle \wr; wset\ s\ t = None;$

$\bigwedge LT. t \vdash \langle x, shr\ s \rangle LT \wr \implies \exists lt \in LT. must\text{-}wait\ s\ t\ lt\ (deadlocked\ s \cup final\text{-}threads\ s) \rrbracket$

$\implies t \in deadlocked\ s$

| *deadlockedWait*:

$\bigwedge ln. \llbracket thr\ s\ t = \llbracket(x, ln)\rrbracket; all\text{-}final\text{-}except\ s\ (deadlocked\ s); waiting\ (wset\ s\ t) \rrbracket \implies t \in deadlocked\ s$

| *deadlockedAcquire*:

$\bigwedge ln. \llbracket thr\ s\ t = \llbracket(x, ln)\rrbracket; \neg waiting\ (wset\ s\ t); ln\ \$\ l > 0; has\text{-}lock\ (locks\ s\ \$\ l)\ t'; t' \neq t;$

$$t' \in \text{deadlocked } s \vee \text{final-thread } s \ t' \]] \\ \implies t \in \text{deadlocked } s$$

monos *must-wait-mono UN-mono*

lemma *deadlockedAcquire-must-wait*:

$\bigwedge ln. \llbracket \text{thr } s \ t = \llbracket (x, ln) \rrbracket; \neg \text{waiting } (wset \ s \ t); ln \ \$ \ l > 0; \text{must-wait } s \ t \ (Inl \ l) \ (\text{deadlocked } s \cup \text{final-threads } s) \rrbracket$

$$\implies t \in \text{deadlocked } s$$

$\langle \text{proof} \rangle$

lemma *deadlocked-elim* [*consumes 1, case-names lock wait acquire*]:

assumes $t \in \text{deadlocked } s$

and lock: $\bigwedge x. \llbracket \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket; t \vdash \langle x, \text{shr } s \rangle \wr; wset \ s \ t = \text{None};$

$\bigwedge LT. t \vdash \langle x, \text{shr } s \rangle \text{LT } \wr \implies \exists lt \in LT. \text{must-wait } s \ t \ lt \ (\text{deadlocked } s \cup \text{final-threads } s) \rrbracket$
 $\implies \text{thesis}$

and wait: $\bigwedge x \ ln. \llbracket \text{thr } s \ t = \llbracket (x, ln) \rrbracket; \text{all-final-except } s \ (\text{deadlocked } s); \text{waiting } (wset \ s \ t) \rrbracket$

$$\implies \text{thesis}$$

and acquire: $\bigwedge x \ ln \ l \ t'.$

$\llbracket \text{thr } s \ t = \llbracket (x, ln) \rrbracket; \neg \text{waiting } (wset \ s \ t); 0 < ln \ \$ \ l; \text{has-lock } (\text{locks } s \ \$ \ l) \ t'; t \neq t';$
 $t' \in \text{deadlocked } s \vee \text{final-thread } s \ t' \rrbracket \implies \text{thesis}$

shows *thesis*

$\langle \text{proof} \rangle$

lemma *deadlocked-coinduct*

[*consumes 1, case-names deadlocked, case-conclusion deadlocked Lock Wait Acquire, coinduct set: deadlocked*]:

assumes *major*: $t \in X$

and step:

$\bigwedge t. t \in X \implies$

$(\exists x. \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket \wedge t \vdash \langle x, \text{shr } s \rangle \wr \wedge wset \ s \ t = \text{None} \wedge$

$(\forall LT. t \vdash \langle x, \text{shr } s \rangle \text{LT } \wr \longrightarrow (\exists lt \in LT. \text{must-wait } s \ t \ lt \ (X \cup \text{deadlocked } s \cup \text{final-threads } s))))$

\vee

$(\exists x \ ln. \text{thr } s \ t = \llbracket (x, ln) \rrbracket \wedge \text{all-final-except } s \ (X \cup \text{deadlocked } s) \wedge \text{waiting } (wset \ s \ t)) \vee$

$(\exists x \ l \ t' \ ln. \text{thr } s \ t = \llbracket (x, ln) \rrbracket \wedge \neg \text{waiting } (wset \ s \ t) \wedge 0 < ln \ \$ \ l \wedge \text{has-lock } (\text{locks } s \ \$ \ l) \ t' \wedge$

$t' \neq t \wedge ((t' \in X \vee t' \in \text{deadlocked } s) \vee \text{final-thread } s \ t'))$

shows $t \in \text{deadlocked } s$

$\langle \text{proof} \rangle$

definition *deadlocked'* :: (l, t, x, m, w) *state* $\implies \text{bool}$ **where**

$\text{deadlocked}' \ s \equiv (\forall t. \text{not-final-thread } s \ t \longrightarrow t \in \text{deadlocked } s)$

lemma *deadlocked'I*:

$(\bigwedge t. \text{not-final-thread } s \ t \implies t \in \text{deadlocked } s) \implies \text{deadlocked}' \ s$

$\langle \text{proof} \rangle$

lemma *deadlocked'D2*:

$\llbracket \text{deadlocked}' \ s; \text{not-final-thread } s \ t; t \in \text{deadlocked } s \implies \text{thesis} \rrbracket \implies \text{thesis}$

$\langle \text{proof} \rangle$

lemma *not-deadlocked'I*:

$\llbracket \text{not-final-thread } s \ t; t \notin \text{deadlocked } s \rrbracket \implies \neg \text{deadlocked}' \ s$

$\langle \text{proof} \rangle$

lemma *deadlocked'-intro*:

$\llbracket \forall t. \text{not-final-thread } s \ t \longrightarrow t \in \text{deadlocked } s \rrbracket \Longrightarrow \text{deadlocked}' \ s$
 <proof>

lemma *deadlocked-thread-exists*:

assumes $t \in \text{deadlocked } s$

and $\bigwedge x \ \text{ln}. \text{thr } s \ t = \llbracket (x, \text{ln}) \rrbracket \Longrightarrow \text{thesis}$

shows *thesis*

<proof>

end

context *multithreaded* **begin**

lemma *red-no-deadlock*:

assumes $P: s \text{-}t \triangleright ta \rightarrow s'$

and $\text{dead}: t \in \text{deadlocked } s$

shows *False*

<proof>

lemma *deadlocked'-no-red*:

$\llbracket s \text{-}t \triangleright ta \rightarrow s'; \text{deadlocked}' \ s \rrbracket \Longrightarrow \text{False}$

<proof>

lemma *not-final-thread-deadlocked-final-thread* [iff]:

$\text{thr } s \ t = \llbracket x \ \text{ln} \rrbracket \Longrightarrow \text{not-final-thread } s \ t \vee t \in \text{deadlocked } s \vee \text{final-thread } s \ t$

<proof>

lemma *all-waiting-deadlocked*:

assumes *not-final-thread* $s \ t$

and *lock-thread-ok* (*locks* s) (*thr* s)

and *normal*: $\bigwedge t \ x. \llbracket \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket; \neg \text{final } x; \text{wset } s \ t = \text{None} \rrbracket$

$\Longrightarrow t \vdash \langle x, \text{shr } s \rangle \wr \wedge (\forall LT. t \vdash \langle x, \text{shr } s \rangle \ LT \wr \longrightarrow (\exists lt \in LT. \text{must-wait } s \ t \ lt \ (\text{final-threads}$

$s)))$

and *acquire*: $\bigwedge t \ x \ \text{ln} \ l. \llbracket \text{thr } s \ t = \llbracket (x, \text{ln}) \rrbracket; \neg \text{waiting } (\text{wset } s \ t); \text{ln } \$ \ l > 0 \rrbracket$

$\Longrightarrow \exists l'. \text{ln } \$ \ l' > 0 \wedge \neg \text{may-lock } (\text{locks } s \ \$ \ l') \ t$

and *wakeup*: $\bigwedge t \ x \ w. \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket \Longrightarrow \text{wset } s \ t \neq \llbracket \text{PostWS } w \rrbracket$

shows $t \in \text{deadlocked } s$

<proof>

Equivalence proof for both notions of deadlock

lemma *deadlock-implies-deadlocked'*:

assumes *dead*: *deadlock* s

shows *deadlocked'* s

<proof>

lemma *deadlocked'-implies-deadlock*:

assumes *dead*: *deadlocked'* s

shows *deadlock* s

<proof>

lemma *deadlock-eq-deadlocked'*:

deadlock = *deadlocked'*

<proof>

lemma *deadlock-no-red*:

$\llbracket s \text{ -}t>ta \rightarrow s'; \text{ deadlock } s \rrbracket \implies \text{False}$
 $\langle \text{proof} \rangle$

lemma *deadlock-no-active-threads*:

assumes *dead*: *deadlock* *s*
shows *active-threads* *s* = {}
 $\langle \text{proof} \rangle$

end

locale *preserve-deadlocked* = *multithreaded final r convert-RA*

for *final* :: 'x \Rightarrow bool

and *r* :: ('l,'t,'x,'m,'w,'o) *semantics* ($\langle \text{-} \vdash \text{ -} \text{ -} \text{ -} \rightarrow \text{-} \rangle$ [50,0,0,50] 80)

and *convert-RA* :: 'l *released-locks* \Rightarrow 'o *list*

+

fixes *wf-state* :: ('l,'t,'x,'m,'w) *state set*

assumes *invariant3p-wf-state*: *invariant3p redT wf-state*

assumes *can-lock-preserved*:

$\llbracket s \in \text{wf-state}; s \text{ -}t>ta' \rightarrow s';$
 $\text{thr } s \text{ } t = \llbracket (x, \text{no-wait-locks}) \rrbracket; t \vdash \langle x, \text{shr } s \rangle \wr \rrbracket$
 $\implies t \vdash \langle x, \text{shr } s' \rangle \wr$

and *can-lock-devreserp*:

$\llbracket s \in \text{wf-state}; s \text{ -}t>ta' \rightarrow s';$
 $\text{thr } s \text{ } t = \llbracket (x, \text{no-wait-locks}) \rrbracket; t \vdash \langle x, \text{shr } s' \rangle L \wr \rrbracket$
 $\implies \exists L' \subseteq L. t \vdash \langle x, \text{shr } s \rangle L' \wr$

begin

lemma *redT-deadlocked-subset*:

assumes *wfs*: *s* \in *wf-state*

and *Red*: *s* $\text{-}t>ta \rightarrow s'$

shows *deadlocked* *s* \subseteq *deadlocked* *s'*

$\langle \text{proof} \rangle$

corollary *RedT-deadlocked-subset*:

assumes *wfs*: *s* \in *wf-state*

and *Red*: *s* $\text{-}t>ttas \rightarrow^* s'$

shows *deadlocked* *s* \subseteq *deadlocked* *s'*

$\langle \text{proof} \rangle$

end

end

1.13 Progress theorem for the multithreaded semantics

theory *FWProgress*

imports

FWDeadlock

begin

locale *progress* = *multithreaded final r convert-RA*

for *final* :: 'x \Rightarrow bool

and $r :: (\ell, t, x, m, w, o)$ semantics $(\langle - \vdash - \dashrightarrow - \rangle [50, 0, 0, 50] 80)$
and $\text{convert-RA} :: \ell \text{ released-locks} \Rightarrow o \text{ list}$
 $+$
fixes $\text{wf-state} :: (\ell, t, x, m, w)$ state set
assumes $\text{wf-stateD}: s \in \text{wf-state} \Rightarrow \text{lock-thread-ok} (\text{locks } s) (\text{thr } s) \wedge \text{wset-final-ok} (\text{wset } s) (\text{thr } s)$
and wf-red :
 $\llbracket s \in \text{wf-state}; \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket;$
 $t \vdash (x, \text{shr } s) -ta \rightarrow (x', m'); \neg \text{waiting} (\text{wset } s \ t) \rrbracket$
 $\Rightarrow \exists ta' x' m'. t \vdash (x, \text{shr } s) -ta' \rightarrow (x', m') \wedge (\text{actions-ok } s \ t \ ta' \vee \text{actions-ok}' s \ t \ ta' \wedge \text{actions-subset } ta' \ ta)$

and $\text{red-wait-set-not-final}$:
 $\llbracket s \in \text{wf-state}; \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket;$
 $t \vdash (x, \text{shr } s) -ta \rightarrow (x', m'); \neg \text{waiting} (\text{wset } s \ t); \text{Suspend } w \in \text{set } \{\{ta\}\}_w \rrbracket$
 $\Rightarrow \neg \text{final } x'$

and wf-progress :
 $\llbracket s \in \text{wf-state}; \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket; \neg \text{final } x \rrbracket$
 $\Rightarrow \exists ta' x' m'. t \vdash (x, \text{shr } s) -ta \rightarrow (x', m')$

and $\text{ta-Wakeup-no-join-no-lock-no-interrupt}$:
 $\llbracket s \in \text{wf-state}; \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket; t \vdash xm -ta \rightarrow xm'; \text{Notified} \in \text{set } \{\{ta\}\}_w \vee \text{WokenUp} \in \text{set } \{\{ta\}\}_w \rrbracket$
 $\Rightarrow \text{collect-waits } ta = \{\}$

and ta-satisfiable :
 $\llbracket s \in \text{wf-state}; \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket; t \vdash (x, \text{shr } s) -ta \rightarrow (x', m') \rrbracket$
 $\Rightarrow \exists s'. \text{actions-ok } s' \ t \ ta$

begin

lemma wf-redE :

assumes $s \in \text{wf-state}$ $\text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket$
and $t \vdash (x, \text{shr } s) -ta \rightarrow (x'', m'') \neg \text{waiting} (\text{wset } s \ t)$
obtains $ta' x' m'$
where $t \vdash (x, \text{shr } s) -ta' \rightarrow (x', m') \text{actions-ok}' s \ t \ ta' \text{actions-subset } ta' \ ta$
 $| \text{ta}' x' m' \text{ where } t \vdash (x, \text{shr } s) -ta' \rightarrow (x', m') \text{actions-ok } s \ t \ ta'$
 $\langle \text{proof} \rangle$

lemma wf-progressE :

assumes $s \in \text{wf-state}$
and $\text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket \neg \text{final } x$
obtains $ta' x' m'$ **where** $t \vdash (x, \text{shr } s) -ta \rightarrow (x', m')$
 $\langle \text{proof} \rangle$

lemma $\text{wf-progress-satisfiable}$:

$\llbracket s \in \text{wf-state}; \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket; \neg \text{final } x \rrbracket$
 $\Rightarrow \exists ta' x' m' s'. t \vdash (x, \text{shr } s) -ta \rightarrow (x', m') \wedge \text{actions-ok } s' \ t \ ta'$
 $\langle \text{proof} \rangle$

theorem redT-progress :

assumes $\text{wfs}: s \in \text{wf-state}$
and $\text{ndead}: \neg \text{deadlock } s$
shows $\exists t' ta' s'. s -t' \triangleright ta' \rightarrow s'$
 $\langle \text{proof} \rangle$

end

end

1.14 Lifting of thread-local properties to the multithreaded case

theory *FWLifting*

imports

FWWellform

begin

Lifting for properties that only involve thread-local state information and the shared memory.

definition

$ts\text{-}ok :: ('t \Rightarrow 'x \Rightarrow 'm \Rightarrow bool) \Rightarrow ('l, 't, 'x) \text{ thread-info} \Rightarrow 'm \Rightarrow bool$

where

$\bigwedge ln. ts\text{-}ok P ts m \equiv \forall t. \text{case } (ts t) \text{ of } None \Rightarrow True \mid [(x, ln)] \Rightarrow P t x m$

lemma *ts-okI*:

$\llbracket \bigwedge t x ln. ts t = [(x, ln)] \Longrightarrow P t x m \rrbracket \Longrightarrow ts\text{-}ok P ts m$

<proof>

lemma *ts-okE*:

$\llbracket ts\text{-}ok P ts m; \llbracket \bigwedge t x ln. ts t = [(x, ln)] \Longrightarrow P t x m \rrbracket \Longrightarrow Q \rrbracket \Longrightarrow Q$

<proof>

lemma *ts-okD*:

$\bigwedge ln. \llbracket ts\text{-}ok P ts m; ts t = [(x, ln)] \rrbracket \Longrightarrow P t x m$

<proof>

lemma *ts-ok-True* [*simp*]:

$ts\text{-}ok (\lambda t m x. True) ts m$

<proof>

lemma *ts-ok-conj*:

$ts\text{-}ok (\lambda t x m. P t x m \wedge Q t x m) = (\lambda ts m. ts\text{-}ok P ts m \wedge ts\text{-}ok Q ts m)$

<proof>

lemma *ts-ok-mono*:

$\llbracket ts\text{-}ok P ts m; \bigwedge t x. P t x m \Longrightarrow Q t x m \rrbracket \Longrightarrow ts\text{-}ok Q ts m$

<proof>

Lifting for properties, that also require additional data that does not change during execution

definition

$ts\text{-}inv :: ('i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool) \Rightarrow ('t \rightarrow 'i) \Rightarrow ('l, 't, 'x) \text{ thread-info} \Rightarrow 'm \Rightarrow bool$

where

$\bigwedge ln. ts\text{-}inv P I ts m \equiv \forall t. \text{case } (ts t) \text{ of } None \Rightarrow True \mid [(x, ln)] \Rightarrow \exists i. I t = [i] \wedge P i t x m$

lemma *ts-invI*:

$\llbracket \bigwedge t x ln. ts t = [(x, ln)] \Longrightarrow \exists i. I t = [i] \wedge P i t x m \rrbracket \Longrightarrow ts\text{-}inv P I ts m$

$\langle \text{proof} \rangle$

lemma *ts-invE*:

$\llbracket ts\text{-inv } P \ I \ ts \ m; \forall t \ x \ ln. \ ts \ t = \lfloor (x, \ ln) \rfloor \longrightarrow (\exists i. \ I \ t = \lfloor i \rfloor \wedge P \ i \ t \ x \ m) \Longrightarrow R \rrbracket \Longrightarrow R$
 $\langle \text{proof} \rangle$

lemma *ts-invD*:

$\bigwedge ln. \llbracket ts\text{-inv } P \ I \ ts \ m; \ ts \ t = \lfloor (x, \ ln) \rfloor \rrbracket \Longrightarrow \exists i. \ I \ t = \lfloor i \rfloor \wedge P \ i \ t \ x \ m$
 $\langle \text{proof} \rangle$

Wellformedness properties for lifting

definition

$ts\text{-inv-ok} :: ('l, 't, 'x) \text{ thread-info} \Rightarrow ('t \rightarrow 'i) \Rightarrow \text{bool}$

where

$ts\text{-inv-ok } ts \ I \equiv \forall t. \ ts \ t = \text{None} \longleftrightarrow I \ t = \text{None}$

lemma *ts-inv-okI*:

$(\bigwedge t. \ ts \ t = \text{None} \longleftrightarrow I \ t = \text{None}) \Longrightarrow ts\text{-inv-ok } ts \ I$
 $\langle \text{proof} \rangle$

lemma *ts-inv-okI2*:

$(\bigwedge t. (\exists v. \ ts \ t = \lfloor v \rfloor) \longleftrightarrow (\exists v. \ I \ t = \lfloor v \rfloor)) \Longrightarrow ts\text{-inv-ok } ts \ I$
 $\langle \text{proof} \rangle$

lemma *ts-inv-okE*:

$\llbracket ts\text{-inv-ok } ts \ I; \forall t. \ ts \ t = \text{None} \longleftrightarrow I \ t = \text{None} \Longrightarrow P \rrbracket \Longrightarrow P$
 $\langle \text{proof} \rangle$

lemma *ts-inv-okE2*:

$\llbracket ts\text{-inv-ok } ts \ I; \forall t. (\exists v. \ ts \ t = \lfloor v \rfloor) \longleftrightarrow (\exists v. \ I \ t = \lfloor v \rfloor) \Longrightarrow P \rrbracket \Longrightarrow P$
 $\langle \text{proof} \rangle$

lemma *ts-inv-okD*:

$ts\text{-inv-ok } ts \ I \Longrightarrow (ts \ t = \text{None}) \longleftrightarrow (I \ t = \text{None})$
 $\langle \text{proof} \rangle$

lemma *ts-inv-okD2*:

$ts\text{-inv-ok } ts \ I \Longrightarrow (\exists v. \ ts \ t = \lfloor v \rfloor) \longleftrightarrow (\exists v. \ I \ t = \lfloor v \rfloor)$
 $\langle \text{proof} \rangle$

lemma *ts-inv-ok-conv-dom-eq*:

$ts\text{-inv-ok } ts \ I \longleftrightarrow (\text{dom } ts = \text{dom } I)$
 $\langle \text{proof} \rangle$

lemma *ts-inv-ok-upd-ts*:

$\llbracket ts \ t = \lfloor x \rfloor; \ ts\text{-inv-ok } ts \ I \rrbracket \Longrightarrow ts\text{-inv-ok } (ts(t \mapsto x')) \ I$
 $\langle \text{proof} \rangle$

lemma *ts-inv-upd-map-option*:

assumes $ts\text{-inv } P \ I \ ts \ m$

and $\bigwedge x \ ln. \ ts \ t = \lfloor (x, \ ln) \rfloor \Longrightarrow P \ (the \ (I \ t)) \ t \ (fst \ (f \ (x, \ ln))) \ m$

shows $ts\text{-inv } P \ I \ (ts(t := (\text{map-option } f \ (ts \ t)))) \ m$

$\langle \text{proof} \rangle$

fun *upd-inv* :: ('t \rightarrow 'i) \Rightarrow ('i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool) \Rightarrow ('t,'x,'m) *new-thread-action* \Rightarrow ('t \rightarrow 'i)
where
upd-inv I P (NewThread t x m) = I(t \mapsto SOME i. P i t x m)
| *upd-inv* I P - = I

fun *upd-invs* :: ('t \rightarrow 'i) \Rightarrow ('i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool) \Rightarrow ('t,'x,'m) *new-thread-action list* \Rightarrow ('t \rightarrow 'i)
where
upd-invs I P [] = I
| *upd-invs* I P (ta#tas) = *upd-invs* (*upd-inv* I P ta) P tas

lemma *upd-invs-append* [simp]:
upd-invs I P (xs @ ys) = *upd-invs* (*upd-invs* I P xs) P ys
<proof>

lemma *ts-inv-ok-upd-inv'*:
ts-inv-ok ts I \Longrightarrow *ts-inv-ok* (redT-updT' ts ta) (*upd-inv* I P ta)
<proof>

lemma *ts-inv-ok-upd-invs'*:
ts-inv-ok ts I \Longrightarrow *ts-inv-ok* (redT-updTs' ts tas) (*upd-invs* I P tas)
<proof>

lemma *ts-inv-ok-upd-inv*:
ts-inv-ok ts I \Longrightarrow *ts-inv-ok* (redT-updT ts ta) (*upd-inv* I P ta)
<proof>

lemma *ts-inv-ok-upd-invs*:
ts-inv-ok ts I \Longrightarrow *ts-inv-ok* (redT-updTs ts tas) (*upd-invs* I P tas)
<proof>

lemma *ts-inv-ok-inv-ext-upd-inv*:
[[*ts-inv-ok* ts I; *thread-ok* ts ta]] \Longrightarrow I \subseteq_m *upd-inv* I P ta
<proof>

lemma *ts-inv-ok-inv-ext-upd-invs*:
[[*ts-inv-ok* ts I; *thread-oks* ts tas]]
 \Longrightarrow I \subseteq_m *upd-invs* I P tas
<proof>

lemma *upd-invs-Some*:
[[*thread-oks* ts tas; I t = [i]; ts t = [x]]] \Longrightarrow *upd-invs* I Q tas t = [i]
<proof>

lemma *upd-inv-Some-eq*:
[[*thread-ok* ts ta; ts t = [x]]] \Longrightarrow *upd-inv* I Q ta t = I t
<proof>

lemma *upd-invs-Some-eq*: [[*thread-oks* ts tas; ts t = [x]]] \Longrightarrow *upd-invs* I Q tas t = I t
<proof>

lemma *SOME-new-thread-upd-invs*:
assumes Qsome: Q (SOME i. Q i t x m) t x m
and nt: NewThread t x m \in set tas

and *cct: thread-oks ts tas*
shows $\exists i. \text{upd-invs } I \ Q \ \text{tas } t = [i] \wedge Q \ i \ t \ x \ m$
 $\langle \text{proof} \rangle$

lemma *ts-ok-into-ts-inv-const:*
assumes *ts-ok P ts m*
obtains *I where ts-inv ($\lambda \cdot$. P) I ts m*
 $\langle \text{proof} \rangle$

lemma *ts-inv-const-into-ts-ok:*
 $ts\text{-inv } (\lambda \cdot. P) \ I \ ts \ m \implies ts\text{-ok } P \ ts \ m$
 $\langle \text{proof} \rangle$

lemma *ts-inv-into-ts-ok-Ex:*
 $ts\text{-inv } Q \ I \ ts \ m \implies ts\text{-ok } (\lambda t \ x \ m. \exists i. Q \ i \ t \ x \ m) \ ts \ m$
 $\langle \text{proof} \rangle$

lemma *ts-ok-Ex-into-ts-inv:*
 $ts\text{-ok } (\lambda t \ x \ m. \exists i. Q \ i \ t \ x \ m) \ ts \ m \implies \exists I. ts\text{-inv } Q \ I \ ts \ m$
 $\langle \text{proof} \rangle$

lemma *Ex-ts-inv-conv-ts-ok:*
 $(\exists I. ts\text{-inv } Q \ I \ ts \ m) \longleftrightarrow (ts\text{-ok } (\lambda t \ x \ m. \exists i. Q \ i \ t \ x \ m) \ ts \ m)$
 $\langle \text{proof} \rangle$

end

1.15 Labelled transition systems

theory *LTS*

imports

../Basic/Auxiliary
Coinductive.TLList

begin

unbundle *no floor-ceiling-syntax*

lemma *rel-option-mono:*
 $\llbracket rel\text{-option } R \ x \ y; \bigwedge x \ y. R \ x \ y \implies R' \ x \ y \rrbracket \implies rel\text{-option } R' \ x \ y$
 $\langle \text{proof} \rangle$

lemma *nth-concat-conv:*
 $n < length \ (concat \ xss)$
 $\implies \exists m \ n'. concat \ xss \ ! \ n = (xss \ ! \ m) \ ! \ n' \wedge n' < length \ (xss \ ! \ m) \wedge$
 $m < length \ xss \wedge n = (\sum_{i < m. length \ (xss \ ! \ i)} + n')$
 $\langle \text{proof} \rangle$

definition *flip* :: $('a \Rightarrow 'b \Rightarrow 'c) \Rightarrow 'b \Rightarrow 'a \Rightarrow 'c$
where *flip* $f = (\lambda b \ a. f \ a \ b)$

Create a dynamic list *flip-simps* of theorems for *flip*

$\langle ML \rangle$

lemma *flip-conv* [*flip-simps*]: $flip\ f\ b\ a = f\ a\ b$
 ⟨*proof*⟩

lemma *flip-flip* [*flip-simps*, *simp*]: $flip\ (flip\ f) = f$
 ⟨*proof*⟩

lemma *list-all2-flip* [*flip-simps*]: $list\ all2\ (flip\ P)\ xs\ ys = list\ all2\ P\ ys\ xs$
 ⟨*proof*⟩

lemma *llist-all2-flip* [*flip-simps*]: $llist\ all2\ (flip\ P)\ xs\ ys = llist\ all2\ P\ ys\ xs$
 ⟨*proof*⟩

lemma *rtranclp-flipD*:
 assumes $(flip\ r)^{**}\ x\ y$
 shows $r^{**}\ y\ x$
 ⟨*proof*⟩

lemma *rtranclp-flip* [*flip-simps*]:
 $(flip\ r)^{**} = flip\ r^{**}$
 ⟨*proof*⟩

lemma *rel-prod-flip* [*flip-simps*]:
 $rel\ prod\ (flip\ R)\ (flip\ S) = flip\ (rel\ prod\ R\ S)$
 ⟨*proof*⟩

lemma *rel-option-flip* [*flip-simps*]:
 $rel\ option\ (flip\ R) = flip\ (rel\ option\ R)$
 ⟨*proof*⟩

lemma *tl-list-all2-flip* [*flip-simps*]:
 $tl\ list\ all2\ (flip\ P)\ (flip\ Q)\ xs\ ys \longleftrightarrow tl\ list\ all2\ P\ Q\ ys\ xs$
 ⟨*proof*⟩

1.15.1 Labelled transition systems

type-synonym $(\ 'a, \ 'b)\ trsys = \ 'a \Rightarrow \ 'b \Rightarrow \ 'a \Rightarrow \ bool$

locale *trsys* =
 fixes $trsys :: (\ 's, \ 'tl)\ trsys\ (\ \leftarrow / \ \longrightarrow / \ \rightarrow\ [50, 0, 50]\ 60)$
begin

abbreviation $Trsys :: (\ 's, \ 'tl\ list)\ trsys\ (\ \leftarrow / \ \longrightarrow^* / \ \rightarrow\ [50, 0, 50]\ 60)$
where $\bigwedge tl. s -tl \rightarrow^* s' \equiv rtrancl3p\ trsys\ s\ tl\ s'$

coinductive *inf-step* :: $\ 's \Rightarrow \ 'tl\ llist \Rightarrow \ bool\ (\ \leftarrow \ \longrightarrow^* \ \infty\ [50, 0]\ 80)$
where $inf\ stepI: \llbracket trsys\ a\ b\ a';\ a' -bs \rightarrow^* \infty \rrbracket \Longrightarrow a -LCons\ b\ bs \rightarrow^* \infty$

coinductive *inf-step-table* :: $\ 's \Rightarrow (\ 's \times \ 'tl \times \ 's)\ llist \Rightarrow \ bool\ (\ \leftarrow \ \longrightarrow^* t \ \infty\ [50, 0]\ 80)$
where

inf-step-tableI:
 $\bigwedge tl. \llbracket trsys\ s\ tl\ s';\ s' -stls \rightarrow^* t \ \infty \rrbracket$
 $\Longrightarrow s -LCons\ (s, tl, s')\ stls \rightarrow^* t \ \infty$

definition *inf-step2inf-step-table* :: $\ 's \Rightarrow \ 'tl\ llist \Rightarrow (\ 's \times \ 'tl \times \ 's)\ llist$

where

inf-step2inf-step-table s tls =
unfold-llist
 $(\lambda(s, tls). \text{lnull } tls)$
 $(\lambda(s, tls). (s, \text{lhs } tls, \text{SOME } s'. \text{trsys } s (\text{lhs } tls) s' \wedge s' -\text{ttl } tls \rightarrow^* \infty))$
 $(\lambda(s, tls). (\text{SOME } s'. \text{trsys } s (\text{lhs } tls) s' \wedge s' -\text{ttl } tls \rightarrow^* \infty, \text{ttl } tls))$
 (s, tls)

coinductive *Rtrancl3p* :: ' s \Rightarrow (' tl , ' s) *tllist* \Rightarrow *bool*

where

Rtrancl3p-stop: $(\bigwedge tl s'. \neg s -tl \rightarrow s') \Longrightarrow \text{Rtrancl3p } s (\text{TNil } s)$
| *Rtrancl3p-into-Rtrancl3p*: $\bigwedge tl. \llbracket s -tl \rightarrow s'; \text{Rtrancl3p } s' \text{ } tlss \rrbracket \Longrightarrow \text{Rtrancl3p } s (\text{TCons } tl \text{ } tlss)$

inductive-simps *Rtrancl3p-simps*:

Rtrancl3p $s (\text{TNil } s')$
Rtrancl3p $s (\text{TCons } tl' \text{ } tlss)$

inductive-cases *Rtrancl3p-cases*:

Rtrancl3p $s (\text{TNil } s')$
Rtrancl3p $s (\text{TCons } tl' \text{ } tlss)$

coinductive *Runs* :: ' s \Rightarrow ' tl *llist* \Rightarrow *bool*

where

Stuck: $(\bigwedge tl s'. \neg s -tl \rightarrow s') \Longrightarrow \text{Runs } s \text{ LNil}$
| *Step*: $\bigwedge tl. \llbracket s -tl \rightarrow s'; \text{Runs } s' \text{ } tll \rrbracket \Longrightarrow \text{Runs } s (\text{LCons } tl \text{ } tll)$

coinductive *Runs-table* :: ' s \Rightarrow (' s \times ' tl \times ' s) *llist* \Rightarrow *bool*

where

Stuck: $(\bigwedge tl s'. \neg s -tl \rightarrow s') \Longrightarrow \text{Runs-table } s \text{ LNil}$
| *Step*: $\bigwedge tl. \llbracket s -tl \rightarrow s'; \text{Runs-table } s' \text{ } stlls \rrbracket \Longrightarrow \text{Runs-table } s (\text{LCons } (s, tl, s') \text{ } stlls)$

inductive-simps *Runs-table-simps*:

Runs-table $s \text{ LNil}$
Runs-table $s (\text{LCons } stls \text{ } stlls)$

lemma *inf-step-not-finite-llist*:

assumes $r: s -bs \rightarrow^* \infty$

shows $\neg \text{lfinit } bs$

<proof>

lemma *inf-step2inf-step-table-LNil* [*simp*]: *inf-step2inf-step-table* $s \text{ LNil} = \text{LNil}$

<proof>

lemma *inf-step2inf-step-table-LCons* [*simp*]:

fixes tl **shows**

inf-step2inf-step-table $s (\text{LCons } tl \text{ } tll) =$
 $\text{LCons } (s, tl, \text{SOME } s'. \text{trsys } s \text{ } tl \text{ } s' \wedge s' -tll \rightarrow^* \infty)$
 $(\text{inf-step2inf-step-table } (\text{SOME } s'. \text{trsys } s \text{ } tl \text{ } s' \wedge s' -tll \rightarrow^* \infty) \text{ } tll)$

<proof>

lemma *lnull-inf-step2inf-step-table* [*simp*]:

lnull (*inf-step2inf-step-table* $s \text{ } tll$) $\longleftrightarrow \text{lnull } tll$

<proof>

lemma *inf-step2inf-step-table-eq-LNil*:
 $inf\text{-step2inf-step-table } s \text{ } tls = LNil \longleftrightarrow tls = LNil$
 ⟨proof⟩

lemma *lhd-inf-step2inf-step-table [simp]*:
 $\neg lnull \text{ } tls$
 $\implies lhd (inf\text{-step2inf-step-table } s \text{ } tls) =$
 $(s, lhd \text{ } tls, SOME \text{ } s'. \text{ } trsys \text{ } s (lhd \text{ } tls) \text{ } s' \wedge s' -ltl \text{ } tls \rightarrow * \infty)$
 ⟨proof⟩

lemma *ltl-inf-step2inf-step-table [simp]*:
 $ltl (inf\text{-step2inf-step-table } s \text{ } tls) =$
 $inf\text{-step2inf-step-table } (SOME \text{ } s'. \text{ } trsys \text{ } s (lhd \text{ } tls) \text{ } s' \wedge s' -ltl \text{ } tls \rightarrow * \infty) (ltl \text{ } tls)$
 ⟨proof⟩

lemma *lmap-inf-step2inf-step-table*: $lmap (fst \circ snd) (inf\text{-step2inf-step-table } s \text{ } tls) = tls$
 ⟨proof⟩

lemma *inf-step-imp-inf-step-table*:
assumes $s -tls \rightarrow * \infty$
shows $\exists stls. s -stls \rightarrow * \infty \wedge tls = lmap (fst \circ snd) \text{ } stls$
 ⟨proof⟩

lemma *inf-step-table-imp-inf-step*:
 $s -stls \rightarrow * \infty \implies s -lmap (fst \circ snd) \text{ } stls \rightarrow * \infty$
 ⟨proof⟩

lemma *Runs-table-into-Runs*:
 $Runs\text{-table } s \text{ } stlss \implies Runs \text{ } s (lmap (\lambda(s, tl, s'). \text{ } tl) \text{ } stlss)$
 ⟨proof⟩

lemma *Runs-into-Runs-table*:
assumes $Runs \text{ } s \text{ } tls$
obtains $stlss$
where $tls = lmap (\lambda(s, tl, s'). \text{ } tl) \text{ } stlss$
and $Runs\text{-table } s \text{ } stlss$
 ⟨proof⟩

lemma *Runs-lappendE*:
assumes $Runs \text{ } \sigma (lappend \text{ } tls \text{ } tls')$
and $lfinite \text{ } tls$
obtains σ' **where** $\sigma -list\text{-of } tls \rightarrow * \sigma'$
and $Runs \text{ } \sigma' \text{ } tls'$
 ⟨proof⟩

lemma *Trsys-into-Runs*:
assumes $s -tls \rightarrow * s'$
and $Runs \text{ } s' \text{ } tls'$
shows $Runs \text{ } s (lappend (l\text{list-of } tls) \text{ } tls')$
 ⟨proof⟩

lemma *rtrancl3p-into-Rtrancl3p*:
 $\llbracket rtrancl3p \text{ } trsys \text{ } a \text{ } bs \text{ } a'; \bigwedge b \text{ } a''. \neg a' -b \rightarrow a'' \rrbracket \implies Rtrancl3p \text{ } a (tllist\text{-of-l\text{list } a' (l\text{list-of } bs))$
 ⟨proof⟩

lemma *Rtrancl3p-into-Runs*:

$Rtrancl3p\ s\ tss \implies Runs\ s\ (l\text{-}list\text{-}of\text{-}t\text{-}list\ tss)$
 $\langle proof \rangle$

lemma *Runs-into-Rtrancl3p*:

assumes $Runs\ s\ tss$
obtains tss **where** $tss = l\text{-}list\text{-}of\text{-}t\text{-}list\ tss\ Rtrancl3p\ s\ tss$
 $\langle proof \rangle$

lemma *fixes tl*

assumes $Rtrancl3p\ s\ tss\ t\text{-}finite\ tss$
shows $Rtrancl3p\text{-}into\text{-}Trsys: Trsys\ s\ (l\text{-}list\text{-}of\ (l\text{-}list\text{-}of\text{-}t\text{-}list\ tss))\ (terminal\ tss)$
and $terminal\text{-}Rtrancl3p\text{-}final: \neg terminal\ tss\ \text{-}tl \rightarrow s'$
 $\langle proof \rangle$

end

1.15.2 Labelled transition systems with internal actions

locale $\tau trsys = trsys +$

constrains $trsys :: ('s, 'tl)\ trsys$
fixes $\tau move :: ('s, 'tl)\ trsys$

begin

inductive $silent\text{-}move :: 's \Rightarrow 's \Rightarrow bool\ (\langle\text{-}\ \text{-}\tau \rightarrow \text{-}\ \rangle [50, 50]\ 60)$
where $[intro]: !!tl. \llbracket trsys\ s\ tl\ s'; \tau move\ s\ tl\ s' \rrbracket \implies s\ \text{-}\tau \rightarrow s'$

declare $silent\text{-}move.cases\ [elim]$

lemma $silent\text{-}move\text{-}iff: silent\text{-}move = (\lambda s\ s'. (\exists tl. trsys\ s\ tl\ s' \wedge \tau move\ s\ tl\ s'))$
 $\langle proof \rangle$

abbreviation $silent\text{-}moves :: 's \Rightarrow 's \Rightarrow bool\ (\langle\text{-}\ \text{-}\tau \rightarrow * \text{-}\ \rangle [50, 50]\ 60)$
where $silent\text{-}moves == silent\text{-}move\ \widehat{\text{-}}^{**}$

abbreviation $silent\text{-}movet :: 's \Rightarrow 's \Rightarrow bool\ (\langle\text{-}\ \text{-}\tau \rightarrow + \text{-}\ \rangle [50, 50]\ 60)$
where $silent\text{-}movet == silent\text{-}move\ \widehat{\text{-}}^{++}$

coinductive $\tau\text{-}diverge :: 's \Rightarrow bool\ (\langle\text{-}\ \text{-}\tau \rightarrow \infty \rangle [50]\ 60)$
where

$\tau\text{-}divergeI: \llbracket s\ \text{-}\tau \rightarrow s'; s'\ \text{-}\tau \rightarrow \infty \rrbracket \implies s\ \text{-}\tau \rightarrow \infty$

coinductive $\tau\text{-}inf\text{-}step :: 's \Rightarrow 'tl\ llist \Rightarrow bool\ (\langle\text{-}\ \text{-}\tau \text{-}\text{-}\rightarrow * \infty \rangle [50, 0]\ 60)$

where

$\tau\text{-}inf\text{-}step\text{-}Cons: \bigwedge tl. \llbracket s\ \text{-}\tau \rightarrow * s'; s'\ \text{-}tl \rightarrow s''; \neg \tau move\ s'\ tl\ s''; s''\ \text{-}\tau\text{-}tls \rightarrow * \infty \rrbracket \implies s\ \text{-}\tau\text{-}LCons\ tl\ tls \rightarrow * \infty$

$\tau\text{-}inf\text{-}step\text{-}Nil: s\ \text{-}\tau \rightarrow \infty \implies s\ \text{-}\tau\text{-}LNil \rightarrow * \infty$

coinductive $\tau\text{-}inf\text{-}step\text{-}table :: 's \Rightarrow ('s \times 's \times 'tl \times 's)\ llist \Rightarrow bool\ (\langle\text{-}\ \text{-}\tau \text{-}\text{-}\rightarrow * t \infty \rangle [50, 0]\ 80)$

where

$\tau\text{-}inf\text{-}step\text{-}table\text{-}Cons:$
 $\bigwedge tl. \llbracket s\ \text{-}\tau \rightarrow * s'; s'\ \text{-}tl \rightarrow s''; \neg \tau move\ s'\ tl\ s''; s''\ \text{-}\tau\text{-}tls \rightarrow * t \infty \rrbracket \implies s\ \text{-}\tau\text{-}LCons\ (s, s', tl, s'')\ tls \rightarrow * t \infty$

| τ inf-step-table-Nil:
 $s -\tau \rightarrow \infty \implies s -\tau -LNil \rightarrow *t \infty$

definition τ inf-step2 τ inf-step-table :: 's \Rightarrow 'tl llist \Rightarrow ('s \times 's \times 'tl \times 's) llist

where

τ inf-step2 τ inf-step-table s tls =
 unfold-llist
 ($\lambda(s, tls). \text{Inull } tls$)
 ($\lambda(s, tls). \text{let } (s', s'') = \text{SOME } (s', s''). s -\tau \rightarrow * s' \wedge s' -\text{lhd } tls \rightarrow s'' \wedge \neg \tau \text{move } s' (\text{lhd } tls) s'' \wedge s'' -\tau -\text{ttl } tls \rightarrow * \infty$)
 in (s, s', lhd tls, s'')
 ($\lambda(s, tls). \text{let } (s', s'') = \text{SOME } (s', s''). s -\tau \rightarrow * s' \wedge s' -\text{lhd } tls \rightarrow s'' \wedge \neg \tau \text{move } s' (\text{lhd } tls) s'' \wedge s'' -\tau -\text{ttl } tls \rightarrow * \infty$)
 in (s'', ttl tls)
 (s, tls)

definition silent-move-from :: 's \Rightarrow 's \Rightarrow 's \Rightarrow bool

where silent-move-from s0 s1 s2 \longleftrightarrow silent-moves s0 s1 \wedge silent-move s1 s2

inductive τ rtrancl3p :: 's \Rightarrow 'tl list \Rightarrow 's \Rightarrow bool ($\langle \cdot -\tau \dashrightarrow * \rightarrow [50, 0, 50] 60$)

where

τ rtrancl3p-refl: τ rtrancl3p s \square s
 | τ rtrancl3p-step: $\bigwedge tl. \llbracket s -tl \rightarrow s'; \neg \tau \text{move } s \text{ tl } s'; \tau$ rtrancl3p s' tls s'' $\rrbracket \implies \tau$ rtrancl3p s (tl # tls) s''
 | τ rtrancl3p- τ step: $\bigwedge tl. \llbracket s -tl \rightarrow s'; \tau \text{move } s \text{ tl } s'; \tau$ rtrancl3p s' tls s'' $\rrbracket \implies \tau$ rtrancl3p s tls s''

coinductive τ Runs :: 's \Rightarrow ('tl, 's option) tllist \Rightarrow bool ($\langle \cdot \Downarrow \rightarrow [50, 50] 51$)

where

Terminate: $\llbracket s -\tau \rightarrow * s'; \bigwedge tl s''. \neg s' -tl \rightarrow s'' \rrbracket \implies s \Downarrow TNil [s']$
 | Diverge: $s -\tau \rightarrow \infty \implies s \Downarrow TNil \text{None}$
 | Proceed: $\bigwedge tl. \llbracket s -\tau \rightarrow * s'; s' -tl \rightarrow s''; \neg \tau \text{move } s' \text{ tl } s''; s'' \Downarrow tls \rrbracket \implies s \Downarrow TCons \text{tl } tls$

inductive-simps τ Runs-simps:

$s \Downarrow TNil (\text{Some } s')$
 $s \Downarrow TNil \text{None}$
 $s \Downarrow TCons \text{tl}' tls$

coinductive τ Runs-table :: 's \Rightarrow ('tl \times 's, 's option) tllist \Rightarrow bool

where

Terminate: $\llbracket s -\tau \rightarrow * s'; \bigwedge tl s''. \neg s' -tl \rightarrow s'' \rrbracket \implies \tau$ Runs-table s (TNil [s'])
 | Diverge: $s -\tau \rightarrow \infty \implies \tau$ Runs-table s (TNil None)
 | Proceed:
 $\bigwedge tl. \llbracket s -\tau \rightarrow * s'; s' -tl \rightarrow s''; \neg \tau \text{move } s' \text{ tl } s''; \tau$ Runs-table s'' tls \rrbracket
 $\implies \tau$ Runs-table s (TCons (tl, s'') tls)

definition silent-move2 :: 's \Rightarrow 'tl \Rightarrow 's \Rightarrow bool

where $\bigwedge tl. \text{silent-move2 } s \text{ tl } s' \longleftrightarrow s -tl \rightarrow s' \wedge \tau \text{move } s \text{ tl } s'$

abbreviation silent-moves2 :: 's \Rightarrow 'tl list \Rightarrow 's \Rightarrow bool

where silent-moves2 \equiv rtrancl3p silent-move2

coinductive τ Runs-table2 :: 's \Rightarrow ('tl list \times 's \times 'tl \times 's, ('tl list \times 's) + 'tl llist) tllist \Rightarrow bool

where

Terminate: $\llbracket \text{silent-moves2 } s \text{ tls } s'; \bigwedge tl s''. \neg s' -tl \rightarrow s'' \rrbracket \implies \tau$ Runs-table2 s (TNil (Inl (tls, s')))

| *Diverge*: $\text{trsys.inf-step silent-move2 } s \text{ tls} \implies \tau\text{Runs-table2 } s \text{ (TNil (Inr tls))}$

| *Proceed*:

$\wedge \text{tl. } \llbracket \text{silent-moves2 } s \text{ tls } s'; s' - \text{tl} \rightarrow s''; \neg \tau\text{move } s' \text{ tl } s''; \tau\text{Runs-table2 } s'' \text{ tlstlss} \rrbracket$
 $\implies \tau\text{Runs-table2 } s \text{ (TCons (tls, } s', \text{ tl, } s'') \text{ tlstlss)}$

inductive-simps $\tau\text{Runs-table2-simps}$:

$\tau\text{Runs-table2 } s \text{ (TNil tlls)}$

$\tau\text{Runs-table2 } s \text{ (TCons tlstls tlstlss)}$

lemma *inf-step-table-all- τ -into- τ diverge*:

$\llbracket s - \text{stls} \rightarrow^* t \infty; \forall (s, \text{tl}, s') \in \text{lset stls. } \tau\text{move } s \text{ tl } s' \rrbracket \implies s - \tau \rightarrow \infty$
 $\langle \text{proof} \rangle$

lemma *inf-step-table-lappend-llist-ofD*:

$s - \text{lappend (llist-of stls) (LCons (x, tl', x') xs)} \rightarrow^* t \infty$

$\implies (s - \text{map (fst } \circ \text{ snd) stls} \rightarrow^* x) \wedge (x - \text{LCons (x, tl', x') xs} \rightarrow^* t \infty)$

$\langle \text{proof} \rangle$

lemma *inf-step-table-lappend-llist-of- τ -into- τ moves*:

assumes lfinite stls

shows $\llbracket s - \text{lappend stls (LCons (x, tl' x') xs)} \rightarrow^* t \infty; \forall (s, \text{tl}, s') \in \text{lset stls. } \tau\text{move } s \text{ tl } s' \rrbracket \implies s - \tau \rightarrow^* x$

$\langle \text{proof} \rangle$

lemma *inf-step-table-into- τ inf-step*:

$s - \text{stls} \rightarrow^* t \infty \implies s - \tau - \text{lmap (fst } \circ \text{ snd) (lfilter } (\lambda(s, \text{tl}, s'). \neg \tau\text{move } s \text{ tl } s') \text{ stls)} \rightarrow^* \infty$
 $\langle \text{proof} \rangle$

lemma *inf-step-into- τ inf-step*:

assumes $s - \text{tls} \rightarrow^* \infty$

shows $\exists A. s - \tau - \text{lnths tls } A \rightarrow^* \infty$

$\langle \text{proof} \rangle$

lemma *silent-moves-into- τ rtrancl3p*:

$s - \tau \rightarrow^* s' \implies s - \tau - \square \rightarrow^* s'$
 $\langle \text{proof} \rangle$

lemma *τ rtrancl3p-into-silent-moves*:

$s - \tau - \square \rightarrow^* s' \implies s - \tau \rightarrow^* s'$
 $\langle \text{proof} \rangle$

lemma *τ rtrancl3p-Nil-eq- τ moves*:

$s - \tau - \square \rightarrow^* s' \iff s - \tau \rightarrow^* s'$
 $\langle \text{proof} \rangle$

lemma *τ rtrancl3p-trans [trans]*:

$\llbracket s - \tau - \text{tls} \rightarrow^* s'; s' - \tau - \text{tls}' \rightarrow^* s'' \rrbracket \implies s - \tau - \text{tls} @ \text{tls}' \rightarrow^* s''$
 $\langle \text{proof} \rangle$

lemma *τ rtrancl3p-SingletonE*:

fixes tl

assumes $\text{red: } s - \tau - [\text{tl}] \rightarrow^* s'''$

obtains $s' s''$ **where** $s - \tau \rightarrow^* s' s' - \text{tl} \rightarrow s'' \neg \tau\text{move } s' \text{ tl } s'' s'' - \tau \rightarrow^* s'''$

<proof>

lemma $\tau\text{rtrancl3p-snocI}$:

$\bigwedge tl. \llbracket \tau\text{rtrancl3p } s \text{ tls } s''; s'' -\tau \rightarrow^* s'''; s''' -tl \rightarrow s'; \neg \tau\text{move } s''' \text{ tl } s' \rrbracket$
 $\implies \tau\text{rtrancl3p } s \text{ (tls @ [tl]) } s'$

<proof>

lemma $\tau\text{diverge-rtranclp-silent-move}$:

$\llbracket \text{silent-move}^{\wedge**} s s'; s' -\tau \rightarrow \infty \rrbracket \implies s -\tau \rightarrow \infty$

<proof>

lemma $\tau\text{diverge-trancl-coinduct}$ [consumes 1, case-names $\tau\text{diverge}$]:

assumes $X: X s$

and step: $\bigwedge s. X s \implies \exists s'. \text{silent-move}^{\wedge++} s s' \wedge (X s' \vee s' -\tau \rightarrow \infty)$

shows $s -\tau \rightarrow \infty$

<proof>

lemma $\tau\text{diverge-trancl-measure-coinduct}$ [consumes 2, case-names $\tau\text{diverge}$]:

assumes major: $X s t \text{ wfP } \mu$

and step: $\bigwedge s t. X s t \implies \exists s' t'. (\mu t' t \wedge s' = s \vee \text{silent-move}^{\wedge++} s s') \wedge (X s' t' \vee s' -\tau \rightarrow \infty)$

shows $s -\tau \rightarrow \infty$

<proof>

lemma $\tau\text{inf-step2}\tau\text{inf-step-table-LNil}$ [simp]: $\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ LNil} = \text{LNil}$

<proof>

lemma $\tau\text{inf-step2}\tau\text{inf-step-table-LCons}$ [simp]:

fixes $s \text{ tl } ss \text{ tls}$

defines $ss \equiv \text{SOME } (s', s''). s -\tau \rightarrow^* s' \wedge s' -tl \rightarrow s'' \wedge \neg \tau\text{move } s' \text{ tl } s'' \wedge s'' -\tau -\text{tls} \rightarrow^* \infty$

shows

$\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ (LCons tl tls)} =$

$\text{LCons } (s, \text{fst } ss, \text{tl}, \text{snd } ss) (\tau\text{inf-step2}\tau\text{inf-step-table } (\text{snd } ss) \text{ tls})$

<proof>

lemma $\text{lnull-}\tau\text{inf-step2}\tau\text{inf-step-table}$ [simp]:

$\text{lnull } (\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ tls}) \longleftrightarrow \text{lnull } \text{tls}$

<proof>

lemma $\text{lhd-}\tau\text{inf-step2}\tau\text{inf-step-table}$ [simp]:

$\neg \text{lnull } \text{tls} \implies \text{lhd } (\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ tls}) =$

$(\text{let } (s', s'') = \text{SOME } (s', s''). s -\tau \rightarrow^* s' \wedge s' -\text{lhd } \text{tls} \rightarrow s'' \wedge \neg \tau\text{move } s' \text{ (lhd } \text{tls}) } s'' \wedge s'' -\tau -\text{ttl}$
 $\text{tls} \rightarrow^* \infty$

$\text{in } (s, s', \text{lhd } \text{tls}, s''))$

<proof>

lemma $\text{ttl-}\tau\text{inf-step2}\tau\text{inf-step-table}$ [simp]:

$\neg \text{lnull } \text{tls} \implies \text{ttl } (\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ tls}) =$

$(\text{let } (s', s'') = \text{SOME } (s', s''). s -\tau \rightarrow^* s' \wedge s' -\text{lhd } \text{tls} \rightarrow s'' \wedge \neg \tau\text{move } s' \text{ (lhd } \text{tls}) } s'' \wedge s'' -\tau -\text{ttl}$
 $\text{tls} \rightarrow^* \infty$

$\text{in } \tau\text{inf-step2}\tau\text{inf-step-table } s'' \text{ (ttl } \text{tls}))$

<proof>

lemma $\text{lmap-}\tau\text{inf-step2}\tau\text{inf-step-table}$: $\text{lmap } (\text{fst} \circ \text{snd} \circ \text{snd}) (\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ tls}) = \text{tls}$

<proof>

lemma τ inf-step-into- τ inf-step-table:

$s -\tau-tls \rightarrow^* \infty \implies s -\tau-\tau$ inf-step2 τ inf-step-table s $tls \rightarrow^* t \infty$
 \langle proof \rangle

lemma τ inf-step-imp- τ inf-step-table:

assumes $s -\tau-tls \rightarrow^* \infty$
shows \exists $sstls$. $s -\tau-sstls \rightarrow^* t \infty \wedge tls = lmap$ ($fst \circ snd \circ snd$) $sstls$
 \langle proof \rangle

lemma τ inf-step-table-into- τ inf-step:

$s -\tau-sstls \rightarrow^* t \infty \implies s -\tau-lmap$ ($fst \circ snd \circ snd$) $sstls \rightarrow^* \infty$
 \langle proof \rangle

lemma *silent-move-fromI* [intro]:

\llbracket *silent-moves* $s0$ $s1$; *silent-move* $s1$ $s2$ $\rrbracket \implies$ *silent-move-from* $s0$ $s1$ $s2$
 \langle proof \rangle

lemma *silent-move-fromE* [elim]:

assumes *silent-move-from* $s0$ $s1$ $s2$
obtains *silent-moves* $s0$ $s1$ *silent-move* $s1$ $s2$
 \langle proof \rangle

lemma *rtrancplp-silent-move-from-imp-silent-moves*:

assumes $s'x$: *silent-move*^{**} s' x
shows (*silent-move-from* s')^{**} x $z \implies$ *silent-moves* s' z
 \langle proof \rangle

lemma τ diverge-not-wfP-silent-move-from:

assumes $s -\tau \rightarrow \infty$
shows \neg wfP (*flip* (*silent-move-from* s))
 \langle proof \rangle

lemma wfP-silent-move-from-unroll:

assumes wfPs': $\bigwedge s'. s -\tau \rightarrow s' \implies$ wfP (*flip* (*silent-move-from* s'))
shows wfP (*flip* (*silent-move-from* s))
 \langle proof \rangle

lemma not-wfP-silent-move-from- τ diverge:

assumes \neg wfP (*flip* (*silent-move-from* s))
shows $s -\tau \rightarrow \infty$
 \langle proof \rangle

lemma τ diverge-neq-wfP-silent-move-from:

$s -\tau \rightarrow \infty \neq$ wfP (*flip* (*silent-move-from* s))
 \langle proof \rangle

lemma not- τ diverge-to-no- τ move:

assumes \neg $s -\tau \rightarrow \infty$
shows $\exists s'. s -\tau \rightarrow^* s' \wedge (\forall s''. \neg s' -\tau \rightarrow s'')$
 \langle proof \rangle

lemma τ diverge-conv- τ Runs:

$s -\tau \rightarrow \infty \iff s \Downarrow TNil$ None

$\langle \text{proof} \rangle$

lemma $\tau \text{inf-step-into-}\tau \text{Runs}$:

$$s \text{--}\tau\text{--}t\text{ls}\text{--}\rightarrow^* \infty \implies s \Downarrow \text{tllist-of-llist None tls}$$

$\langle \text{proof} \rangle$

lemma $\tau\text{-into-}\tau \text{Runs}$:

$$\llbracket s \text{--}\tau \rightarrow s'; s' \Downarrow \text{tls} \rrbracket \implies s \Downarrow \text{tls}$$

$\langle \text{proof} \rangle$

lemma $\tau \text{rtrancl3p-into-}\tau \text{Runs}$:

assumes $s \text{--}\tau\text{--}t\text{ls}\text{--}\rightarrow^* s'$

and $s' \Downarrow \text{tls}'$

shows $s \Downarrow \text{lappendt (llist-of tls) tls}'$

$\langle \text{proof} \rangle$

lemma $\tau \text{Runs-table-into-}\tau \text{Runs}$:

$$\tau \text{Runs-table } s \text{ stlsss} \implies s \Downarrow \text{tmap fst id stlsss}$$

$\langle \text{proof} \rangle$

definition $\tau \text{Runs2}\tau \text{Runs-table} :: 's \Rightarrow ('tl, 's \text{ option}) \text{tllist} \Rightarrow ('tl \times 's, 's \text{ option}) \text{tllist}$

where

$$\tau \text{Runs2}\tau \text{Runs-table } s \text{ tls} = \text{unfold-tllist}$$

$$(\lambda(s, \text{tls}). \text{is-TNil tls})$$

$$(\lambda(s, \text{tls}). \text{terminal tls})$$

$$(\lambda(s, \text{tls}). (\text{thd tls}, \text{SOME } s''. \exists s'. s \text{--}\tau \rightarrow^* s' \wedge s' \text{--thd tls}\text{--}\rightarrow s'' \wedge \neg \tau \text{move } s' (\text{thd tls}) s'' \wedge s'' \Downarrow \text{ttl tls}))$$

$$(\lambda(s, \text{tls}). (\text{SOME } s''. \exists s'. s \text{--}\tau \rightarrow^* s' \wedge s' \text{--thd tls}\text{--}\rightarrow s'' \wedge \neg \tau \text{move } s' (\text{thd tls}) s'' \wedge s'' \Downarrow \text{ttl tls}, \text{ttl tls}))$$

$$(s, \text{tls})$$

lemma $\text{is-TNil-}\tau \text{Runs2}\tau \text{Runs-table}$ [simp]:

$$\text{is-TNil } (\tau \text{Runs2}\tau \text{Runs-table } s \text{ tls}) \longleftrightarrow \text{is-TNil tls}$$

thm $\text{unfold-tllist.disc}$

$\langle \text{proof} \rangle$

lemma $\text{thd-}\tau \text{Runs2}\tau \text{Runs-table}$ [simp]:

$$\neg \text{is-TNil tls} \implies$$

$$\text{thd } (\tau \text{Runs2}\tau \text{Runs-table } s \text{ tls}) =$$

$$(\text{thd tls}, \text{SOME } s''. \exists s'. s \text{--}\tau \rightarrow^* s' \wedge s' \text{--thd tls}\text{--}\rightarrow s'' \wedge \neg \tau \text{move } s' (\text{thd tls}) s'' \wedge s'' \Downarrow \text{ttl tls})$$

$\langle \text{proof} \rangle$

lemma $\text{ttl-}\tau \text{Runs2}\tau \text{Runs-table}$ [simp]:

$$\neg \text{is-TNil tls} \implies$$

$$\text{ttl } (\tau \text{Runs2}\tau \text{Runs-table } s \text{ tls}) =$$

$$\tau \text{Runs2}\tau \text{Runs-table } (\text{SOME } s''. \exists s'. s \text{--}\tau \rightarrow^* s' \wedge s' \text{--thd tls}\text{--}\rightarrow s'' \wedge \neg \tau \text{move } s' (\text{thd tls}) s'' \wedge s'' \Downarrow \text{ttl tls}) (\text{ttl tls})$$

$\langle \text{proof} \rangle$

lemma $\text{terminal-}\tau \text{Runs2}\tau \text{Runs-table}$ [simp]:

$$\text{is-TNil tls} \implies \text{terminal } (\tau \text{Runs2}\tau \text{Runs-table } s \text{ tls}) = \text{terminal tls}$$

$\langle \text{proof} \rangle$

lemma $\tau \text{Runs2}\tau \text{Runs-table-simps}$ [simp, nitpick-simp]:

$\tau\text{Runs2}\tau\text{Runs-table } s \text{ (TNil } so) = \text{TNil } so$
 $\wedge tl.$
 $\tau\text{Runs2}\tau\text{Runs-table } s \text{ (TCons } tl \ tls) =$
 $(\text{let } s'' = \text{SOME } s''. \exists s'. s -\tau \rightarrow^* s' \wedge s' -tl \rightarrow s'' \wedge \neg \tau\text{move } s' \ tl \ s'' \wedge s'' \Downarrow tls$
 $\text{in TCons } (tl, s'') \ (\tau\text{Runs2}\tau\text{Runs-table } s'' \ tls))$
 $\langle\text{proof}\rangle$

lemma $\tau\text{Runs2}\tau\text{Runs-table-inverse}$:
 $tmap \text{fst id } (\tau\text{Runs2}\tau\text{Runs-table } s \ tls) = tls$
 $\langle\text{proof}\rangle$

lemma $\tau\text{Runs-into-}\tau\text{Runs-table}$:
assumes $s \Downarrow tls$
shows $\exists stlsss. tls = tmap \text{fst id } stlsss \wedge \tau\text{Runs-table } s \ stlsss$
 $\langle\text{proof}\rangle$

lemma $\tau\text{Runs-lappendE}$:
assumes $\sigma \Downarrow \text{lappendt } tls \ tls'$
and $\text{lfinite } tls$
obtains σ' **where** $\sigma -\tau\text{-list-of } tls \rightarrow^* \sigma'$
and $\sigma' \Downarrow tls'$
 $\langle\text{proof}\rangle$

lemma $\tau\text{Runs-total}$:
 $\exists tls. \sigma \Downarrow tls$
 $\langle\text{proof}\rangle$

lemma $\text{silent-move2-into-silent-move}$:
fixes tl
assumes $\text{silent-move2 } s \ tl \ s'$
shows $s -\tau \rightarrow s'$
 $\langle\text{proof}\rangle$

lemma $\text{silent-move-into-silent-move2}$:
assumes $s -\tau \rightarrow s'$
shows $\exists tl. \text{silent-move2 } s \ tl \ s'$
 $\langle\text{proof}\rangle$

lemma $\text{silent-moves2-into-silent-moves}$:
assumes $\text{silent-moves2 } s \ tls \ s'$
shows $s -\tau \rightarrow^* s'$
 $\langle\text{proof}\rangle$

lemma $\text{silent-moves-into-silent-moves2}$:
assumes $s -\tau \rightarrow^* s'$
shows $\exists tls. \text{silent-moves2 } s \ tls \ s'$
 $\langle\text{proof}\rangle$

lemma $\text{inf-step-silent-move2-into-}\tau\text{diverge}$:
 $\text{trsys.inf-step } \text{silent-move2 } s \ tls \Longrightarrow s -\tau \rightarrow \infty$
 $\langle\text{proof}\rangle$

lemma $\tau\text{diverge-into-inf-step-silent-move2}$:
assumes $s -\tau \rightarrow \infty$

obtains tls **where** $trsys.inf\text{-}step\ silent\text{-}move2\ s\ tls$
 ⟨proof⟩

lemma $\tauRuns\text{-}into\text{-}\tau rtrancl3p$:
assumes $runs: s \Downarrow tlss$
and $fin: tfinite\ tlss$
and $terminal: terminal\ tlss = Some\ s'$
shows $\tau rtrancl3p\ s\ (list\text{-}of\ (l\text{-}list\text{-}of\ t\text{-}list\ tlss))\ s'$
 ⟨proof⟩

lemma $\tauRuns\text{-}terminal\text{-}stuck$:
assumes $Runs: s \Downarrow tlss$
and $fin: tfinite\ tlss$
and $terminal: terminal\ tlss = Some\ s'$
and $proceed: s' -tls\rightarrow s''$
shows $False$
 ⟨proof⟩

lemma $Runs\text{-}table\text{-}silent\text{-}diverge$:
 [$Runs\text{-}table\ s\ stlss; \forall (s, tl, s') \in lset\ stlss. \tau move\ s\ tl\ s'; \neg lfinite\ stlss$]
 $\implies s -\tau \rightarrow \infty$
 ⟨proof⟩

lemma $Runs\text{-}table\text{-}silent\text{-}rtrancl$:
assumes $lfinite\ stlss$
and $Runs\text{-}table\ s\ stlss$
and $\forall (s, tl, s') \in lset\ stlss. \tau move\ s\ tl\ s'$
shows $s -\tau \rightarrow^* llast\ (LCons\ s\ (lmap\ (\lambda(s, tl, s'). s')\ stlss))$ (**is** $?thesis1$)
and $llast\ (LCons\ s\ (lmap\ (\lambda(s, tl, s'). s')\ stlss)) -tl' \rightarrow s'' \implies False$ (**is** $PROP\ ?thesis2$)
 ⟨proof⟩

lemma $Runs\text{-}table\text{-}silent\text{-}lappendD$:
fixes $s\ stlss$
defines $s' \equiv llast\ (LCons\ s\ (lmap\ (\lambda(s, tl, s'). s')\ stlss))$
assumes $Runs: Runs\text{-}table\ s\ (lappend\ stlss\ stlss')$
and $fin: lfinite\ stlss$
and $silent: \forall (s, tl, s') \in lset\ stlss. \tau move\ s\ tl\ s'$
shows $s -\tau \rightarrow^* s'$ (**is** $?thesis1$)
and $Runs\text{-}table\ s'\ stlss'$ (**is** $?thesis2$)
and $stlss' \neq LNil \implies s' = fst\ (lhd\ stlss')$ (**is** $PROP\ ?thesis3$)
 ⟨proof⟩

lemma $Runs\text{-}table\text{-}into\text{-}\tauRuns$:
fixes $s\ stlss$
defines $tls \equiv tmap\ (\lambda(s, tl, s'). tl)\ id\ (tfilter\ None\ (\lambda(s, tl, s'). \neg \tau move\ s\ tl\ s'))\ (t\text{-}list\text{-}of\ l\text{-}list\ (Some\ (llast\ (LCons\ s\ (lmap\ (\lambda(s, tl, s'). s')\ stlss))))\ stlss)$
 (**is** $- \equiv ?conv\ s\ stlss$)
assumes $Runs\text{-}table\ s\ stlss$
shows $\tauRuns\ s\ tls$
 ⟨proof⟩

lemma $\tauRuns\text{-}table2\text{-}into\text{-}\tauRuns$:
 $\tauRuns\text{-}table2\ s\ t\text{-}stlss$
 $\implies s \Downarrow tmap\ (\lambda(tls, s', tl, s'). tl)\ (\lambda x. case\ x\ of\ Inl\ (tls, s') \Rightarrow Some\ s' \mid Inr\ - \Rightarrow None)\ t\text{-}stlss$

<proof>

lemma τ Runs-into- τ Runs-table2:

assumes $s \Downarrow tls$

obtains $tlsstlss$

where τ Runs-table2 s $tlsstlss$

and $tls = tmap (\lambda(tls, s', tl, s''). tl) (\lambda x. case\ x\ of\ Inl\ (tls, s') \Rightarrow Some\ s' \mid Inr\ - \Rightarrow None)\ tlsstlss$

<proof>

lemma τ Runs-table2-into-Runs:

assumes τ Runs-table2 s $tlsstlss$

shows $Runs\ s\ (lconcat\ (lappend\ (lmap\ (\lambda(tls, s, tl, s').\ llist-of\ (tls\ @\ [tl]))\ (llist-of-tl\ list\ tlsstlss))\ (LCons\ (case\ terminal\ tlsstlss\ of\ Inl\ (tls, s') \Rightarrow llist-of\ tls \mid Inr\ tls \Rightarrow tls)\ LNil)))$

(is $Runs - (?conv\ tlsstlss)$

<proof>

lemma τ Runs-table2-silentsD:

fixes tl

assumes $Runs: \tau$ Runs-table2 s $tlsstlss$

and $tset: (tls, s', tl', s'') \in tset\ tlsstlss$

and $set: tl \in set\ tls$

shows $\exists s''' s'''.\ silent-move2\ s''' tl s''''$

<proof>

lemma τ Runs-table2-terminal-silentsD:

assumes $Runs: \tau$ Runs-table2 s $tlsstlss$

and $fin: lfinite\ (l\ list-of-tl\ list\ tlsstlss)$

and $terminal: terminal\ tlsstlss = Inl\ (tls, s'')$

shows $\exists s'.\ silent-moves2\ s' tls s''$

<proof>

lemma τ Runs-table2-terminal-inf-stepD:

assumes $Runs: \tau$ Runs-table2 s $tlsstlss$

and $fin: lfinite\ (l\ list-of-tl\ list\ tlsstlss)$

and $terminal: terminal\ tlsstlss = Inr\ tls$

shows $\exists s'.\ trsys.inf-step\ silent-move2\ s' tls$

<proof>

lemma τ Runs-table2-lappendtD:

assumes $Runs: \tau$ Runs-table2 s $(lappendt\ tlsstlss\ tlsstlss')$

and $fin: lfinite\ tlsstlss$

shows $\exists s'.\ \tau$ Runs-table2 s' $tlsstlss'$

<proof>

end

lemma τ moves-False: τ trsys.silent-move r $(\lambda s\ ta\ s'.\ False) = (\lambda s\ s'.\ False)$

<proof>

lemma τ rtrancl3p-False-eq-rtrancl3p: τ trsys. τ rtrancl3p r $(\lambda s\ tl\ s'.\ False) = rtrancl3p\ r$

<proof>

lemma τ diverge-empty- τ move:

τ trsys. τ diverge r $(\lambda s\ ta\ s'.\ False) = (\lambda s.\ False)$

<proof>

end

1.16 The multithreaded semantics as a labelled transition system

theory *FWLTS*

imports

FWProgressAux

FWLifting

LTS

begin

sublocale *multithreaded-base* < *trsys r t* **for** *t* *<proof>*

sublocale *multithreaded-base* < *mthr: trsys redT* *<proof>*

definition *redT-upd-ε* :: (l, t, x, m, w) *state* \Rightarrow $t \Rightarrow x \Rightarrow m \Rightarrow (l, t, x, m, w)$ *state*

where [*simp*]: *redT-upd-ε s t x' m'* = (*locks s*, ((*thr s*)($t \mapsto (x', \text{snd } (\text{the } (\text{thr } s \ t))))$), *m'*), *wset s*, *interrupts s*)

lemma *redT-upd-ε-redT-upd*:

redT-upd s t ε x' m' (redT-upd-ε s t x' m')

<proof>

context *multithreaded* **begin**

sublocale *trsys r t* **for** *t* *<proof>*

sublocale *mthr: trsys redT* *<proof>*

end

1.16.1 The multithreaded semantics with internal actions

type-synonym

(l, t, x, m, w, o) *τmoves* =

$x \times m \Rightarrow (l, t, x, m, w, o)$ *thread-action* $\Rightarrow x \times m \Rightarrow \text{bool}$

pretty printing for *τmoves*

<ML>

typ (l, t, x, m, w, o) *τmoves*

locale *τmultithreaded* = *multithreaded-base* +

constrains *final* :: $x \Rightarrow \text{bool}$

and *r* :: (l, t, x, m, w, o) *semantics*

and *convert-RA* :: l *released-locks* \Rightarrow *o list*

fixes *τmove* :: (l, t, x, m, w, o) *τmoves*

sublocale *τmultithreaded* < *τtrsys r t τmove* **for** *t* *<proof>*

context *τmultithreaded* **begin**

inductive *mτmove* :: $((l, t, x, m, w)$ *state*, $t \times (l, t, x, m, w, o)$ *thread-action*) *trsys*

where

$\llbracket \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket; \text{thr } s' \ t = \llbracket (x', \text{ln}') \rrbracket; \tau \text{move } (x, \text{shr } s) \ \text{ta } (x', \text{shr } s') \rrbracket$
 $\implies m\tau \text{move } s \ (t, \text{ta}) \ s'$

end

sublocale $\tau \text{multithreaded} < \text{mthr}: \tau \text{trsys } \text{redT} \ m\tau \text{move} \ \langle \text{proof} \rangle$

context $\tau \text{multithreaded}$ **begin**

abbreviation $\tau \text{mredT} :: ('l, 't, 'x, 'm, 'w) \ \text{state} \Rightarrow ('l, 't, 'x, 'm, 'w) \ \text{state} \Rightarrow \text{bool}$

where $\tau \text{mredT} == \text{mthr}.\text{silent-move}$

end

lemma (in *multithreaded-base*) $\tau \text{rtrancl3p-redT-thread-not-disappear}$:

assumes $\tau \text{trsys}.\tau \text{rtrancl3p } \text{redT} \ \tau \text{move } s \ \text{ttas } s' \ \text{thr } s \ t \neq \text{None}$

shows $\text{thr } s' \ t \neq \text{None}$

$\langle \text{proof} \rangle$

lemma *m τ move-False*: $\tau \text{multithreaded}.\text{m}\tau \text{move} \ (\lambda s \ \text{ta} \ s'. \ \text{False}) = (\lambda s \ \text{ta} \ s'. \ \text{False})$

$\langle \text{proof} \rangle$

declare *split-paired-Ex* [*simp del*]

locale $\tau \text{multithreaded-wf} =$

$\tau \text{multithreaded} \ - \ - \ - \ \tau \text{move} \ +$

multithreaded final r convert-RA

for $\tau \text{move} :: ('l, 't, 'x, 'm, 'w, 'o) \ \tau \text{moves} \ +$

assumes $\tau \text{move-heap}: \llbracket t \vdash (x, m) \ -\text{ta} \rightarrow (x', m') \rrbracket; \tau \text{move} \ (x, m) \ \text{ta} \ (x', m') \rrbracket \implies m = m'$

assumes *silent-tl*: $\tau \text{move} \ s \ \text{ta} \ s' \implies \text{ta} = \varepsilon$

begin

lemma *m τ move-silentD*: $m\tau \text{move} \ s \ (t, \text{ta}) \ s' \implies \text{ta} = (K\$ \ \square, \ \square, \ \square, \ \square, \ \square, \ \square)$

$\langle \text{proof} \rangle$

lemma *m τ move-heap*:

assumes *redT*: $\text{redT} \ s \ (t, \text{ta}) \ s'$

and *m τ move*: $m\tau \text{move} \ s \ (t, \text{ta}) \ s'$

shows $\text{shr } s' = \text{shr } s$

$\langle \text{proof} \rangle$

lemma *τmredT -thread-preserved*:

$\tau \text{mredT} \ s \ s' \implies \text{thr } s \ t = \text{None} \iff \text{thr } s' \ t = \text{None}$

$\langle \text{proof} \rangle$

lemma *τmRedT -thread-preserved*:

$\tau \text{mredT}^{\wedge**} \ s \ s' \implies \text{thr } s \ t = \text{None} \iff \text{thr } s' \ t = \text{None}$

$\langle \text{proof} \rangle$

lemma *τmtRedT -thread-preserved*:

$\tau \text{mredT}^{\wedge++} \ s \ s' \implies \text{thr } s \ t = \text{None} \iff \text{thr } s' \ t = \text{None}$

$\langle \text{proof} \rangle$

lemma $\tau mredT$ -add-thread-inv:

assumes τred : $\tau mredT\ s\ s'$ **and** tst : $thr\ s\ t = None$

shows $\tau mredT\ (locks\ s,\ ((thr\ s)(t \mapsto xln),\ shr\ s),\ wset\ s,\ interrupts\ s)\ (locks\ s',\ ((thr\ s')(t \mapsto xln),\ shr\ s'),\ wset\ s',\ interrupts\ s')$

$\langle proof \rangle$

lemma $\tau mRedT$ -add-thread-inv:

$\llbracket \tau mredT^{\wedge**}\ s\ s';\ thr\ s\ t = None \rrbracket$

$\implies \tau mredT^{\wedge**}\ (locks\ s,\ ((thr\ s)(t \mapsto xln),\ shr\ s),\ wset\ s,\ interrupts\ s)\ (locks\ s',\ ((thr\ s')(t \mapsto xln),\ shr\ s'),\ wset\ s',\ interrupts\ s')$

$\langle proof \rangle$

lemma $\tau mtRed$ -add-thread-inv:

$\llbracket \tau mredT^{\wedge++}\ s\ s';\ thr\ s\ t = None \rrbracket$

$\implies \tau mredT^{\wedge++}\ (locks\ s,\ ((thr\ s)(t \mapsto xln),\ shr\ s),\ wset\ s,\ interrupts\ s)\ (locks\ s',\ ((thr\ s')(t \mapsto xln),\ shr\ s'),\ wset\ s',\ interrupts\ s')$

$\langle proof \rangle$

lemma *silent-move-into-RedT- τ -inv*:

assumes *move*: *silent-move* $t\ (x,\ shr\ s)\ (x',\ m')$

and *state*: $thr\ s\ t = \llbracket (x,\ no\ wait\ locks) \rrbracket\ wset\ s\ t = None$

shows $\tau mredT\ s\ (redT\ upd\ \varepsilon\ s\ t\ x'\ m')$

$\langle proof \rangle$

lemma *silent-moves-into-RedT- τ -inv*:

assumes *major*: *silent-moves* $t\ (x,\ shr\ s)\ (x',\ m')$

and *state*: $thr\ s\ t = \llbracket (x,\ no\ wait\ locks) \rrbracket\ wset\ s\ t = None$

shows $\tau mredT^{\wedge**}\ s\ (redT\ upd\ \varepsilon\ s\ t\ x'\ m')$

$\langle proof \rangle$

lemma *red-rtrancl- τ -heapD-inv*:

$\llbracket silent\ moves\ t\ s\ s';\ wfs\ t\ s \rrbracket \implies snd\ s' = snd\ s$

$\langle proof \rangle$

lemma *red-trancl- τ -heapD-inv*:

$\llbracket silent\ movet\ t\ s\ s';\ wfs\ t\ s \rrbracket \implies snd\ s' = snd\ s$

$\langle proof \rangle$

lemma *red-trancl- τ -into-RedT- τ -inv*:

assumes *major*: *silent-movet* $t\ (x,\ shr\ s)\ (x',\ m')$

and *state*: $thr\ s\ t = \llbracket (x,\ no\ wait\ locks) \rrbracket\ wset\ s\ t = None$

shows $\tau mredT^{\wedge++}\ s\ (redT\ upd\ \varepsilon\ s\ t\ x'\ m')$

$\langle proof \rangle$

lemma τ diverge-into- $\tau mredT$:

assumes τ diverge $t\ (x,\ shr\ s)$

and $thr\ s\ t = \llbracket (x,\ no\ wait\ locks) \rrbracket\ wset\ s\ t = None$

shows $mthr.\tau$ diverge s

$\langle proof \rangle$

lemma τ diverge- $\tau mredTD$:

assumes *div*: $mthr.\tau$ diverge s

and *fin*: *finite* (*dom* ($thr\ s$))

shows $\exists t\ x.\ thr\ s\ t = \llbracket (x,\ no\ wait\ locks) \rrbracket \wedge wset\ s\ t = None \wedge \tau$ diverge $t\ (x,\ shr\ s)$

<proof>

lemma $\tau mredT$ -preserves-final-thread:

$\llbracket \tau mredT\ s\ s';\ final\text{-thread}\ s\ t \rrbracket \implies final\text{-thread}\ s'\ t$
<proof>

lemma $\tau mRedT$ -preserves-final-thread:

$\llbracket \tau mredT^{\wedge**}\ s\ s';\ final\text{-thread}\ s\ t \rrbracket \implies final\text{-thread}\ s'\ t$
<proof>

lemma *silent-moves2-silentD*:

assumes $rtrancl3p\ mthr.\text{silent-move2}\ s\ ttas\ s'$
and $(t, ta) \in set\ ttas$
shows $ta = \varepsilon$
<proof>

lemma *inf-step-silentD*:

assumes $step:\ trsys.\text{inf-step}\ mthr.\text{silent-move2}\ s\ ttas$
and $lset:\ (t, ta) \in lset\ ttas$
shows $ta = \varepsilon$
<proof>

end

1.16.2 The multithreaded semantics with a well-founded relation on states

locale *multithreaded-base-measure* = *multithreaded-base* +

constrains $final :: 'x \Rightarrow bool$
and $r :: ('l, 't, 'x, 'm, 'w, 'o)\ \text{semantics}$
and $convert\text{-}RA :: 'l\ \text{released-locks} \Rightarrow 'o\ \text{list}$
fixes $\mu :: ('x \times 'm) \Rightarrow ('x \times 'm) \Rightarrow bool$

begin

inductive $m\mu t :: 'm \Rightarrow ('l, 't, 'x)\ \text{thread-info} \Rightarrow ('l, 't, 'x)\ \text{thread-info} \Rightarrow bool$

for m **and** ts **and** ts'

where

$m\mu tI:$
 $\bigwedge ln. \llbracket finite\ (dom\ ts);\ ts\ t = \lfloor (x, ln) \rfloor; ts'\ t = \lfloor (x', ln') \rfloor; \mu\ (x, m)\ (x', m); \bigwedge t'. t' \neq t \implies ts\ t' = ts'\ t' \rrbracket$
 $\implies m\mu t\ m\ ts\ ts'$

definition $m\mu :: ('l, 't, 'x, 'm, 'w)\ \text{state} \Rightarrow ('l, 't, 'x, 'm, 'w)\ \text{state} \Rightarrow bool$

where $m\mu\ s\ s' \iff shr\ s = shr\ s' \wedge m\mu t\ (shr\ s)\ (thr\ s)\ (thr\ s')$

lemma $m\mu t\text{-thr-dom-eq}$: $m\mu t\ m\ ts\ ts' \implies dom\ ts = dom\ ts'$

<proof>

lemma $m\mu\text{-finite-thrD}$:

assumes $m\mu t\ m\ ts\ ts'$
shows $finite\ (dom\ ts)\ finite\ (dom\ ts')$
<proof>

end

```

locale multithreaded-base-measure-wf = multithreaded-base-measure +
  constrains final :: 'x ⇒ bool
  and r :: ('l,'t,'x,'m,'w,'o) semantics
  and convert-RA :: 'l released-locks ⇒ 'o list
  and  $\mu$  :: ('x × 'm) ⇒ ('x × 'm) ⇒ bool
  assumes wf- $\mu$ : wfP  $\mu$ 
begin

lemma wf-m $\mu$ t: wfP (m $\mu$ t m)
  ⟨proof⟩

lemma wf-m $\mu$ : wfP m $\mu$ 
  ⟨proof⟩

end

end

```

1.17 Various notions of bisimulation

theory *Bisimulation*

imports

LTS

begin

type-synonym ('a, 'b) *bisim* = 'a ⇒ 'b ⇒ bool

1.17.1 Strong bisimulation

```

locale bisimulation-base = r1: trsys trsys1 + r2: trsys trsys2
  for trsys1 :: ('s1, 'tl1) trsys (⟨- / -1---> / -> [50,0,50] 60)
  and trsys2 :: ('s2, 'tl2) trsys (⟨- / -2---> / -> [50,0,50] 60) +
  fixes bisim :: ('s1, 's2) bisim (⟨- / ≈ -> [50, 50] 60)
  and tlsim :: ('tl1, 'tl2) bisim (⟨- / ~ -> [50, 50] 60)
begin

```

notation

r1.*Trsys* (⟨- / -1--->*/ -> [50,0,50] 60) **and**
r2.*Trsys* (⟨- / -2--->*/ -> [50,0,50] 60)

notation

r1.*inf-step* (⟨- -1--->* ∞> [50, 0] 80) **and**
r2.*inf-step* (⟨- -2--->* ∞> [50, 0] 80)

notation

r1.*inf-step-table* (⟨- -1--->*t ∞> [50, 0] 80) **and**
r2.*inf-step-table* (⟨- -2--->*t ∞> [50, 0] 80)

abbreviation *Tlsim* :: ('tl1 *list*, 'tl2 *list*) *bisim* (⟨- / [~] -> [50, 50] 60)

where *Tlsim* *tl1* *tl2* ≡ *list-all2* *tlsim* *tl1* *tl2*

abbreviation *Tlsiml* :: ('tl1 *llist*, 'tl2 *llist*) *bisim* (⟨- / [[~]] -> [50, 50] 60)

where *Tlsiml* *tl1* *tl2* ≡ *llist-all2* *tlsim* *tl1* *tl2*

end

locale *bisimulation* = *bisimulation-base* +
constrains *trsys1* :: ('s1, 'tl1) *trsys*
and *trsys2* :: ('s2, 'tl2) *trsys*
and *bisim* :: ('s1, 's2) *bisim*
and *tlsim* :: ('tl1, 'tl2) *bisim*
assumes *simulation1*: $\llbracket s1 \approx s2; s1 -1-tl1 \rightarrow s1' \rrbracket \implies \exists s2' tl2. s2 -2-tl2 \rightarrow s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$
and *simulation2*: $\llbracket s1 \approx s2; s2 -2-tl2 \rightarrow s2' \rrbracket \implies \exists s1' tl1. s1 -1-tl1 \rightarrow s1' \wedge s1' \approx s2' \wedge tl1 \sim tl2$
begin

lemma *bisimulation-flip*:
bisimulation trsys2 trsys1 (flip bisim) (flip tlsim)
 $\langle proof \rangle$

end

lemma *bisimulation-flip-simps* [*flip-simps*]:
bisimulation trsys2 trsys1 (flip bisim) (flip tlsim) = bisimulation trsys1 trsys2 bisim tlsim
 $\langle proof \rangle$

context *bisimulation begin*

lemma *simulation1-rtrancl*:
 $\llbracket s1 -1-tls1 \rightarrow^* s1'; s1 \approx s2 \rrbracket$
 $\implies \exists s2' tls2. s2 -2-tls2 \rightarrow^* s2' \wedge s1' \approx s2' \wedge tls1 [\sim] tls2$
 $\langle proof \rangle$

lemma *simulation2-rtrancl*:
 $\llbracket s2 -2-tls2 \rightarrow^* s2'; s1 \approx s2 \rrbracket$
 $\implies \exists s1' tls1. s1 -1-tls1 \rightarrow^* s1' \wedge s1' \approx s2' \wedge tls1 [\sim] tls2$
 $\langle proof \rangle$

lemma *simulation1-inf-step*:
assumes *red1*: $s1 -1-tls1 \rightarrow^* \infty$ **and** *bisim*: $s1 \approx s2$
shows $\exists tls2. s2 -2-tls2 \rightarrow^* \infty \wedge tls1 [[\sim]] tls2$
 $\langle proof \rangle$

lemma *simulation2-inf-step*:
 $\llbracket s2 -2-tls2 \rightarrow^* \infty; s1 \approx s2 \rrbracket \implies \exists tls1. s1 -1-tls1 \rightarrow^* \infty \wedge tls1 [[\sim]] tls2$
 $\langle proof \rangle$

end

locale *bisimulation-final-base* =
bisimulation-base +
constrains *trsys1* :: ('s1, 'tl1) *trsys*
and *trsys2* :: ('s2, 'tl2) *trsys*
and *bisim* :: ('s1, 's2) *bisim*
and *tlsim* :: ('tl1, 'tl2) *bisim*
fixes *final1* :: 's1 \Rightarrow bool
and *final2* :: 's2 \Rightarrow bool

locale *bisimulation-final* = *bisimulation-final-base* + *bisimulation* +
constrains *trsys1* :: ('s1, 'tl1) *trsys*
and *trsys2* :: ('s2, 'tl2) *trsys*
and *bisim* :: ('s1, 's2) *bisim*
and *tlsim* :: ('tl1, 'tl2) *bisim*
and *final1* :: 's1 \Rightarrow bool
and *final2* :: 's2 \Rightarrow bool
assumes *bisim-final*: $s1 \approx s2 \implies \text{final1 } s1 \longleftrightarrow \text{final2 } s2$

begin

lemma *bisimulation-final-flip*:

bisimulation-final trsys2 trsys1 (flip bisim) (flip tlsim) final2 final1
 $\langle \text{proof} \rangle$

end

lemma *bisimulation-final-flip-simps* [*flip-simps*]:

bisimulation-final trsys2 trsys1 (flip bisim) (flip tlsim) final2 final1 =
bisimulation-final trsys1 trsys2 bisim tlsim final1 final2
 $\langle \text{proof} \rangle$

context *bisimulation-final* **begin**

lemma *final-simulation1*:

$\llbracket s1 \approx s2; s1 \text{ -1-tls1} \rightarrow^* s1'; \text{final1 } s1' \rrbracket$
 $\implies \exists s2' \text{ tls2}. s2 \text{ -2-tls2} \rightarrow^* s2' \wedge s1' \approx s2' \wedge \text{final2 } s2' \wedge \text{tls1 } [\sim] \text{tls2}$
 $\langle \text{proof} \rangle$

lemma *final-simulation2*:

$\llbracket s1 \approx s2; s2 \text{ -2-tls2} \rightarrow^* s2'; \text{final2 } s2' \rrbracket$
 $\implies \exists s1' \text{ tls1}. s1 \text{ -1-tls1} \rightarrow^* s1' \wedge s1' \approx s2' \wedge \text{final1 } s1' \wedge \text{tls1 } [\sim] \text{tls2}$
 $\langle \text{proof} \rangle$

end

1.17.2 Delay bisimulation

locale *delay-bisimulation-base* =

bisimulation-base +
trsys1? : $\tau \text{trsys } \text{trsys1 } \tau \text{move1}$ +
trsys2? : $\tau \text{trsys } \text{trsys2 } \tau \text{move2}$
for $\tau \text{move1 } \tau \text{move2}$ +
constrains *trsys1* :: ('s1, 'tl1) *trsys*
and *trsys2* :: ('s2, 'tl2) *trsys*
and *bisim* :: ('s1, 's2) *bisim*
and *tlsim* :: ('tl1, 'tl2) *bisim*
and τmove1 :: ('s1, 'tl1) *trsys*
and τmove2 :: ('s2, 'tl2) *trsys*

begin

notation

trsys1.silent-move ($\langle \text{-/ -}\tau 1 \rightarrow \text{-} \rangle$ [50, 50] 60) **and**

trsys2.silent-move ($\langle \cdot / -\tau 2 \rightarrow \cdot \rangle [50, 50] 60$)

notation

trsys1.silent-moves ($\langle \cdot / -\tau 1 \rightarrow * \cdot \rangle [50, 50] 60$) **and**
trsys2.silent-moves ($\langle \cdot / -\tau 2 \rightarrow * \cdot \rangle [50, 50] 60$)

notation

trsys1.silent-movet ($\langle \cdot / -\tau 1 \rightarrow + \cdot \rangle [50, 50] 60$) **and**
trsys2.silent-movet ($\langle \cdot / -\tau 2 \rightarrow + \cdot \rangle [50, 50] 60$)

notation

trsys1. τ trancl3p ($\langle \cdot -\tau 1 \dashrightarrow * \cdot \rangle [50, 0, 50] 60$) **and**
trsys2. τ trancl3p ($\langle \cdot -\tau 2 \dashrightarrow * \cdot \rangle [50, 0, 50] 60$)

notation

trsys1. τ inf-step ($\langle \cdot -\tau 1 \dashrightarrow * \infty \rangle [50, 0] 80$) **and**
trsys2. τ inf-step ($\langle \cdot -\tau 2 \dashrightarrow * \infty \rangle [50, 0] 80$)

notation

trsys1. τ diverge ($\langle \cdot -\tau 1 \rightarrow \infty \rangle [50] 80$) **and**
trsys2. τ diverge ($\langle \cdot -\tau 2 \rightarrow \infty \rangle [50] 80$)

notation

trsys1. τ inf-step-table ($\langle \cdot -\tau 1 \dashrightarrow * t \infty \rangle [50, 0] 80$) **and**
trsys2. τ inf-step-table ($\langle \cdot -\tau 2 \dashrightarrow * t \infty \rangle [50, 0] 80$)

notation

trsys1. τ Runs ($\langle \cdot \Downarrow 1 \cdot \rangle [50, 50] 51$) **and**
trsys2. τ Runs ($\langle \cdot \Downarrow 2 \cdot \rangle [50, 50] 51$)

lemma *simulation-silent1I'*:

assumes $\exists s2'$. (if $\mu 1 s1' s1$ then *trsys2.silent-moves* else *trsys2.silent-movet*) $s2 s2' \wedge s1' \approx s2'$
shows $s1' \approx s2 \wedge \mu 1 \hat{\cdot} ++ s1' s1 \vee (\exists s2'. s2 -\tau 2 \rightarrow + s2' \wedge s1' \approx s2')$

<proof>

lemma *simulation-silent2I'*:

assumes $\exists s1'$. (if $\mu 2 s2' s2$ then *trsys1.silent-moves* else *trsys1.silent-movet*) $s1 s1' \wedge s1' \approx s2'$
shows $s1 \approx s2' \wedge \mu 2 \hat{\cdot} ++ s2' s2 \vee (\exists s1'. s1 -\tau 1 \rightarrow + s1' \wedge s1' \approx s2')$

<proof>

end

locale *delay-bisimulation-obs = delay-bisimulation-base - - - τ move1 τ move2*

for τ move1 :: 's1 \Rightarrow 'tl1 \Rightarrow 's1 \Rightarrow bool

and τ move2 :: 's2 \Rightarrow 'tl2 \Rightarrow 's2 \Rightarrow bool +

assumes *simulation1*:

$\llbracket s1 \approx s2; s1 -1-tl1 \rightarrow s1'; \neg \tau$ move1 $s1 tl1 s1' \rrbracket$

$\implies \exists s2' s2'' tl2. s2 -\tau 2 \rightarrow * s2' \wedge s2' -2-tl2 \rightarrow s2'' \wedge \neg \tau$ move2 $s2' tl2 s2'' \wedge s1' \approx s2'' \wedge tl1$

$\sim tl2$

and *simulation2*:

$\llbracket s1 \approx s2; s2 -2-tl2 \rightarrow s2'; \neg \tau$ move2 $s2 tl2 s2' \rrbracket$

$\implies \exists s1' s1'' tl1. s1 -\tau 1 \rightarrow * s1' \wedge s1' -1-tl1 \rightarrow s1'' \wedge \neg \tau$ move1 $s1' tl1 s1'' \wedge s1'' \approx s2' \wedge tl1$

$\sim tl2$

begin

lemma *delay-bisimulation-obs-flip*: *delay-bisimulation-obs trsys2 trsys1 (flip bisim) (flip tlsim) τ move2 τ move1*
 <proof>

end

lemma *delay-bisimulation-obs-flip-simps [flip-simps]*:
delay-bisimulation-obs trsys2 trsys1 (flip bisim) (flip tlsim) τ move2 τ move1 =
delay-bisimulation-obs trsys1 trsys2 bisim tlsim τ move1 τ move2
 <proof>

locale *delay-bisimulation-diverge = delay-bisimulation-obs - - - τ move1 τ move2*
for *τ move1 :: 's1 \Rightarrow 'tl1 \Rightarrow 's1 \Rightarrow bool*
and *τ move2 :: 's2 \Rightarrow 'tl2 \Rightarrow 's2 \Rightarrow bool +*
assumes *simulation-silent1:*
 $\llbracket s1 \approx s2; s1 \text{--}\tau 1 \rightarrow s1' \rrbracket \Longrightarrow \exists s2'. s2 \text{--}\tau 2 \rightarrow^* s2' \wedge s1' \approx s2'$
and *simulation-silent2:*
 $\llbracket s1 \approx s2; s2 \text{--}\tau 2 \rightarrow s2' \rrbracket \Longrightarrow \exists s1'. s1 \text{--}\tau 1 \rightarrow^* s1' \wedge s1' \approx s2'$
and *τ diverge-bisim-inv: $s1 \approx s2 \Longrightarrow s1 \text{--}\tau 1 \rightarrow \infty \longleftrightarrow s2 \text{--}\tau 2 \rightarrow \infty$*
begin

lemma *delay-bisimulation-diverge-flip*: *delay-bisimulation-diverge trsys2 trsys1 (flip bisim) (flip tlsim)*
 τ move2 τ move1
 <proof>

end

lemma *delay-bisimulation-diverge-flip-simps [flip-simps]*:
delay-bisimulation-diverge trsys2 trsys1 (flip bisim) (flip tlsim) τ move2 τ move1 =
delay-bisimulation-diverge trsys1 trsys2 bisim tlsim τ move1 τ move2
 <proof>

context *delay-bisimulation-diverge begin*

lemma *simulation-silents1:*
assumes *bisim: $s1 \approx s2$ and moves: $s1 \text{--}\tau 1 \rightarrow^* s1'$*
shows $\exists s2'. s2 \text{--}\tau 2 \rightarrow^* s2' \wedge s1' \approx s2'$
 <proof>

lemma *simulation-silents2:*
 $\llbracket s1 \approx s2; s2 \text{--}\tau 2 \rightarrow^* s2' \rrbracket \Longrightarrow \exists s1'. s1 \text{--}\tau 1 \rightarrow^* s1' \wedge s1' \approx s2'$
 <proof>

lemma *simulation1- τ rtrancl3p:*
 $\llbracket s1 \text{--}\tau 1 \text{--}tls1 \rightarrow^* s1'; s1 \approx s2 \rrbracket$
 $\Longrightarrow \exists tls2 s2'. s2 \text{--}\tau 2 \text{--}tls2 \rightarrow^* s2' \wedge s1' \approx s2' \wedge tls1 [\sim] tls2$
 <proof>

lemma *simulation2- τ rtrancl3p:*
 $\llbracket s2 \text{--}\tau 2 \text{--}tls2 \rightarrow^* s2'; s1 \approx s2 \rrbracket$
 $\Longrightarrow \exists tls1 s1'. s1 \text{--}\tau 1 \text{--}tls1 \rightarrow^* s1' \wedge s1' \approx s2' \wedge tls1 [\sim] tls2$
 <proof>

lemma *simulation1- τ inf-step*:

assumes τ inf1: $s1 \text{ } \neg\tau 1 \text{ } \neg\text{tls1} \rightarrow * \infty$ **and** *bisim*: $s1 \approx s2$

shows $\exists \text{tls2}. s2 \text{ } \neg\tau 2 \text{ } \neg\text{tls2} \rightarrow * \infty \wedge \text{tls1} \llbracket \sim \rrbracket \text{tls2}$

<proof>

lemma *simulation2- τ inf-step*:

$\llbracket s2 \text{ } \neg\tau 2 \text{ } \neg\text{tls2} \rightarrow * \infty; s1 \approx s2 \rrbracket \implies \exists \text{tls1}. s1 \text{ } \neg\tau 1 \text{ } \neg\text{tls1} \rightarrow * \infty \wedge \text{tls1} \llbracket \sim \rrbracket \text{tls2}$

<proof>

lemma *no- τ move1- τ s-to-no- τ move2*:

assumes $s1 \approx s2$

and *no- τ moves1*: $\bigwedge s1'. \neg s1 \text{ } \neg\tau 1 \rightarrow s1'$

shows $\exists s2'. s2 \text{ } \neg\tau 2 \rightarrow * s2' \wedge (\forall s2''. \neg s2' \text{ } \neg\tau 2 \rightarrow s2'') \wedge s1 \approx s2'$

<proof>

lemma *no- τ move2- τ s-to-no- τ move1*:

$\llbracket s1 \approx s2; \bigwedge s2'. \neg s2 \text{ } \neg\tau 2 \rightarrow s2' \rrbracket \implies \exists s1'. s1 \text{ } \neg\tau 1 \rightarrow * s1' \wedge (\forall s1''. \neg s1' \text{ } \neg\tau 1 \rightarrow s1'') \wedge s1' \approx s2$

<proof>

lemma *no-move1-to-no-move2*:

assumes $s1 \approx s2$

and *no-moves1*: $\bigwedge \text{tl1} s1'. \neg s1 \text{ } \neg 1 \text{ } \neg\text{tl1} \rightarrow s1'$

shows $\exists s2'. s2 \text{ } \neg\tau 2 \rightarrow * s2' \wedge (\forall \text{tl2} s2''. \neg s2' \text{ } \neg 2 \text{ } \neg\text{tl2} \rightarrow s2'') \wedge s1 \approx s2'$

<proof>

lemma *no-move2-to-no-move1*:

$\llbracket s1 \approx s2; \bigwedge \text{tl2} s2'. \neg s2 \text{ } \neg 2 \text{ } \neg\text{tl2} \rightarrow s2' \rrbracket$

$\implies \exists s1'. s1 \text{ } \neg\tau 1 \rightarrow * s1' \wedge (\forall \text{tl1} s1''. \neg s1' \text{ } \neg 1 \text{ } \neg\text{tl1} \rightarrow s1'') \wedge s1' \approx s2$

<proof>

lemma *simulation- τ Runs-table1*:

assumes *bisim*: $s1 \approx s2$

and *run1*: *trsys1*. τ Runs-table $s1$ *stlsss1*

shows $\exists \text{stlsss2}. \text{trsys2}.\tau\text{Runs-table } s2 \text{ } \text{stlsss2} \wedge \text{tllist-all2 } (\lambda(\text{tl1}, s1'') (\text{tl2}, s2''). \text{tl1} \sim \text{tl2} \wedge s1'' \approx s2'') \text{ (rel-option bisim) } \text{stlsss1 } \text{stlsss2}$

<proof>

lemma *simulation- τ Runs-table2*:

assumes $s1 \approx s2$

and *trsys2*. τ Runs-table $s2$ *stlsss2*

shows $\exists \text{stlsss1}. \text{trsys1}.\tau\text{Runs-table } s1 \text{ } \text{stlsss1} \wedge \text{tllist-all2 } (\lambda(\text{tl1}, s1'') (\text{tl2}, s2''). \text{tl1} \sim \text{tl2} \wedge s1'' \approx s2'') \text{ (rel-option bisim) } \text{stlsss1 } \text{stlsss2}$

<proof>

lemma *simulation- τ Runs1*:

assumes *bisim*: $s1 \approx s2$

and *run1*: $s1 \Downarrow 1 \text{ } \text{tls1}$

shows $\exists \text{tls2}. s2 \Downarrow 2 \text{ } \text{tls2} \wedge \text{tllist-all2 } \text{tlsim} \text{ (rel-option bisim) } \text{tls1 } \text{tls2}$

<proof>

lemma *simulation- τ Runs2*:

$\llbracket s1 \approx s2; s2 \Downarrow 2 \text{ } \text{tls2} \rrbracket$

$\implies \exists t1s1. s1 \Downarrow_1 t1s1 \wedge t1list\text{-all}2\ t1sim\ (rel\text{-option}\ bisim)\ t1s1\ t1s2$
 <proof>

end

locale *delay-bisimulation-final-base* =
delay-bisimulation-base - - - $\tau\text{move}1\ \tau\text{move}2$ +
bisimulation-final-base - - - *final1 final2*
for $\tau\text{move}1 :: ('s1, 'tl1)\ trsys$
and $\tau\text{move}2 :: ('s2, 'tl2)\ trsys$
and *final1* :: $'s1 \Rightarrow bool$
and *final2* :: $'s2 \Rightarrow bool$ +
assumes *final1-simulation*: $\llbracket s1 \approx s2; final1\ s1 \rrbracket \implies \exists s2'. s2 \text{-}\tau 2 \text{-}\rightarrow^* s2' \wedge s1 \approx s2' \wedge final2\ s2'$
and *final2-simulation*: $\llbracket s1 \approx s2; final2\ s2 \rrbracket \implies \exists s1'. s1 \text{-}\tau 1 \text{-}\rightarrow^* s1' \wedge s1' \approx s2 \wedge final1\ s1'$
begin

lemma *delay-bisimulation-final-base-flip*:

delay-bisimulation-final-base trsys2 trsys1 (flip bisim) $\tau\text{move}2\ \tau\text{move}1\ final2\ final1$
 <proof>

end

lemma *delay-bisimulation-final-base-flip-simps* [*flip-simps*]:

delay-bisimulation-final-base trsys2 trsys1 (flip bisim) $\tau\text{move}2\ \tau\text{move}1\ final2\ final1$ =
delay-bisimulation-final-base trsys1 trsys2 bisim $\tau\text{move}1\ \tau\text{move}2\ final1\ final2$
 <proof>

context *delay-bisimulation-final-base* **begin**

lemma $\tau\text{Runs-terminate-final1}$:

assumes $s1 \Downarrow_1 t1s1$
and $s2 \Downarrow_2 t1s2$
and *t1list-all2 t1sim (rel-option bisim) t1s1 t1s2*
and *tfinite t1s1*
and *terminal t1s1 = Some s1'*
and *final1 s1'*
shows $\exists s2'. tfinite\ t1s2 \wedge terminal\ t1s2 = Some\ s2' \wedge final2\ s2'$
 <proof>

lemma $\tau\text{Runs-terminate-final2}$:

$\llbracket s1 \Downarrow_1 t1s1; s2 \Downarrow_2 t1s2; t1list\text{-all}2\ t1sim\ (rel\text{-option}\ bisim)\ t1s1\ t1s2;$
 $tfinite\ t1s2; terminal\ t1s2 = Some\ s2'; final2\ s2' \rrbracket$
 $\implies \exists s1'. tfinite\ t1s1 \wedge terminal\ t1s1 = Some\ s1' \wedge final1\ s1'$
 <proof>

end

locale *delay-bisimulation-diverge-final* =
delay-bisimulation-diverge +
delay-bisimulation-final-base +
constrains *trsys1* :: $('s1, 'tl1)\ trsys$
and *trsys2* :: $('s2, 'tl2)\ trsys$
and *bisim* :: $('s1, 's2)\ bisim$
and *t1sim* :: $('tl1, 'tl2)\ bisim$

```

and  $\tau\text{move1} :: ('s1, 'tl1) \text{trsys}$ 
and  $\tau\text{move2} :: ('s2, 'tl2) \text{trsys}$ 
and  $\text{final1} :: 's1 \Rightarrow \text{bool}$ 
and  $\text{final2} :: 's2 \Rightarrow \text{bool}$ 
begin

```

lemma *delay-bisimulation-diverge-final-flip*:

```

   $\text{delay-bisimulation-diverge-final trsys2 trsys1 (flip bisim) (flip tlsim) \tau\text{move2} \tau\text{move1 final2 final1}$ 
  <proof>

```

end

lemma *delay-bisimulation-diverge-final-flip-simps [flip-simps]*:

```

   $\text{delay-bisimulation-diverge-final trsys2 trsys1 (flip bisim) (flip tlsim) \tau\text{move2} \tau\text{move1 final2 final1} =$ 
   $\text{delay-bisimulation-diverge-final trsys1 trsys2 bisim tlsim \tau\text{move1} \tau\text{move2 final1 final2}$ 
  <proof>

```

context *delay-bisimulation-diverge-final* **begin**

lemma *delay-bisimulation-diverge*:

```

   $\text{delay-bisimulation-diverge trsys1 trsys2 bisim tlsim \tau\text{move1} \tau\text{move2}$ 
  <proof>

```

lemma *delay-bisimulation-final-base*:

```

   $\text{delay-bisimulation-final-base trsys1 trsys2 bisim \tau\text{move1} \tau\text{move2 final1 final2}$ 
  <proof>

```

lemma *final-simulation1*:

```

   $\llbracket s1 \approx s2; s1 \text{-}\tau1\text{-}t1s1 \rightarrow^* s1'; \text{final1 } s1' \rrbracket$ 
   $\implies \exists s2' t2s2. s2 \text{-}\tau2\text{-}t2s2 \rightarrow^* s2' \wedge s1' \approx s2' \wedge \text{final2 } s2' \wedge t1s1 [\sim] t2s2$ 
  <proof>

```

lemma *final-simulation2*:

```

   $\llbracket s1 \approx s2; s2 \text{-}\tau2\text{-}t2s2 \rightarrow^* s2'; \text{final2 } s2' \rrbracket$ 
   $\implies \exists s1' t1s1. s1 \text{-}\tau1\text{-}t1s1 \rightarrow^* s1' \wedge s1' \approx s2' \wedge \text{final1 } s1' \wedge t1s1 [\sim] t2s2$ 
  <proof>

```

end

locale *delay-bisimulation-measure-base* =

```

   $\text{delay-bisimulation-base} +$ 
  constrains  $\text{trsys1} :: 's1 \Rightarrow 'tl1 \Rightarrow 's1 \Rightarrow \text{bool}$ 
  and  $\text{trsys2} :: 's2 \Rightarrow 'tl2 \Rightarrow 's2 \Rightarrow \text{bool}$ 
  and  $\text{bisim} :: 's1 \Rightarrow 's2 \Rightarrow \text{bool}$ 
  and  $\text{tlsim} :: 'tl1 \Rightarrow 'tl2 \Rightarrow \text{bool}$ 
  and  $\tau\text{move1} :: 's1 \Rightarrow 'tl1 \Rightarrow 's1 \Rightarrow \text{bool}$ 
  and  $\tau\text{move2} :: 's2 \Rightarrow 'tl2 \Rightarrow 's2 \Rightarrow \text{bool}$ 
  fixes  $\mu1 :: 's1 \Rightarrow 's1 \Rightarrow \text{bool}$ 
  and  $\mu2 :: 's2 \Rightarrow 's2 \Rightarrow \text{bool}$ 

```

locale *delay-bisimulation-measure* =

```

   $\text{delay-bisimulation-measure-base} \text{ - - - } \tau\text{move1} \tau\text{move2} \mu1 \mu2 +$ 
   $\text{delay-bisimulation-obs trsys1 trsys2 bisim tlsim \tau\text{move1} \tau\text{move2}$ 
  for  $\tau\text{move1} :: 's1 \Rightarrow 'tl1 \Rightarrow 's1 \Rightarrow \text{bool}$ 

```

and $\tau\text{move}2 :: 's2 \Rightarrow 'tl2 \Rightarrow 's2 \Rightarrow \text{bool}$
and $\mu1 :: 's1 \Rightarrow 's1 \Rightarrow \text{bool}$
and $\mu2 :: 's2 \Rightarrow 's2 \Rightarrow \text{bool} +$
assumes *simulation-silent1*:
 $\llbracket s1 \approx s2; s1 \text{ } -\tau1 \rightarrow s1' \rrbracket \Longrightarrow s1' \approx s2 \wedge \mu1^{\wedge++} s1' s1 \vee (\exists s2'. s2 \text{ } -\tau2 \rightarrow s2' \wedge s1' \approx s2')$
and *simulation-silent2*:
 $\llbracket s1 \approx s2; s2 \text{ } -\tau2 \rightarrow s2' \rrbracket \Longrightarrow s1 \approx s2' \wedge \mu2^{\wedge++} s2' s2 \vee (\exists s1'. s1 \text{ } -\tau1 \rightarrow s1' \wedge s1' \approx s2')$
and *wf- $\mu1$* : *wfP* $\mu1$
and *wf- $\mu2$* : *wfP* $\mu2$
begin

lemma *delay-bisimulation-measure-flip*:

delay-bisimulation-measure trsys2 trsys1 (flip bisim) (flip tlsim) $\tau\text{move}2 \tau\text{move}1 \mu2 \mu1$
 $\langle \text{proof} \rangle$

end

lemma *delay-bisimulation-measure-flip-simps* [*flip-simps*]:

delay-bisimulation-measure trsys2 trsys1 (flip bisim) (flip tlsim) $\tau\text{move}2 \tau\text{move}1 \mu2 \mu1 =$
delay-bisimulation-measure trsys1 trsys2 bisim tlsim $\tau\text{move}1 \tau\text{move}2 \mu1 \mu2$
 $\langle \text{proof} \rangle$

context *delay-bisimulation-measure* **begin**

lemma *simulation-silentst1*:

assumes *bisim*: $s1 \approx s2$ **and** *moves*: $s1 \text{ } -\tau1 \rightarrow s1'$
shows $s1' \approx s2 \wedge \mu1^{\wedge++} s1' s1 \vee (\exists s2'. s2 \text{ } -\tau2 \rightarrow s2' \wedge s1' \approx s2')$
 $\langle \text{proof} \rangle$

lemma *simulation-silentst2*:

$\llbracket s1 \approx s2; s2 \text{ } -\tau2 \rightarrow s2' \rrbracket \Longrightarrow s1 \approx s2' \wedge \mu2^{\wedge++} s2' s2 \vee (\exists s1'. s1 \text{ } -\tau1 \rightarrow s1' \wedge s1' \approx s2')$
 $\langle \text{proof} \rangle$

lemma *τ diverge-simulation1*:

assumes *diverge1*: $s1 \text{ } -\tau1 \rightarrow \infty$
and *bisim*: $s1 \approx s2$
shows $s2 \text{ } -\tau2 \rightarrow \infty$
 $\langle \text{proof} \rangle$

lemma *τ diverge-simulation2*:

$\llbracket s2 \text{ } -\tau2 \rightarrow \infty; s1 \approx s2 \rrbracket \Longrightarrow s1 \text{ } -\tau1 \rightarrow \infty$
 $\langle \text{proof} \rangle$

lemma *τ diverge-bisim-inv*:

$s1 \approx s2 \Longrightarrow s1 \text{ } -\tau1 \rightarrow \infty \longleftrightarrow s2 \text{ } -\tau2 \rightarrow \infty$
 $\langle \text{proof} \rangle$

end

sublocale *delay-bisimulation-measure* < *delay-bisimulation-diverge*

$\langle \text{proof} \rangle$

Counter example for *delay-bisimulation-diverge trsys1 trsys2 bisim tlsim $\tau\text{move}1 \tau\text{move}2$*
 $\Longrightarrow \exists \mu1 \mu2. \text{delay-bisimulation-measure trsys1 trsys2 bisim tlsim } \tau\text{move}1 \tau\text{move}2 \mu1 \mu2$

(only τ moves):

```
--|
| v
--a ~ x
|   |
|   |
v   v
--b ~ y--
| ^   ^ |
--|   |--
```

```
locale delay-bisimulation-measure-final =
  delay-bisimulation-measure +
  delay-bisimulation-final-base +
constrains trsys1 :: ('s1, 'tl1) trsys
and trsys2 :: ('s2, 'tl2) trsys
and bisim :: ('s1, 's2) bisim
and tlsim :: ('tl1, 'tl2) bisim
and  $\tau$ move1 :: ('s1, 'tl1) trsys
and  $\tau$ move2 :: ('s2, 'tl2) trsys
and  $\mu$ 1 :: 's1  $\Rightarrow$  's1  $\Rightarrow$  bool
and  $\mu$ 2 :: 's2  $\Rightarrow$  's2  $\Rightarrow$  bool
and final1 :: 's1  $\Rightarrow$  bool
and final2 :: 's2  $\Rightarrow$  bool
```

```
sublocale delay-bisimulation-measure-final < delay-bisimulation-diverge-final
<proof>
```

```
locale  $\tau$ inv = delay-bisimulation-base +
constrains trsys1 :: ('s1, 'tl1) trsys
and trsys2 :: ('s2, 'tl2) trsys
and bisim :: ('s1, 's2) bisim
and tlsim :: ('tl1, 'tl2) bisim
and  $\tau$ move1 :: ('s1, 'tl1) trsys
and  $\tau$ move2 :: ('s2, 'tl2) trsys
and  $\tau$ moves1 :: 's1  $\Rightarrow$  's1  $\Rightarrow$  bool
and  $\tau$ moves2 :: 's2  $\Rightarrow$  's2  $\Rightarrow$  bool
assumes  $\tau$ inv:  $\llbracket s1 \approx s2; s1 -1-tl1 \rightarrow s1'; s2 -2-tl2 \rightarrow s2'; s1' \approx s2'; tl1 \sim tl2 \rrbracket$ 
 $\implies \tau$ move1 s1 tl1 s1'  $\longleftrightarrow \tau$ move2 s2 tl2 s2'
```

begin

```
lemma  $\tau$ inv-flip:
   $\tau$ inv trsys2 trsys1 (flip bisim) (flip tlsim)  $\tau$ move2  $\tau$ move1
<proof>
```

end

```
lemma  $\tau$ inv-flip-simps [flip-simps]:
   $\tau$ inv trsys2 trsys1 (flip bisim) (flip tlsim)  $\tau$ move2  $\tau$ move1 =  $\tau$ inv trsys1 trsys2 bisim tlsim  $\tau$ move1
 $\tau$ move2
<proof>
```

locale *bisimulation-into-delay* =
bisimulation + τ *inv* +
constrains *trsys1* :: ('s1, 'tl1) *trsys*
and *trsys2* :: ('s2, 'tl2) *trsys*
and *bisim* :: ('s1, 's2) *bisim*
and *tlsim* :: ('tl1, 'tl2) *bisim*
and τ *move1* :: ('s1, 'tl1) *trsys*
and τ *move2* :: ('s2, 'tl2) *trsys*
begin

lemma *bisimulation-into-delay-flip*:

bisimulation-into-delay trsys2 trsys1 (flip bisim) (flip tlsim) τ move2 τ move1
<proof>

end

lemma *bisimulation-into-delay-flip-simps* [*flip-simps*]:

bisimulation-into-delay trsys2 trsys1 (flip bisim) (flip tlsim) τ move2 τ move1 =
bisimulation-into-delay trsys1 trsys2 bisim tlsim τ move1 τ move2
<proof>

context *bisimulation-into-delay* **begin**

lemma *simulation-silent1-aux*:

assumes *bisim*: $s1 \approx s2$ **and** $s1 \xrightarrow{-\tau} s1'$
shows $s1' \approx s2 \wedge \mu1^{++} s1' s1 \vee (\exists s2'. s2 \xrightarrow{-\tau} s2' \wedge s1' \approx s2')$
<proof>

lemma *simulation-silent2-aux*:

$\llbracket s1 \approx s2; s2 \xrightarrow{-\tau} s2' \rrbracket \implies s1 \approx s2' \wedge \mu2^{++} s2' s2 \vee (\exists s1'. s1 \xrightarrow{-\tau} s1' \wedge s1' \approx s2')$
<proof>

lemma *simulation1-aux*:

assumes *bisim*: $s1 \approx s2$ **and** *tr1*: $s1 \xrightarrow{-1-tl1} s1'$ **and** $\tau1$: $\neg \tau$ *move1* $s1$ $tl1$ $s1'$
shows $\exists s2' s2'' tl2. s2 \xrightarrow{-\tau} s2' \wedge s2' \xrightarrow{-2-tl2} s2'' \wedge \neg \tau$ *move2* $s2' tl2 s2'' \wedge s1' \approx s2'' \wedge$
 $tl1 \sim tl2$
<proof>

lemma *simulation2-aux*:

$\llbracket s1 \approx s2; s2 \xrightarrow{-2-tl2} s2'; \neg \tau$ *move2* $s2 tl2 s2' \rrbracket$
 $\implies \exists s1' s1'' tl1. s1 \xrightarrow{-\tau} s1' \wedge s1' \xrightarrow{-1-tl1} s1'' \wedge \neg \tau$ *move1* $s1' tl1 s1'' \wedge s1'' \approx s2' \wedge$
 $tl1 \sim tl2$
<proof>

lemma *delay-bisimulation-measure*:

assumes *wf- $\mu1$* : *wfP* $\mu1$
and *wf- $\mu2$* : *wfP* $\mu2$
shows *delay-bisimulation-measure trsys1 trsys2 bisim tlsim τ move1 τ move2 $\mu1 \mu2$*
<proof>

lemma *delay-bisimulation*:

delay-bisimulation-diverge trsys1 trsys2 bisim tlsim τ move1 τ move2
<proof>

end

sublocale *bisimulation-into-delay* < *delay-bisimulation-diverge*
 ⟨*proof*⟩

lemma *delay-bisimulation-conv-bisimulation*:

delay-bisimulation-diverge *trsys1* *trsys2* *bisim* *tlsim* ($\lambda s\ tl\ s'.\ False$) ($\lambda s\ tl\ s'.\ False$) =
bisimulation *trsys1* *trsys2* *bisim* *tlsim*
 (is ?lhs = ?rhs)
 ⟨*proof*⟩

context *bisimulation-final* **begin**

lemma *delay-bisimulation-final-base*:

delay-bisimulation-final-base *trsys1* *trsys2* *bisim* τ *move1* τ *move2* *final1* *final2*
 ⟨*proof*⟩

end

sublocale *bisimulation-final* < *delay-bisimulation-final-base*
 ⟨*proof*⟩

1.17.3 Transitivity for bisimulations

definition *bisim-compose* :: (*'s1*, *'s2*) *bisim* \Rightarrow (*'s2*, *'s3*) *bisim* \Rightarrow (*'s1*, *'s3*) *bisim* (**infixr** \circ_B) 60)
where (*bisim1* \circ_B *bisim2*) *s1* *s3* $\equiv \exists s2.$ *bisim1* *s1* *s2* \wedge *bisim2* *s2* *s3*

lemma *bisim-composeI* [*intro*]:

$\llbracket \textit{bisim12}\ s1\ s2; \textit{bisim23}\ s2\ s3 \rrbracket \Longrightarrow (\textit{bisim12} \circ_B \textit{bisim23})\ s1\ s3$
 ⟨*proof*⟩

lemma *bisim-composeE* [*elim!*]:

assumes *bisim*: (*bisim12* \circ_B *bisim23*) *s1* *s3*
obtains *s2* **where** *bisim12* *s1* *s2* *bisim23* *s2* *s3*
 ⟨*proof*⟩

lemma *bisim-compose-assoc* [*simp*]:

(*bisim12* \circ_B *bisim23*) \circ_B *bisim34* = *bisim12* \circ_B *bisim23* \circ_B *bisim34*
 ⟨*proof*⟩

lemma *bisim-compose-conv-relcomp*:

case-prod (*bisim-compose* *bisim12* *bisim23*) = ($\lambda x.$ *x* \in *relcomp* (*Collect* (*case-prod* *bisim12*))) (*Collect* (*case-prod* *bisim23*)))
 ⟨*proof*⟩

lemma *list-all2-bisim-composeI*:

$\llbracket \textit{list-all2}\ A\ xs\ ys; \textit{list-all2}\ B\ ys\ zs \rrbracket$
 $\Longrightarrow \textit{list-all2}\ (A \circ_B B)\ xs\ zs$
 ⟨*proof*⟩

lemma *delay-bisimulation-diverge-compose*:

assumes *wbisim12*: *delay-bisimulation-diverge* *trsys1* *trsys2* *bisim12* *tlsim12* τ *move1* τ *move2*
and *wbisim23*: *delay-bisimulation-diverge* *trsys2* *trsys3* *bisim23* *tlsim23* τ *move2* τ *move3*
shows *delay-bisimulation-diverge* *trsys1* *trsys3* (*bisim12* \circ_B *bisim23*) (*tlsim12* \circ_B *tlsim23*) τ *move1*

$\tau move3$
 $\langle proof \rangle$

lemma *bisimulation-bisim-compose*:

$\llbracket bisimulation\ trsys1\ trsys2\ bisim12\ tlim12; bisimulation\ trsys2\ trsys3\ bisim23\ tlim23 \rrbracket$
 $\implies bisimulation\ trsys1\ trsys3\ (bisim-compose\ bisim12\ bisim23)\ (bisim-compose\ tlim12\ tlim23)$
 $\langle proof \rangle$

lemma *delay-bisimulation-diverge-final-compose*:

fixes $\tau move1\ \tau move2$
assumes $wbisim12: delay-bisimulation-diverge-final\ trsys1\ trsys2\ bisim12\ tlim12\ \tau move1\ \tau move2$
 $final1\ final2$
and $wbisim23: delay-bisimulation-diverge-final\ trsys2\ trsys3\ bisim23\ tlim23\ \tau move2\ \tau move3\ final2$
 $final3$
shows $delay-bisimulation-diverge-final\ trsys1\ trsys3\ (bisim12\ \circ_B\ bisim23)\ (tlim12\ \circ_B\ tlim23)$
 $\tau move1\ \tau move3\ final1\ final3$
 $\langle proof \rangle$

end

1.18 Bisimulation relations for the multithreaded semantics

theory *FWBisimulation*

imports

FWLTS

Bisimulation

begin

1.18.1 Definitions for lifting bisimulation relations

primrec *nta-bisim* :: $(t \Rightarrow (x1 \times m1, x2 \times m2)\ bisim) \Rightarrow ((t, x1, m1)\ new-thread-action,$
 $(t, x2, m2)\ new-thread-action)\ bisim$

where

$[code\ del]: nta-bisim\ bisim\ (NewThread\ t\ x\ m)\ ta = (\exists x'\ m'. ta = NewThread\ t\ x'\ m' \wedge bisim\ t\ (x,$
 $m)\ (x',\ m'))$

$| nta-bisim\ bisim\ (ThreadExists\ t\ b)\ ta = (ta = ThreadExists\ t\ b)$

lemma *nta-bisim-1-code* $[code]$:

$nta-bisim\ bisim\ (NewThread\ t\ x\ m)\ ta = (case\ ta\ of\ NewThread\ t'\ x'\ m' \Rightarrow t = t' \wedge bisim\ t\ (x,\ m)$
 $(x',\ m')\ | - \Rightarrow False)$

$\langle proof \rangle$

lemma *nta-bisim-simps-sym* $[simp]$:

$nta-bisim\ bisim\ ta\ (NewThread\ t\ x\ m) = (\exists x'\ m'. ta = NewThread\ t\ x'\ m' \wedge bisim\ t\ (x',\ m')\ (x,\ m))$
 $nta-bisim\ bisim\ ta\ (ThreadExists\ t\ b) = (ta = ThreadExists\ t\ b)$

$\langle proof \rangle$

definition *ta-bisim* :: $(t \Rightarrow (x1 \times m1, x2 \times m2)\ bisim) \Rightarrow ((l, t, x1, m1, w, o)\ thread-action,$
 $(l, t, x2, m2, w, o)\ thread-action)\ bisim$

where

$ta-bisim\ bisim\ ta1\ ta2 \equiv$

$\{\!\! \{ ta1 \}\!\!\}_l = \{\!\! \{ ta2 \}\!\!\}_l \wedge \{\!\! \{ ta1 \}\!\!\}_w = \{\!\! \{ ta2 \}\!\!\}_w \wedge \{\!\! \{ ta1 \}\!\!\}_c = \{\!\! \{ ta2 \}\!\!\}_c \wedge \{\!\! \{ ta1 \}\!\!\}_o = \{\!\! \{ ta2 \}\!\!\}_o \wedge \{\!\! \{ ta1 \}\!\!\}_i = \{\!\! \{ ta2 \}\!\!\}_i \wedge$

$list-all2\ (nta-bisim\ bisim)\ \{\!\! \{ ta1 \}\!\!\}_t\ \{\!\! \{ ta2 \}\!\!\}_t$

lemma *ta-bisim-empty* [*iff*]: *ta-bisim bisim* ε ε
 ⟨*proof*⟩

lemma *ta-bisim- ε* [*simp*]:
ta-bisim $b \varepsilon ta'$ $\longleftrightarrow ta' = \varepsilon ta$ -*bisim* $b ta \varepsilon \longleftrightarrow ta = \varepsilon$
 ⟨*proof*⟩

lemma *nta-bisim-mono*:
assumes *major*: *nta-bisim bisim* $ta ta'$
and *mono*: $\bigwedge t s1 s2. bisim t s1 s2 \implies bisim' t s1 s2$
shows *nta-bisim bisim'* $ta ta'$
 ⟨*proof*⟩

lemma *ta-bisim-mono*:
assumes *major*: *ta-bisim bisim* $ta1 ta2$
and *mono*: $\bigwedge t s1 s2. bisim t s1 s2 \implies bisim' t s1 s2$
shows *ta-bisim bisim'* $ta1 ta2$
 ⟨*proof*⟩

lemma *nta-bisim-flip* [*flip-simps*]:
nta-bisim ($\lambda t. flip (bisim t)$) = *flip* (*nta-bisim bisim*)
 ⟨*proof*⟩

lemma *ta-bisim-flip* [*flip-simps*]:
ta-bisim ($\lambda t. flip (bisim t)$) = *flip* (*ta-bisim bisim*)
 ⟨*proof*⟩

locale *FWbisimulation-base* =
r1: *multithreaded-base final1 r1 convert-RA* +
r2: *multithreaded-base final2 r2 convert-RA*
for *final1* :: $'x1 \Rightarrow bool$
and *r1* :: $(l, 't, 'x1, 'm1, 'w, 'o)$ *semantics* ($\langle \cdot \vdash - -1 \dashrightarrow \cdot \rangle$ [50, 0, 0, 50] 80)
and *final2* :: $'x2 \Rightarrow bool$
and *r2* :: $(l, 't, 'x2, 'm2, 'w, 'o)$ *semantics* ($\langle \cdot \vdash - -2 \dashrightarrow \cdot \rangle$ [50, 0, 0, 50] 80)
and *convert-RA* :: l *released-locks* $\Rightarrow 'o$ *list*
 +
fixes *bisim* :: $t \Rightarrow ('x1 \times 'm1, 'x2 \times 'm2)$ *bisim* ($\langle \cdot \vdash - / \approx \cdot \rangle$ [50, 50, 50] 60)
and *bisim-wait* :: $(x1, x2)$ *bisim* ($\langle \cdot / \approx_w \cdot \rangle$ [50, 50] 60)
begin

notation *r1.redT-syntax1* ($\langle \cdot -1 \dashrightarrow \cdot \rangle$ [50, 0, 0, 50] 80)
notation *r2.redT-syntax1* ($\langle \cdot -2 \dashrightarrow \cdot \rangle$ [50, 0, 0, 50] 80)

notation *r1.RedT* ($\langle \cdot -1 \dashrightarrow \cdot \rangle^*$ [50, 0, 50] 80)
notation *r2.RedT* ($\langle \cdot -2 \dashrightarrow \cdot \rangle^*$ [50, 0, 50] 80)

notation *r1.must-sync* ($\langle \cdot \vdash \langle \cdot / \cdot \rangle / \lambda 1 \rangle$ [50, 0, 0] 81)
notation *r2.must-sync* ($\langle \cdot \vdash \langle \cdot / \cdot \rangle / \lambda 2 \rangle$ [50, 0, 0] 81)

notation *r1.can-sync* ($\langle \cdot \vdash \langle \cdot / \cdot \rangle / - / \lambda 1 \rangle$ [50, 0, 0, 0] 81)
notation *r2.can-sync* ($\langle \cdot \vdash \langle \cdot / \cdot \rangle / - / \lambda 2 \rangle$ [50, 0, 0, 0] 81)

abbreviation *ta-bisim-bisim-syntax* ($\langle \cdot / \sim_m \cdot \rangle$ [50, 50] 60)

where $ta1 \sim_m ta2 \equiv ta\text{-bisim bisim } ta1 ta2$

definition $t\text{bisim} :: \text{bool} \Rightarrow 't \Rightarrow ('x1 \times 'l \text{ released-locks}) \text{ option} \Rightarrow 'm1 \Rightarrow ('x2 \times 'l \text{ released-locks}) \text{ option} \Rightarrow 'm2 \Rightarrow \text{bool}$ **where**

$$\bigwedge ln. t\text{bisim } nw t ts1 m1 ts2 m2 \longleftrightarrow$$

$$(\text{case } ts1 \text{ of } \text{None} \Rightarrow ts2 = \text{None}$$

$$| [(x1, ln)] \Rightarrow (\exists x2. ts2 = [(x2, ln)] \wedge t \vdash (x1, m1) \approx (x2, m2) \wedge (nw \vee x1 \approx_w x2)))$$

lemma $t\text{bisim-NoneI}$: $t\text{bisim } w t \text{None } m \text{None } m'$
 $\langle \text{proof} \rangle$

lemma $t\text{bisim-SomeI}$:

$$\bigwedge ln. \llbracket t \vdash (x, m) \approx (x', m'); nw \vee x \approx_w x' \rrbracket \Longrightarrow t\text{bisim } nw t (\text{Some } (x, ln)) m (\text{Some } (x', ln)) m'$$
 $\langle \text{proof} \rangle$

lemma $t\text{bisim-cases}$ [*consumes 1, case-names None Some*]:

assumes *major*: $t\text{bisim } nw t ts1 m1 ts2 m2$

and $\llbracket ts1 = \text{None}; ts2 = \text{None} \rrbracket \Longrightarrow \text{thesis}$

and $\bigwedge x ln x'. \llbracket ts1 = [(x, ln)]; ts2 = [(x', ln)]; t \vdash (x, m1) \approx (x', m2); nw \vee x \approx_w x' \rrbracket \Longrightarrow \text{thesis}$

shows *thesis*

$\langle \text{proof} \rangle$

definition $mbisim :: (('l, 't, 'x1, 'm1, 'w) \text{ state}, ('l, 't, 'x2, 'm2, 'w) \text{ state}) \text{ bisim } (\prec \approx_m \rightarrow [50, 50] 60)$
where

$$s1 \approx_m s2 \equiv$$

$$\text{finite } (\text{dom } (\text{thr } s1)) \wedge \text{locks } s1 = \text{locks } s2 \wedge \text{wset } s1 = \text{wset } s2 \wedge \text{wset-thread-ok } (\text{wset } s1) (\text{thr } s1)$$

\wedge

$$\text{interrupts } s1 = \text{interrupts } s2 \wedge$$

$$(\forall t. t\text{bisim } (\text{wset } s2 t = \text{None}) t (\text{thr } s1 t) (\text{shr } s1) (\text{thr } s2 t) (\text{shr } s2))$$

lemma $mbisim-thrNone\text{-eq}$: $s1 \approx_m s2 \Longrightarrow \text{thr } s1 t = \text{None} \longleftrightarrow \text{thr } s2 t = \text{None}$
 $\langle \text{proof} \rangle$

lemma $mbisim-thrD1$:

$$\bigwedge ln. \llbracket s1 \approx_m s2; \text{thr } s1 t = [(x, ln)] \rrbracket$$

$$\Longrightarrow \exists x'. \text{thr } s2 t = [(x', ln)] \wedge t \vdash (x, \text{shr } s1) \approx (x', \text{shr } s2) \wedge (\text{wset } s1 t = \text{None} \vee x \approx_w x')$$

$\langle \text{proof} \rangle$

lemma $mbisim-thrD2$:

$$\bigwedge ln. \llbracket s1 \approx_m s2; \text{thr } s2 t = [(x, ln)] \rrbracket$$

$$\Longrightarrow \exists x'. \text{thr } s1 t = [(x', ln)] \wedge t \vdash (x', \text{shr } s1) \approx (x, \text{shr } s2) \wedge (\text{wset } s2 t = \text{None} \vee x' \approx_w x)$$

$\langle \text{proof} \rangle$

lemma $mbisim\text{-dom}\text{-eq}$: $s1 \approx_m s2 \Longrightarrow \text{dom } (\text{thr } s1) = \text{dom } (\text{thr } s2)$
 $\langle \text{proof} \rangle$

lemma $mbisim\text{-wset}\text{-thread}\text{-ok}1$:

$$s1 \approx_m s2 \Longrightarrow \text{wset-thread-ok } (\text{wset } s1) (\text{thr } s1)$$

$\langle \text{proof} \rangle$

lemma $mbisim\text{-wset}\text{-thread}\text{-ok}2$:

assumes $s1 \approx_m s2$

shows $\text{wset-thread-ok } (\text{wset } s2) (\text{thr } s2)$

$\langle \text{proof} \rangle$

lemma mbisimI:

[[finite (dom (thr s1)); locks s1 = locks s2; wset s1 = wset s2; interrupts s1 = interrupts s2;
 wset-thread-ok (wset s1) (thr s1);
 $\bigwedge t. \text{thr } s1 \ t = \text{None} \implies \text{thr } s2 \ t = \text{None};$
 $\bigwedge t \ x1 \ \text{ln}. \text{thr } s1 \ t = [(x1, \text{ln})] \implies \exists x2. \text{thr } s2 \ t = [(x2, \text{ln})] \wedge t \vdash (x1, \text{shr } s1) \approx (x2, \text{shr } s2)$
 $\wedge (\text{wset } s2 \ t = \text{None} \vee x1 \approx_w x2)$]
 $\implies s1 \approx_m s2$
 <proof>

lemma mbisimI2:

[[finite (dom (thr s2)); locks s1 = locks s2; wset s1 = wset s2; interrupts s1 = interrupts s2;
 wset-thread-ok (wset s2) (thr s2);
 $\bigwedge t. \text{thr } s2 \ t = \text{None} \implies \text{thr } s1 \ t = \text{None};$
 $\bigwedge t \ x2 \ \text{ln}. \text{thr } s2 \ t = [(x2, \text{ln})] \implies \exists x1. \text{thr } s1 \ t = [(x1, \text{ln})] \wedge t \vdash (x1, \text{shr } s1) \approx (x2, \text{shr } s2)$
 $\wedge (\text{wset } s2 \ t = \text{None} \vee x1 \approx_w x2)$]
 $\implies s1 \approx_m s2$
 <proof>

lemma mbisim-finite1:

$s1 \approx_m s2 \implies \text{finite } (\text{dom } (\text{thr } s1))$
 <proof>

lemma mbisim-finite2:

$s1 \approx_m s2 \implies \text{finite } (\text{dom } (\text{thr } s2))$
 <proof>

definition mta-bisim :: $(t \times (l, t, x1, m1, w, o)$ thread-action,
 $t \times (l, t, x2, m2, w, o)$ thread-action) bisim
 ($\langle \cdot \rangle \sim T \rightarrow [50, 50]$ 60)

where $tta1 \sim T tta2 \equiv \text{fst } tta1 = \text{fst } tta2 \wedge \text{snd } tta1 \sim_m \text{snd } tta2$

lemma mta-bisim-conv [simp]: $(t, ta1) \sim T (t', ta2) \longleftrightarrow t = t' \wedge ta1 \sim_m ta2$

<proof>

definition bisim-inv :: bool **where**

$\text{bisim-inv} \equiv (\forall s1 \ ta1 \ s1' \ s2 \ t. t \vdash s1 \approx s2 \longrightarrow t \vdash s1 \ -1\text{-}ta1 \rightarrow s1' \longrightarrow (\exists s2'. t \vdash s1' \approx s2')) \wedge$
 $(\forall s2 \ ta2 \ s2' \ s1 \ t. t \vdash s1 \approx s2 \longrightarrow t \vdash s2 \ -2\text{-}ta2 \rightarrow s2' \longrightarrow (\exists s1'. t \vdash s1' \approx s2'))$

lemma bisim-invI:

[[$\bigwedge s1 \ ta1 \ s1' \ s2 \ t. \llbracket t \vdash s1 \approx s2; t \vdash s1 \ -1\text{-}ta1 \rightarrow s1' \rrbracket \implies \exists s2'. t \vdash s1' \approx s2';$
 $\bigwedge s2 \ ta2 \ s2' \ s1 \ t. \llbracket t \vdash s1 \approx s2; t \vdash s2 \ -2\text{-}ta2 \rightarrow s2' \rrbracket \implies \exists s1'. t \vdash s1' \approx s2'$]
 $\implies \text{bisim-inv}$
 <proof>

lemma bisim-invD1:

[[$\text{bisim-inv}; t \vdash s1 \approx s2; t \vdash s1 \ -1\text{-}ta1 \rightarrow s1' \rrbracket \implies \exists s2'. t \vdash s1' \approx s2'$
 <proof>

lemma bisim-invD2:

[[$\text{bisim-inv}; t \vdash s1 \approx s2; t \vdash s2 \ -2\text{-}ta2 \rightarrow s2' \rrbracket \implies \exists s1'. t \vdash s1' \approx s2'$
 <proof>

lemma thread-oks-bisim-inv:

$$\llbracket \forall t. ts1\ t = \text{None} \longleftrightarrow ts2\ t = \text{None}; \text{list-all2}\ (nta\text{-bisim}\ \text{bisim})\ tas1\ tas2 \rrbracket$$

$$\implies \text{thread-oks}\ ts1\ tas1 \longleftrightarrow \text{thread-oks}\ ts2\ tas2$$
 <proof>

lemma *redT-updT-nta-bisim-inv*:

$$\llbracket nta\text{-bisim}\ \text{bisim}\ ta1\ ta2; ts1\ T = \text{None} \longleftrightarrow ts2\ T = \text{None} \rrbracket \implies \text{redT-updT}\ ts1\ ta1\ T = \text{None}$$

$$\longleftrightarrow \text{redT-updT}\ ts2\ ta2\ T = \text{None}$$
 <proof>

lemma *redT-updTs-nta-bisim-inv*:

$$\llbracket \text{list-all2}\ (nta\text{-bisim}\ \text{bisim})\ tas1\ tas2; ts1\ T = \text{None} \longleftrightarrow ts2\ T = \text{None} \rrbracket$$

$$\implies \text{redT-updTs}\ ts1\ tas1\ T = \text{None} \longleftrightarrow \text{redT-updTs}\ ts2\ tas2\ T = \text{None}$$
 <proof>

end

lemma *tbisim-flip [flip-simps]*:

$$FW\text{bisimulation-base.tbisim}\ (\lambda t. \text{flip}\ (\text{bisim}\ t))\ (\text{flip}\ \text{bisim-wait})\ w\ t\ ts2\ m2\ ts1\ m1 =$$

$$FW\text{bisimulation-base.tbisim}\ \text{bisim}\ \text{bisim-wait}\ w\ t\ ts1\ m1\ ts2\ m2$$
 <proof>

lemma *mbisim-flip [flip-simps]*:

$$FW\text{bisimulation-base.mbisim}\ (\lambda t. \text{flip}\ (\text{bisim}\ t))\ (\text{flip}\ \text{bisim-wait})\ s2\ s1 =$$

$$FW\text{bisimulation-base.mbisim}\ \text{bisim}\ \text{bisim-wait}\ s1\ s2$$
 <proof>

lemma *mta-bisim-flip [flip-simps]*:

$$FW\text{bisimulation-base.mta-bisim}\ (\lambda t. \text{flip}\ (\text{bisim}\ t)) = \text{flip}\ (FW\text{bisimulation-base.mta-bisim}\ \text{bisim})$$
 <proof>

lemma *flip-const [simp]*: $\text{flip}\ (\lambda a\ b. c) = (\lambda a\ b. c)$

<proof>

lemma *mbisim-K-flip [flip-simps]*:

$$FW\text{bisimulation-base.mbisim}\ (\lambda t. \text{flip}\ (\text{bisim}\ t))\ (\lambda x1\ x2. c)\ s1\ s2 =$$

$$FW\text{bisimulation-base.mbisim}\ \text{bisim}\ (\lambda x1\ x2. c)\ s2\ s1$$
 <proof>

context *FWbisimulation-base begin*

lemma *mbisim-actions-ok-bisim-no-join-12*:

assumes *mbisim*: $mbisim\ s1\ s2$
and *collect-cond-actions* $\{\{ta1\}\}_c = \{\}$
and *ta-bisim* $\text{bisim}\ ta1\ ta2$
and *r1.actions-ok* $s1\ t\ ta1$
shows *r2.actions-ok* $s2\ t\ ta2$

<proof>

lemma *mbisim-actions-ok-bisim-no-join-21*:

$$\llbracket mbisim\ s1\ s2; \text{collect-cond-actions}\ \{\{ta2\}\}_c = \{\}; \text{ta-bisim}\ \text{bisim}\ ta1\ ta2; r2.\text{actions-ok}\ s2\ t\ ta2 \rrbracket$$

$$\implies r1.\text{actions-ok}\ s1\ t\ ta1$$

<proof>

lemma *mbisim-actions-ok-bisim-no-join*:

```

[[ mbisim s1 s2; collect-cond-actions {ta1}_c = {}; ta-bisim bisim ta1 ta2 ]]
  ==> r1.actions-ok s1 t ta1 = r2.actions-ok s2 t ta2
<proof>

```

end

```

locale FWbisimulation-base-aux = FWbisimulation-base +
  r1: multithreaded final1 r1 convert-RA +
  r2: multithreaded final2 r2 convert-RA +
  constrains final1 :: 'x1 => bool
  and r1 :: ('l,'t,'x1,'m1,'w, 'o) semantics
  and final2 :: 'x2 => bool
  and r2 :: ('l,'t,'x2,'m2,'w, 'o) semantics
  and convert-RA :: 'l released-locks => 'o list
  and bisim :: 't => ('x1 × 'm1, 'x2 × 'm2) bisim
  and bisim-wait :: ('x1, 'x2) bisim

```

begin

lemma FWbisimulation-base-aux-flip:

```

  FWbisimulation-base-aux final2 r2 final1 r1
<proof>

```

end

lemma FWbisimulation-base-aux-flip-simps [flip-simps]:

```

  FWbisimulation-base-aux final2 r2 final1 r1 = FWbisimulation-base-aux final1 r1 final2 r2
<proof>

```

sublocale FWbisimulation-base-aux < mthr:

```

  bisimulation-final-base
  r1.redT
  r2.redT
  mbisim
  mta-bisim
  r1.mfinal
  r2.mfinal

```

<proof>

declare split-paired-Ex [simp del]

1.18.2 Lifting for delay bisimulations

locale FWdelay-bisimulation-base =

```

  FWbisimulation-base - - - r2 convert-RA bisim bisim-wait +
  r1: τmultithreaded final1 r1 convert-RA τmove1 +
  r2: τmultithreaded final2 r2 convert-RA τmove2
  for r2 :: ('l,'t,'x2,'m2,'w,'o) semantics (◁ ⊢ - - 2 → → [50,0,0,50] 80)
  and convert-RA :: 'l released-locks => 'o list
  and bisim :: 't => ('x1 × 'm1, 'x2 × 'm2) bisim (◁ ⊢ - / ≈ → [50, 50, 50] 60)
  and bisim-wait :: ('x1, 'x2) bisim (◁ - / ≈ w → [50, 50] 60)
  and τmove1 :: ('l,'t,'x1,'m1,'w,'o) τmoves
  and τmove2 :: ('l,'t,'x2,'m2,'w,'o) τmoves

```

begin

abbreviation $\tau mred1 :: ('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow ('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow \text{bool}$
where $\tau mred1 \equiv r1.\tau mredT$

abbreviation $\tau mred2 :: ('l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow ('l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow \text{bool}$
where $\tau mred2 \equiv r2.\tau mredT$

abbreviation $m\tau move1 :: (('l, 't, 'x1, 'm1, 'w) \text{ state}, 't \times ('l, 't, 'x1, 'm1, 'w, 'o) \text{ thread-action}) \text{ trsys}$
where $m\tau move1 \equiv r1.m\tau move$

abbreviation $m\tau move2 :: (('l, 't, 'x2, 'm2, 'w) \text{ state}, 't \times ('l, 't, 'x2, 'm2, 'w, 'o) \text{ thread-action}) \text{ trsys}$
where $m\tau move2 \equiv r2.m\tau move$

abbreviation $\tau mRed1 :: ('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow ('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow \text{bool}$
where $\tau mRed1 \equiv \tau mred1^{\wedge**}$

abbreviation $\tau mRed2 :: ('l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow ('l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow \text{bool}$
where $\tau mRed2 \equiv \tau mred2^{\wedge**}$

abbreviation $\tau mtRed1 :: ('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow ('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow \text{bool}$
where $\tau mtRed1 \equiv \tau mred1^{\wedge++}$

abbreviation $\tau mtRed2 :: ('l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow ('l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow \text{bool}$
where $\tau mtRed2 \equiv \tau mred2^{\wedge++}$

lemma *bisim-inv- $\tau s1$ -inv*:
assumes *inv*: *bisim-inv*
and *bisim*: $t \vdash s1 \approx s2$
and *red*: $r1.\text{silent-moves } t \ s1 \ s1'$
obtains $s2'$ **where** $t \vdash s1' \approx s2'$
 $\langle \text{proof} \rangle$

lemma *bisim-inv- $\tau s2$ -inv*:
assumes *inv*: *bisim-inv*
and *bisim*: $t \vdash s1 \approx s2$
and *red*: $r2.\text{silent-moves } t \ s2 \ s2'$
obtains $s1'$ **where** $t \vdash s1' \approx s2'$
 $\langle \text{proof} \rangle$

primrec *activate-cond-action1* :: $('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow ('l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow$
 $'t \text{ conditional-action} \Rightarrow ('l, 't, 'x1, 'm1, 'w) \text{ state}$

where

activate-cond-action1 $s1 \ s2 \ (\text{Join } t) =$
 $(\text{case } \text{thr } s1 \ t \ \text{of } \text{None} \Rightarrow s1$
 $\quad | \ [(x1, \text{ln}1)] \Rightarrow (\text{case } \text{thr } s2 \ t \ \text{of } \text{None} \Rightarrow s1$
 $\quad \quad | \ [(x2, \text{ln}2)] \Rightarrow$
 $\text{if } \text{final}2 \ x2 \ \wedge \ \text{ln}2 = \text{no-wait-locks}$
 $\text{then } \text{redT-upd-}\varepsilon \ s1 \ t$
 $\quad (\text{SOME } x1'. \ r1.\text{silent-moves } t \ (x1, \text{shr } s1) \ (x1', \text{shr } s1) \ \wedge \ \text{final}1 \ x1' \ \wedge$
 $\quad \quad t \vdash (x1', \text{shr } s1) \approx (x2, \text{shr } s2))$
 $\quad (\text{shr } s1)$
 $\text{else } s1))$
 $| \ \text{activate-cond-action1 } s1 \ s2 \ \text{Yield} = s1$

primrec *activate-cond-actions1* :: $('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow ('l, 't, 'x2, 'm2, 'w) \text{ state}$

$$\Rightarrow ('t \text{ conditional-action}) \text{ list} \Rightarrow ('l, 't, 'x1, 'm1, 'w) \text{ state}$$

where

$activate\text{-}cond\text{-}actions1 \ s1 \ s2 \ [] = s1$
 $| activate\text{-}cond\text{-}actions1 \ s1 \ s2 \ (ct \ \# \ cts) = activate\text{-}cond\text{-}actions1 \ (activate\text{-}cond\text{-}action1 \ s1 \ s2 \ ct) \ s2 \ cts$

primrec $activate\text{-}cond\text{-}action2 :: ('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow ('l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow$
 $'t \text{ conditional-action} \Rightarrow ('l, 't, 'x2, 'm2, 'w) \text{ state}$

where

$activate\text{-}cond\text{-}action2 \ s1 \ s2 \ (Join \ t) =$
 $(case \ thr \ s2 \ t \ of \ None \ \Rightarrow \ s2$
 $\quad | \ [(x2, \ ln2)] \ \Rightarrow \ (case \ thr \ s1 \ t \ of \ None \ \Rightarrow \ s2$
 $\quad \quad | \ [(x1, \ ln1)] \ \Rightarrow$
 $\quad \quad \quad if \ final1 \ x1 \ \wedge \ ln1 = no\text{-}wait\text{-}locks$
 $\quad \quad \quad then \ redT\text{-}upd\text{-}\varepsilon \ s2 \ t$
 $\quad \quad \quad \quad (SOME \ x2'. \ r2.\text{silent}\text{-}moves \ t \ (x2, \ shr \ s2) \ (x2', \ shr \ s2) \ \wedge \ final2 \ x2' \ \wedge$
 $\quad \quad \quad \quad \quad t \vdash (x1, \ shr \ s1) \approx (x2', \ shr \ s2))$
 $\quad \quad \quad \quad (shr \ s2)$
 $\quad \quad \quad else \ s2))$
 $| activate\text{-}cond\text{-}action2 \ s1 \ s2 \ Yield = s2$

primrec $activate\text{-}cond\text{-}actions2 :: ('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow ('l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow$
 $('t \text{ conditional-action}) \text{ list} \Rightarrow ('l, 't, 'x2, 'm2, 'w) \text{ state}$

where

$activate\text{-}cond\text{-}actions2 \ s1 \ s2 \ [] = s2$
 $| activate\text{-}cond\text{-}actions2 \ s1 \ s2 \ (ct \ \# \ cts) = activate\text{-}cond\text{-}actions2 \ s1 \ (activate\text{-}cond\text{-}action2 \ s1 \ s2 \ ct) \ cts$

end

lemma $activate\text{-}cond\text{-}action1\text{-}flip \ [flip\text{-}simps]:$

$FWdelay\text{-}bisimulation\text{-}base.activate\text{-}cond\text{-}action1 \ final2 \ r2 \ final1 \ (\lambda t. \ flip \ (bisim \ t)) \ \tau\text{move2} \ s2 \ s1 =$
 $FWdelay\text{-}bisimulation\text{-}base.activate\text{-}cond\text{-}action2 \ final1 \ final2 \ r2 \ bisim \ \tau\text{move2} \ s1 \ s2$
 $\langle proof \rangle$

lemma $activate\text{-}cond\text{-}actions1\text{-}flip \ [flip\text{-}simps]:$

$FWdelay\text{-}bisimulation\text{-}base.activate\text{-}cond\text{-}actions1 \ final2 \ r2 \ final1 \ (\lambda t. \ flip \ (bisim \ t)) \ \tau\text{move2} \ s2 \ s1 =$
 $FWdelay\text{-}bisimulation\text{-}base.activate\text{-}cond\text{-}actions2 \ final1 \ final2 \ r2 \ bisim \ \tau\text{move2} \ s1 \ s2$
 $(is \ ?lhs = ?rhs)$
 $\langle proof \rangle$

lemma $activate\text{-}cond\text{-}action2\text{-}flip \ [flip\text{-}simps]:$

$FWdelay\text{-}bisimulation\text{-}base.activate\text{-}cond\text{-}action2 \ final2 \ final1 \ r1 \ (\lambda t. \ flip \ (bisim \ t)) \ \tau\text{move1} \ s2 \ s1 =$
 $FWdelay\text{-}bisimulation\text{-}base.activate\text{-}cond\text{-}action1 \ final1 \ r1 \ final2 \ bisim \ \tau\text{move1} \ s1 \ s2$
 $\langle proof \rangle$

lemma $activate\text{-}cond\text{-}actions2\text{-}flip \ [flip\text{-}simps]:$

$FWdelay\text{-}bisimulation\text{-}base.activate\text{-}cond\text{-}actions2 \ final2 \ final1 \ r1 \ (\lambda t. \ flip \ (bisim \ t)) \ \tau\text{move1} \ s2 \ s1 =$
 $FWdelay\text{-}bisimulation\text{-}base.activate\text{-}cond\text{-}actions1 \ final1 \ r1 \ final2 \ bisim \ \tau\text{move1} \ s1 \ s2$
 $(is \ ?lhs = ?rhs)$
 $\langle proof \rangle$

context *FWdelay-bisimulation-base* **begin**

lemma *shr-activate-cond-action1* [*simp*]: $\text{shr} (\text{activate-cond-action1 } s1 \ s2 \ ct) = \text{shr } s1$
 $\langle \text{proof} \rangle$

lemma *shr-activate-cond-actions1* [*simp*]: $\text{shr} (\text{activate-cond-actions1 } s1 \ s2 \ cts) = \text{shr } s1$
 $\langle \text{proof} \rangle$

lemma *shr-activate-cond-action2* [*simp*]: $\text{shr} (\text{activate-cond-action2 } s1 \ s2 \ ct) = \text{shr } s2$
 $\langle \text{proof} \rangle$

lemma *shr-activate-cond-actions2* [*simp*]: $\text{shr} (\text{activate-cond-actions2 } s1 \ s2 \ cts) = \text{shr } s2$
 $\langle \text{proof} \rangle$

lemma *locks-activate-cond-action1* [*simp*]: $\text{locks} (\text{activate-cond-action1 } s1 \ s2 \ ct) = \text{locks } s1$
 $\langle \text{proof} \rangle$

lemma *locks-activate-cond-actions1* [*simp*]: $\text{locks} (\text{activate-cond-actions1 } s1 \ s2 \ cts) = \text{locks } s1$
 $\langle \text{proof} \rangle$

lemma *locks-activate-cond-action2* [*simp*]: $\text{locks} (\text{activate-cond-action2 } s1 \ s2 \ ct) = \text{locks } s2$
 $\langle \text{proof} \rangle$

lemma *locks-activate-cond-actions2* [*simp*]: $\text{locks} (\text{activate-cond-actions2 } s1 \ s2 \ cts) = \text{locks } s2$
 $\langle \text{proof} \rangle$

lemma *wset-activate-cond-action1* [*simp*]: $\text{wset} (\text{activate-cond-action1 } s1 \ s2 \ ct) = \text{wset } s1$
 $\langle \text{proof} \rangle$

lemma *wset-activate-cond-actions1* [*simp*]: $\text{wset} (\text{activate-cond-actions1 } s1 \ s2 \ cts) = \text{wset } s1$
 $\langle \text{proof} \rangle$

lemma *wset-activate-cond-action2* [*simp*]: $\text{wset} (\text{activate-cond-action2 } s1 \ s2 \ ct) = \text{wset } s2$
 $\langle \text{proof} \rangle$

lemma *wset-activate-cond-actions2* [*simp*]: $\text{wset} (\text{activate-cond-actions2 } s1 \ s2 \ cts) = \text{wset } s2$
 $\langle \text{proof} \rangle$

lemma *interrupts-activate-cond-action1* [*simp*]: $\text{interrupts} (\text{activate-cond-action1 } s1 \ s2 \ ct) = \text{interrupts } s1$
 $\langle \text{proof} \rangle$

lemma *interrupts-activate-cond-actions1* [*simp*]: $\text{interrupts} (\text{activate-cond-actions1 } s1 \ s2 \ cts) = \text{interrupts } s1$
 $\langle \text{proof} \rangle$

lemma *interrupts-activate-cond-action2* [*simp*]: $\text{interrupts} (\text{activate-cond-action2 } s1 \ s2 \ ct) = \text{interrupts } s2$
 $\langle \text{proof} \rangle$

lemma *interrupts-activate-cond-actions2* [*simp*]: $\text{interrupts} (\text{activate-cond-actions2 } s1 \ s2 \ cts) = \text{interrupts } s2$
 $\langle \text{proof} \rangle$

end

locale *FWdelay-bisimulation-lift-aux* =
FWdelay-bisimulation-base - - - - - τmove1 τmove2 +
 $r1: \tau\text{multithreaded-wf } final1 \ r1 \ \text{convert-RA } \tau\text{move1}$ +
 $r2: \tau\text{multithreaded-wf } final2 \ r2 \ \text{convert-RA } \tau\text{move2}$
for $\tau\text{move1} :: ('l, 't, 'x1, 'm1, 'w, 'o) \ \tau\text{moves}$
and $\tau\text{move2} :: ('l, 't, 'x2, 'm2, 'w, 'o) \ \tau\text{moves}$
begin

lemma *FWdelay-bisimulation-lift-aux-flip*:
FWdelay-bisimulation-lift-aux $final2 \ r2 \ final1 \ r1 \ \tau\text{move2} \ \tau\text{move1}$
 $\langle\text{proof}\rangle$

end

lemma *FWdelay-bisimulation-lift-aux-flip-simps* [*flip-simps*]:
FWdelay-bisimulation-lift-aux $final2 \ r2 \ final1 \ r1 \ \tau\text{move2} \ \tau\text{move1} =$
FWdelay-bisimulation-lift-aux $final1 \ r1 \ final2 \ r2 \ \tau\text{move1} \ \tau\text{move2}$
 $\langle\text{proof}\rangle$

context *FWdelay-bisimulation-lift-aux* **begin**

lemma *cond-actions-ok- $\tau\text{mred1-inv}$* :
assumes $red: \tau\text{mred1} \ s1 \ s1'$
and $ct: r1.\text{cond-action-ok} \ s1 \ t \ ct$
shows $r1.\text{cond-action-ok} \ s1' \ t \ ct$
 $\langle\text{proof}\rangle$

lemma *cond-actions-ok- $\tau\text{mred2-inv}$* :
 $\llbracket \tau\text{mred2} \ s2 \ s2'; r2.\text{cond-action-ok} \ s2 \ t \ ct \rrbracket \implies r2.\text{cond-action-ok} \ s2' \ t \ ct$
 $\langle\text{proof}\rangle$

lemma *cond-actions-ok- $\tau\text{mRed1-inv}$* :
 $\llbracket \tau\text{mRed1} \ s1 \ s1'; r1.\text{cond-action-ok} \ s1 \ t \ ct \rrbracket \implies r1.\text{cond-action-ok} \ s1' \ t \ ct$
 $\langle\text{proof}\rangle$

lemma *cond-actions-ok- $\tau\text{mRed2-inv}$* :
 $\llbracket \tau\text{mRed2} \ s2 \ s2'; r2.\text{cond-action-ok} \ s2 \ t \ ct \rrbracket \implies r2.\text{cond-action-ok} \ s2' \ t \ ct$
 $\langle\text{proof}\rangle$

end

locale *FWdelay-bisimulation-lift* =
FWdelay-bisimulation-lift-aux +
constrains $final1 :: 'x1 \Rightarrow \text{bool}$
and $r1 :: ('l, 't, 'x1, 'm1, 'w, 'o) \ \text{semantics}$
and $final2 :: 'x2 \Rightarrow \text{bool}$
and $r2 :: ('l, 't, 'x2, 'm2, 'w, 'o) \ \text{semantics}$
and $\text{convert-RA} :: 'l \ \text{released-locks} \Rightarrow 'o \ \text{list}$
and $\text{bisim} :: 't \Rightarrow ('x1 \times 'm1, 'x2 \times 'm2) \ \text{bisim}$
and $\text{bisim-wait} :: ('x1, 'x2) \ \text{bisim}$
and $\tau\text{move1} :: ('l, 't, 'x1, 'm1, 'w, 'o) \ \tau\text{moves}$
and $\tau\text{move2} :: ('l, 't, 'x2, 'm2, 'w, 'o) \ \tau\text{moves}$

assumes $\tau\text{inv-locale}$: $\tau\text{inv } (r1\ t)\ (r2\ t)\ (\text{bisim } t)\ (\text{ta-bisim } \text{bisim})\ \tau\text{move1}\ \tau\text{move2}$

sublocale $\text{FWdelay-bisimulation-lift} < \tau\text{inv } r1\ t\ r2\ t\ \text{bisim } t\ \text{ta-bisim } \text{bisim}\ \tau\text{move1}\ \tau\text{move2}$ **for** t
 $\langle\text{proof}\rangle$

context $\text{FWdelay-bisimulation-lift}$ **begin**

lemma $\text{FWdelay-bisimulation-lift-flip}$:

$\text{FWdelay-bisimulation-lift } \text{final2 } r2\ \text{final1 } r1\ (\lambda t. \text{flip } (\text{bisim } t))\ \tau\text{move2}\ \tau\text{move1} =$
 $\langle\text{proof}\rangle$

end

lemma $\text{FWdelay-bisimulation-lift-flip-simps}$ [flip-simps]:

$\text{FWdelay-bisimulation-lift } \text{final2 } r2\ \text{final1 } r1\ (\lambda t. \text{flip } (\text{bisim } t))\ \tau\text{move2}\ \tau\text{move1} =$
 $\text{FWdelay-bisimulation-lift } \text{final1 } r1\ \text{final2 } r2\ \text{bisim } \tau\text{move1}\ \tau\text{move2}$
 $\langle\text{proof}\rangle$

context $\text{FWdelay-bisimulation-lift}$ **begin**

lemma $\tau\text{inv-lift}$: $\tau\text{inv } r1.\text{redT } r2.\text{redT } \text{mbisim } \text{mta-bisim } m\tau\text{move1 } m\tau\text{move2}$

$\langle\text{proof}\rangle$

end

sublocale $\text{FWdelay-bisimulation-lift} < \text{mthr}$: $\tau\text{inv } r1.\text{redT } r2.\text{redT } \text{mbisim } \text{mta-bisim } m\tau\text{move1 } m\tau\text{move2}$
 $\langle\text{proof}\rangle$

locale $\text{FWdelay-bisimulation-final-base} =$

$\text{FWdelay-bisimulation-lift-aux} +$

constrains $\text{final1} :: 'x1 \Rightarrow \text{bool}$

and $r1 :: ('l, 't, 'x1, 'm1, 'w, 'o)\ \text{semantics}$

and $\text{final2} :: 'x2 \Rightarrow \text{bool}$

and $r2 :: ('l, 't, 'x2, 'm2, 'w, 'o)\ \text{semantics}$

and $\text{convert-RA} :: 'l\ \text{released-locks} \Rightarrow 'o\ \text{list}$

and $\text{bisim} :: 't \Rightarrow ('x1 \times 'm1, 'x2 \times 'm2)\ \text{bisim}$

and $\text{bisim-wait} :: ('x1, 'x2)\ \text{bisim}$

and $\tau\text{move1} :: ('l, 't, 'x1, 'm1, 'w, 'o)\ \tau\text{moves}$

and $\tau\text{move2} :: ('l, 't, 'x2, 'm2, 'w, 'o)\ \tau\text{moves}$

assumes $\text{delay-bisim-locale}$:

$\text{delay-bisimulation-final-base } (r1\ t)\ (r2\ t)\ (\text{bisim } t)\ \tau\text{move1}\ \tau\text{move2}\ (\lambda(x1, m). \text{final1 } x1)\ (\lambda(x2, m). \text{final2 } x2)$

sublocale $\text{FWdelay-bisimulation-final-base} <$

$\text{delay-bisimulation-final-base } r1\ t\ r2\ t\ \text{bisim } t\ \text{ta-bisim } \text{bisim}\ \tau\text{move1}\ \tau\text{move2}$

$\lambda(x1, m). \text{final1 } x1\ \lambda(x2, m). \text{final2 } x2$

for t

$\langle\text{proof}\rangle$

context $\text{FWdelay-bisimulation-final-base}$ **begin**

lemma $\text{FWdelay-bisimulation-final-base-flip}$:

$\text{FWdelay-bisimulation-final-base } \text{final2 } r2\ \text{final1 } r1\ (\lambda t. \text{flip } (\text{bisim } t))\ \tau\text{move2}\ \tau\text{move1}$
 $\langle\text{proof}\rangle$

end

lemma *FWdelay-bisimulation-final-base-flip-simps* [*flip-simps*]:

FWdelay-bisimulation-final-base final2 r2 final1 r1 ($\lambda t. \text{flip } (\text{bisim } t)$) τmove2 $\tau\text{move1} =$

FWdelay-bisimulation-final-base final1 r1 final2 r2 bisim τmove1 τmove2

$\langle \text{proof} \rangle$

context *FWdelay-bisimulation-final-base* **begin**

lemma *cond-actions-ok-bisim-ex- τ 1-inv*:

fixes *ls ts1 m1 ws is ts2 m2 ct*

defines $s1' \equiv \text{activate-cond-action1 } (ls, (ts1, m1), ws, is) (ls, (ts2, m2), ws, is) ct$

assumes *mbisim*: $\bigwedge t'. t' \neq t \implies \text{tbisim } (ws \ t' = \text{None}) \ t' \ (ts1 \ t') \ m1 \ (ts2 \ t') \ m2$

and *ts1t*: $ts1 \ t = \text{Some } xln$

and *ts2t*: $ts2 \ t = \text{Some } xln'$

and *ct*: $r2.\text{cond-action-ok } (ls, (ts2, m2), ws, is) \ t \ ct$

shows $\tau mRed1 \ (ls, (ts1, m1), ws, is) \ s1'$

and $\bigwedge t'. t' \neq t \implies \text{tbisim } (ws \ t' = \text{None}) \ t' \ (\text{thr } s1' \ t') \ m1 \ (ts2 \ t') \ m2$

and $r1.\text{cond-action-ok } s1' \ t \ ct$

and $\text{thr } s1' \ t = \text{Some } xln$

$\langle \text{proof} \rangle$

lemma *cond-actions-oks-bisim-ex- τ 1-inv*:

fixes *ls ts1 m1 ws is ts2 m2 cts*

defines $s1' \equiv \text{activate-cond-actions1 } (ls, (ts1, m1), ws, is) (ls, (ts2, m2), ws, is) cts$

assumes *tbisim*: $\bigwedge t'. t' \neq t \implies \text{tbisim } (ws \ t' = \text{None}) \ t' \ (ts1 \ t') \ m1 \ (ts2 \ t') \ m2$

and *ts1t*: $ts1 \ t = \text{Some } xln$

and *ts2t*: $ts2 \ t = \text{Some } xln'$

and *ct*: $r2.\text{cond-action-oks } (ls, (ts2, m2), ws, is) \ t \ cts$

shows $\tau mRed1 \ (ls, (ts1, m1), ws, is) \ s1'$

and $\bigwedge t'. t' \neq t \implies \text{tbisim } (ws \ t' = \text{None}) \ t' \ (\text{thr } s1' \ t') \ m1 \ (ts2 \ t') \ m2$

and $r1.\text{cond-action-oks } s1' \ t \ cts$

and $\text{thr } s1' \ t = \text{Some } xln$

$\langle \text{proof} \rangle$

lemma *cond-actions-ok-bisim-ex- τ 2-inv*:

fixes *ls ts1 m1 is ws ts2 m2 ct*

defines $s2' \equiv \text{activate-cond-action2 } (ls, (ts1, m1), ws, is) (ls, (ts2, m2), ws, is) ct$

assumes *mbisim*: $\bigwedge t'. t' \neq t \implies \text{tbisim } (ws \ t' = \text{None}) \ t' \ (ts1 \ t') \ m1 \ (ts2 \ t') \ m2$

and *ts1t*: $ts1 \ t = \text{Some } xln$

and *ts2t*: $ts2 \ t = \text{Some } xln'$

and *ct*: $r1.\text{cond-action-ok } (ls, (ts1, m1), ws, is) \ t \ ct$

shows $\tau mRed2 \ (ls, (ts2, m2), ws, is) \ s2'$

and $\bigwedge t'. t' \neq t \implies \text{tbisim } (ws \ t' = \text{None}) \ t' \ (ts1 \ t') \ m1 \ (\text{thr } s2' \ t') \ m2$

and $r2.\text{cond-action-ok } s2' \ t \ ct$

and $\text{thr } s2' \ t = \text{Some } xln'$

$\langle \text{proof} \rangle$

lemma *cond-actions-oks-bisim-ex- τ 2-inv*:

fixes *ls ts1 m1 ws is ts2 m2 cts*

defines $s2' \equiv \text{activate-cond-actions2 } (ls, (ts1, m1), ws, is) (ls, (ts2, m2), ws, is) cts$

assumes *tbisim*: $\bigwedge t'. t' \neq t \implies \text{tbisim } (ws \ t' = \text{None}) \ t' \ (ts1 \ t') \ m1 \ (ts2 \ t') \ m2$

and *ts1t*: $ts1 \ t = \text{Some } xln$

and $ts2t: ts2\ t = \text{Some } xln'$
and $ct: r1.\text{cond-action-oks } (ls, (ts1, m1), ws, is)\ t\ cts$
shows $\tau mRed2\ (ls, (ts2, m2), ws, is)\ s2'$
and $\bigwedge t'. t' \neq t \implies \text{tbisim } (ws\ t' = \text{None})\ t'\ (ts1\ t')\ m1\ (\text{thr } s2'\ t')\ m2$
and $r2.\text{cond-action-oks } s2'\ t\ cts$
and $\text{thr } s2'\ t = \text{Some } xln'$
 <proof>

lemma *mfinal1-inv-simulation:*

assumes $s1 \approx_m s2$
shows $\exists s2'. r2.\text{mthr.silent-moves } s2\ s2' \wedge s1 \approx_m s2' \wedge r1.\text{final-threads } s1 \subseteq r2.\text{final-threads } s2'$
 $\wedge \text{shr } s2' = \text{shr } s2$
 <proof>

lemma *mfinal2-inv-simulation:*

$s1 \approx_m s2 \implies \exists s1'. r1.\text{mthr.silent-moves } s1\ s1' \wedge s1' \approx_m s2 \wedge r2.\text{final-threads } s2 \subseteq r1.\text{final-threads } s1'$
 $\wedge \text{shr } s1' = \text{shr } s1$
 <proof>

lemma *mfinal1-simulation:*

assumes $s1 \approx_m s2$ **and** $r1.\text{mfinal } s1$
shows $\exists s2'. r2.\text{mthr.silent-moves } s2\ s2' \wedge s1 \approx_m s2' \wedge r2.\text{mfinal } s2' \wedge \text{shr } s2' = \text{shr } s2$
 <proof>

lemma *mfinal2-simulation:*

$\llbracket s1 \approx_m s2; r2.\text{mfinal } s2 \rrbracket$
 $\implies \exists s1'. r1.\text{mthr.silent-moves } s1\ s1' \wedge s1' \approx_m s2 \wedge r1.\text{mfinal } s1' \wedge \text{shr } s1' = \text{shr } s1$
 <proof>

end

locale *FWdelay-bisimulation-obs =*

$FWdelay\text{-bisimulation-final-base} \text{ } \dots \text{ } \tau\text{move1 } \tau\text{move2}$
for $\tau\text{move1} :: ('l, 't, 'x1, 'm1, 'w, 'o)\ \tau\text{moves}$
and $\tau\text{move2} :: ('l, 't, 'x2, 'm2, 'w, 'o)\ \tau\text{moves} +$
assumes $\text{delay-bisimulation-obs-locale: delay-bisimulation-obs } (r1\ t)\ (r2\ t)\ (\text{bisim } t)\ (\text{ta-bisim } \text{bisim})$
 $\tau\text{move1 } \tau\text{move2}$

and *bisim-inv-red-other:*

$\llbracket t' \vdash (x, m1) \approx (x, m2); t \vdash (x1, m1) \approx (x2, m2);$
 $r1.\text{silent-moves } t\ (x1, m1)\ (x1', m1);$
 $t \vdash (x1', m1) \text{ } \text{--}1\text{--}ta1 \rightarrow (x1'', m1'); \neg \tau\text{move1 } (x1', m1)\ ta1\ (x1'', m1');$
 $r2.\text{silent-moves } t\ (x2, m2)\ (x2', m2);$
 $t \vdash (x2', m2) \text{ } \text{--}2\text{--}ta2 \rightarrow (x2'', m2'); \neg \tau\text{move2 } (x2', m2)\ ta2\ (x2'', m2');$
 $t \vdash (x1'', m1') \approx (x2'', m2'); \text{ta-bisim } \text{bisim } ta1\ ta2 \rrbracket$
 $\implies t' \vdash (x, m1') \approx (x, m2')$

and *bisim-waitI:*

$\llbracket t \vdash (x1, m1) \approx (x2, m2); r1.\text{silent-moves } t\ (x1, m1)\ (x1', m1);$
 $t \vdash (x1', m1) \text{ } \text{--}1\text{--}ta1 \rightarrow (x1'', m1'); \neg \tau\text{move1 } (x1', m1)\ ta1\ (x1'', m1');$
 $r2.\text{silent-moves } t\ (x2, m2)\ (x2', m2);$
 $t \vdash (x2', m2) \text{ } \text{--}2\text{--}ta2 \rightarrow (x2'', m2'); \neg \tau\text{move2 } (x2', m2)\ ta2\ (x2'', m2');$
 $t \vdash (x1'', m1') \approx (x2'', m2'); \text{ta-bisim } \text{bisim } ta1\ ta2;$
 $\text{Suspend } w \in \text{set } \{ta1\}_w; \text{Suspend } w \in \text{set } \{ta2\}_w \rrbracket$
 $\implies x1'' \approx_w x2''$

and *simulation-Wakeup1:*

$\llbracket t \vdash (x1, m1) \approx (x2, m2); x1 \approx_w x2; t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1'); \text{Notified} \in \text{set } \{ta1\}_w \vee \text{WokenUp} \in \text{set } \{ta1\}_w \rrbracket$

$\implies \exists ta2 x2' m2'. t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2') \wedge t \vdash (x1', m1') \approx (x2', m2') \wedge ta\text{-bisim bisim } ta1 ta2$

and simulation-Wakeup2:

$\llbracket t \vdash (x1, m1) \approx (x2, m2); x1 \approx_w x2; t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2'); \text{Notified} \in \text{set } \{ta2\}_w \vee \text{WokenUp} \in \text{set } \{ta2\}_w \rrbracket$

$\implies \exists ta1 x1' m1'. t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1') \wedge t \vdash (x1', m1') \approx (x2', m2') \wedge ta\text{-bisim bisim } ta1 ta2$

and ex-final1-conv-ex-final2:

$(\exists x1. \text{final1 } x1) \longleftrightarrow (\exists x2. \text{final2 } x2)$

sublocale FWdelay-bisimulation-obs <

delay-bisimulation-obs *r1 t r2 t bisim t ta-bisim bisim* τmove1 τmove2 **for** *t*

<proof>

context FWdelay-bisimulation-obs begin

lemma FWdelay-bisimulation-obs-flip:

FWdelay-bisimulation-obs final2 r2 final1 r1 $(\lambda t. \text{flip } (\text{bisim } t))$ *(flip bisim-wait)* τmove2 τmove1

<proof>

end

lemma FWdelay-bisimulation-obs-flip-simps [flip-simps]:

FWdelay-bisimulation-obs final2 r2 final1 r1 $(\lambda t. \text{flip } (\text{bisim } t))$ *(flip bisim-wait)* τmove2 $\tau\text{move1} =$

FWdelay-bisimulation-obs final1 r1 final2 r2 bisim bisim-wait τmove1 τmove2

<proof>

context FWdelay-bisimulation-obs begin

lemma mbisim-redT-upd:

fixes *s1 t ta1 x1' m1' s2 ta2 x2' m2' ln*

assumes *s1': redT-upd s1 t ta1 x1' m1' s1'*

and *s2': redT-upd s2 t ta2 x2' m2' s2'*

and *[simp]: wset s1 = wset s2 locks s1 = locks s2*

and *wset: wset s1' = wset s2'*

and *interrupts: interrupts s1' = interrupts s2'*

and *fin1: finite (dom (thr s1))*

and *wsts: wset-thread-ok (wset s1) (thr s1)*

and *tst: thr s1 t = [(x1, ln)]*

and *tst': thr s2 t = [(x2, ln)]*

and *aoe1: r1.actions-ok s1 t ta1*

and *aoe2: r2.actions-ok s2 t ta2*

and *tasim: ta-bisim bisim ta1 ta2*

and *bisim': t \vdash (x1', m1') \approx (x2', m2')*

and *bisimw: wset s1' t = None \vee x1' \approx_w x2'*

and $\tau\text{red1: } r1.\text{silent-moves } t (x1'', \text{shr } s1) (x1, \text{shr } s1)$

and $\text{red1: } t \vdash (x1, \text{shr } s1) -1-ta1 \rightarrow (x1', m1')$

and $\tau\text{red2: } r2.\text{silent-moves } t (x2'', \text{shr } s2) (x2, \text{shr } s2)$

and $\text{red2: } t \vdash (x2, \text{shr } s2) -2-ta2 \rightarrow (x2', m2')$

and *bisim: t \vdash (x1'', shr s1) \approx (x2'', shr s2)*

and $\tau1: \neg \tau\text{move1 } (x1, \text{shr } s1) ta1 (x1', m1')$

and $\tau 2: \neg \tau \text{move} 2 (x2, \text{shr } s2) \text{ta} 2 (x2', m2')$
and $\text{tbisim}: \bigwedge t'. t \neq t' \implies \text{tbisim} (\text{uset } s1 \ t' = \text{None}) \ t' \ (\text{thr } s1 \ t') \ (\text{shr } s1) \ (\text{thr } s2 \ t') \ (\text{shr } s2)$
shows $s1' \approx_m s2'$
 <proof>

theorem *mbisim-simulation1*:

assumes $\text{mbisim}: \text{mbisim } s1 \ s2$ **and** $\neg m\tau \text{move} 1 \ s1 \ \text{tl} 1 \ s1' \ r1.\text{redT } s1 \ \text{tl} 1 \ s1'$
shows $\exists s2' \ s2'' \ \text{tl} 2. \ r2.\text{mthr}.\text{silent-moves } s2 \ s2' \wedge \ r2.\text{redT } s2' \ \text{tl} 2 \ s2'' \wedge$
 $\neg m\tau \text{move} 2 \ s2' \ \text{tl} 2 \ s2'' \wedge \text{mbisim } s1' \ s2'' \wedge \text{mta-bisim } \text{tl} 1 \ \text{tl} 2$

<proof>

theorem *mbisim-simulation2*:

$\llbracket \text{mbisim } s1 \ s2; \ r2.\text{redT } s2 \ \text{tl} 2 \ s2'; \ \neg m\tau \text{move} 2 \ s2 \ \text{tl} 2 \ s2' \rrbracket$
 $\implies \exists s1' \ s1'' \ \text{tl} 1. \ r1.\text{mthr}.\text{silent-moves } s1 \ s1' \wedge \ r1.\text{redT } s1' \ \text{tl} 1 \ s1'' \wedge \neg m\tau \text{move} 1 \ s1' \ \text{tl} 1 \ s1'' \wedge$
 $\text{mbisim } s1'' \ s2' \wedge \text{mta-bisim } \text{tl} 1 \ \text{tl} 2$

<proof>

end

locale *FWdelay-bisimulation-diverge* =

FWdelay-bisimulation-obs - - - - - $\tau \text{move} 1 \ \tau \text{move} 2$

for $\tau \text{move} 1 :: ('l, 't, 'x1, 'm1, 'w, 'o) \ \tau \text{moves}$

and $\tau \text{move} 2 :: ('l, 't, 'x2, 'm2, 'w, 'o) \ \tau \text{moves} +$

assumes *delay-bisimulation-diverge-locale*: *delay-bisimulation-diverge* ($r1 \ t$) ($r2 \ t$) (*bisim* t) (*ta-bisim* *bisim*) $\tau \text{move} 1 \ \tau \text{move} 2$

sublocale *FWdelay-bisimulation-diverge* <

delay-bisimulation-diverge $r1 \ t \ r2 \ t \ \text{bisim } t \ \text{ta-bisim } \text{bisim} \ \tau \text{move} 1 \ \tau \text{move} 2$ **for** t

<proof>

context *FWdelay-bisimulation-diverge* **begin**

lemma *FWdelay-bisimulation-diverge-flip*:

FWdelay-bisimulation-diverge *final2* $r2 \ \text{final1} \ r1 \ (\lambda t. \ \text{flip} \ (\text{bisim } t)) \ (\text{flip } \text{bisim-wait}) \ \tau \text{move} 2 \ \tau \text{move} 1$

<proof>

end

lemma *FWdelay-bisimulation-diverge-flip-simps* [*flip-simps*]:

FWdelay-bisimulation-diverge *final2* $r2 \ \text{final1} \ r1 \ (\lambda t. \ \text{flip} \ (\text{bisim } t)) \ (\text{flip } \text{bisim-wait}) \ \tau \text{move} 2 \ \tau \text{move} 1$

=

FWdelay-bisimulation-diverge *final1* $r1 \ \text{final2} \ r2 \ \text{bisim } \text{bisim-wait} \ \tau \text{move} 1 \ \tau \text{move} 2$

<proof>

context *FWdelay-bisimulation-diverge* **begin**

lemma *bisim-inv1*:

assumes *bisim*: $t \vdash s1 \approx s2$

and *red*: $t \vdash s1 \ -1\text{-ta} 1 \rightarrow s1'$

obtains $s2'$ **where** $t \vdash s1' \approx s2'$

<proof>

lemma *bisim-inv2*:

assumes $t \vdash s1 \approx s2 \ t \vdash s2 \ -2\text{-ta} 2 \rightarrow s2'$

obtains $s1'$ **where** $t \vdash s1' \approx s2'$
 ⟨proof⟩

lemma *bisim-inv: bisim-inv*
 ⟨proof⟩

lemma *bisim-inv- τ s1*:
assumes $t \vdash s1 \approx s2$ **and** $r1.silent-moves\ t\ s1\ s1'$
obtains $s2'$ **where** $t \vdash s1' \approx s2'$
 ⟨proof⟩

lemma *bisim-inv- τ s2*:
assumes $t \vdash s1 \approx s2$ **and** $r2.silent-moves\ t\ s2\ s2'$
obtains $s1'$ **where** $t \vdash s1' \approx s2'$
 ⟨proof⟩

lemma *red1-rtrancl- τ -into-RedT- τ* :
assumes $r1.silent-moves\ t\ (x1, shr\ s1)\ (x1', m1')$ $t \vdash (x1, shr\ s1) \approx (x2, m2)$
and $thr\ s1\ t = [(x1, no-wait-locks)]\ wset\ s1\ t = None$
shows $\tau mRed1\ s1\ (redT-upd-\varepsilon\ s1\ t\ x1'\ m1')$
 ⟨proof⟩

lemma *red2-rtrancl- τ -into-RedT- τ* :
assumes $r2.silent-moves\ t\ (x2, shr\ s2)\ (x2', m2')$
and $t \vdash (x1, m1) \approx (x2, shr\ s2)$ $thr\ s2\ t = [(x2, no-wait-locks)]\ wset\ s2\ t = None$
shows $\tau mRed2\ s2\ (redT-upd-\varepsilon\ s2\ t\ x2'\ m2')$
 ⟨proof⟩

lemma *red1-rtrancl- τ -heapD*:
 $\llbracket r1.silent-moves\ t\ s1\ s1';\ t \vdash s1 \approx s2 \rrbracket \implies snd\ s1' = snd\ s1$
 ⟨proof⟩

lemma *red2-rtrancl- τ -heapD*:
 $\llbracket r2.silent-moves\ t\ s2\ s2';\ t \vdash s1 \approx s2 \rrbracket \implies snd\ s2' = snd\ s2$
 ⟨proof⟩

lemma *mbisim-simulation-silent1*:
assumes $m\tau'$: $r1.mthr.silent-move\ s1\ s1'$ **and** $mbisim$: $s1 \approx_m s2$
shows $\exists s2'.\ r2.mthr.silent-moves\ s2\ s2' \wedge s1' \approx_m s2'$
 ⟨proof⟩

lemma *mbisim-simulation-silent2*:
 $\llbracket mbisim\ s1\ s2;\ r2.mthr.silent-move\ s2\ s2' \rrbracket$
 $\implies \exists s1'.\ r1.mthr.silent-moves\ s1\ s1' \wedge mbisim\ s1'\ s2'$
 ⟨proof⟩

lemma *mbisim-simulation1'*:
assumes $mbisim$: $mbisim\ s1\ s2$ **and** $\neg m\tau move1\ s1\ tl1\ s1'\ r1.redT\ s1\ tl1\ s1'$
shows $\exists s2'\ s2''\ tl2.\ r2.mthr.silent-moves\ s2\ s2' \wedge r2.redT\ s2'\ tl2\ s2'' \wedge$
 $\neg m\tau move2\ s2'\ tl2\ s2'' \wedge mbisim\ s1'\ s2'' \wedge mta-bisim\ tl1\ tl2$
 ⟨proof⟩

lemma *mbisim-simulation2'*:
 $\llbracket mbisim\ s1\ s2;\ r2.redT\ s2\ tl2\ s2';\ \neg m\tau move2\ s2\ tl2\ s2' \rrbracket$

$$\implies \exists s1' s1'' tl1. r1.mthr.silent-moves s1 s1' \wedge r1.redT s1' tl1 s1'' \wedge \neg m\tau move1 s1' tl1 s1'' \wedge mbisim s1'' s2' \wedge mta-bisim tl1 tl2$$

<proof>

lemma *m τ diverge-simulation1*:

assumes *s1 \approx_m s2*

and *r1.mthr. τ diverge s1*

shows *r2.mthr. τ diverge s2*

<proof>

lemma *τ diverge-mbisim-inv*:

s1 \approx_m s2 \implies r1.mthr. τ diverge s1 \longleftrightarrow r2.mthr. τ diverge s2

<proof>

lemma *mbisim-delay-bisimulation*:

delay-bisimulation-diverge r1.redT r2.redT mbisim mta-bisim m τ move1 m τ move2

<proof>

theorem *mdelay-bisimulation-final-base*:

delay-bisimulation-final-base r1.redT r2.redT mbisim m τ move1 m τ move2 r1.mfinal r2.mfinal

<proof>

end

sublocale *FWdelay-bisimulation-diverge < mthr: delay-bisimulation-diverge r1.redT r2.redT mbisim mta-bisim m τ move1 m τ move2*

<proof>

sublocale *FWdelay-bisimulation-diverge <*

mthr: delay-bisimulation-final-base r1.redT r2.redT mbisim mta-bisim m τ move1 m τ move2 r1.mfinal r2.mfinal

<proof>

context *FWdelay-bisimulation-diverge begin*

lemma *mthr-delay-bisimulation-diverge-final*:

delay-bisimulation-diverge-final r1.redT r2.redT mbisim mta-bisim m τ move1 m τ move2 r1.mfinal r2.mfinal

<proof>

end

sublocale *FWdelay-bisimulation-diverge <*

mthr: delay-bisimulation-diverge-final r1.redT r2.redT mbisim mta-bisim m τ move1 m τ move2 r1.mfinal r2.mfinal

<proof>

1.18.3 Strong bisimulation as corollary

locale *FWbisimulation = FWbisimulation-base - - - r2 convert-RA bisim $\lambda x1 x2. True +$*

r1: multithreaded final1 r1 convert-RA +

r2: multithreaded final2 r2 convert-RA

for *r2 :: ('l,'t,'x2,'m2,'w,'o) semantics ($\vdash \vdash - - 2 \dashrightarrow \dashrightarrow$) [50,0,0,50] 80)*

and *convert-RA :: 'l released-locks \implies 'o list*

and *bisim* :: 't ⇒ ('x1 × 'm1, 'x2 × 'm2) *bisim* (⟨- ⊢ -/ ≈ -⟩ [50, 50, 50] 60) +
assumes *bisimulation-locale*: *bisimulation* (r1 t) (r2 t) (*bisim* t) (*ta-bisim* *bisim*)
and *bisim-final*: t ⊢ (x1, m1) ≈ (x2, m2) ⇒ *final1* x1 ↔ *final2* x2
and *bisim-inv-red-other*:

$$\llbracket t' \vdash (x, m1) \approx (xx, m2); t \vdash (x1, m1) \approx (x2, m2);$$

$$t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1'); t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2');$$

$$t \vdash (x1', m1') \approx (x2', m2'); ta-bisim\ bisim\ ta1\ ta2 \rrbracket$$

$$\implies t' \vdash (x, m1') \approx (xx, m2')$$
and *ex-final1-conv-ex-final2*:

$$(\exists x1. final1\ x1) \longleftrightarrow (\exists x2. final2\ x2)$$

sublocale *FWbisimulation* < *bisim?*: *bisimulation* r1 t r2 t *bisim* t *ta-bisim* *bisim* **for** t
 ⟨*proof*⟩

sublocale *FWbisimulation* < *bisim-diverge?*:

FWdelay-bisimulation-diverge *final1* r1 *final2* r2 *convert-RA* *bisim* λx1 x2. *True* λs *ta* s'. *False* λs *ta* s'. *False*
 ⟨*proof*⟩

context *FWbisimulation* **begin**

lemma *FWbisimulation-flip*: *FWbisimulation* *final2* r2 *final1* r1 (λt. *flip* (*bisim* t))
 ⟨*proof*⟩

end

lemma *FWbisimulation-flip-simps* [*flip-simps*]:

FWbisimulation *final2* r2 *final1* r1 (λt. *flip* (*bisim* t)) = *FWbisimulation* *final1* r1 *final2* r2 *bisim*
 ⟨*proof*⟩

context *FWbisimulation* **begin**

The notation for *mbisim* is lost because *bisim-wait* is instantiated to λx1 x2. *True*. This reintroduces the syntax, but it does not work for output mode. This would require a new abbreviation.

notation *mbisim* (⟨- ≈m -⟩ [50, 50] 60)

theorem *mbisim-bisimulation*:

bisimulation r1.*redT* r2.*redT* *mbisim* *mta-bisim*
 ⟨*proof*⟩

lemma *mbisim-wset-eq*:

s1 ≈m *s2* ⇒ *wset* *s1* = *wset* *s2*
 ⟨*proof*⟩

lemma *mbisim-mfinal*:

s1 ≈m *s2* ⇒ r1.*mfinal* *s1* ↔ r2.*mfinal* *s2*
 ⟨*proof*⟩

end

sublocale *FWbisimulation* < *mthr*: *bisimulation* r1.*redT* r2.*redT* *mbisim* *mta-bisim*
 ⟨*proof*⟩

sublocale *FWbisimulation* < *mthr*: *bisimulation-final* *r1.redT* *r2.redT* *mbisim* *mta-bisim* *r1.mfinal* *r2.mfinal*
 <proof>

end

1.19 Preservation of deadlock across bisimulations

theory *FWBisimDeadlock*

imports

FWBisimulation

FWDeadlock

begin

context *FWdelay-bisimulation-obs* **begin**

lemma *actions-ok1-ex-actions-ok2*:

assumes *r1.actions-ok* *s1* *t* *ta1*

and *ta1* \sim_m *ta2*

obtains *s2* **where** *r2.actions-ok* *s2* *t* *ta2*

<proof>

lemma *actions-ok2-ex-actions-ok1*:

assumes *r2.actions-ok* *s2* *t* *ta2*

and *ta1* \sim_m *ta2*

obtains *s1* **where** *r1.actions-ok* *s1* *t* *ta1*

<proof>

lemma *ex-actions-ok1-conv-ex-actions-ok2*:

ta1 \sim_m *ta2* $\implies (\exists s1. r1.actions-ok s1 t ta1) \longleftrightarrow (\exists s2. r2.actions-ok s2 t ta2)$

<proof>

end

context *FWdelay-bisimulation-diverge* **begin**

lemma *no- τ Move1- τ s-to-no- τ Move2*:

fixes *no- τ moves1* *no- τ moves2*

defines *no- τ moves1* $\equiv \lambda s1 t. wset s1 t = None \wedge (\exists x. thr s1 t = [(x, no-wait-locks)] \wedge (\forall x' m'. \neg r1.silent-move t (x, shr s1) (x', m')))$

defines *no- τ moves2* $\equiv \lambda s2 t. wset s2 t = None \wedge (\exists x. thr s2 t = [(x, no-wait-locks)] \wedge (\forall x' m'. \neg r2.silent-move t (x, shr s2) (x', m')))$

assumes *mbisim*: *s1* \approx_m (*ls2*, (*ts2*, *m2*), *ws2*, *is2*)

shows $\exists ts2'. r2.mthr.silent-moves (ls2, (ts2, m2), ws2, is2) (ls2, (ts2', m2), ws2, is2) \wedge (\forall t. no- τ moves1 s1 t \longrightarrow no- τ moves2 (ls2, (ts2', m2), ws2, is2) t) \wedge s1 \approx_m (ls2, (ts2', m2), ws2, is2)$

<proof>

lemma *no- τ Move2- τ s-to-no- τ Move1*:

fixes *no- τ moves1* *no- τ moves2*

defines *no- τ moves1* $\equiv \lambda s1 t. wset s1 t = None \wedge (\exists x. thr s1 t = [(x, no-wait-locks)] \wedge (\forall x' m'. \neg r1.silent-move t (x, shr s1) (x', m')))$

defines $no\text{-}\tau\text{moves}2 \equiv \lambda s2 t. \text{uset } s2 t = \text{None} \wedge (\exists x. \text{thr } s2 t = [(x, \text{no-wait-locks})] \wedge (\forall x' m'. \neg r2.\text{silent-move } t (x, \text{shr } s2) (x', m')))$

assumes $(ls1, (ts1, m1), ws1, is1) \approx_m s2$

shows $\exists ts1'. r1.\text{mthr}.\text{silent-moves } (ls1, (ts1, m1), ws1, is1) (ls1, (ts1', m1), ws1, is1) \wedge$
 $(\forall t. no\text{-}\tau\text{moves}2 s2 t \longrightarrow no\text{-}\tau\text{moves}1 (ls1, (ts1', m1), ws1, is1) t) \wedge (ls1, (ts1', m1),$
 $ws1, is1) \approx_m s2$
 $\langle \text{proof} \rangle$

lemma *deadlock-mbisim-not-final-thread-pres:*

assumes $\text{dead}: t \in r1.\text{deadlocked } s1 \vee r1.\text{deadlock } s1$

and $n\text{fin}: r1.\text{not-final-thread } s1 t$

and $\text{fin}: r1.\text{final-thread } s1 t \implies r2.\text{final-thread } s2 t$

and $\text{mbisim}: s1 \approx_m s2$

shows $r2.\text{not-final-thread } s2 t$

$\langle \text{proof} \rangle$

lemma *deadlocked1-imp- τ s-deadlocked2:*

assumes $\text{mbisim}: s1 \approx_m s2$

and $\text{dead}: t \in r1.\text{deadlocked } s1$

shows $\exists s2'. r2.\text{mthr}.\text{silent-moves } s2 s2' \wedge t \in r2.\text{deadlocked } s2' \wedge s1 \approx_m s2'$

$\langle \text{proof} \rangle$

lemma *deadlocked2-imp- τ s-deadlocked1:*

$\llbracket s1 \approx_m s2; t \in r2.\text{deadlocked } s2 \rrbracket$

$\implies \exists s1'. r1.\text{mthr}.\text{silent-moves } s1 s1' \wedge t \in r1.\text{deadlocked } s1' \wedge s1' \approx_m s2$

$\langle \text{proof} \rangle$

lemma *deadlock1-imp- τ s-deadlock2:*

assumes $\text{mbisim}: s1 \approx_m s2$

and $\text{dead}: r1.\text{deadlock } s1$

shows $\exists s2'. r2.\text{mthr}.\text{silent-moves } s2 s2' \wedge r2.\text{deadlock } s2' \wedge s1 \approx_m s2'$

$\langle \text{proof} \rangle$

lemma *deadlock2-imp- τ s-deadlock1:*

$\llbracket s1 \approx_m s2; r2.\text{deadlock } s2 \rrbracket$

$\implies \exists s1'. r1.\text{mthr}.\text{silent-moves } s1 s1' \wedge r1.\text{deadlock } s1' \wedge s1' \approx_m s2$

$\langle \text{proof} \rangle$

lemma *deadlocked'1-imp- τ s-deadlocked'2:*

$\llbracket s1 \approx_m s2; r1.\text{deadlocked}' s1 \rrbracket$

$\implies \exists s2'. r2.\text{mthr}.\text{silent-moves } s2 s2' \wedge r2.\text{deadlocked}' s2' \wedge s1 \approx_m s2'$

$\langle \text{proof} \rangle$

lemma *deadlocked'2-imp- τ s-deadlocked'1:*

$\llbracket s1 \approx_m s2; r2.\text{deadlocked}' s2 \rrbracket \implies \exists s1'. r1.\text{mthr}.\text{silent-moves } s1 s1' \wedge r1.\text{deadlocked}' s1' \wedge s1' \approx_m s2$

$\langle \text{proof} \rangle$

end

context *FWbisimulation* **begin**

lemma *mbisim-final-thread-preserve1:*

assumes $mbisim: s1 \approx_m s2$ **and** $fin: r1.final-thread\ s1\ t$
shows $r2.final-thread\ s2\ t$

$\langle proof \rangle$

lemma *mbisim-final-thread-preserve2*:

$\llbracket s1 \approx_m s2; r2.final-thread\ s2\ t \rrbracket \implies r1.final-thread\ s1\ t$

$\langle proof \rangle$

lemma *mbisim-final-thread-inv*:

$s1 \approx_m s2 \implies r1.final-thread\ s1\ t \longleftrightarrow r2.final-thread\ s2\ t$

$\langle proof \rangle$

lemma *mbisim-not-final-thread-inv*:

assumes $bisim: mbisim\ s1\ s2$

shows $r1.not-final-thread\ s1 = r2.not-final-thread\ s2$

$\langle proof \rangle$

lemma *mbisim-deadlocked-preserve1*:

assumes $mbisim: s1 \approx_m s2$ **and** $dead: t \in r1.deadlocked\ s1$

shows $t \in r2.deadlocked\ s2$

$\langle proof \rangle$

lemma *mbisim-deadlocked-preserve2*:

$\llbracket s1 \approx_m s2; t \in r2.deadlocked\ s2 \rrbracket \implies t \in r1.deadlocked\ s1$

$\langle proof \rangle$

lemma *mbisim-deadlocked-inv*:

$s1 \approx_m s2 \implies r1.deadlocked\ s1 = r2.deadlocked\ s2$

$\langle proof \rangle$

lemma *mbisim-deadlocked'-inv*:

$s1 \approx_m s2 \implies r1.deadlocked'\ s1 \longleftrightarrow r2.deadlocked'\ s2$

$\langle proof \rangle$

lemma *mbisim-deadlock-inv*:

$s1 \approx_m s2 \implies r1.deadlock\ s1 = r2.deadlock\ s2$

$\langle proof \rangle$

end

context *FWbisimulation* **begin**

lemma *bisim-can-sync-preserve1*:

assumes $bisim: t \vdash (x1, m1) \approx (x2, m2)$ **and** $cs: t \vdash \langle x1, m1 \rangle LT\ \imath 1$

shows $t \vdash \langle x2, m2 \rangle LT\ \imath 2$

$\langle proof \rangle$

lemma *bisim-can-sync-preserve2*:

$\llbracket t \vdash (x1, m1) \approx (x2, m2); t \vdash \langle x2, m2 \rangle LT\ \imath 2 \rrbracket \implies t \vdash \langle x1, m1 \rangle LT\ \imath 1$

$\langle proof \rangle$

lemma *bisim-can-sync-inv*:

$t \vdash (x1, m1) \approx (x2, m2) \implies t \vdash \langle x1, m1 \rangle LT \wr 1 \longleftrightarrow t \vdash \langle x2, m2 \rangle LT \wr 2$
 ⟨proof⟩

lemma *bisim-must-sync-preserve1*:

assumes *bisim*: $t \vdash (x1, m1) \approx (x2, m2)$ **and** *ms*: $t \vdash \langle x1, m1 \rangle \wr 1$
shows $t \vdash \langle x2, m2 \rangle \wr 2$
 ⟨proof⟩

lemma *bisim-must-sync-preserve2*:

$\llbracket t \vdash (x1, m1) \approx (x2, m2); t \vdash \langle x2, m2 \rangle \wr 2 \rrbracket \implies t \vdash \langle x1, m1 \rangle \wr 1$
 ⟨proof⟩

lemma *bisim-must-sync-inv*:

$t \vdash (x1, m1) \approx (x2, m2) \implies t \vdash \langle x1, m1 \rangle \wr 1 \longleftrightarrow t \vdash \langle x2, m2 \rangle \wr 2$
 ⟨proof⟩

end

end

1.20 Semantic properties of lifted predicates

theory *FWLiftingSem*

imports

FWSemantics

FWLifting

begin

context *multithreaded-base* **begin**

lemma *redT-preserves-ts-inv-ok*:

$\llbracket s \dashv t a \rightarrow s'; ts\text{-inv-ok} (thr\ s) I \rrbracket$
 $\implies ts\text{-inv-ok} (thr\ s') (upd\text{-invs } I\ P \ \{\!\{ta}\!\}_t)$
 ⟨proof⟩

lemma *RedT-preserves-ts-inv-ok*:

$\llbracket s \dashv t t a s \rightarrow^* s'; ts\text{-inv-ok} (thr\ s) I \rrbracket$
 $\implies ts\text{-inv-ok} (thr\ s') (upd\text{-invs } I\ Q\ (concat\ (map\ (thr\text{-a } \circ\ snd)\ t t a s)))$
 ⟨proof⟩

lemma *redT-upd-inv-ext*:

fixes $I :: 't \rightarrow 'i$
shows $\llbracket s \dashv t a \rightarrow s'; ts\text{-inv-ok} (thr\ s) I \rrbracket \implies I \subseteq_m\ upd\text{-invs } I\ P\ \{\!\{ta}\!\}_t$
 ⟨proof⟩

lemma *RedT-upd-inv-ext*:

fixes $I :: 't \rightarrow 'i$
shows $\llbracket s \dashv t t a s \rightarrow^* s'; ts\text{-inv-ok} (thr\ s) I \rrbracket$
 $\implies I \subseteq_m\ upd\text{-invs } I\ P\ (concat\ (map\ (thr\text{-a } \circ\ snd)\ t t a s))$
 ⟨proof⟩

end

locale *lifting-inv = multithreaded final r convert-RA*

for *final* :: 'x ⇒ bool

and *r* :: ('l,'t,'x,'m,'w,'o) semantics (⟨- ⊢ - - -> -> [50,0,0,50] 80)

and *convert-RA* :: 'l released-locks ⇒ 'o list

+

fixes *P* :: 'i ⇒ 't ⇒ 'x ⇒ 'm ⇒ bool

assumes *invariant-red*: $\llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; P \ i \ t \ x \ m \rrbracket \Longrightarrow P \ i \ t \ x' \ m'$

and *invariant-NewThread*: $\llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; P \ i \ t \ x \ m; \text{NewThread } t'' \ x'' \ m' \in \text{set } \{\{ta\}_t\} \rrbracket$

$\Longrightarrow \exists i''. P \ i'' \ t'' \ x'' \ m'$

and *invariant-other*: $\llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; P \ i \ t \ x \ m; P \ i'' \ t'' \ x'' \ m \rrbracket \Longrightarrow P \ i'' \ t'' \ x'' \ m'$

begin

lemma *redT-updTs-invariant*:

fixes *ln*

assumes *tsiP*: *ts-inv P I ts m*

and *red*: $t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle$

and *tao*: *thread-oks ts* $\{\{ta\}_t\}$

and *tst*: $ts \ t = \lfloor (x, ln) \rfloor$

shows *ts-inv P (upd-invs I P* $\{\{ta\}_t\}$) *((redT-updTs ts* $\{\{ta\}_t\}$)*(t* $\mapsto (x', ln'))$) *m'*

<proof>

theorem *redT-invariant*:

assumes *redT*: $s -t \triangleright ta \rightarrow s'$

and *esinvP*: *ts-inv P I (thr s) (shr s)*

shows *ts-inv P (upd-invs I P* $\{\{ta\}_t\}$) *(thr s')* *(shr s')*

<proof>

theorem *RedT-invariant*:

assumes *RedT*: $s -\triangleright t \text{tas} \rightarrow * s'$

and *esinvQ*: *ts-inv P I (thr s) (shr s)*

shows *ts-inv P (upd-invs I P (concat (map (thr-a ∘ snd) ttas))) (thr s')* *(shr s')*

<proof>

lemma *invariant3p-ts-inv: invariant3p redT* $\{s. \exists I. ts\text{-inv } P \ I \ (thr \ s) \ (shr \ s)\}$

<proof>

end

locale *lifting-wf = multithreaded final r convert-RA*

for *final* :: 'x ⇒ bool

and *r* :: ('l,'t,'x,'m,'w,'o) semantics (⟨- ⊢ - - -> -> [50,0,0,50] 80)

and *convert-RA* :: 'l released-locks ⇒ 'o list

+

fixes *P* :: 't ⇒ 'x ⇒ 'm ⇒ bool

assumes *preserves-red*: $\llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; P \ t \ x \ m \rrbracket \Longrightarrow P \ t \ x' \ m'$

and *preserves-NewThread*: $\llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; P \ t \ x \ m; \text{NewThread } t'' \ x'' \ m' \in \text{set } \{\{ta\}_t\} \rrbracket$

$\Longrightarrow P \ t'' \ x'' \ m'$

and *preserves-other*: $\llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; P \ t \ x \ m; P \ t'' \ x'' \ m \rrbracket \Longrightarrow P \ t'' \ x'' \ m'$

begin

lemma *lifting-inv: lifting-inv final r* ($\lambda - :: \text{unit. } P$)

<proof>

lemma *redT-updT_s-preserves*:

fixes *ln*

assumes *esokQ*: $ts\text{-ok } P \text{ } ts \text{ } m$

and *red*: $t \vdash \langle x, m \rangle \text{-}ta \rightarrow \langle x', m' \rangle$

and $ts \text{ } t = \lfloor (x, ln) \rfloor$

and *thread-oks* $ts \text{ } \{ta\}_t$

shows $ts\text{-ok } P \text{ } ((redT\text{-}updTs \text{ } ts \text{ } \{ta\}_t)(t \mapsto (x', ln'))) \text{ } m'$

<proof>

theorem *redT-preserves*:

assumes *redT*: $s \text{-}t \triangleright ta \rightarrow s'$

and *esokQ*: $ts\text{-ok } P \text{ } (thr \text{ } s) \text{ } (shr \text{ } s)$

shows $ts\text{-ok } P \text{ } (thr \text{ } s') \text{ } (shr \text{ } s')$

<proof>

theorem *RedT-preserves*:

$\llbracket s \text{-}t \triangleright ta \rightarrow s'; ts\text{-ok } P \text{ } (thr \text{ } s) \text{ } (shr \text{ } s) \rrbracket \implies ts\text{-ok } P \text{ } (thr \text{ } s') \text{ } (shr \text{ } s')$

<proof>

lemma *invariant3p-ts-ok*: $invariant3p \text{ } redT \text{ } \{s. ts\text{-ok } P \text{ } (thr \text{ } s) \text{ } (shr \text{ } s)\}$

<proof>

end

lemma *lifting-wf-Const* [*intro!*]:

assumes *multithreaded* *final* *r*

shows *lifting-wf* *final* *r* $(\lambda t \text{ } x \text{ } m. k)$

<proof>

end

1.21 Synthetic first and last actions for each thread

theory *FWInitFinLift*

imports

FWLTS

FWLiftingSem

begin

datatype *status* =

PreStart

| *Running*

| *Finished*

abbreviation *convert-TA-initial* :: $(l, t, x, m, w, o) \text{ thread-action} \Rightarrow (l, t, status \times x, m, w, o) \text{ thread-action}$

where *convert-TA-initial* == *convert-extTA* (*Pair* *PreStart*)

lemma *convert-obs-initial-convert-TA-initial*:

$convert\text{-}obs\text{-}initial \text{ } (convert\text{-}TA\text{-}initial \text{ } ta) = convert\text{-}TA\text{-}initial \text{ } (convert\text{-}obs\text{-}initial \text{ } ta)$

<proof>

lemma *convert-TA-initial-inject* [*simp*]:

$convert\text{-}TA\text{-}initial \text{ } ta = convert\text{-}TA\text{-}initial \text{ } ta' \iff ta = ta'$

<proof>

context *final-thread* **begin**

primrec *init-fin-final* :: *status* × *'x* ⇒ *bool*

where *init-fin-final* (*status*, *x*) ⇔ *status* = *Finished* ∧ *final x*

end

context *multithreaded-base* **begin**

inductive *init-fin* :: (*'l*, *'t*, *status* × *'x*, *'m*, *'w*, *'o* *action*) *semantics* (*⋄* - *---* *i* -> [50,0,0,51] 51)

where

NormalAction:

$t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle$

⇒ $t \vdash ((Running, x), m) -convert-TA-initial (convert-obs-initial ta) \rightarrow_i ((Running, x'), m')$

| *InitialThreadAction*:

$t \vdash ((PreStart, x), m) -\{\!\!\{InitialThreadAction\}\!\!\} \rightarrow_i ((Running, x), m)$

| *ThreadFinishAction*:

final x ⇒ $t \vdash ((Running, x), m) -\{\!\!\{ThreadFinishAction\}\!\!\} \rightarrow_i ((Finished, x), m)$

end

declare *split-paired-Ex* [*simp del*]

inductive-simps (**in** *multithreaded-base*) *init-fin-simps* [*simp*]:

$t \vdash ((Finished, x), m) -ta \rightarrow_i xm'$

$t \vdash ((PreStart, x), m) -ta \rightarrow_i xm'$

$t \vdash ((Running, x), m) -ta \rightarrow_i xm'$

$t \vdash xm -ta \rightarrow_i ((Finished, x'), m')$

$t \vdash xm -ta \rightarrow_i ((Running, x'), m')$

$t \vdash xm -ta \rightarrow_i ((PreStart, x'), m')$

declare *split-paired-Ex* [*simp*]

context *multithreaded* **begin**

lemma *multithreaded-init-fin*: *multithreaded init-fin-final init-fin*

<proof>

end

locale *if-multithreaded-base* = *multithreaded-base* +

constrains *final* :: *'x* ⇒ *bool*

and *r* :: (*'l*, *'t*, *'x*, *'m*, *'w*, *'o*) *semantics*

and *convert-RA* :: *'l* *released-locks* ⇒ *'o* *list*

sublocale *if-multithreaded-base* < *if*: *multithreaded-base*

init-fin-final

init-fin

map NormalAction ○ *convert-RA*

<proof>

locale *if-multithreaded* = *if-multithreaded-base* + *multithreaded* +
constrains *final* :: 'x ⇒ bool
and *r* :: ('l,'t,'x,'m,'w,'o) semantics
and *convert-RA* :: 'l released-locks ⇒ 'o list

sublocale *if-multithreaded* < *if*: *multithreaded*
init-fin-final
init-fin
map NormalAction ◦ *convert-RA*
⟨proof⟩

context τ *multithreaded* **begin**

inductive *init-fin- τ move* :: ('l,'t,status × 'x,'m,'w,'o action) τ moves

where

τ move (x, m) ta (x', m')
⇒ *init-fin- τ move* ((*Running*, x), m) (*convert-TA-initial* (*convert-obs-initial* ta)) ((*Running*, x'), m')

lemma *init-fin- τ move-simps* [*simp*]:

init-fin- τ move ((*PreStart*, x), m) ta x'm' = *False*
init-fin- τ move xm ta ((*PreStart*, x'), m') = *False*
init-fin- τ move ((*Running*, x), m) ta ((s, x'), m') ⇔
(∃ ta'. ta = *convert-TA-initial* (*convert-obs-initial* ta') ∧ s = *Running* ∧ τ move (x, m) ta' (x', m'))
init-fin- τ move ((s, x), m) ta ((*Running*, x'), m') ⇔
s = *Running* ∧ (∃ ta'. ta = *convert-TA-initial* (*convert-obs-initial* ta') ∧ τ move (x, m) ta' (x', m'))
init-fin- τ move ((*Finished*, x), m) ta x'm' = *False*
init-fin- τ move xm ta ((*Finished*, x'), m') = *False*
⟨proof⟩

lemma *init-fin-silent-move-RunningI*:

assumes *silent-move* t (x, m) (x', m')
shows τ trsys.*silent-move* (*init-fin* t) *init-fin- τ move* ((*Running*, x), m) ((*Running*, x'), m')

⟨proof⟩

lemma *init-fin-silent-moves-RunningI*:

assumes *silent-moves* t (x, m) (x', m')
shows τ trsys.*silent-moves* (*init-fin* t) *init-fin- τ move* ((*Running*, x), m) ((*Running*, x'), m')

⟨proof⟩

lemma *init-fin-silent-moveD*:

assumes τ trsys.*silent-move* (*init-fin* t) *init-fin- τ move* ((s, x), m) ((s', x'), m')
shows *silent-move* t (x, m) (x', m') ∧ s = s' ∧ s' = *Running*

⟨proof⟩

lemma *init-fin-silent-movesD*:

assumes τ trsys.*silent-moves* (*init-fin* t) *init-fin- τ move* ((s, x), m) ((s', x'), m')
shows *silent-moves* t (x, m) (x', m') ∧ s = s'

⟨proof⟩

lemma *init-fin- τ divergeD*:

assumes τ trsys. τ *diverge* (*init-fin* t) *init-fin- τ move* ((*status*, x), m)
shows τ *diverge* t (x, m) ∧ *status* = *Running*

<proof>

lemma *init-fin- τ diverge-RunningI*:

assumes τ diverge $t (x, m)$

shows τ trsys. τ diverge (*init-fin* t) *init-fin- τ move* ((*Running*, x), m)

<proof>

lemma *init-fin- τ diverge-conv*:

τ trsys. τ diverge (*init-fin* t) *init-fin- τ move* ((*status*, x), m) \longleftrightarrow

τ diverge $t (x, m) \wedge$ *status* = *Running*

<proof>

end

lemma *init-fin- τ moves-False*:

τ multithreaded.*init-fin- τ move* (λ - - . *False*) = (λ - - . *False*)

<proof>

locale *if- τ multithreaded* = *if-multithreaded-base* + τ multithreaded +

constrains *final* :: ' $x \Rightarrow$ bool

and r :: (' $l, 't, 'x, 'm, 'w, 'o$) *semantics*

and *convert-RA* :: ' l *released-locks* \Rightarrow ' o *list*

and τ move :: (' $l, 't, 'x, 'm, 'w, 'o$) τ moves

sublocale *if- τ multithreaded* < *if*: τ multithreaded

init-fin-final

init-fin

map NormalAction \circ *convert-RA*

init-fin- τ move

<proof>

locale *if- τ multithreaded-wf* = *if-multithreaded-base* + τ multithreaded-wf +

constrains *final* :: ' $x \Rightarrow$ bool

and r :: (' $l, 't, 'x, 'm, 'w, 'o$) *semantics*

and *convert-RA* :: ' l *released-locks* \Rightarrow ' o *list*

and τ move :: (' $l, 't, 'x, 'm, 'w, 'o$) τ moves

sublocale *if- τ multithreaded-wf* < *if-multithreaded*

<proof>

sublocale *if- τ multithreaded-wf* < *if- τ multithreaded* *<proof>*

context τ multithreaded-wf **begin**

lemma τ multithreaded-wf-*init-fin*:

τ multithreaded-wf *init-fin-final* *init-fin* *init-fin- τ move*

<proof>

end

sublocale *if- τ multithreaded-wf* < *if*: τ multithreaded-wf

init-fin-final

init-fin

map NormalAction \circ *convert-RA*

init-fin- τ move
 ⟨proof⟩

primrec *init-fin-lift-inv* :: ('i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool) \Rightarrow 'i \Rightarrow 't \Rightarrow status \times 'x \Rightarrow 'm \Rightarrow bool
where *init-fin-lift-inv* P I t (s, x) = P I t x

context *lifting-inv* **begin**

lemma *lifting-inv-init-fin-lift-inv*:
lifting-inv init-fin-final init-fin (init-fin-lift-inv P)
 ⟨proof⟩

end

locale *if-lifting-inv* =
if-multithreaded +
lifting-inv +
constrains *final* :: 'x \Rightarrow bool
and *r* :: ('l, 't, 'x, 'm, 'w, 'o) *semantics*
and *convert-RA* :: 'l *released-locks* \Rightarrow 'o *list*
and *P* :: 'i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool

sublocale *if-lifting-inv* < *if*: *lifting-inv*
init-fin-final
init-fin
map NormalAction \circ *convert-RA*
init-fin-lift-inv P
 ⟨proof⟩

primrec *init-fin-lift* :: ('t \Rightarrow 'x \Rightarrow 'm \Rightarrow bool) \Rightarrow 't \Rightarrow status \times 'x \Rightarrow 'm \Rightarrow bool
where *init-fin-lift* P t (s, x) = P t x

context *lifting-wf* **begin**

lemma *lifting-wf-init-fin-lift*:
lifting-wf init-fin-final init-fin (init-fin-lift P)
 ⟨proof⟩

end

locale *if-lifting-wf* =
if-multithreaded +
lifting-wf +
constrains *final* :: 'x \Rightarrow bool
and *r* :: ('l, 't, 'x, 'm, 'w, 'o) *semantics*
and *convert-RA* :: 'l *released-locks* \Rightarrow 'o *list*
and *P* :: 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool

sublocale *if-lifting-wf* < *if*: *lifting-wf*
init-fin-final
init-fin
map NormalAction \circ *convert-RA*
init-fin-lift P

$\langle proof \rangle$

lemma (in *if-lifting-wf*) *if-lifting-inv*:
if-lifting-inv final r ($\lambda::unit. P$)

$\langle proof \rangle$

locale τ *lifting-inv* = τ *multithreaded-wf* +
lifting-inv +
constrains *final* :: 'x \Rightarrow bool
and *r* :: ('l,'t,'x,'m,'w,'o) *semantics*
and *convert-RA* :: 'l *released-locks* \Rightarrow 'o *list*
and τ *move* :: ('l,'t,'x,'m,'w,'o) τ *moves*
and *P* :: 'i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool
begin

lemma *redT-silent-move-invariant*:

$\llbracket \tau mredT s s'; ts\text{-inv } P \text{ Is } (thr s) (shr s) \rrbracket \Longrightarrow ts\text{-inv } P \text{ Is } (thr s') (shr s')$

$\langle proof \rangle$

lemma *redT-silent-moves-invariant*:

$\llbracket mthr.\text{silent-moves } s s'; ts\text{-inv } P \text{ Is } (thr s) (shr s) \rrbracket \Longrightarrow ts\text{-inv } P \text{ Is } (thr s') (shr s')$

$\langle proof \rangle$

lemma *redT- τ rtrancl3p-invariant*:

$\llbracket mthr.\tau rtrancl3p s ttas s'; ts\text{-inv } P \text{ Is } (thr s) (shr s) \rrbracket$
 $\Longrightarrow ts\text{-inv } P (upd\text{-invs Is } P (concat (map (thr\text{-a} \circ snd) ttas))) (thr s') (shr s')$

$\langle proof \rangle$

end

locale τ *lifting-wf* = τ *multithreaded* +
lifting-wf +
constrains *final* :: 'x \Rightarrow bool
and *r* :: ('l,'t,'x,'m,'w,'o) *semantics*
and *convert-RA* :: 'l *released-locks* \Rightarrow 'o *list*
and τ *move* :: ('l,'t,'x,'m,'w,'o) τ *moves*
and *P* :: 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool
begin

lemma *redT-silent-move-preserves*:

$\llbracket \tau mredT s s'; ts\text{-ok } P (thr s) (shr s) \rrbracket \Longrightarrow ts\text{-ok } P (thr s') (shr s')$

$\langle proof \rangle$

lemma *redT-silent-moves-preserves*:

$\llbracket mthr.\text{silent-moves } s s'; ts\text{-ok } P (thr s) (shr s) \rrbracket \Longrightarrow ts\text{-ok } P (thr s') (shr s')$

$\langle proof \rangle$

lemma *redT- τ rtrancl3p-preserves*:

$\llbracket mthr.\tau rtrancl3p s ttas s'; ts\text{-ok } P (thr s) (shr s) \rrbracket \Longrightarrow ts\text{-ok } P (thr s') (shr s')$

$\langle proof \rangle$

end

definition *init-fin-lift-state* :: *status* \Rightarrow ('l,'t,'x,'m,'w) *state* \Rightarrow ('l,'t,*status* \times 'x,'m,'w) *state*

where $\text{init-fin-lift-state } s \sigma = (\text{locks } \sigma, (\lambda t. \text{map-option } (\lambda(x, \text{ln}). ((s, x), \text{ln})) (\text{thr } \sigma t), \text{shr } \sigma), \text{wset } \sigma, \text{interrupts } \sigma)$

definition $\text{init-fin-descend-thr} :: ('l, 't, 'status \times 'x) \text{thread-info} \Rightarrow ('l, 't, 'x) \text{thread-info}$
where $\text{init-fin-descend-thr } ts = \text{map-option } (\lambda((s, x), \text{ln}). (x, \text{ln})) \circ ts$

definition $\text{init-fin-descend-state} :: ('l, 't, 'status \times 'x, 'm, 'w) \text{state} \Rightarrow ('l, 't, 'x, 'm, 'w) \text{state}$
where $\text{init-fin-descend-state } \sigma = (\text{locks } \sigma, (\text{init-fin-descend-thr } (\text{thr } \sigma), \text{shr } \sigma), \text{wset } \sigma, \text{interrupts } \sigma)$

lemma $\text{ts-ok-init-fin-lift-init-fin-lift-state}$ [simp]:

$\text{ts-ok } (\text{init-fin-lift } P) (\text{thr } (\text{init-fin-lift-state } s \sigma)) (\text{shr } (\text{init-fin-lift-state } s \sigma)) \longleftrightarrow \text{ts-ok } P (\text{thr } \sigma)$
 $(\text{shr } \sigma)$
 <proof>

lemma $\text{ts-inv-init-fin-lift-inv-init-fin-lift-state}$ [simp]:

$\text{ts-inv } (\text{init-fin-lift-inv } P) I (\text{thr } (\text{init-fin-lift-state } s \sigma)) (\text{shr } (\text{init-fin-lift-state } s \sigma)) \longleftrightarrow$
 $\text{ts-inv } P I (\text{thr } \sigma) (\text{shr } \sigma)$
 <proof>

lemma $\text{init-fin-lift-state-conv-simps}$:

shows $\text{shr-init-fin-lift-state}$: $\text{shr } (\text{init-fin-lift-state } s \sigma) = \text{shr } \sigma$
and $\text{locks-init-fin-lift-state}$: $\text{locks } (\text{init-fin-lift-state } s \sigma) = \text{locks } \sigma$
and $\text{wset-init-fin-lift-state}$: $\text{wset } (\text{init-fin-lift-state } s \sigma) = \text{wset } \sigma$
and $\text{interrupts-init-fin-lift-state}$: $\text{interrupts } (\text{init-fin-lift-state } s \sigma) = \text{interrupts } \sigma$
and $\text{thr-init-fin-lift-state}$:
 $\text{thr } (\text{init-fin-lift-state } s \sigma) t = \text{map-option } (\lambda(x, \text{ln}). ((s, x), \text{ln})) (\text{thr } \sigma t)$
 <proof>

lemma $\text{thr-init-fin-lift-state}'$:

$\text{thr } (\text{init-fin-lift-state } s \sigma) = \text{map-option } (\lambda(x, \text{ln}). ((s, x), \text{ln})) \circ \text{thr } \sigma$
 <proof>

lemma $\text{init-fin-descend-thr-Some-conv}$ [simp]:

$\bigwedge \text{ln}. \text{ts } t = \lfloor ((\text{status}, x), \text{ln}) \rfloor \Longrightarrow \text{init-fin-descend-thr } \text{ts } t = \lfloor (x, \text{ln}) \rfloor$
 <proof>

lemma $\text{init-fin-descend-thr-None-conv}$ [simp]:

$\text{ts } t = \text{None} \Longrightarrow \text{init-fin-descend-thr } \text{ts } t = \text{None}$
 <proof>

lemma $\text{init-fin-descend-thr-eq-None}$ [simp]:

$\text{init-fin-descend-thr } \text{ts } t = \text{None} \longleftrightarrow \text{ts } t = \text{None}$
 <proof>

lemma $\text{init-fin-descend-state-simps}$ [simp]:

$\text{init-fin-descend-state } (\text{ls}, (\text{ts}, m), \text{ws}, \text{is}) = (\text{ls}, (\text{init-fin-descend-thr } \text{ts}, m), \text{ws}, \text{is})$
 $\text{locks } (\text{init-fin-descend-state } s) = \text{locks } s$
 $\text{thr } (\text{init-fin-descend-state } s) = \text{init-fin-descend-thr } (\text{thr } s)$
 $\text{shr } (\text{init-fin-descend-state } s) = \text{shr } s$
 $\text{wset } (\text{init-fin-descend-state } s) = \text{wset } s$
 $\text{interrupts } (\text{init-fin-descend-state } s) = \text{interrupts } s$
 <proof>

lemma $\text{init-fin-descend-thr-update}$ [simp]:

$init_fin_descend_thr (ts(t := v)) = (init_fin_descend_thr ts)(t := map_option (\lambda((status, x), ln). (x, ln)) v)$
 ⟨proof⟩

lemma *ts-ok-init-fin-descend-state*:

$ts_ok P (init_fin_descend_thr ts) = ts_ok (init_fin_lift P) ts$
 ⟨proof⟩

lemma *free-thread-id-init-fin-descend-thr* [simp]:

$free_thread_id (init_fin_descend_thr ts) = free_thread_id ts$
 ⟨proof⟩

lemma *redT-updT'-init-fin-descend-thr-eq-None* [simp]:

$redT_updT' (init_fin_descend_thr ts) nt t = None \longleftrightarrow redT_updT' ts nt t = None$
 ⟨proof⟩

lemma *thread-ok-init-fin-descend-thr* [simp]:

$thread_ok (init_fin_descend_thr ts) nta = thread_ok ts nta$
 ⟨proof⟩

lemma *threads-ok-init-fin-descend-thr* [simp]:

$thread_oks (init_fin_descend_thr ts) ntas = thread_oks ts ntas$
 ⟨proof⟩

lemma *init-fin-descend-thr-redT-updT* [simp]:

$init_fin_descend_thr (redT_updT ts (convert_new_thread_action (Pair status) nt)) = redT_updT (init_fin_descend_thr ts) nt$
 ⟨proof⟩

lemma *init-fin-descend-thr-redT-updTs* [simp]:

$init_fin_descend_thr (redT_updTs ts (map (convert_new_thread_action (Pair status)) nts)) = redT_updTs (init_fin_descend_thr ts) nts$
 ⟨proof⟩

context *final-thread* **begin**

lemma *cond-action-ok-init-fin-descend-stateI* [simp]:

$final_thread.cond_action_ok init_fin_final s t ct \implies cond_action_ok (init_fin_descend_state s) t ct$
 ⟨proof⟩

lemma *cond-action-oks-init-fin-descend-stateI* [simp]:

$final_thread.cond_action_oks init_fin_final s t cts \implies cond_action_oks (init_fin_descend_state s) t cts$
 ⟨proof⟩

end

definition *lift-start-obs* :: 't ⇒ 'o list ⇒ ('t × 'o action) list

where *lift-start-obs* t obs = (t, InitialThreadAction) # map (λob. (t, NormalAction ob)) obs

lemma *length-lift-start-obs* [simp]: $length (lift_start_obs t obs) = Suc (length obs)$

⟨proof⟩

lemma *set-lift-start-obs* [simp]:

$set (lift\text{-}start\text{-}obs\ t\ obs) =$
 $insert (t, InitialThreadAction) ((Pair\ t\ \circ\ NormalAction)\ 'set\ obs)$
 <proof>

lemma *distinct-lift-start-obs* [simp]: $distinct (lift\text{-}start\text{-}obs\ t\ obs) = distinct\ obs$
 <proof>

end

theory *FWBisimLift* **imports**

FWInitFinLift

FWBisimulation

begin

context *FWbisimulation-base* **begin**

inductive *init-fin-bisim* :: $'t \Rightarrow ((status \times 'x1) \times 'm1, (status \times 'x2) \times 'm2)\ bisim$
 $(\langle - \vdash - \approx_i \rangle [50,50,50]\ 60)$

for $t :: 't$

where

$PreStart: t \vdash (x1, m1) \approx (x2, m2) \Longrightarrow t \vdash ((PreStart, x1), m1) \approx_i ((PreStart, x2), m2)$
 $Running: t \vdash (x1, m1) \approx (x2, m2) \Longrightarrow t \vdash ((Running, x1), m1) \approx_i ((Running, x2), m2)$
 $Finished:$
 $\llbracket t \vdash (x1, m1) \approx (x2, m2); final1\ x1; final2\ x2 \rrbracket$
 $\Longrightarrow t \vdash ((Finished, x1), m1) \approx_i ((Finished, x2), m2)$

definition *init-fin-bisim-wait* :: $(status \times 'x1, status \times 'x2)\ bisim (\langle - \approx_{iw} \rangle [50,50]\ 60)$

where

$init\text{-}fin\text{-}bisim\text{-}wait = (\lambda(status1, x1)\ (status2, x2).\ status1 = Running \wedge status2 = Running \wedge x1 \approx_w x2)$

inductive-simps *init-fin-bisim-simps* [simp]:

$t \vdash ((PreStart, x1), m1) \approx_i ((s2, x2), m2)$
 $t \vdash ((Running, x1), m1) \approx_i ((s2, x2), m2)$
 $t \vdash ((Finished, x1), m1) \approx_i ((s2, x2), m2)$
 $t \vdash ((s1, x1), m1) \approx_i ((PreStart, x2), m2)$
 $t \vdash ((s1, x1), m1) \approx_i ((Running, x2), m2)$
 $t \vdash ((s1, x1), m1) \approx_i ((Finished, x2), m2)$

lemma *init-fin-bisim-iff*:

$t \vdash ((s1, x1), m1) \approx_i ((s2, x2), m2) \longleftrightarrow$
 $s1 = s2 \wedge t \vdash (x1, m1) \approx (x2, m2) \wedge (s2 = Finished \longrightarrow final1\ x1 \wedge final2\ x2)$
 <proof>

lemma *nta-bisim-init-fin-bisim* [simp]:

$nta\text{-}bisim\ init\text{-}fin\text{-}bisim (convert\text{-}new\text{-}thread\text{-}action (Pair\ PreStart)\ nt1)$
 $(convert\text{-}new\text{-}thread\text{-}action (Pair\ PreStart)\ nt2) =$
 $nta\text{-}bisim\ bisim\ nt1\ nt2$
 <proof>

lemma *ta-bisim-init-fin-bisim-convert* [simp]:

$ta\text{-}bisim\ init\text{-}fin\text{-}bisim (convert\text{-}TA\text{-}initial (convert\text{-}obs\text{-}initial\ ta1)) (convert\text{-}TA\text{-}initial (convert\text{-}obs\text{-}initial\ ta2)) \longleftrightarrow ta1 \sim_m ta2$
 <proof>

lemma *ta-bisim-init-fin-bisim-InitialThreadAction* [simp]:

ta-bisim init-fin-bisim $\{\text{InitialThreadAction}\}$ $\{\text{InitialThreadAction}\}$
 $\langle \text{proof} \rangle$

lemma *ta-bisim-init-fin-bisim-ThreadFinishAction* [simp]:

ta-bisim init-fin-bisim $\{\text{ThreadFinishAction}\}$ $\{\text{ThreadFinishAction}\}$
 $\langle \text{proof} \rangle$

lemma *init-fin-bisim-wait-simps* [simp]:

$(\text{status1}, x1) \approx_{iw} (\text{status2}, x2) \iff \text{status1} = \text{Running} \wedge \text{status2} = \text{Running} \wedge x1 \approx_w x2$
 $\langle \text{proof} \rangle$

lemma *init-fin-lift-state-mbisimI*:

$s \approx_m s' \implies$
FWbisimulation-base.mbisim init-fin-bisim init-fin-bisim-wait (*init-fin-lift-state Running s*) (*init-fin-lift-state Running s'*)
 $\langle \text{proof} \rangle$

end

context *FWdelay-bisimulation-base* **begin**

lemma *init-fin-delay-bisimulation-final-base*:

delay-bisimulation-final-base (*r1.init-fin t*) (*r2.init-fin t*) (*init-fin-bisim t*)
 $r1.\text{init-fin-}\tau\text{move } r2.\text{init-fin-}\tau\text{move } (\lambda(x1, m). r1.\text{init-fin-final } x1) (\lambda(x2, m). r2.\text{init-fin-final } x2)$
 $\langle \text{proof} \rangle$

end

lemma *init-fin-bisim-flip* [flip-simps]:

FWbisimulation-base.init-fin-bisim final2 final1 ($\lambda t. \text{flip } (\text{bisim } t)$) =
 $(\lambda t. \text{flip } (\text{FWbisimulation-base.init-fin-bisim final1 final2 bisim } t))$
 $\langle \text{proof} \rangle$

lemma *init-fin-bisim-wait-flip* [flip-simps]:

FWbisimulation-base.init-fin-bisim-wait (*flip bisim-wait*) =
 $\text{flip } (\text{FWbisimulation-base.init-fin-bisim-wait bisim-wait})$
 $\langle \text{proof} \rangle$

context *FWdelay-bisimulation-lift-aux* **begin**

lemma *init-fin-FWdelay-bisimulation-lift-aux*:

FWdelay-bisimulation-lift-aux *r1.init-fin-final r1.init-fin r2.init-fin-final r2.init-fin r1.init-fin-}\tau\text{move}*
 $r2.\text{init-fin-}\tau\text{move}$
 $\langle \text{proof} \rangle$

lemma *init-fin-FWdelay-bisimulation-final-base*:

FWdelay-bisimulation-final-base
 $r1.\text{init-fin-final } r1.\text{init-fin } r2.\text{init-fin-final } r2.\text{init-fin}$
 $\text{init-fin-bisim } r1.\text{init-fin-}\tau\text{move } r2.\text{init-fin-}\tau\text{move}$
 $\langle \text{proof} \rangle$

end

context *FWdelay-bisimulation-obs* **begin**

lemma *init-fin-simulation1*:

assumes *bisim*: $t \vdash s1 \approx_i s2$

and *red1*: $r1.\text{init-fin } t \ s1 \ tl1 \ s1'$

and $\tau1: \neg r1.\text{init-fin-}\tau\text{move } s1 \ tl1 \ s1'$

shows $\exists s2' \ s2'' \ tl2. (\tau\text{trsys.silent-move } (r2.\text{init-fin } t) \ r2.\text{init-fin-}\tau\text{move})^{**} \ s2 \ s2' \wedge$
 $r2.\text{init-fin } t \ s2' \ tl2 \ s2'' \wedge \neg r2.\text{init-fin-}\tau\text{move } s2' \ tl2 \ s2'' \wedge$
 $t \vdash s1' \approx_i s2'' \wedge \text{ta-bisim } \text{init-fin-bisim } \ tl1 \ tl2$

<proof>

lemma *init-fin-simulation2*:

$\llbracket t \vdash s1 \approx_i s2; r2.\text{init-fin } t \ s2 \ tl2 \ s2'; \neg r2.\text{init-fin-}\tau\text{move } s2 \ tl2 \ s2' \rrbracket$

$\implies \exists s1' \ s1'' \ tl1. (\tau\text{trsys.silent-move } (r1.\text{init-fin } t) \ r1.\text{init-fin-}\tau\text{move})^{**} \ s1 \ s1' \wedge$
 $r1.\text{init-fin } t \ s1' \ tl1 \ s1'' \wedge \neg r1.\text{init-fin-}\tau\text{move } s1' \ tl1 \ s1'' \wedge$
 $t \vdash s1'' \approx_i s2' \wedge \text{ta-bisim } \text{init-fin-bisim } \ tl1 \ tl2$

<proof>

lemma *init-fin-simulation-Wakeup1*:

assumes *bisim*: $t \vdash (sx1, m1) \approx_i (sx2, m2)$

and *wait*: $sx1 \approx_{iw} sx2$

and *red1*: $r1.\text{init-fin } t \ (sx1, m1) \ ta1 \ (sx1', m1')$

and *wakeup*: $\text{Notified} \in \text{set } \{ta1\}_w \vee \text{WokenUp} \in \text{set } \{ta1\}_w$

shows $\exists ta2 \ sx2' \ m2'. r2.\text{init-fin } t \ (sx2, m2) \ ta2 \ (sx2', m2') \wedge t \vdash (sx1', m1') \approx_i (sx2', m2') \wedge$
 $\text{ta-bisim } \text{init-fin-bisim } \ ta1 \ ta2$

<proof>

lemma *init-fin-simulation-Wakeup2*:

$\llbracket t \vdash (sx1, m1) \approx_i (sx2, m2); sx1 \approx_{iw} sx2; r2.\text{init-fin } t \ (sx2, m2) \ ta2 \ (sx2', m2');$

$\text{Notified} \in \text{set } \{ta2\}_w \vee \text{WokenUp} \in \text{set } \{ta2\}_w \rrbracket$

$\implies \exists ta1 \ sx1' \ m1'. r1.\text{init-fin } t \ (sx1, m1) \ ta1 \ (sx1', m1') \wedge t \vdash (sx1', m1') \approx_i (sx2', m2') \wedge$
 $\text{ta-bisim } \text{init-fin-bisim } \ ta1 \ ta2$

<proof>

lemma *init-fin-delay-bisimulation-obs*:

$\text{delay-bisimulation-obs } (r1.\text{init-fin } t) \ (r2.\text{init-fin } t) \ (\text{init-fin-bisim } t) \ (\text{ta-bisim } \text{init-fin-bisim})$
 $r1.\text{init-fin-}\tau\text{move } r2.\text{init-fin-}\tau\text{move}$

<proof>

lemma *init-fin-FWdelay-bisimulation-obs*:

$\text{FWdelay-bisimulation-obs } r1.\text{init-fin-final } r1.\text{init-fin } r2.\text{init-fin-final } r2.\text{init-fin } \text{init-fin-bisim } \text{init-fin-bisim-wait}$
 $r1.\text{init-fin-}\tau\text{move } r2.\text{init-fin-}\tau\text{move}$

<proof>

end

context *FWdelay-bisimulation-diverge* **begin**

lemma *init-fin-simulation-silent1*:

$\llbracket t \vdash \text{sxm1} \approx_i \text{sxm2}; \tau\text{trsys.silent-move } (r1.\text{init-fin } t) \ r1.\text{init-fin-}\tau\text{move } \text{sxm1} \ \text{sxm1}' \rrbracket$

$\implies \exists \text{sxm2}'. \tau\text{trsys.silent-moves } (r2.\text{init-fin } t) \ r2.\text{init-fin-}\tau\text{move } \text{sxm2} \ \text{sxm2}' \wedge t \vdash \text{sxm1}' \approx_i \text{sxm2}'$

<proof>

lemma *init-fin-simulation-silent2*:

$$\llbracket t \vdash \text{sxm1} \approx_i \text{sxm2}; \tau\text{trsys.silent-move} (r2.\text{init-fin } t) r2.\text{init-fin-}\tau\text{move } \text{sxm2 } \text{sxm2}' \rrbracket$$

$$\implies \exists \text{sxm1}'. \tau\text{trsys.silent-moves} (r1.\text{init-fin } t) r1.\text{init-fin-}\tau\text{move } \text{sxm1 } \text{sxm1}' \wedge t \vdash \text{sxm1}' \approx_i \text{sxm2}'$$
 <proof>

lemma *init-fin- τ diverge-bisim-inv:*

$$t \vdash \text{sxm1} \approx_i \text{sxm2}$$

$$\implies \tau\text{trsys.}\tau\text{diverge} (r1.\text{init-fin } t) r1.\text{init-fin-}\tau\text{move } \text{sxm1} =$$

$$\tau\text{trsys.}\tau\text{diverge} (r2.\text{init-fin } t) r2.\text{init-fin-}\tau\text{move } \text{sxm2}$$
 <proof>

lemma *init-fin-delay-bisimulation-diverge:*

$$\text{delay-bisimulation-diverge} (r1.\text{init-fin } t) (r2.\text{init-fin } t) (\text{init-fin-bisim } t) (\text{ta-bisim } \text{init-fin-bisim})$$

$$r1.\text{init-fin-}\tau\text{move } r2.\text{init-fin-}\tau\text{move}$$
 <proof>

lemma *init-fin-FWdelay-bisimulation-diverge:*

$$\text{FWdelay-bisimulation-diverge } r1.\text{init-fin-final } r1.\text{init-fin } r2.\text{init-fin-final } r2.\text{init-fin } \text{init-fin-bisim } \text{init-fin-bisim-wa}$$

$$r1.\text{init-fin-}\tau\text{move } r2.\text{init-fin-}\tau\text{move}$$
 <proof>

end

context *FWbisimulation* **begin**

lemma *init-fin-simulation1:*

assumes $t \vdash s1 \approx_i s2$ **and** $r1.\text{init-fin } t s1 \text{tl1 } s1'$
shows $\exists s2' \text{tl2}. r2.\text{init-fin } t s2 \text{tl2 } s2' \wedge t \vdash s1' \approx_i s2' \wedge \text{ta-bisim } \text{init-fin-bisim } \text{tl1 } \text{tl2}$
 <proof>

lemma *init-fin-simulation2:*

$$\llbracket t \vdash s1 \approx_i s2; r2.\text{init-fin } t s2 \text{tl2 } s2' \rrbracket$$

$$\implies \exists s1' \text{tl1}. r1.\text{init-fin } t s1 \text{tl1 } s1' \wedge t \vdash s1' \approx_i s2' \wedge \text{ta-bisim } \text{init-fin-bisim } \text{tl1 } \text{tl2}$$
 <proof>

lemma *init-fin-bisimulation:*

$$\text{bisimulation} (r1.\text{init-fin } t) (r2.\text{init-fin } t) (\text{init-fin-bisim } t) (\text{ta-bisim } \text{init-fin-bisim})$$
 <proof>

lemma *init-fin-FWbisimulation:*

$$\text{FWbisimulation } r1.\text{init-fin-final } r1.\text{init-fin } r2.\text{init-fin-final } r2.\text{init-fin } \text{init-fin-bisim}$$
 <proof>

end

end

Chapter 2

Data Flow Analysis Framework

2.1 Semilattices

```
theory Semilat
imports Main HOL-Library.While-Combinator
begin

type-synonym 'a ord    = 'a ⇒ 'a ⇒ bool
type-synonym 'a binop  = 'a ⇒ 'a ⇒ 'a
type-synonym 'a sl     = 'a set × 'a ord × 'a binop

definition lesub :: 'a ⇒ 'a ord ⇒ 'a ⇒ bool
  where lesub x r y ↔ r x y

definition lessub :: 'a ⇒ 'a ord ⇒ 'a ⇒ bool
  where lessub x r y ↔ lesub x r y ∧ x ≠ y

definition plussub :: 'a ⇒ ('a ⇒ 'b ⇒ 'c) ⇒ 'b ⇒ 'c
  where plussub x f y = f x y

notation (ASCII)
  lesub  (⟨(- /<='-- -)⟩ [50, 1000, 51] 50) and
  lessub (⟨(- /<'-- -)⟩ [50, 1000, 51] 50) and
  plussub (⟨(- /+'-- -)⟩ [65, 1000, 66] 65)

notation
  lesub  (⟨(- /⊑- -)⟩ [50, 0, 51] 50) and
  lessub (⟨(- /⊒- -)⟩ [50, 0, 51] 50) and
  plussub (⟨(- /⊔- -)⟩ [65, 0, 66] 65)

abbreviation (input)
  lesub1 :: 'a ⇒ 'a ord ⇒ 'a ⇒ bool (⟨(- /⊑- -)⟩ [50, 1000, 51] 50)
  where x ⊑r y == x ⊑r y

abbreviation (input)
  lessub1 :: 'a ⇒ 'a ord ⇒ 'a ⇒ bool (⟨(- /⊒- -)⟩ [50, 1000, 51] 50)
  where x ⊒r y == x ⊒r y

abbreviation (input)
```

plussub1 :: 'a ⇒ ('a ⇒ 'b ⇒ 'c) ⇒ 'b ⇒ 'c (⟨(- /⊥ -)⟩ [65, 1000, 66] 65)
where $x \sqcup_f y == x \sqcup_f y$

definition *ord* :: ('a × 'a) set ⇒ 'a ord

where

$ord\ r = (\lambda x\ y. (x,y) \in r)$

definition *order* :: 'a ord ⇒ bool

where

$order\ r \longleftrightarrow (\forall x. x \sqsubseteq_r x) \wedge (\forall x\ y. x \sqsubseteq_r y \wedge y \sqsubseteq_r x \longrightarrow x=y) \wedge (\forall x\ y\ z. x \sqsubseteq_r y \wedge y \sqsubseteq_r z \longrightarrow x \sqsubseteq_r z)$

definition *top* :: 'a ord ⇒ 'a ⇒ bool

where

$top\ r\ T \longleftrightarrow (\forall x. x \sqsubseteq_r T)$

definition *acc* :: 'a set ⇒ 'a ord ⇒ bool

where

$acc\ A\ r \longleftrightarrow wf\ \{(y,x). x \in A \wedge y \in A \wedge x \sqsubseteq_r y\}$

definition *closed* :: 'a set ⇒ 'a binop ⇒ bool

where

$closed\ A\ f \longleftrightarrow (\forall x \in A. \forall y \in A. x \sqcup_f y \in A)$

definition *semilat* :: 'a sl ⇒ bool

where

$semilat = (\lambda(A,r,f). order\ r \wedge closed\ A\ f \wedge$
 $(\forall x \in A. \forall y \in A. x \sqsubseteq_r x \sqcup_f y) \wedge$
 $(\forall x \in A. \forall y \in A. y \sqsubseteq_r x \sqcup_f y) \wedge$
 $(\forall x \in A. \forall y \in A. \forall z \in A. x \sqsubseteq_r z \wedge y \sqsubseteq_r z \longrightarrow x \sqcup_f y \sqsubseteq_r z))$

definition *is-ub* :: ('a × 'a) set ⇒ 'a ⇒ 'a ⇒ 'a ⇒ bool

where

$is-ub\ r\ x\ y\ u \longleftrightarrow (x,u) \in r \wedge (y,u) \in r$

definition *is-lub* :: ('a × 'a) set ⇒ 'a ⇒ 'a ⇒ 'a ⇒ bool

where

$is-lub\ r\ x\ y\ u \longleftrightarrow is-ub\ r\ x\ y\ u \wedge (\forall z. is-ub\ r\ x\ y\ z \longrightarrow (u,z) \in r)$

definition *some-lub* :: ('a × 'a) set ⇒ 'a ⇒ 'a ⇒ 'a

where

$some-lub\ r\ x\ y = (SOME\ z. is-lub\ r\ x\ y\ z)$

locale *Semilat* =

fixes $A :: 'a\ set$

fixes $r :: 'a\ ord$

fixes $f :: 'a\ binop$

assumes *semilat*: $semilat\ (A, r, f)$

lemma *order-refl* [*simp, intro*]: $order\ r \Longrightarrow x \sqsubseteq_r x$

(*proof*)

lemma *order-antisym*: $\llbracket order\ r; x \sqsubseteq_r y; y \sqsubseteq_r x \rrbracket \Longrightarrow x = y$

(*proof*)

lemma *order-trans*: $\llbracket order\ r; x \sqsubseteq_r y; y \sqsubseteq_r z \rrbracket \Longrightarrow x \sqsubseteq_r z$

<proof>

lemma *order-less-irrefl* [*intro, simp*]: $\text{order } r \implies \neg x \sqsubset_r x$

<proof>

lemma *order-less-trans*: $\llbracket \text{order } r; x \sqsubset_r y; y \sqsubset_r z \rrbracket \implies x \sqsubset_r z$

<proof>

lemma *topD* [*simp, intro*]: $\text{top } r \ T \implies x \sqsubseteq_r T$

<proof>

lemma *top-le-conv* [*simp*]: $\llbracket \text{order } r; \text{top } r \ T \rrbracket \implies (T \sqsubseteq_r x) = (x = T)$

<proof>

lemma *semilat-Def*:

$\text{semilat}(A, r, f) \iff \text{order } r \wedge \text{closed } A \ f \wedge$
 $(\forall x \in A. \forall y \in A. x \sqsubseteq_r x \sqcup_f y) \wedge$
 $(\forall x \in A. \forall y \in A. y \sqsubseteq_r x \sqcup_f y) \wedge$
 $(\forall x \in A. \forall y \in A. \forall z \in A. x \sqsubseteq_r z \wedge y \sqsubseteq_r z \implies x \sqcup_f y \sqsubseteq_r z)$

<proof>

lemma (**in** *Semilat*) *orderI* [*simp, intro*]: $\text{order } r$

<proof>

lemma (**in** *Semilat*) *closedI* [*simp, intro*]: $\text{closed } A \ f$

<proof>

lemma *closedD*: $\llbracket \text{closed } A \ f; x \in A; y \in A \rrbracket \implies x \sqcup_f y \in A$

<proof>

lemma *closed-UNIV* [*simp*]: $\text{closed } UNIV \ f$

<proof>

lemma (**in** *Semilat*) *closed-f* [*simp, intro*]: $\llbracket x \in A; y \in A \rrbracket \implies x \sqcup_f y \in A$

<proof>

lemma (**in** *Semilat*) *refl-r* [*intro, simp*]: $x \sqsubseteq_r x$ *<proof>*

lemma (**in** *Semilat*) *antisym-r* [*intro?*]: $\llbracket x \sqsubseteq_r y; y \sqsubseteq_r x \rrbracket \implies x = y$

<proof>

lemma (**in** *Semilat*) *trans-r* [*trans, intro?*]: $\llbracket x \sqsubseteq_r y; y \sqsubseteq_r z \rrbracket \implies x \sqsubseteq_r z$

<proof>

lemma (**in** *Semilat*) *ub1* [*simp, intro?*]: $\llbracket x \in A; y \in A \rrbracket \implies x \sqsubseteq_r x \sqcup_f y$

<proof>

lemma (**in** *Semilat*) *ub2* [*simp, intro?*]: $\llbracket x \in A; y \in A \rrbracket \implies y \sqsubseteq_r x \sqcup_f y$

<proof>

lemma (**in** *Semilat*) *lub* [*simp, intro?*]:

$\llbracket x \sqsubseteq_r z; y \sqsubseteq_r z; x \in A; y \in A; z \in A \rrbracket \implies x \sqcup_f y \sqsubseteq_r z$

<proof>

lemma (**in** *Semilat*) *plus-le-conv* [*simp*]:

$\llbracket x \in A; y \in A; z \in A \rrbracket \implies (x \sqcup_f y \sqsubseteq_r z) = (x \sqsubseteq_r z \wedge y \sqsubseteq_r z)$

<proof>

lemma (**in** *Semilat*) *le-iff-plus-unchanged*:

assumes $x \in A$ **and** $y \in A$

shows $x \sqsubseteq_r y \iff x \sqcup_f y = y$ (**is** $?P \iff ?Q$) *<proof>*

lemma (**in** *Semilat*) *le-iff-plus-unchanged2*:

assumes $x \in A$ **and** $y \in A$

shows $x \sqsubseteq_r y \iff y \sqcup_f x = y$ (**is** $?P \iff ?Q$) *<proof>*

lemma (**in** *Semilat*) *plus-assoc* [*simp*]:

assumes $a: a \in A$ **and** $b: b \in A$ **and** $c: c \in A$

shows $a \sqcup_f (b \sqcup_f c) = a \sqcup_f b \sqcup_f c$ *<proof>*

lemma (**in** *Semilat*) *plus-com-lemma*:

$\llbracket a \in A; b \in A \rrbracket \implies a \sqcup_f b \sqsubseteq_r b \sqcup_f a$ *<proof>*

lemma (**in** *Semilat*) *plus-commutative*:

$\llbracket a \in A; b \in A \rrbracket \implies a \sqcup_f b = b \sqcup_f a$

<proof>

lemma *is-lubD*:

$is-lub\ r\ x\ y\ u \implies is-ub\ r\ x\ y\ u \wedge (\forall z. is-ub\ r\ x\ y\ z \longrightarrow (u,z) \in r)$

<proof>

lemma *is-ubI*:

$\llbracket (x,u) \in r; (y,u) \in r \rrbracket \implies is-ub\ r\ x\ y\ u$

<proof>

lemma *is-ubD*:

$is-ub\ r\ x\ y\ u \implies (x,u) \in r \wedge (y,u) \in r$

<proof>

lemma *is-lub-bigger1* [*iff*]:

$is-lub\ (r\hat{*})\ x\ y\ y = ((x,y) \in r\hat{*})$ *<proof>*

lemma *is-lub-bigger2* [*iff*]:

$is-lub\ (r\hat{*})\ x\ y\ x = ((y,x) \in r\hat{*})$ *<proof>*

lemma *extend-lub*:

assumes *single-valued* *r*

and *is-lub* $(r^*)\ x\ y\ u$

and $(x', x) \in r$

shows $\exists v. is-lub\ (r^*)\ x'\ y\ v$ *<proof>*

lemma *single-valued-has-lubs*:

assumes *single-valued* *r*

and *in-r*: $(x, u) \in r^*\ (y, u) \in r^*$

shows $\exists z. is-lub\ (r^*)\ x\ y\ z$ *<proof>*

lemma *some-lub-conv*:

$\llbracket acyclic\ r; is-lub\ (r\hat{*})\ x\ y\ u \rrbracket \implies some-lub\ (r\hat{*})\ x\ y = u$ *<proof>*

lemma *is-lub-some-lub*:

$\llbracket single-valued\ r; acyclic\ r; (x,u) \in r\hat{*}; (y,u) \in r\hat{*} \rrbracket$

$\implies is-lub\ (r\hat{*})\ x\ y\ (some-lub\ (r\hat{*})\ x\ y)$

<proof>

2.1.1 An executable lub-finder

definition *exec-lub* :: $('a * 'a)\ set \Rightarrow ('a \Rightarrow 'a) \Rightarrow 'a\ binop$

where

$exec-lub\ r\ f\ x\ y = while\ (\lambda z. (x,z) \notin r^*)\ f\ y$

lemma *exec-lub-refl*: $exec-lub\ r\ f\ T\ T = T$

<proof>

lemma *acyclic-single-valued-finite*:

$\llbracket acyclic\ r; single-valued\ r; (x,y) \in r^* \rrbracket$

$\implies finite\ (r \cap \{a. (x, a) \in r^*\} \times \{b. (b, y) \in r^*\})$ *<proof>*

lemma *exec-lub-conv*:

$\llbracket acyclic\ r; \forall x\ y. (x,y) \in r \longrightarrow f\ x = y; is-lub\ (r^*)\ x\ y\ u \rrbracket \implies$

$exec-lub\ r\ f\ x\ y = u$ *<proof>*

lemma *is-lub-exec-lub*:

$\llbracket single-valued\ r; acyclic\ r; (x,u):r\hat{*}; (y,u):r\hat{*}; \forall x\ y. (x,y) \in r \longrightarrow f\ x = y \rrbracket$

$\implies is-lub\ (r\hat{*})\ x\ y\ (exec-lub\ r\ f\ x\ y)$

<proof>

end

2.2 The Error Type

```

theory Err
imports Semilat
begin

datatype 'a err = Err | OK 'a

type-synonym 'a ebinop = 'a  $\Rightarrow$  'a  $\Rightarrow$  'a err
type-synonym 'a esl = 'a set  $\times$  'a ord  $\times$  'a ebinop

primrec ok-val :: 'a err  $\Rightarrow$  'a
where
  ok-val (OK x) = x

definition lift :: ('a  $\Rightarrow$  'b err)  $\Rightarrow$  ('a err  $\Rightarrow$  'b err)
where
  lift f e = (case e of Err  $\Rightarrow$  Err | OK x  $\Rightarrow$  f x)

definition lift2 :: ('a  $\Rightarrow$  'b  $\Rightarrow$  'c err)  $\Rightarrow$  'a err  $\Rightarrow$  'b err  $\Rightarrow$  'c err
where
  lift2 f e1 e2 =
    (case e1 of Err  $\Rightarrow$  Err | OK x  $\Rightarrow$  (case e2 of Err  $\Rightarrow$  Err | OK y  $\Rightarrow$  f x y))

definition le :: 'a ord  $\Rightarrow$  'a err ord
where
  le r e1 e2 =
    (case e2 of Err  $\Rightarrow$  True | OK y  $\Rightarrow$  (case e1 of Err  $\Rightarrow$  False | OK x  $\Rightarrow$  x  $\sqsubseteq_r$  y))

definition sup :: ('a  $\Rightarrow$  'b  $\Rightarrow$  'c)  $\Rightarrow$  ('a err  $\Rightarrow$  'b err  $\Rightarrow$  'c err)
where
  sup f = lift2 ( $\lambda$ x y. OK (x  $\sqcup_f$  y))

definition err :: 'a set  $\Rightarrow$  'a err set
where
  err A = insert Err {OK x | x. x  $\in$  A}

definition esl :: 'a sl  $\Rightarrow$  'a esl
where
  esl = ( $\lambda$ (A,r,f). (A, r,  $\lambda$ x y. OK(f x y)))

definition sl :: 'a esl  $\Rightarrow$  'a err sl
where
  sl = ( $\lambda$ (A,r,f). (err A, le r, lift2 f))

abbreviation
  err-semilat :: 'a esl  $\Rightarrow$  bool where
  err-semilat L == semilat(sl L)

primrec strict :: ('a  $\Rightarrow$  'b err)  $\Rightarrow$  ('a err  $\Rightarrow$  'b err)
where
  strict f Err = Err
  | strict f (OK x) = f x

```

lemma *err-def'*:

$err\ A = insert\ Err\ \{x.\ \exists y \in A.\ x = OK\ y\}\langle proof \rangle$

lemma *strict-Some* [simp]:

$(strict\ f\ x = OK\ y) = (\exists z.\ x = OK\ z \wedge f\ z = OK\ y)\langle proof \rangle$

lemma *not-Err-eq*: $(x \neq Err) = (\exists a.\ x = OK\ a)\langle proof \rangle$

lemma *not-OK-eq*: $(\forall y.\ x \neq OK\ y) = (x = Err)\langle proof \rangle$

lemma *unfold-lesub-err*: $e1 \sqsubseteq_{le\ r} e2 = le\ r\ e1\ e2\langle proof \rangle$

lemma *le-err-reft*: $\forall x.\ x \sqsubseteq_r x \implies e \sqsubseteq_{le\ r} e\langle proof \rangle$

lemma *le-err-trans* [rule-format]:

$order\ r \implies e1 \sqsubseteq_{le\ r} e2 \longrightarrow e2 \sqsubseteq_{le\ r} e3 \longrightarrow e1 \sqsubseteq_{le\ r} e3\langle proof \rangle$

lemma *le-err-antisym* [rule-format]:

$order\ r \implies e1 \sqsubseteq_{le\ r} e2 \longrightarrow e2 \sqsubseteq_{le\ r} e1 \longrightarrow e1 = e2\langle proof \rangle$

lemma *OK-le-err-OK*: $(OK\ x \sqsubseteq_{le\ r} OK\ y) = (x \sqsubseteq_r y)\langle proof \rangle$

lemma *order-le-err* [iff]: $order\ (le\ r) = order\ r\langle proof \rangle$

lemma *le-Err* [iff]: $e \sqsubseteq_{le\ r} Err\langle proof \rangle$

lemma *Err-le-conv* [iff]: $Err \sqsubseteq_{le\ r} e = (e = Err)\langle proof \rangle$

lemma *le-OK-conv* [iff]: $e \sqsubseteq_{le\ r} OK\ x = (\exists y.\ e = OK\ y \wedge y \sqsubseteq_r x)\langle proof \rangle$

lemma *OK-le-conv*: $OK\ x \sqsubseteq_{le\ r} e = (e = Err \vee (\exists y.\ e = OK\ y \wedge x \sqsubseteq_r y))\langle proof \rangle$

lemma *top-Err* [iff]: $top\ (le\ r)\ Err\langle proof \rangle$

lemma *OK-less-conv* [rule-format, iff]:

$OK\ x \sqsubseteq_{le\ r} e = (e = Err \vee (\exists y.\ e = OK\ y \wedge x \sqsubseteq_r y))\langle proof \rangle$

lemma *not-Err-less* [rule-format, iff]: $\neg (Err \sqsubseteq_{le\ r} x)\langle proof \rangle$

lemma *semilat-errI* [intro]: **assumes** *Semilat* $A\ r\ f$

shows *semilat*(*err* A , *le* r , *lift2*($\lambda x\ y.\ OK(f\ x\ y)$)) $\langle proof \rangle$

lemma *err-semilat-eslI-aux*:

assumes *Semilat* $A\ r\ f$ **shows** *err-semilat*(*esl*(A, r, f)) $\langle proof \rangle$

lemma *err-semilat-eslI* [intro, simp]:

$semilat\ L \implies err\ semilat\ (esl\ L)\langle proof \rangle$

lemma *acc-err* [simp, intro!]: $acc\ A\ r \implies acc\ (err\ A)\ (le\ r)\langle proof \rangle$

lemma *Err-in-err* [iff]: $Err : err\ A\langle proof \rangle$

lemma *Ok-in-err* [iff]: $(OK\ x \in err\ A) = (x \in A)\langle proof \rangle$

2.2.1 lift

lemma *lift-in-errI*:

$\llbracket e \in err\ S; \forall x \in S.\ e = OK\ x \longrightarrow f\ x \in err\ S \rrbracket \implies lift\ f\ e \in err\ S\langle proof \rangle$

lemma *Err-lift2* [simp]: $Err \sqcup_{lift2}\ f\ x = Err\langle proof \rangle$

lemma *lift2-Err* [simp]: $x \sqcup_{lift2}\ f\ Err = Err\langle proof \rangle$

lemma *OK-lift2-OK* [simp]: $OK\ x \sqcup_{lift2}\ f\ OK\ y = x \sqcup_f\ y\langle proof \rangle$

2.2.2 sup

lemma *Err-sup-Err* [simp]: $Err \sqcup_{sup}\ f\ x = Err\langle proof \rangle$

lemma *Err-sup-Err2* [simp]: $x \sqcup_{sup}\ f\ Err = Err\langle proof \rangle$

lemma *Err-sup-OK* [simp]: $OK\ x \sqcup_{sup}\ f\ OK\ y = OK\ (x \sqcup_f\ y)\langle proof \rangle$

lemma *Err-sup-eq-OK-conv* [iff]:

$(sup\ f\ ex\ ey = OK\ z) = (\exists x\ y.\ ex = OK\ x \wedge ey = OK\ y \wedge f\ x\ y = z)\langle proof \rangle$

lemma *Err-sup-eq-Err* [iff]: $(sup\ f\ ex\ ey = Err) = (ex = Err \vee ey = Err)\langle proof \rangle$

2.2.3 semilat (err A) (le r) f

lemma *semilat-le-err-Err-plus* [simp]:

$\llbracket x \in err\ A; semilat(err\ A, le\ r, f) \rrbracket \implies Err \sqcup_f\ x = Err\langle proof \rangle$

lemma *semilat-le-err-plus-Err* [simp]:

$\llbracket x \in \text{err } A; \text{semilat}(\text{err } A, \text{le } r, f) \rrbracket \implies x \sqcup_f \text{Err} = \text{Err}\langle \text{proof} \rangle$
lemma *semilat-le-err-OK1*:
 $\llbracket x \in A; y \in A; \text{semilat}(\text{err } A, \text{le } r, f); \text{OK } x \sqcup_f \text{OK } y = \text{OK } z \rrbracket$
 $\implies x \sqsubseteq_r z \langle \text{proof} \rangle$
lemma *semilat-le-err-OK2*:
 $\llbracket x \in A; y \in A; \text{semilat}(\text{err } A, \text{le } r, f); \text{OK } x \sqcup_f \text{OK } y = \text{OK } z \rrbracket$
 $\implies y \sqsubseteq_r z \langle \text{proof} \rangle$
lemma *eq-order-le*:
 $\llbracket x = y; \text{order } r \rrbracket \implies x \sqsubseteq_r y \langle \text{proof} \rangle$
lemma *OK-plus-OK-eq-Err-conv* [*simp*]:
assumes $x \in A \quad y \in A \quad \text{semilat}(\text{err } A, \text{le } r, fe)$
shows $(\text{OK } x \sqcup_{fe} \text{OK } y = \text{Err}) = (\neg(\exists z \in A. x \sqsubseteq_r z \wedge y \sqsubseteq_r z)) \langle \text{proof} \rangle$

2.2.4 semilat (err(Union AS))

lemma *all-bex-swap-lemma* [*iff*]:
 $(\forall x. (\exists y \in A. x = f y) \longrightarrow P x) = (\forall y \in A. P(f y)) \langle \text{proof} \rangle$
lemma *closed-err-Union-lift2I*:
 $\llbracket \forall A \in AS. \text{closed}(\text{err } A) (\text{lift2 } f); AS \neq \{\};$
 $\forall A \in AS. \forall B \in AS. A \neq B \longrightarrow (\forall a \in A. \forall b \in B. a \sqcup_f b = \text{Err}) \rrbracket$
 $\implies \text{closed}(\text{err}(\text{Union } AS)) (\text{lift2 } f) \langle \text{proof} \rangle$

If $AS = \{\}$ the thm collapses to $\text{order } r \wedge \text{closed } \{\text{Err}\} f \wedge \text{Err} \sqcup_f \text{Err} = \text{Err}$ which may not hold

lemma *err-semilat-UnionI*:
 $\llbracket \forall A \in AS. \text{err-semilat}(A, r, f); AS \neq \{\};$
 $\forall A \in AS. \forall B \in AS. A \neq B \longrightarrow (\forall a \in A. \forall b \in B. \neg a \sqsubseteq_r b \wedge a \sqcup_f b = \text{Err}) \rrbracket$
 $\implies \text{err-semilat}(\text{Union } AS, r, f) \langle \text{proof} \rangle$
end

2.3 More about Options

theory *Opt*
imports
Err
begin

definition *le* :: $'a \text{ ord} \Rightarrow 'a \text{ option ord}$
where
 $le \ r \ o_1 \ o_2 =$
 $(\text{case } o_2 \text{ of } \text{None} \Rightarrow o_1 = \text{None} \mid \text{Some } y \Rightarrow (\text{case } o_1 \text{ of } \text{None} \Rightarrow \text{True} \mid \text{Some } x \Rightarrow x \sqsubseteq_r y))$

definition *opt* :: $'a \text{ set} \Rightarrow 'a \text{ option set}$
where
 $opt \ A = \text{insert } \text{None} \ \{\text{Some } y \mid y. y \in A\}$

definition *sup* :: $'a \text{ ebinop} \Rightarrow 'a \text{ option ebinop}$
where
 $sup \ f \ o_1 \ o_2 =$
 $(\text{case } o_1 \text{ of } \text{None} \Rightarrow \text{OK } o_2$
 $\mid \text{Some } x \Rightarrow (\text{case } o_2 \text{ of } \text{None} \Rightarrow \text{OK } o_1$
 $\mid \text{Some } y \Rightarrow (\text{case } f \ x \ y \text{ of } \text{Err} \Rightarrow \text{Err} \mid \text{OK } z \Rightarrow \text{OK } (\text{Some } z))))$

definition *esl* :: $'a \text{ esl} \Rightarrow 'a \text{ option esl}$

where

$$esl = (\lambda(A,r,f). (opt\ A, le\ r, sup\ f))$$

lemma *unfold-le-opt*:

$$o_1 \sqsubseteq_{le\ r} o_2 = \\ (case\ o_2\ of\ None \Rightarrow o_1 = None \mid \\ \quad Some\ y \Rightarrow (case\ o_1\ of\ None \Rightarrow True \mid Some\ x \Rightarrow x \sqsubseteq_r y)) \langle proof \rangle$$

lemma *le-opt-refl*: $order\ r \Longrightarrow x \sqsubseteq_{le\ r} x \langle proof \rangle \langle proof \rangle \langle proof \rangle \langle proof \rangle \langle proof \rangle \langle proof \rangle \langle proof \rangle \langle proof \rangle \langle proof \rangle \langle proof \rangle \langle proof \rangle$

2.4 Products as Semilattices

theory *Product*

imports *Err*

begin

definition *le* :: 'a ord \Rightarrow 'b ord \Rightarrow ('a \times 'b) ord

where

$$le\ r_A\ r_B = (\lambda(a_1,b_1)\ (a_2,b_2). a_1 \sqsubseteq_{r_A} a_2 \wedge b_1 \sqsubseteq_{r_B} b_2)$$

definition *sup* :: 'a ebinop \Rightarrow 'b ebinop \Rightarrow ('a \times 'b) ebinop

where

$$sup\ f\ g = (\lambda(a_1,b_1)(a_2,b_2). Err.sup\ Pair\ (a_1 \sqcup_f a_2)\ (b_1 \sqcup_g b_2))$$

definition *esl* :: 'a esl \Rightarrow 'b esl \Rightarrow ('a \times 'b) esl

where

$$esl = (\lambda(A,r_A,f_A)\ (B,r_B,f_B). (A \times B, le\ r_A\ r_B, sup\ f_A\ f_B))$$

abbreviation

$$lesubprod :: 'a \times 'b \Rightarrow ('a \Rightarrow 'a \Rightarrow bool) \Rightarrow ('b \Rightarrow 'b \Rightarrow bool) \Rightarrow 'a \times 'b \Rightarrow bool \\ (\langle (- / \sqsubseteq'(-,-) -) \rangle [50, 0, 0, 51] 50) \textbf{ where} \\ p \sqsubseteq (r_A, r_B)\ q == p \sqsubseteq_{Product.le\ r_A\ r_B}\ q$$

lemma *unfold-lesub-prod*: $x \sqsubseteq (r_A, r_B)\ y = le\ r_A\ r_B\ x\ y \langle proof \rangle$

lemma *le-prod-Pair-conv* [iff]: $((a_1, b_1) \sqsubseteq (r_A, r_B)\ (a_2, b_2)) = (a_1 \sqsubseteq_{r_A} a_2 \ \&\ b_1 \sqsubseteq_{r_B} b_2) \langle proof \rangle$

lemma *less-prod-Pair-conv*:

$$((a_1, b_1) \sqsubset_{Product.le\ r_A\ r_B}\ (a_2, b_2)) = \\ (a_1 \sqsubset_{r_A} a_2 \ \&\ b_1 \sqsubset_{r_B} b_2 \mid a_1 \sqsubseteq_{r_A} a_2 \ \&\ b_1 \sqsubset_{r_B} b_2) \langle proof \rangle$$

lemma *order-le-prod* [iff]: $order(Product.le\ r_A\ r_B) = (order\ r_A \ \&\ order\ r_B) \langle proof \rangle$

lemma *acc-le-prodI* [intro!]:

$$\llbracket acc\ A\ r_A; acc\ B\ r_B \rrbracket \Longrightarrow acc\ (A \times B)\ (Product.le\ r_A\ r_B) \langle proof \rangle$$

lemma *closed-lift2-sup*:

$$\llbracket closed\ (err\ A)\ (lift2\ f); closed\ (err\ B)\ (lift2\ g) \rrbracket \Longrightarrow \\ closed\ (err\ (A \times B))\ (lift2\ (sup\ f\ g)) \langle proof \rangle$$

lemma *unfold-plussub-lift2*: $e_1 \sqcup_{lift2\ f} e_2 = lift2\ f\ e_1\ e_2 \langle proof \rangle$

lemma *plus-eq-Err-conv* [simp]:

assumes $x \in A\ y \in A\ semilat(err\ A, Err.le\ r, lift2\ f)$

shows $(x \sqcup_f y = Err) = (\neg(\exists z \in A. x \sqsubseteq_r z \wedge y \sqsubseteq_r z)) \langle proof \rangle$

lemma *err-semilat-Product-esl*:

$$\bigwedge L_1\ L_2. \llbracket err-semilat\ L_1; err-semilat\ L_2 \rrbracket \Longrightarrow err-semilat(Product.esl\ L_1\ L_2) \langle proof \rangle$$

end

2.5 Fixed Length Lists

theory *Listn*

imports *Err*

begin

definition *list* :: *nat* \Rightarrow *'a set* \Rightarrow *'a list set*

where

$$\text{list } n \ A = \{xs. \text{size } xs = n \wedge \text{set } xs \subseteq A\}$$

definition *le* :: *'a ord* \Rightarrow (*'a list*)*ord*

where

$$\text{le } r = \text{list-all2 } (\lambda x \ y. x \sqsubseteq_r \ y)$$

abbreviation

lesublist :: *'a list* \Rightarrow *'a ord* \Rightarrow *'a list* \Rightarrow *bool* ($\langle(- /[\sqsubseteq_r] -)\rangle$ [50, 0, 51] 50) **where**

$$x \ [\sqsubseteq_r] \ y == x <=-(\text{Listn.le } r) \ y$$

abbreviation

lessublist :: *'a list* \Rightarrow *'a ord* \Rightarrow *'a list* \Rightarrow *bool* ($\langle(- /[\sqsubseteq_r] -)\rangle$ [50, 0, 51] 50) **where**

$$x \ [\sqsubseteq_r] \ y == x <-(\text{Listn.le } r) \ y$$

abbreviation

plussublist :: *'a list* \Rightarrow (*'a* \Rightarrow *'b* \Rightarrow *'c*) \Rightarrow *'b list* \Rightarrow *'c list*

($\langle(- /[\sqcup_f] -)\rangle$ [65, 0, 66] 65) **where**

$$x \ [\sqcup_f] \ y == x \sqcup_{\text{map2}} f \ y$$

primrec *coalesce* :: *'a err list* \Rightarrow *'a list err*

where

$$\text{coalesce } [] = \text{OK} []$$

$$| \text{coalesce } (ex\#\text{exs}) = \text{Err.sup } (\#) \ ex \ (\text{coalesce } \text{exs})$$

definition *sl* :: *nat* \Rightarrow *'a sl* \Rightarrow *'a list sl*

where

$$\text{sl } n = (\lambda(A,r,f). (\text{list } n \ A, \ \text{le } r, \ \text{map2 } f))$$

definition *sup* :: (*'a* \Rightarrow *'b* \Rightarrow *'c err*) \Rightarrow *'a list* \Rightarrow *'b list* \Rightarrow *'c list err*

where

$$\text{sup } f = (\lambda xs \ ys. \text{if } \text{size } xs = \text{size } ys \ \text{then } \text{coalesce}(xs \ [\sqcup_f] \ ys) \ \text{else } \text{Err})$$

definition *upto-esl* :: *nat* \Rightarrow *'a esl* \Rightarrow *'a list esl*

where

$$\text{upto-esl } m = (\lambda(A,r,f). (\text{Union}\{\text{list } n \ A \mid n. n \leq m\}, \ \text{le } r, \ \text{sup } f))$$

lemmas [*simp*] = *set-update-subsetI*

lemma *unfold-lesub-list*: *xs* $[\sqsubseteq_r]$ *ys* = *Listn.le r xs ys* \langle *proof* \rangle

lemma *Nil-le-conv* [*iff*]: ($[] \ [\sqsubseteq_r] \ ys$) = (*ys* = $[]$) \langle *proof* \rangle

lemma *Cons-notle-Nil* [*iff*]: $\neg x\#\text{xs} \ [\sqsubseteq_r] \ []$ \langle *proof* \rangle

lemma *Cons-le-Cons* [iff]: $x\#xs \sqsubseteq_r y\#ys = (x \sqsubseteq_r y \wedge xs \sqsubseteq_r ys)\langle proof \rangle$

lemma *Cons-less-Conss* [simp]:

$order\ r \implies x\#xs \sqsubseteq_r y\#ys = (x \sqsubseteq_r y \wedge xs \sqsubseteq_r ys \vee x = y \wedge xs \sqsubseteq_r ys)\langle proof \rangle$

lemma *list-update-le-cong*:

$\llbracket i < size\ xs; xs \sqsubseteq_r ys; x \sqsubseteq_r y \rrbracket \implies xs[i:=x] \sqsubseteq_r ys[i:=y]\langle proof \rangle$

lemma *le-listD*: $\llbracket xs \sqsubseteq_r ys; p < size\ xs \rrbracket \implies xs!p \sqsubseteq_r ys!p\langle proof \rangle$

lemma *le-list-refl*: $\forall x. x \sqsubseteq_r x \implies xs \sqsubseteq_r xs\langle proof \rangle$

lemma *le-list-trans*: $\llbracket order\ r; xs \sqsubseteq_r ys; ys \sqsubseteq_r zs \rrbracket \implies xs \sqsubseteq_r zs\langle proof \rangle$

lemma *le-list-antisym*: $\llbracket order\ r; xs \sqsubseteq_r ys; ys \sqsubseteq_r xs \rrbracket \implies xs = ys\langle proof \rangle$

lemma *order-listI* [simp, intro!]: $order\ r \implies order(Listn.le\ r)\langle proof \rangle$

lemma *lesub-list-impl-same-size* [simp]: $xs \sqsubseteq_r ys \implies size\ ys = size\ xs\langle proof \rangle$

lemma *lesssub-lengthD*: $xs \sqsubseteq_r ys \implies size\ ys = size\ xs\langle proof \rangle$

lemma *le-list-appendI*: $a \sqsubseteq_r b \implies c \sqsubseteq_r d \implies a@c \sqsubseteq_r b@d\langle proof \rangle$

lemma *le-listI*:

assumes $length\ a = length\ b$

assumes $\bigwedge n. n < length\ a \implies a!n \sqsubseteq_r b!n$

shows $a \sqsubseteq_r b\langle proof \rangle$

lemma *listI*: $\llbracket size\ xs = n; set\ xs \subseteq A \rrbracket \implies xs \in list\ n\ A\langle proof \rangle$

lemma *listE-length* [simp]: $xs \in list\ n\ A \implies size\ xs = n\langle proof \rangle$

lemma *less-lengthI*: $\llbracket xs \in list\ n\ A; p < n \rrbracket \implies p < size\ xs\langle proof \rangle$

lemma *listE-set* [simp]: $xs \in list\ n\ A \implies set\ xs \subseteq A\langle proof \rangle$

lemma *list-0* [simp]: $list\ 0\ A = \{\}\langle proof \rangle$

lemma *in-list-Suc-iff*:

$(xs \in list\ (Suc\ n)\ A) = (\exists y \in A. \exists ys \in list\ n\ A. xs = y\#ys)\langle proof \rangle$

lemma *Cons-in-list-Suc* [iff]:

$(x\#xs \in list\ (Suc\ n)\ A) = (x \in A \wedge xs \in list\ n\ A)\langle proof \rangle$

lemma *list-not-empty*:

$\exists a. a \in A \implies \exists xs. xs \in list\ n\ A\langle proof \rangle$

lemma *nth-in* [rule-format, simp]:

$\forall i\ n. size\ xs = n \longrightarrow set\ xs \subseteq A \longrightarrow i < n \longrightarrow (xs!i) \in A\langle proof \rangle$

lemma *listE-nth-in*: $\llbracket xs \in list\ n\ A; i < n \rrbracket \implies xs!i \in A\langle proof \rangle$

lemma *listn-Cons-Suc* [elim!]:

$l\#xs \in list\ n\ A \implies (\bigwedge n'. n = Suc\ n' \implies l \in A \implies xs \in list\ n'\ A \implies P) \implies P\langle proof \rangle$

lemma *listn-appendE* [elim!]:

$a@b \in list\ n\ A \implies (\bigwedge n1\ n2. n = n1 + n2 \implies a \in list\ n1\ A \implies b \in list\ n2\ A \implies P) \implies P\langle proof \rangle$

lemma *listt-update-in-list* [simp, intro!]:

$\llbracket xs \in list\ n\ A; x \in A \rrbracket \implies xs[i := x] \in list\ n\ A\langle proof \rangle$

lemma *list-appendI* [intro?]:

$\llbracket a \in list\ n\ A; b \in list\ m\ A \rrbracket \implies a @ b \in list\ (n+m)\ A\langle proof \rangle$

lemma *list-map* [simp]: $(map\ f\ xs \in list\ (size\ xs)\ A) = (f\ ' set\ xs \subseteq A)\langle proof \rangle$

lemma *list-replicateI* [intro]: $x \in A \implies replicate\ n\ x \in list\ n\ A\langle proof \rangle$

lemma *plus-list-Nil* [simp]: $\llbracket \sqcup_f\ xs = \llbracket \langle proof \rangle$

lemma *plus-list-Cons* [simp]:

$(x\#xs) \sqcup_f\ ys = (case\ ys\ of\ \llbracket \Rightarrow \llbracket \mid y\#ys \Rightarrow (x \sqcup_f\ y)\#(xs \sqcup_f\ ys)\rangle\langle proof \rangle$

lemma *length-plus-list* [rule-format, simp]:

$\forall ys. size(xs \sqcup_f\ ys) = min(size\ xs)\ (size\ ys)\langle proof \rangle$

lemma *nth-plus-list* [rule-format, simp]:

$\forall xs\ ys\ i. size\ xs = n \longrightarrow size\ ys = n \longrightarrow i < n \longrightarrow (xs \sqcup_f\ ys)!i = (xs!i) \sqcup_f\ (ys!i)\langle proof \rangle$

lemma (in *Semilat*) *plus-list-ub1* [rule-format]:

```

[[ set xs ⊆ A; set ys ⊆ A; size xs = size ys ]]
  ⇒ xs [⊆r] xs [⊔f] ys⟨proof⟩
lemma (in Semilat) plus-list-ub2:
[[ set xs ⊆ A; set ys ⊆ A; size xs = size ys ]] ⇒ ys [⊆r] xs [⊔f] ys⟨proof⟩
lemma (in Semilat) plus-list-lub [rule-format]:
shows ∀ xs ys zs. set xs ⊆ A → set ys ⊆ A → set zs ⊆ A
  → size xs = n ∧ size ys = n →
  xs [⊆r] zs ∧ ys [⊆r] zs → xs [⊔f] ys [⊆r] zs⟨proof⟩
lemma (in Semilat) list-update-incr [rule-format]:
x ∈ A ⇒ set xs ⊆ A →
  (∀ i. i < size xs → xs [⊆r] xs[i := x ⊔f xs!i])⟨proof⟩
lemma acc-le-listI' [intro!]:
[[ order r; acc A r ]] ⇒ acc (⋃ n. list n A) (Listn.le r)⟨proof⟩⟨proof⟩⟨proof⟩⟨proof⟩⟨proof⟩⟨proof⟩⟨proof⟩⟨proof⟩⟨proof⟩⟨proof⟩

```

2.6 Typing and Dataflow Analysis Framework

theory *Typing-Framework*

imports

Semilattices

begin

The relationship between dataflow analysis and a welltyped-instruction predicate.

type-synonym

's step-type = nat ⇒ 's ⇒ (nat × 's) list

definition *stable* :: 's ord ⇒ 's step-type ⇒ 's list ⇒ nat ⇒ bool

where

stable r step τs p ⇔ (∀ (q,τ) ∈ set (step p (τs!p)). τ [⊆_r] τs!q)

definition *stables* :: 's ord ⇒ 's step-type ⇒ 's list ⇒ bool

where

stables r step τs ⇔ (∀ p < size τs. *stable* r step τs p)

definition *wt-step* :: 's ord ⇒ 's ⇒ 's step-type ⇒ 's list ⇒ bool

where

wt-step r T step τs ⇔ (∀ p < size τs. τs!p ≠ T ∧ *stable* r step τs p)

definition *is-bcv* :: 's ord ⇒ 's ⇒ 's step-type ⇒ nat ⇒ 's set ⇒ ('s list ⇒ 's list) ⇒ bool

where

is-bcv r T step n A *bcv* ⇔ (∀ τs₀ ∈ list n A.

(∀ p < n. (bcv τs₀)!p ≠ T) = (∃ τs ∈ list n A. τs₀ [⊆_r] τs ∧ *wt-step* r T step τs))

end

2.7 More on Semilattices

theory *SemilatAlg*

imports *Typing-Framework*

begin

definition *lesubstep-type* :: (nat × 's) set ⇒ 's ord ⇒ (nat × 's) set ⇒ bool

(⟨(- /{⊆} -)⟩ [50, 0, 51] 50)

where A {⊆_r} B ≡ ∀ (p,τ) ∈ A. ∃ τ'. (p,τ') ∈ B ∧ τ [⊆_r] τ'

notation (*ASCII*)

lesubstep-type $\langle (- / \{ \leq' - \} -) \rangle$ [50, 0, 51] 50)

primrec *pluslussub* :: 'a list \Rightarrow ('a \Rightarrow 'a \Rightarrow 'a) \Rightarrow 'a \Rightarrow 'a $\langle (- / \sqcup -) \rangle$ [65, 0, 66] 65)

where

pluslussub [] $f y = y$

| *pluslussub* (x#xs) $f y = pluslussub xs f (x \sqcup_f y)$

definition *bounded* :: 's step-type \Rightarrow nat \Rightarrow bool

where

bounded step n $\longleftrightarrow (\forall p < n. \forall \tau. \forall (q, \tau') \in set (step p \tau). q < n)$

definition *pres-type* :: 's step-type \Rightarrow nat \Rightarrow 's set \Rightarrow bool

where

pres-type step n A $\longleftrightarrow (\forall \tau \in A. \forall p < n. \forall (q, \tau') \in set (step p \tau). \tau' \in A)$

definition *mono* :: 's ord \Rightarrow 's step-type \Rightarrow nat \Rightarrow 's set \Rightarrow bool

where

mono r step n A \longleftrightarrow

$(\forall \tau p \tau'. \tau \in A \wedge p < n \wedge \tau \sqsubseteq_r \tau' \longrightarrow set (step p \tau) \{\sqsubseteq_r\} set (step p \tau'))$

lemma [*iff*]: {} $\{\sqsubseteq_r\} B$

$\langle proof \rangle$

lemma [*iff*]: (A $\{\sqsubseteq_r\}$ {}) = (A = {})

$\langle proof \rangle$

lemma *lesubstep-union*:

$\llbracket A_1 \{\sqsubseteq_r\} B_1; A_2 \{\sqsubseteq_r\} B_2 \rrbracket \Longrightarrow A_1 \cup A_2 \{\sqsubseteq_r\} B_1 \cup B_2$

$\langle proof \rangle$

lemma *pres-typeD*:

$\llbracket pres-type step n A; s \in A; p < n; (q, s') \in set (step p s) \rrbracket \Longrightarrow s' \in A \langle proof \rangle$

lemma *monoD*:

$\llbracket mono r step n A; p < n; s \in A; s \sqsubseteq_r t \rrbracket \Longrightarrow set (step p s) \{\sqsubseteq_r\} set (step p t) \langle proof \rangle$

lemma *boundedD*:

$\llbracket bounded step n; p < n; (q, t) \in set (step p xs) \rrbracket \Longrightarrow q < n \langle proof \rangle$

lemma *lesubstep-type-refl* [*simp, intro*]:

$(\wedge x. x \sqsubseteq_r x) \Longrightarrow A \{\sqsubseteq_r\} A \langle proof \rangle$

lemma *lesub-step-typeD*:

$A \{\sqsubseteq_r\} B \Longrightarrow (x, y) \in A \Longrightarrow \exists y'. (x, y') \in B \wedge y \sqsubseteq_r y' \langle proof \rangle$

lemma *list-update-le-listI* [*rule-format*]:

$set xs \subseteq A \longrightarrow set ys \subseteq A \longrightarrow xs \{\sqsubseteq_r\} ys \longrightarrow p < size xs \longrightarrow$

$x \sqsubseteq_r ys!p \longrightarrow semilat(A, r, f) \longrightarrow x \in A \longrightarrow$

$xs[p := x \sqcup_f xs!p] \{\sqsubseteq_r\} ys \langle proof \rangle$

lemma *plusplus-closed*: **assumes** *Semilat A r f* **shows**

$\wedge y. \llbracket set x \subseteq A; y \in A \rrbracket \Longrightarrow x \sqcup_f y \in A \langle proof \rangle$

lemma (**in** *Semilat*) *pp-ub2*:

$\wedge y. \llbracket set x \subseteq A; y \in A \rrbracket \Longrightarrow y \sqsubseteq_r x \sqcup_f y \langle proof \rangle$

lemma (**in** *Semilat*) *pp-ub1*:

shows $\wedge y. \llbracket set ls \subseteq A; y \in A; x \in set ls \rrbracket \Longrightarrow x \sqsubseteq_r ls \sqcup_f y \langle proof \rangle$

lemma (**in** *Semilat*) *pp-lub*:

assumes $z: z \in A$

shows

$$\bigwedge y. y \in A \implies \text{set } xs \subseteq A \implies \forall x \in \text{set } xs. x \sqsubseteq_r z \implies y \sqsubseteq_r z \implies xs \sqcup_f y \sqsubseteq_r z \langle \text{proof} \rangle$$

lemma ub1': **assumes** *Semilat A r f*
shows $\llbracket \forall (p,s) \in \text{set } S. s \in A; y \in A; (a,b) \in \text{set } S \rrbracket$
 $\implies b \sqsubseteq_r \text{map snd } \llbracket (p', t') \leftarrow S. p' = a \rrbracket \sqcup_f y \langle \text{proof} \rangle$

lemma plusplus-empty:
 $\forall s'. (q, s') \in \text{set } S \longrightarrow s' \sqcup_f ss ! q = ss ! q \implies$
 $(\text{map snd } \llbracket (p', t') \leftarrow S. p' = q \rrbracket \sqcup_f ss ! q) = ss ! q \langle \text{proof} \rangle$

end

2.8 Lifting the Typing Framework to err, app, and eff

theory *Typing-Framework-err*

imports

Typing-Framework

SemilatAlg

begin

definition *wt-err-step* :: $'s \text{ ord} \Rightarrow 's \text{ err step-type} \Rightarrow 's \text{ err list} \Rightarrow \text{bool}$

where

$\text{wt-err-step } r \text{ step } \tau s \longleftrightarrow \text{wt-step } (\text{Err.le } r) \text{ Err step } \tau s$

definition *wt-app-eff* :: $'s \text{ ord} \Rightarrow (\text{nat} \Rightarrow 's \Rightarrow \text{bool}) \Rightarrow 's \text{ step-type} \Rightarrow 's \text{ list} \Rightarrow \text{bool}$

where

$\text{wt-app-eff } r \text{ app step } \tau s \longleftrightarrow$
 $(\forall p < \text{size } \tau s. \text{app } p (\tau s ! p) \wedge (\forall (q,\tau) \in \text{set } (\text{step } p (\tau s ! p)). \tau \leq\text{-}r \tau s ! q))$

definition *map-snd* :: $('b \Rightarrow 'c) \Rightarrow ('a \times 'b) \text{ list} \Rightarrow ('a \times 'c) \text{ list}$

where

$\text{map-snd } f = \text{map } (\lambda(x,y). (x, f y))$

definition *error* :: $\text{nat} \Rightarrow (\text{nat} \times 'a \text{ err}) \text{ list}$

where

$\text{error } n = \text{map } (\lambda x. (x, \text{Err})) [0..<n]$

definition *err-step* :: $\text{nat} \Rightarrow (\text{nat} \Rightarrow 's \Rightarrow \text{bool}) \Rightarrow 's \text{ step-type} \Rightarrow 's \text{ err step-type}$

where

$\text{err-step } n \text{ app step } p t =$
 $(\text{case } t \text{ of}$
 $\quad \text{Err} \Rightarrow \text{error } n$
 $\quad | \text{OK } \tau \Rightarrow \text{if app } p \tau \text{ then map-snd OK (step } p \tau) \text{ else error } n)$

definition *app-mono* :: $'s \text{ ord} \Rightarrow (\text{nat} \Rightarrow 's \Rightarrow \text{bool}) \Rightarrow \text{nat} \Rightarrow 's \text{ set} \Rightarrow \text{bool}$

where

$\text{app-mono } r \text{ app } n A \longleftrightarrow$
 $(\forall s p t. s \in A \wedge p < n \wedge s \sqsubseteq_r t \longrightarrow \text{app } p t \longrightarrow \text{app } p s)$

lemmas *err-step-defs = err-step-def map-snd-def error-def*

lemma *bounded-err-stepD*:

\llbracket *bounded* (*err-step* *n* *app step*) *n*;
 $p < n$; *app* *p* *a*; $(q, b) \in \text{set } (\text{step } p \ a)$ $\rrbracket \implies q < n \langle \text{proof} \rangle$

lemma *in-map-sndD*: $(a, b) \in \text{set } (\text{map-snd } f \ xs) \implies \exists b'. (a, b') \in \text{set } xs \langle \text{proof} \rangle$

lemma *bounded-err-stepI*:

$\forall p. p < n \longrightarrow (\forall s. \text{ap } p \ s \longrightarrow (\forall (q, s') \in \text{set } (\text{step } p \ s). q < n))$
 $\implies \text{bounded } (\text{err-step } n \ \text{ap } \text{step}) \ n \langle \text{proof} \rangle$

lemma *bounded-lift*:

bounded step n \implies *bounded* (*err-step* *n* *app step*) *n* $\langle \text{proof} \rangle$

lemma *le-list-map-OK* [*simp*]:

$\bigwedge b. (\text{map } OK \ a \ [\sqsubseteq_{Err.le \ r}] \ \text{map } OK \ b) = (a \ [\sqsubseteq_r] \ b) \langle \text{proof} \rangle$

lemma *map-snd-lessI*:

set xs $\{\sqsubseteq_r\}$ *set ys* $\implies \text{set } (\text{map-snd } OK \ xs) \ \{\sqsubseteq_{Err.le \ r}\} \ \text{set } (\text{map-snd } OK \ ys) \langle \text{proof} \rangle$

lemma *mono-lift*:

\llbracket *order* *r*; *app-mono* *r* *app* *n* *A*; *bounded* (*err-step* *n* *app step*) *n*;
 $\forall s \ p \ t. s \in A \wedge p < n \wedge s \sqsubseteq_r t \longrightarrow \text{app } p \ t \longrightarrow \text{set } (\text{step } p \ s) \ \{\sqsubseteq_r\} \ \text{set } (\text{step } p \ t)$ \rrbracket
 $\implies \text{mono } (Err.le \ r) \ (\text{err-step } n \ \text{app } \text{step}) \ n \ (\text{err } A) \langle \text{proof} \rangle$

lemma *in-errorD*: $(x, y) \in \text{set } (\text{error } n) \implies y = Err \langle \text{proof} \rangle$

lemma *pres-type-lift*:

$\forall s \in A. \forall p. p < n \longrightarrow \text{app } p \ s \longrightarrow (\forall (q, s') \in \text{set } (\text{step } p \ s). s' \in A)$
 $\implies \text{pres-type } (\text{err-step } n \ \text{app } \text{step}) \ n \ (\text{err } A) \langle \text{proof} \rangle$

lemma *wt-err-imp-wt-app-eff*:

assumes *wt*: *wt-err-step* *r* (*err-step* (*size* *ts*) *app step*) *ts*
assumes *b*: *bounded* (*err-step* (*size* *ts*) *app step*) (*size* *ts*)
shows *wt-app-eff* *r* *app step* (*map ok-val* *ts*) $\langle \text{proof} \rangle$

lemma *wt-app-eff-imp-wt-err*:

assumes *app-eff*: *wt-app-eff* *r* *app step* *ts*
assumes *bounded*: *bounded* (*err-step* (*size* *ts*) *app step*) (*size* *ts*)
shows *wt-err-step* *r* (*err-step* (*size* *ts*) *app step*) (*map OK* *ts*) $\langle \text{proof} \rangle$

end

2.9 Kildall's Algorithm

theory *Kildall*

imports *SemilatAlg ../Basic/Auxiliary*

begin

locale *Kildall-base* =

fixes *s-α* :: $'w \Rightarrow \text{nat set}$
and *s-empty* :: $'w$
and *s-is-empty* :: $'w \Rightarrow \text{bool}$
and *s-choose* :: $'w \Rightarrow \text{nat}$
and *s-remove* :: $\text{nat} \Rightarrow 'w \Rightarrow 'w$
and *s-insert* :: $\text{nat} \Rightarrow 'w \Rightarrow 'w$

begin

primrec *propa* :: 's binop \Rightarrow (nat \times 's) list \Rightarrow 's list \Rightarrow 'w \Rightarrow 's list * 'w

where

propa f [] τs w = ($\tau s, w$)
| *propa* f (q'#qs) τs w = (let (q, τ) = q';
 $u = \tau \sqcup_f \tau s!q$;
 $w' = (\text{if } u = \tau s!q \text{ then } w \text{ else } s\text{-insert } q \text{ } w)$
in *propa* f qs ($\tau s[q := u]$) w')

definition *iter* :: 's binop \Rightarrow 's step-type \Rightarrow 's list \Rightarrow 'w \Rightarrow 's list \times 'w

where

iter f step τs w =
while ($\lambda(\tau s, w). \neg s\text{-is-empty } w$)
($\lambda(\tau s, w). \text{let } p = s\text{-choose } w \text{ in } \text{propa } f \text{ (step } p \text{ } (\tau s!p)) \tau s \text{ (s-remove } p \text{ } w)$)
($\tau s, w$)

definition *unstables* :: 's ord \Rightarrow 's step-type \Rightarrow 's list \Rightarrow 'w

where

unstables r step $\tau s = \text{foldr } s\text{-insert (filter } (\lambda p. \neg \text{stable } r \text{ step } \tau s \text{ } p) [0..<\text{size } \tau s]) \text{ } s\text{-empty}$

definition *kildall* :: 's ord \Rightarrow 's binop \Rightarrow 's step-type \Rightarrow 's list \Rightarrow 's list

where *kildall* r f step $\tau s \equiv \text{fst}(\text{iter } f \text{ step } \tau s \text{ (unstables } r \text{ step } \tau s))$

primrec *t- α* :: 's list \times 'w \Rightarrow 's list \times nat set

where *t- α* ($\tau s, w$) = ($\tau s, s\text{-}\alpha \text{ } w$)

end

primrec *merges* :: 's binop \Rightarrow (nat \times 's) list \Rightarrow 's list \Rightarrow 's list

where

merges f [] $\tau s = \tau s$
| *merges* f (p'#ps) $\tau s = (\text{let } (p, \tau) = p' \text{ in } \text{merges } f \text{ } ps \text{ } (\tau s[p := \tau \sqcup_f \tau s!p]))$

locale *Kildall* =

Kildall-base +

assumes *empty-spec* [*simp*]: $s\text{-}\alpha \text{ } s\text{-empty} = \{\}$
and *is-empty-spec* [*simp*]: $s\text{-is-empty } A \longleftrightarrow s\text{-}\alpha \text{ } A = \{\}$
and *choose-spec*: $s\text{-}\alpha \text{ } A \neq \{\} \Longrightarrow s\text{-choose } A \in s\text{-}\alpha \text{ } A$
and *remove-spec* [*simp*]: $s\text{-}\alpha \text{ (s-remove } n \text{ } A) = s\text{-}\alpha \text{ } A - \{n\}$
and *insert-spec* [*simp*]: $s\text{-}\alpha \text{ (s-insert } n \text{ } A) = \text{insert } n \text{ (s-}\alpha \text{ } A)$

begin

lemma *s- α -foldr-s-insert*:

$s\text{-}\alpha \text{ (foldr } s\text{-insert } xs \text{ } A) = \text{foldr } \text{insert } xs \text{ (s-}\alpha \text{ } A)$

$\langle \text{proof} \rangle$

lemma *unstables-spec* [*simp*]: $s\text{-}\alpha \text{ (unstables } r \text{ step } \tau s) = \{p. p < \text{size } \tau s \wedge \neg \text{stable } r \text{ step } \tau s \text{ } p\}$

$\langle \text{proof} \rangle$

end

lemmas [*simp*] = *Let-def Semilat.le-iff-plus-unchanged* [*OF Semilat.intro, symmetric*]

lemma (in *Semilat*) *nth-merges*:

$$\begin{aligned} & \bigwedge ss. \llbracket p < \text{length } ss; ss \in \text{list } n \ A; \forall (p,t) \in \text{set } ps. p < n \wedge t \in A \rrbracket \implies \\ & (\text{merges } f \ ps \ ss)!p = \text{map } \text{snd} \ [(p',t') \leftarrow ps. p'=p] \sqcup_f \ ss!p \\ & (\text{is } \bigwedge ss. \llbracket -; -; ?\text{steptype } ps \rrbracket \implies ?P \ ss \ ps) \langle \text{proof} \rangle \end{aligned}$$

lemma *length-merges* [*simp*]:

$$\bigwedge ss. \text{size}(\text{merges } f \ ps \ ss) = \text{size } ss \langle \text{proof} \rangle$$

lemma (in *Semilat*) *merges-preserves-type-lemma*:

shows $\forall xs. xs \in \text{list } n \ A \longrightarrow (\forall (p,x) \in \text{set } ps. p < n \wedge x \in A) \longrightarrow \text{merges } f \ ps \ xs \in \text{list } n \ A \langle \text{proof} \rangle$

lemma (in *Semilat*) *merges-preserves-type* [*simp*]:

$$\begin{aligned} & \llbracket xs \in \text{list } n \ A; \forall (p,x) \in \text{set } ps. p < n \wedge x \in A \rrbracket \\ & \implies \text{merges } f \ ps \ xs \in \text{list } n \ A \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma (in *Semilat*) *merges-incr-lemma*:

$$\forall xs. xs \in \text{list } n \ A \longrightarrow (\forall (p,x) \in \text{set } ps. p < \text{size } xs \wedge x \in A) \longrightarrow xs \sqsubseteq_r \text{merges } f \ ps \ xs \langle \text{proof} \rangle$$

lemma (in *Semilat*) *merges-incr*:

$$\begin{aligned} & \llbracket xs \in \text{list } n \ A; \forall (p,x) \in \text{set } ps. p < \text{size } xs \wedge x \in A \rrbracket \\ & \implies xs \sqsubseteq_r \text{merges } f \ ps \ xs \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma (in *Semilat*) *merges-same-conv* [*rule-format*]:

$$\begin{aligned} & (\forall xs. xs \in \text{list } n \ A \longrightarrow (\forall (p,x) \in \text{set } ps. p < \text{size } xs \wedge x \in A) \longrightarrow \\ & (\text{merges } f \ ps \ xs = xs) = (\forall (p,x) \in \text{set } ps. x \sqsubseteq_r \ xs!p)) \langle \text{proof} \rangle \end{aligned}$$

lemma (in *Semilat*) *list-update-le-listI* [*rule-format*]:

$$\begin{aligned} & \text{set } xs \subseteq A \longrightarrow \text{set } ys \subseteq A \longrightarrow xs \sqsubseteq_r \ ys \longrightarrow p < \text{size } xs \longrightarrow \\ & x \sqsubseteq_r \ ys!p \longrightarrow x \in A \longrightarrow xs[p := x \sqcup_f \ xs!p] \sqsubseteq_r \ ys \langle \text{proof} \rangle \end{aligned}$$

lemma (in *Semilat*) *merges-pres-le-ub*:

assumes $\text{set } ts \subseteq A \ \text{set } ss \subseteq A$
 $\forall (p,t) \in \text{set } ps. t \sqsubseteq_r \ ts!p \wedge t \in A \wedge p < \text{size } ts \ \ ss \sqsubseteq_r \ ts$
shows $\text{merges } f \ ps \ ss \sqsubseteq_r \ ts \langle \text{proof} \rangle$

context *Kildall* **begin**

2.9.1 *propa*

lemma *decomp-propa*:

$$\begin{aligned} & \bigwedge ss \ w. (\forall (q,t) \in \text{set } qs. q < \text{size } ss) \implies \\ & t\text{-}\alpha \ (\text{propa } f \ qs \ ss \ w) = \\ & (\text{merges } f \ qs \ ss, \{q. \exists t. (q,t) \in \text{set } qs \wedge t \sqcup_f \ ss!q \neq ss!q\} \cup s\text{-}\alpha \ w) \\ & \langle \text{proof} \rangle \end{aligned}$$

end

lemma (in *Semilat*) *stable-pres-lemma*:

shows $\llbracket \text{pres-type } \text{step } n \ A; \text{bounded } \text{step } n;$

$$\begin{aligned} & \text{ss} \in \text{list } n \ A; p \in w; \forall q \in w. q < n; \\ & \forall q. q < n \longrightarrow q \notin w \longrightarrow \text{stable } r \ \text{step } ss \ q; q < n; \\ & \forall s'. (q,s') \in \text{set } (\text{step } p \ (ss!p)) \longrightarrow s' \sqcup_f \ ss!q = ss!q; \\ & q \notin w \vee q = p \rrbracket \end{aligned}$$

\implies *stable r step (merges f (step p (ss!p)) ss) q*⟨proof⟩

lemma (in *Semilat*) *merges-bounded-lemma*:

[[*mono r step n A*; *bounded step n*;
 $\forall (p',s') \in \text{set } (\text{step } p \text{ (ss!p)}). s' \in A; ss \in \text{list } n \text{ } A; ts \in \text{list } n \text{ } A; p < n;$
 $ss \llbracket \sqsubseteq_r \rrbracket ts; \forall p. p < n \implies \text{stable } r \text{ step } ts \text{ } p$]]
 \implies *merges f (step p (ss!p)) ss* $\llbracket \sqsubseteq_r \rrbracket$ *ts*⟨proof⟩

lemma *termination-lemma*: **assumes** *Semilat A r f*

shows [[$ss \in \text{list } n \text{ } A; \forall (q,t) \in \text{set } qs. q < n \wedge t \in A; p \in w$]]
 \implies $ss \llbracket \sqsubseteq_r \rrbracket \text{merges } f \text{ } qs \text{ } ss \vee$

$\text{merges } f \text{ } qs \text{ } ss = ss \wedge \{q. \exists t. (q,t) \in \text{set } qs \wedge t \sqcup_f ss!q \neq ss!q\} \cup (w - \{p\}) \subset w$ ⟨proof⟩

context *Kildall-base* **begin**

definition *s-finite-psubset* :: (*'w * 'w*) *set*

where *s-finite-psubset* == $\{(A,B). s\text{-}\alpha \text{ } A < s\text{-}\alpha \text{ } B \ \& \ \text{finite } (s\text{-}\alpha \text{ } B)\}$

lemma *s-finite-psubset-inv-image*:

$s\text{-finite-psubset} = \text{inv-image } \text{finite-psubset } s\text{-}\alpha$
 ⟨proof⟩

lemma *wf-s-finite-psubset [simp]*: *wf s-finite-psubset*

⟨proof⟩

end

context *Kildall* **begin**

2.9.2 *iter*

lemma *iter-properties*[*rule-format*]: **assumes** *Semilat A r f*

shows [[*acc A r*; *pres-type step n A*; *mono r step n A*;

bounded step n; $\forall p \in s\text{-}\alpha \text{ } w0. p < n; ss0 \in \text{list } n \text{ } A;$

$\forall p < n. p \notin s\text{-}\alpha \text{ } w0 \implies \text{stable } r \text{ step } ss0 \text{ } p$]]
 \implies

$t\text{-}\alpha \text{ (iter } f \text{ step } ss0 \text{ } w0) = (ss',w')$

\longrightarrow

$ss' \in \text{list } n \text{ } A \wedge \text{stables } r \text{ step } ss' \wedge ss0 \llbracket \sqsubseteq_r \rrbracket ss' \wedge$

$(\forall ts \in \text{list } n \text{ } A. ss0 \llbracket \sqsubseteq_r \rrbracket ts \wedge \text{stables } r \text{ step } ts \longrightarrow ss' \llbracket \sqsubseteq_r \rrbracket ts)$ ⟨proof⟩

lemma *kildall-properties*: **assumes** *Semilat A r f*

shows [[*acc A r*; *pres-type step n A*; *mono r step n A*;

bounded step n; $ss0 \in \text{list } n \text{ } A$]]
 \implies

kildall r f step $ss0 \in \text{list } n \text{ } A \wedge$

stables r step (kildall r f step $ss0) \wedge$

$ss0 \llbracket \sqsubseteq_r \rrbracket \text{kildall } r \text{ f step } ss0 \wedge$

$(\forall ts \in \text{list } n \text{ } A. ss0 \llbracket \sqsubseteq_r \rrbracket ts \wedge \text{stables } r \text{ step } ts \longrightarrow$

$\text{kildall } r \text{ f step } ss0 \llbracket \sqsubseteq_r \rrbracket ts)$ ⟨proof⟩⟨proof⟩

end

interpretation *Kildall set* [] $\lambda xs. xs = [] \text{hd removeAll Cons}$

⟨proof⟩

lemmas *kildall-code* [*code*] =

kildall-def

Kildall-base.prop.simps

Kildall-base.iter-def
Kildall-base.unstables-def
Kildall-base.kildall-def

end

2.10 The Lightweight Bytecode Verifier

theory *LBVSpec*
imports *SemilatAlg Opt*
begin

type-synonym

's certificate = *'s list*

primrec *merge* :: *'s certificate* \Rightarrow *'s binop* \Rightarrow *'s ord* \Rightarrow *'s* \Rightarrow *nat* \Rightarrow (*nat* \times *'s*) *list* \Rightarrow *'s* \Rightarrow *'s*
where

merge cert f r T pc [] $x = x$
| *merge cert f r T pc (s#ss)* $x = \text{merge cert f r T pc ss (let (pc',s') = s in$
 $\quad \text{if } pc' = pc + 1 \text{ then } s' \sqcup_f x$
 $\quad \text{else if } s' \sqsubseteq_r \text{ cert!pc' then } x$
 $\quad \text{else } T)$

definition *wtl-inst* :: *'s certificate* \Rightarrow *'s binop* \Rightarrow *'s ord* \Rightarrow *'s* \Rightarrow
's step-type \Rightarrow *nat* \Rightarrow *'s* \Rightarrow *'s*

where

wtl-inst cert f r T step pc s = *merge cert f r T pc (step pc s) (cert!(pc+1))*

definition *wtl-cert* :: *'s certificate* \Rightarrow *'s binop* \Rightarrow *'s ord* \Rightarrow *'s* \Rightarrow *'s* \Rightarrow
's step-type \Rightarrow *nat* \Rightarrow *'s* \Rightarrow *'s*

where

wtl-cert cert f r T B step pc s =
(if cert!pc = B then
 $\quad \text{wtl-inst cert f r T step pc s}$
else
 $\quad \text{if } s \sqsubseteq_r \text{ cert!pc then wtl-inst cert f r T step pc (cert!pc) else } T)$

primrec *wtl-inst-list* :: *'a list* \Rightarrow *'s certificate* \Rightarrow *'s binop* \Rightarrow *'s ord* \Rightarrow *'s* \Rightarrow *'s* \Rightarrow
's step-type \Rightarrow *nat* \Rightarrow *'s* \Rightarrow *'s*

where

wtl-inst-list [] $\text{cert f r T B step pc s} = s$
| *wtl-inst-list (i#is)* $\text{cert f r T B step pc s} =$
 $\quad \text{(let } s' = \text{wtl-cert cert f r T B step pc s in}$
 $\quad \text{if } s' = T \vee s = T \text{ then } T \text{ else wtl-inst-list is cert f r T B step (pc+1) } s')$

definition *cert-ok* :: *'s certificate* \Rightarrow *nat* \Rightarrow *'s* \Rightarrow *'s* \Rightarrow *'s set* \Rightarrow *bool*

where

cert-ok cert n T B A $\longleftrightarrow (\forall i < n. \text{cert!i} \in A \wedge \text{cert!i} \neq T) \wedge (\text{cert!n} = B)$

definition *bottom* :: *'a ord* \Rightarrow *'a* \Rightarrow *bool*

where

bottom r B $\longleftrightarrow (\forall x. B \sqsubseteq_r x)$

locale *lbv* = *Semilat* +
fixes *T* :: 'a (\top)
fixes *B* :: 'a (\perp)
fixes *step* :: 'a *step-type*
assumes *top*: *top* *r* \top
assumes *T-A*: $\top \in A$
assumes *bot*: *bottom* *r* \perp
assumes *B-A*: $\perp \in A$

fixes *merge* :: 'a *certificate* \Rightarrow *nat* \Rightarrow (*nat* \times 'a) *list* \Rightarrow 'a \Rightarrow 'a
defines *mrg-def*: *merge cert* \equiv *LBVSpec.merge cert f r* \top

fixes *wti* :: 'a *certificate* \Rightarrow *nat* \Rightarrow 'a \Rightarrow 'a
defines *wti-def*: *wti cert* \equiv *wtl-inst cert f r* \top *step*

fixes *wtc* :: 'a *certificate* \Rightarrow *nat* \Rightarrow 'a \Rightarrow 'a
defines *wtc-def*: *wtc cert* \equiv *wtl-cert cert f r* \top \perp *step*

fixes *wtl* :: 'b *list* \Rightarrow 'a *certificate* \Rightarrow *nat* \Rightarrow 'a \Rightarrow 'a
defines *wtl-def*: *wtl ins cert* \equiv *wtl-inst-list ins cert f r* \top \perp *step*

lemma (in *lbv*) *wti*:

wti c pc s = *merge c pc (step pc s) (c!(pc+1))*
$\langle proof \rangle$

lemma (in *lbv*) *wtc*:

wtc c pc s = (if *c!pc* = \perp then *wti c pc s* else if *s* \sqsubseteq_r *c!pc* then *wti c pc (c!pc)* else \top)
$\langle proof \rangle$

lemma *cert-okD1* [*intro?*]:

cert-ok c n T B A \Longrightarrow *pc* < *n* \Longrightarrow *c!pc* $\in A$
$\langle proof \rangle$

lemma *cert-okD2* [*intro?*]:

cert-ok c n T B A \Longrightarrow *c!n* = *B*
$\langle proof \rangle$

lemma *cert-okD3* [*intro?*]:

cert-ok c n T B A \Longrightarrow *B* $\in A$ \Longrightarrow *pc* < *n* \Longrightarrow *c!Suc pc* $\in A$
$\langle proof \rangle$

lemma *cert-okD4* [*intro?*]:

cert-ok c n T B A \Longrightarrow *pc* < *n* \Longrightarrow *c!pc* $\neq T$
$\langle proof \rangle$

declare *Let-def* [*simp*]

2.10.1 more semilattice lemmas

lemma (in *lbv*) *sup-top* [*simp*, *elim*]:

assumes *x*: *x* $\in A$
shows *x* \sqcup_f \top = \top $\langle proof \rangle$

lemma (in *lbv*) *plusplussup-top* [*simp*, *elim*]:

set xs $\subseteq A$ \Longrightarrow *xs* \sqcup_f \top = \top
$\langle proof \rangle$

lemma (in *Semilat*) *pp-ub1*':

assumes S : $\text{snd}'\text{set } S \subseteq A$
assumes y : $y \in A$ **and** ab : $(a, b) \in \text{set } S$
shows $b \sqsubseteq_r \text{map snd } [(p', t') \leftarrow S . p' = a] \sqcup_f y \langle \text{proof} \rangle$
lemma (**in** lbv) *bottom-le* [*simp*, *intro!*]: $\perp \sqsubseteq_r x$
 $\langle \text{proof} \rangle$

lemma (**in** lbv) *le-bottom* [*simp*]: $x \sqsubseteq_r \perp = (x = \perp)$
 $\langle \text{proof} \rangle$

2.10.2 merge

lemma (**in** lbv) *merge-Nil* [*simp*]:
 $\text{merge } c \text{ pc } [] \ x = x \langle \text{proof} \rangle$

lemma (**in** lbv) *merge-Cons* [*simp*]:
 $\text{merge } c \text{ pc } (l\#\text{ls}) \ x = \text{merge } c \text{ pc } \text{ls}$ (if $\text{fst } l = \text{pc} + 1$ then $\text{snd } l \dashv\text{-f } x$
else if $\text{snd } l \sqsubseteq_r \text{c!fst } l$ then x
else \top)
 $\langle \text{proof} \rangle$

lemma (**in** lbv) *merge-Err* [*simp*]:
 $\text{snd}'\text{set } ss \subseteq A \implies \text{merge } c \text{ pc } ss \ \top = \top$
 $\langle \text{proof} \rangle$

lemma (**in** lbv) *merge-not-top*:
 $\bigwedge x. \text{snd}'\text{set } ss \subseteq A \implies \text{merge } c \text{ pc } ss \ x \neq \top \implies$
 $\forall (pc', s') \in \text{set } ss. (pc' \neq \text{pc} + 1 \longrightarrow s' \sqsubseteq_r \text{c!pc}')$
(is $\bigwedge x. ?\text{set } ss \implies ?\text{merge } ss \ x \implies ?P \ ss) \langle \text{proof} \rangle$

lemma (**in** lbv) *merge-def*:
shows
 $\bigwedge x. x \in A \implies \text{snd}'\text{set } ss \subseteq A \implies$
 $\text{merge } c \text{ pc } ss \ x =$
(if $\forall (pc', s') \in \text{set } ss. pc' \neq \text{pc} + 1 \longrightarrow s' \sqsubseteq_r \text{c!pc}'$ then
 $\text{map snd } [(p', t') \leftarrow ss. p' = \text{pc} + 1] \sqcup_f x$
else \top)
(is $\bigwedge x. - \implies - \implies ?\text{merge } ss \ x = ?\text{if } ss \ x \text{ is } \bigwedge x. - \implies - \implies ?P \ ss \ x) \langle \text{proof} \rangle$

lemma (**in** lbv) *merge-not-top-s*:
assumes x : $x \in A$ **and** ss : $\text{snd}'\text{set } ss \subseteq A$
assumes m : $\text{merge } c \text{ pc } ss \ x \neq \top$
shows $\text{merge } c \text{ pc } ss \ x = (\text{map snd } [(p', t') \leftarrow ss. p' = \text{pc} + 1] \sqcup_f x) \langle \text{proof} \rangle$

2.10.3 wtl-inst-list

lemmas [*iff*] = *not-Err-eq*

lemma (**in** lbv) *wtl-Nil* [*simp*]: $\text{wtl } [] \ c \ \text{pc } s = s$
 $\langle \text{proof} \rangle$

lemma (**in** lbv) *wtl-Cons* [*simp*]:
 $\text{wtl } (i\#\text{is}) \ c \ \text{pc } s =$
(let $s' = \text{wte } c \ \text{pc } s$ in if $s' = \top \vee s = \top$ then \top else $\text{wtl } \text{is } c \ (\text{pc} + 1) \ s'$)
 $\langle \text{proof} \rangle$

lemma (in *lbv*) *wtl-Cons-not-top*:

$wtl (i\#is) c pc s \neq \top =$
 $(wtc c pc s \neq \top \wedge s \neq T \wedge wtl is c (pc+1) (wtc c pc s) \neq \top)$
 ⟨proof⟩

lemma (in *lbv*) *wtl-top [simp]*: $wtl ls c pc \top = \top$

⟨proof⟩

lemma (in *lbv*) *wtl-not-top*:

$wtl ls c pc s \neq \top \implies s \neq \top$
 ⟨proof⟩

lemma (in *lbv*) *wtl-append [simp]*:

$\bigwedge pc s. wtl (a@b) c pc s = wtl b c (pc+length a) (wtl a c pc s)$
 ⟨proof⟩

lemma (in *lbv*) *wtl-take*:

$wtl is c pc s \neq \top \implies wtl (take pc' is) c pc s \neq \top$
 (is ?wtl is \neq - \implies -)⟨proof⟩

lemma *take-Suc*:

$\forall n. n < length l \implies take (Suc n) l = (take n l)@[!n] (is ?P l)$ ⟨proof⟩

lemma (in *lbv*) *wtl-Suc*:

assumes *suc*: $pc+1 < length is$
assumes *wtl*: $wtl (take pc is) c 0 s \neq \top$
shows $wtl (take (pc+1) is) c 0 s = wtc c pc (wtl (take pc is) c 0 s)$ ⟨proof⟩

lemma (in *lbv*) *wtl-all*:

assumes *all*: $wtl is c 0 s \neq \top$ (is ?wtl is \neq -)
assumes *pc*: $pc < length is$
shows $wtc c pc (wtl (take pc is) c 0 s) \neq \top$ ⟨proof⟩

2.10.4 preserves-type

lemma (in *lbv*) *merge-pres*:

assumes *s0*: $snd'set ss \subseteq A$ **and** *x*: $x \in A$
shows $merge c pc ss x \in A$ ⟨proof⟩

lemma *pres-typeD2*:

$pres-type step n A \implies s \in A \implies p < n \implies snd'set (step p s) \subseteq A$
 ⟨proof⟩

lemma (in *lbv*) *wti-pres [intro?]*:

assumes *pres*: $pres-type step n A$
assumes *cert*: $c!(pc+1) \in A$
assumes *s-pc*: $s \in A pc < n$
shows $wti c pc s \in A$ ⟨proof⟩

lemma (in *lbv*) *wtc-pres*:

assumes *pres-type step n A*
assumes $c!pc \in A$ **and** $c!(pc+1) \in A$
assumes $s \in A$ **and** $pc < n$
shows $wtc c pc s \in A$ ⟨proof⟩

lemma (in *lbv*) *wtl-pres*:

assumes *pres*: $pres-type step (length is) A$
assumes *cert*: $cert-ok c (length is) \top \perp A$
assumes *s*: $s \in A$
assumes *all*: $wtl is c 0 s \neq \top$

```

  shows  $pc < \text{length } is \implies \text{wtl } (\text{take } pc \text{ } is) \ c \ 0 \ s \in A$ 
  (is ?len pc  $\implies$  ?wtl pc  $\in A$ )<proof>
end

```

2.11 Correctness of the LBV

```
theory LBVCorrect
```

```
imports LBVSpec Typing-Framework
```

```
begin
```

```
locale lbus = lbv +
```

```
  fixes  $s_0 :: 'a$ 
```

```
  fixes  $c :: 'a \text{ list}$ 
```

```
  fixes  $ins :: 'b \text{ list}$ 
```

```
  fixes  $\tau s :: 'a \text{ list}$ 
```

```
  defines  $\text{phi-def}$ :
```

```

 $\tau s \equiv \text{map } (\lambda pc. \text{if } c!pc = \perp \text{ then wtl } (\text{take } pc \text{ } ins) \ c \ 0 \ s_0 \text{ else } c!pc)$ 
  [0.. $\text{size } ins$ ]

```

```
  assumes  $\text{bounded}$ :  $\text{bounded step } (\text{size } ins)$ 
```

```
  assumes  $\text{cert}$ :  $\text{cert-ok } c \ (\text{size } ins) \ \top \ \perp \ A$ 
```

```
  assumes  $\text{pres}$ :  $\text{pres-type step } (\text{size } ins) \ A$ 
```

```
lemma (in lbus)  $\text{phi-None}$  [intro?]:
```

```
   $\llbracket pc < \text{size } ins; c!pc = \perp \rrbracket \implies \tau s!pc = \text{wtl } (\text{take } pc \text{ } ins) \ c \ 0 \ s_0$ <proof>
```

```
lemma (in lbus)  $\text{phi-Some}$  [intro?]:
```

```
   $\llbracket pc < \text{size } ins; c!pc \neq \perp \rrbracket \implies \tau s!pc = c!pc$ <proof>
```

```
lemma (in lbus)  $\text{phi-len}$  [simp]:  $\text{size } \tau s = \text{size } ins$ <proof>
```

```
lemma (in lbus)  $\text{wtl-suc-pc}$ :
```

```
  assumes  $\text{all}$ :  $\text{wtl } ins \ c \ 0 \ s_0 \neq \top$ 
```

```
  assumes  $\text{pc}$ :  $pc+1 < \text{size } ins$ 
```

```
  shows  $\text{wtl } (\text{take } (pc+1) \text{ } ins) \ c \ 0 \ s_0 \sqsubseteq_r \tau s!(pc+1)$ <proof>
```

```
lemma (in lbus)  $\text{wtl-stable}$ :
```

```
  assumes  $\text{wtl}$ :  $\text{wtl } ins \ c \ 0 \ s_0 \neq \top$ 
```

```
  assumes  $s_0$ :  $s_0 \in A$  and  $\text{pc}$ :  $pc < \text{size } ins$ 
```

```
  shows  $\text{stable } r \text{ step } \tau s \ pc$ <proof>
```

```
lemma (in lbus)  $\text{phi-not-top}$ :
```

```
  assumes  $\text{wtl}$ :  $\text{wtl } ins \ c \ 0 \ s_0 \neq \top$  and  $\text{pc}$ :  $pc < \text{size } ins$ 
```

```
  shows  $\tau s!pc \neq \top$ <proof>
```

```
lemma (in lbus)  $\text{phi-in-A}$ :
```

```
  assumes  $\text{wtl}$ :  $\text{wtl } ins \ c \ 0 \ s_0 \neq \top$  and  $s_0$ :  $s_0 \in A$ 
```

```
  shows  $\tau s \in \text{list } (\text{size } ins) \ A$ <proof>
```

```
lemma (in lbus)  $\text{phi0}$ :
```

```
  assumes  $\text{wtl}$ :  $\text{wtl } ins \ c \ 0 \ s_0 \neq \top$  and  $0$ :  $0 < \text{size } ins$ 
```

```
  shows  $s_0 \sqsubseteq_r \tau s!0$ <proof>
```

```
theorem (in lbus)  $\text{wtl-sound}$ :
```

```
  assumes  $\text{wtl}$ :  $\text{wtl } ins \ c \ 0 \ s_0 \neq \top$  and  $s_0$ :  $s_0 \in A$ 
```

```
  shows  $\exists \tau s. \text{wt-step } r \ \top \ \text{step } \tau s$ <proof>
```

```
theorem (in lbus)  $\text{wtl-sound-strong}$ :
```

```
  assumes  $\text{wtl}$ :  $\text{wtl } ins \ c \ 0 \ s_0 \neq \top$ 
```

```
  assumes  $s_0$ :  $s_0 \in A$  and  $ins$ :  $0 < \text{size } ins$ 
```


shows $\exists \tau s \in \text{list } (size \text{ ins}) \ A. \text{ wt-step } r \ \top \ \text{step } \tau s \wedge s_0 \sqsubseteq_r \tau s!0 \langle \text{proof} \rangle$
end

2.12 Completeness of the LBV

theory *LBVComplete*

imports *LBVSpec Typing-Framework*

begin

definition *is-target* :: 's step-type \Rightarrow 's list \Rightarrow nat \Rightarrow bool **where**
is-target step τs $pc' \longleftrightarrow (\exists pc \ s'. \ pc' \neq pc+1 \wedge pc < size \ \tau s \wedge (pc', s') \in set \ (step \ pc \ (\tau s!pc)))$

definition *make-cert* :: 's step-type \Rightarrow 's list \Rightarrow 's \Rightarrow 's certificate **where**
make-cert step τs $B = map \ (\lambda pc. \ \text{if } is\text{-target} \ \text{step } \tau s \ pc \ \text{then } \tau s!pc \ \text{else } B) \ [0..<size \ \tau s] \ @ \ [B]$

lemma [*code*]:

is-target step τs $pc' =$

list-ex $(\lambda pc. \ pc' \neq pc+1 \wedge List.member \ (map \ fst \ (step \ pc \ (\tau s!pc))) \ pc') \ [0..<size \ \tau s] \langle \text{proof} \rangle$

locale *lbvc* = *lbv* +

fixes $\tau s :: 'a \ \text{list}$

fixes $c :: 'a \ \text{list}$

defines *cert-def*: $c \equiv make\text{-cert} \ \text{step } \tau s \ \perp$

assumes *mono*: $mono \ r \ step \ (size \ \tau s) \ A$

assumes *pres*: $pres\text{-type} \ step \ (size \ \tau s) \ A$

assumes τs : $\forall pc < size \ \tau s. \ \tau s!pc \in A \wedge \tau s!pc \neq \top$

assumes *bounded*: $bounded \ step \ (size \ \tau s)$

assumes *B-neq-T*: $\perp \neq \top$

lemma (**in** *lbvc*) *cert*: $cert\text{-ok} \ c \ (size \ \tau s) \ \top \ \perp \ A \langle \text{proof} \rangle$

lemmas [*simp del*] = *split-paired-Ex*

lemma (**in** *lbvc*) *cert-target* [*intro?*]:

$\llbracket (pc', s') \in set \ (step \ pc \ (\tau s!pc));$

$pc' \neq pc+1; \ pc < size \ \tau s; \ pc' < size \ \tau s \rrbracket$

$\implies c!pc' = \tau s!pc' \langle \text{proof} \rangle$

lemma (**in** *lbvc*) *cert-approx* [*intro?*]:

$\llbracket pc < size \ \tau s; \ c!pc \neq \perp \rrbracket \implies c!pc = \tau s!pc \langle \text{proof} \rangle$

lemma (**in** *lbv*) *le-top* [*simp, intro*]: $x <=-r \ \top \langle \text{proof} \rangle$

lemma (**in** *lbv*) *merge-mono*:

assumes *less*: $set \ ss_2 \ \{\sqsubseteq_r\} \ set \ ss_1$

assumes x : $x \in A$

assumes ss_1 : $snd' \ set \ ss_1 \subseteq A$

assumes ss_2 : $snd' \ set \ ss_2 \subseteq A$

shows $merge \ c \ pc \ ss_2 \ x \sqsubseteq_r \ merge \ c \ pc \ ss_1 \ x \ (\text{is } ?s_2 \sqsubseteq_r \ ?s_1) \langle \text{proof} \rangle$

lemma (**in** *lbvc*) *wti-mono*:

assumes *less*: $s_2 \sqsubseteq_r \ s_1$

assumes pc : $pc < size \ \tau s$ **and** s_1 : $s_1 \in A$ **and** s_2 : $s_2 \in A$

shows $wti \ c \ pc \ s_2 \sqsubseteq_r \ wti \ c \ pc \ s_1 \ (\text{is } ?s_2' \sqsubseteq_r \ ?s_1') \langle \text{proof} \rangle$

lemma (**in** *lbvc*) *wtc-mono*:

assumes *less*: $s_2 \sqsubseteq_r \ s_1$

assumes $pc: pc < size\ \tau s$ **and** $s_1: s_1 \in A$ **and** $s_2: s_2 \in A$
shows $wtc\ c\ pc\ s_2 \sqsubseteq_r wtc\ c\ pc\ s_1$ (**is** $?s_2' \sqsubseteq_r ?s_1'$) $\langle proof \rangle$
lemma (**in** lbv) *top-le-conv* [*simp*]: $\top \sqsubseteq_r x = (x = \top)$ $\langle proof \rangle$
lemma (**in** lbv) *neg-top* [*simp*, *elim*]: $\llbracket x \sqsubseteq_r y; y \neq \top \rrbracket \implies x \neq \top$ $\langle proof \rangle$
lemma (**in** $lbvc$) *stable-wti*:
assumes *stable*: $stable\ r\ step\ \tau s\ pc$ **and** $pc: pc < size\ \tau s$
shows $wti\ c\ pc\ (\tau s!pc) \neq \top$ $\langle proof \rangle$
lemma (**in** $lbvc$) *wti-less*:
assumes *stable*: $stable\ r\ step\ \tau s\ pc$ **and** $suc\text{-}pc: Suc\ pc < size\ \tau s$
shows $wti\ c\ pc\ (\tau s!pc) \sqsubseteq_r \tau s!Suc\ pc$ (**is** $?wti \sqsubseteq_r -$) $\langle proof \rangle$
lemma (**in** $lbvc$) *stable-wtc*:
assumes *stable*: $stable\ r\ step\ \tau s\ pc$ **and** $pc: pc < size\ \tau s$
shows $wtc\ c\ pc\ (\tau s!pc) \neq \top$ $\langle proof \rangle$
lemma (**in** $lbvc$) *wtc-less*:
assumes *stable*: $stable\ r\ step\ \tau s\ pc$ **and** $suc\text{-}pc: Suc\ pc < size\ \tau s$
shows $wtc\ c\ pc\ (\tau s!pc) \sqsubseteq_r \tau s!Suc\ pc$ (**is** $?wtc \sqsubseteq_r -$) $\langle proof \rangle$
lemma (**in** $lbvc$) *wt-step-wtl-lemma*:
assumes *wt-step*: $wt\text{-}step\ r\ \top\ step\ \tau s$
shows $\bigwedge pc\ s. pc + size\ ls = size\ \tau s \implies s \sqsubseteq_r \tau s!pc \implies s \in A \implies s \neq \top \implies$
 $wtl\ ls\ c\ pc\ s \neq \top$
(is $\bigwedge pc\ s. - \implies - \implies - \implies - \implies ?wtl\ ls\ pc\ s \neq -$) $\langle proof \rangle$
theorem (**in** $lbvc$) *wtl-complete*:
assumes *wt*: $wt\text{-}step\ r\ \top\ step\ \tau s$
assumes $s: s \sqsubseteq_r \tau s!0$ $s \in A$ $s \neq \top$ **and** $eq: size\ ins = size\ \tau s$
shows $wtl\ ins\ c\ 0\ s \neq \top$ $\langle proof \rangle$
end

Chapter 3

Concepts for all JinjaThreads Languages

3.1 JinjaThreads types

```
theory Type
imports
  ../Basic/Auxiliary
begin

type-synonym cname = String.literal — class names
type-synonym mname = String.literal — method name
type-synonym vname = String.literal — names for local/field variables

definition Object :: cname
where Object ≡ STR "java/lang/Object"

definition Thread :: cname
where Thread ≡ STR "java/lang/Thread"

definition Throwable :: cname
where Throwable ≡ STR "java/lang/Throwable"

definition this :: vname
where this ≡ STR "this"

definition run :: mname
where run ≡ STR "run() V"

definition start :: mname
where start ≡ STR "start() V"

definition wait :: mname
where wait ≡ STR "wait() V"

definition notify :: mname
where notify ≡ STR "notify() V"

definition notifyAll :: mname
```

where *notifyAll* \equiv *STR* "notifyAll() V"

definition *join* :: *mname*

where *join* \equiv *STR* "join() V"

definition *interrupt* :: *mname*

where *interrupt* \equiv *STR* "interrupt() V"

definition *isInterrupted* :: *mname*

where *isInterrupted* \equiv *STR* "isInterrupted() Z"

definition *hashCode* :: *mname*

where *hashCode* = *STR* "hashCode() I"

definition *clone* :: *mname*

where *clone* = *STR* "clone()Ljava/lang/Object;"

definition *print* :: *mname*

where *print* = *STR* "~print(I) V"

definition *currentThread* :: *mname*

where *currentThread* = *STR* "~Thread.currentThread()Ljava/lang/Thread;"

definition *interrupted* :: *mname*

where *interrupted* = *STR* "~Thread.interrupted() Z"

definition *yield* :: *mname*

where *yield* = *STR* "~Thread.yield() V"

lemmas *identifier-name-defs* [*code-unfold*] =

*this-def run-def start-def wait-def notify-def notifyAll-def join-def interrupt-def isInterrupted-def
hashCode-def clone-def print-def currentThread-def interrupted-def yield-def*

lemma *Object-Thread-Throwable-neq* [*simp*]:

Thread \neq *Object* *Object* \neq *Thread*

Object \neq *Throwable* *Throwable* \neq *Object*

Thread \neq *Throwable* *Throwable* \neq *Thread*

\langle *proof* \rangle

lemma *synth-method-names-neq-aux*:

start \neq *wait* *start* \neq *notify* *start* \neq *notifyAll* *start* \neq *join* *start* \neq *interrupt* *start* \neq *isInterrupted*

start \neq *hashCode* *start* \neq *clone* *start* \neq *print* *start* \neq *currentThread*

start \neq *interrupted* *start* \neq *yield* *start* \neq *run*

wait \neq *notify* *wait* \neq *notifyAll* *wait* \neq *join* *wait* \neq *interrupt* *wait* \neq *isInterrupted*

wait \neq *hashCode* *wait* \neq *clone* *wait* \neq *print* *wait* \neq *currentThread*

wait \neq *interrupted* *wait* \neq *yield* *wait* \neq *run*

notify \neq *notifyAll* *notify* \neq *join* *notify* \neq *interrupt* *notify* \neq *isInterrupted*

notify \neq *hashCode* *notify* \neq *clone* *notify* \neq *print* *notify* \neq *currentThread*

notify \neq *interrupted* *notify* \neq *yield* *notify* \neq *run*

notifyAll \neq *join* *notifyAll* \neq *interrupt* *notifyAll* \neq *isInterrupted*

notifyAll \neq *hashCode* *notifyAll* \neq *clone* *notifyAll* \neq *print* *notifyAll* \neq *currentThread*

notifyAll \neq *interrupted* *notifyAll* \neq *yield* *notifyAll* \neq *run*

```

join ≠ interrupt join ≠ isInterrupted
join ≠ hashCode join ≠ clone join ≠ print join ≠ currentThread
join ≠ interrupted join ≠ yield join ≠ run
interrupt ≠ isInterrupted
interrupt ≠ hashCode interrupt ≠ clone interrupt ≠ print interrupt ≠ currentThread
interrupt ≠ interrupted interrupt ≠ yield interrupt ≠ run
isInterrupted ≠ hashCode isInterrupted ≠ clone isInterrupted ≠ print isInterrupted ≠ currentThread

isInterrupted ≠ interrupted isInterrupted ≠ yield isInterrupted ≠ run
hashCode ≠ clone hashCode ≠ print hashCode ≠ currentThread
hashCode ≠ interrupted hashCode ≠ yield hashCode ≠ run
clone ≠ print clone ≠ currentThread
clone ≠ interrupted clone ≠ yield clone ≠ run
print ≠ currentThread
print ≠ interrupted print ≠ yield print ≠ run
currentThread ≠ interrupted currentThread ≠ yield currentThread ≠ run
interrupted ≠ yield interrupted ≠ run
yield ≠ run
⟨proof⟩

```

lemmas *synth-method-names-neq* [simp] = *synth-method-names-neq-aux* *synth-method-names-neq-aux*[symmetric]

— types

datatype *ty*

```

= Void      — type of statements
| Boolean
| Integer
| NT        — null type
| Class cname — class type
| Array ty  (⟨-[]⟩ 95) — array type

```

context

notes [[*inductive-internals*]]

begin

inductive *is-refT* :: *ty* ⇒ *bool* **where**

```

is-refT NT
| is-refT (Class C)
| is-refT (A[])

```

declare *is-refT.intros*[iff]

end

lemmas *refTE* [consumes 1, case-names *NT Class Array*] = *is-refT.cases*

lemma *not-refTE* [consumes 1, case-names *Void Boolean Integer*]:

```

[[ ¬is-refT T; T = Void ⇒ P; T = Boolean ⇒ P; T = Integer ⇒ P ]] ⇒ P
⟨proof⟩

```

fun *ground-type* :: *ty* ⇒ *ty* **where**

```

ground-type (Array T) = ground-type T
| ground-type T = T

```

abbreviation *is-NT-Array* :: *ty* \Rightarrow *bool* **where**
is-NT-Array *T* \equiv *ground-type T = NT*

primrec *the-Class* :: *ty* \Rightarrow *cname*
where
the-Class (*Class C*) = *C*

primrec *the-Array* :: *ty* \Rightarrow *ty*
where
the-Array (*T*[]) = *T*

datatype *htype* =
Class-type cname
| *Array-type ty nat*

primrec *ty-of-htype* :: *htype* \Rightarrow *ty*
where
ty-of-htype (*Class-type C*) = *Class C*
| *ty-of-htype* (*Array-type T n*) = *Array T*

primrec *alen-of-htype* :: *htype* \Rightarrow *nat*
where
alen-of-htype (*Array-type T n*) = *n*

primrec *class-type-of* :: *htype* \Rightarrow *cname*
where
class-type-of (*Class-type C*) = *C*
| *class-type-of* (*Array-type T n*) = *Object*

fun *class-type-of'* :: *ty* \Rightarrow *cname option*
where
class-type-of' (*Class C*) = [*C*]
| *class-type-of'* (*Array T*) = [*Object*]
| *class-type-of'* - = *None*

lemma *rec-htype-is-case* [*simp*]: *rec-htype = case-htype*
 \langle *proof* \rangle

lemma *ty-of-htype-eq-convs* [*simp*]:
shows *ty-of-htype-eq-Boolean*: *ty-of-htype hT* \neq *Boolean*
and *ty-of-htype-eq-Void*: *ty-of-htype hT* \neq *Void*
and *ty-of-htype-eq-Integer*: *ty-of-htype hT* \neq *Integer*
and *ty-of-htype-eq-NT*: *ty-of-htype hT* \neq *NT*
and *ty-of-htype-eq-Class*: *ty-of-htype hT = Class C* \iff *hT = Class-type C*
and *ty-of-htype-eq-Array*: *ty-of-htype hT = Array T* \iff ($\exists n. hT = Array-type T n$)
 \langle *proof* \rangle

lemma *class-type-of-eq*:
class-type-of hT =
(*case hT of Class-type C* \Rightarrow *C* | *Array-type T n* \Rightarrow *Object*)
 \langle *proof* \rangle

lemma *class-type-of'-ty-of-htype* [*simp*]:

```

class-type-of' (ty-of-hType hT) = [class-type-of hT]
⟨proof⟩

```

```

fun is-Array :: ty ⇒ bool
where
  is-Array (Array T) = True
| is-Array - = False

```

```

lemma is-Array-conv [simp]: is-Array T ⟷ (∃ U. T = Array U)
⟨proof⟩

```

```

fun is-Class :: ty ⇒ bool
where
  is-Class (Class C) = True
| is-Class - = False

```

```

lemma is-Class-conv [simp]: is-Class T ⟷ (∃ C. T = Class C)
⟨proof⟩

```

3.1.1 Code generator setup

```

code-pred is-refT ⟨proof⟩

```

```

end

```

3.2 Class Declarations and Programs

```

theory Decl
imports
  Type
begin

```

```

type-synonym volatile = bool

```

```

record fmod =
  volatile :: volatile

```

```

type-synonym fdecl = vname × ty × fmod — field declaration
type-synonym 'm mdecl = mname × ty list × ty × 'm — method = name, arg. types, return
type, body
type-synonym 'm mdecl' = mname × ty list × ty × 'm option — method = name, arg. types,
return type, possible body
type-synonym 'm class = cname × fdecl list × 'm mdecl' list — class = superclass, fields,
methods
type-synonym 'm cdecl = cname × 'm class — class declaration

```

```

datatype
  'm prog = Program 'm cdecl list

```

```

translations

```

```

(type) fdecl <= (type) String.literal × ty × fmod
(type) 'c mdecl <= (type) String.literal × ty list × ty × 'c
(type) 'c mdecl' <= (type) String.literal × ty list × ty × 'c option
(type) 'c class <= (type) String.literal × fdecl list × ('c mdecl) list

```

(type) 'c cdecl <= (type) String.literal × ('c class)

notation (input) None (⟨Native⟩)

primrec classes :: 'm prog ⇒ 'm cdecl list

where

classes (Program P) = P

primrec class :: 'm prog ⇒ cname → 'm class

where

class (Program p) = map-of p

locale prog =

fixes P :: 'm prog

definition is-class :: 'm prog ⇒ cname ⇒ bool

where

is-class P C ≡ class P C ≠ None

lemma finite-is-class: finite {C. is-class P C}⟨proof⟩

primrec is-type :: 'm prog ⇒ ty ⇒ bool

where

is-type-void: is-type P Void = True

| is-type-bool: is-type P Boolean = True

| is-type-int: is-type P Integer = True

| is-type-nt: is-type P NT = True

| is-type-class: is-type P (Class C) = is-class P C

| is-type-array: is-type P (A[]) = (case ground-type A of NT ⇒ False | Class C ⇒ is-class P C | - ⇒ True)

lemma is-type-ArrayD: is-type P (T[]) ⇒ is-type P T
⟨proof⟩

lemma is-type-ground-type:

is-type P T ⇒ is-type P (ground-type T)

⟨proof⟩

abbreviation types :: 'm prog ⇒ ty set

where types P ≡ {T. is-type P T}

abbreviation is-htype :: 'm prog ⇒ htype ⇒ bool

where is-htype P hT ≡ is-type P (ty-of-htype hT)

3.2.1 Code generation

lemma is-class-intros [code-pred-intro]:

class P C ≠ None ⇒ is-class P C

⟨proof⟩

code-pred

(modes: i ⇒ i ⇒ bool)

is-class

⟨proof⟩

declare *is-class-def*[code]

end

3.3 Relations between Jinja Types

theory *TypeRel*

imports

Decl

begin

3.3.1 The subclass relations

inductive *subcls1* :: 'm prog \Rightarrow cname \Rightarrow cname \Rightarrow bool ($\langle \cdot \vdash \cdot \prec^1 \cdot \rangle$ [71, 71, 71] 70)

for $P :: 'm prog$

where *subcls1I*: $\llbracket \text{class } P \ C = \text{Some } (D, \text{rest}); C \neq \text{Object} \rrbracket \Longrightarrow P \vdash C \prec^1 D$

abbreviation *subcls* :: 'm prog \Rightarrow cname \Rightarrow cname \Rightarrow bool ($\langle \cdot \vdash \cdot \preceq^* \cdot \rangle$ [71, 71, 71] 70)

where $P \vdash C \preceq^* D \equiv (\text{subcls1 } P)^{**} C D$

lemma *subcls1D*:

$P \vdash C \prec^1 D \Longrightarrow C \neq \text{Object} \wedge (\exists fs \ ms. \text{class } P \ C = \text{Some } (D, fs, ms))$

$\langle \text{proof} \rangle$

lemma *Object-subcls1* [iff]: $\neg P \vdash \text{Object} \prec^1 C$

$\langle \text{proof} \rangle$

lemma *Object-subcls-conv* [iff]: $(P \vdash \text{Object} \preceq^* C) = (C = \text{Object})$

$\langle \text{proof} \rangle$

lemma *finite-subcls1*: *finite* $\{(C, D). P \vdash C \prec^1 D\}$

$\langle \text{proof} \rangle$

lemma *finite-subcls1'*:

finite $\{(D, C). P \vdash C \prec^1 D\}$

$\langle \text{proof} \rangle$

lemma *subcls-is-class*: $(\text{subcls1 } P)^{++} C D \Longrightarrow \text{is-class } P \ C$

$\langle \text{proof} \rangle$

lemma *subcls-is-class1*: $\llbracket P \vdash C \preceq^* D; \text{is-class } P \ D \rrbracket \Longrightarrow \text{is-class } P \ C$

$\langle \text{proof} \rangle$

3.3.2 The subtype relations

inductive *widen* :: 'm prog \Rightarrow ty \Rightarrow ty \Rightarrow bool ($\langle \cdot \vdash \cdot \leq \cdot \rangle$ [71, 71, 71] 70)

for $P :: 'm prog$

where

widen-refl[iff]: $P \vdash T \leq T$

| *widen-subcls*: $P \vdash C \preceq^* D \Longrightarrow P \vdash \text{Class } C \leq \text{Class } D$

| *widen-null*[iff]: $P \vdash NT \leq \text{Class } C$

| *widen-null-array*[iff]: $P \vdash NT \leq \text{Array } A$

| *widen-array-object*: $P \vdash \text{Array } A \leq \text{Class } \text{Object}$

| *widen-array-array*: $P \vdash A \leq B \Longrightarrow P \vdash \text{Array } A \leq \text{Array } B$

abbreviation

$widens :: 'm\ prog \Rightarrow ty\ list \Rightarrow ty\ list \Rightarrow bool \ (\leftarrow \vdash - [\leq] \rightarrow [71,71,71] 70)$

where

$P \vdash Ts [\leq] Ts' == list-all2\ (widen\ P)\ Ts\ Ts'$

lemma [iff]: $(P \vdash T \leq Void) = (T = Void)\langle proof \rangle$

lemma [iff]: $(P \vdash T \leq Boolean) = (T = Boolean)\langle proof \rangle$

lemma [iff]: $(P \vdash T \leq Integer) = (T = Integer)\langle proof \rangle$

lemma [iff]: $(P \vdash Void \leq T) = (T = Void)\langle proof \rangle$

lemma [iff]: $(P \vdash Boolean \leq T) = (T = Boolean)\langle proof \rangle$

lemma [iff]: $(P \vdash Integer \leq T) = (T = Integer)\langle proof \rangle$

lemma *Class-widen*: $P \vdash Class\ C \leq T \Longrightarrow \exists D. T = Class\ D$
 $\langle proof \rangle$

lemma *Array-Array-widen*:

$P \vdash Array\ T \leq Array\ U \Longrightarrow P \vdash T \leq U$

$\langle proof \rangle$

lemma *widen-Array*: $(P \vdash T \leq U[]) \longleftrightarrow (T = NT \vee (\exists V. T = V[] \wedge P \vdash V \leq U))$

$\langle proof \rangle$

lemma *Array-widen*: $P \vdash Array\ A \leq T \Longrightarrow (\exists B. T = Array\ B \wedge P \vdash A \leq B) \vee T = Class\ Object$

$\langle proof \rangle$

lemma [iff]: $(P \vdash T \leq NT) = (T = NT)$

$\langle proof \rangle$

lemma *Class-widen-Class* [iff]: $(P \vdash Class\ C \leq Class\ D) = (P \vdash C \preceq^* D)$

$\langle proof \rangle$

lemma *widen-Class*: $(P \vdash T \leq Class\ C) = (T = NT \vee (\exists D. T = Class\ D \wedge P \vdash D \preceq^* C) \vee (C = Object \wedge (\exists A. T = Array\ A)))$

$\langle proof \rangle$

lemma *NT-widen*:

$P \vdash NT \leq T = (T = NT \vee (\exists C. T = Class\ C) \vee (\exists U. T = U[]))$

$\langle proof \rangle$

lemma *Class-widen2*: $P \vdash Class\ C \leq T = (\exists D. T = Class\ D \wedge P \vdash C \preceq^* D)$

$\langle proof \rangle$

lemma *Object-widen*: $P \vdash Class\ Object \leq T \Longrightarrow T = Class\ Object$

$\langle proof \rangle$

lemma *NT-Array-widen-Object*:

$is-NT-Array\ T \Longrightarrow P \vdash T \leq Class\ Object$

$\langle proof \rangle$

lemma *widen-trans*[*trans*]:

assumes $P \vdash S \leq U\ P \vdash U \leq T$

shows $P \vdash S \leq T$

$\langle proof \rangle$

lemma *widens-trans*: $\llbracket P \vdash Ss \leq Ts; P \vdash Ts \leq Us \rrbracket \implies P \vdash Ss \leq Us$
 ⟨proof⟩

lemma *class-type-of'-widenD*:
 $class\text{-}type\text{-}of' T = \lfloor C \rrbracket \implies P \vdash T \leq Class C$
 ⟨proof⟩

lemma *widen-is-class-type-of*:
assumes $class\text{-}type\text{-}of' T = \lfloor C \rrbracket P \vdash T' \leq T T' \neq NT$
obtains C' **where** $class\text{-}type\text{-}of' T' = \lfloor C' \rrbracket P \vdash C' \preceq^* C$
 ⟨proof⟩

lemma *widens-refl*: $P \vdash Ts \leq Ts$
 ⟨proof⟩

lemma *widen-append1*:
 $P \vdash (xs @ ys) \leq Ts = (\exists Ts1 Ts2. Ts = Ts1 @ Ts2 \wedge length xs = length Ts1 \wedge length ys = length Ts2 \wedge P \vdash xs \leq Ts1 \wedge P \vdash ys \leq Ts2)$
 ⟨proof⟩

lemmas *widens-Cons* [iff] = *list-all2-Cons1* [of widen P] **for** P

lemma *widens-lengthD*:
 $P \vdash xs \leq ys \implies length xs = length ys$
 ⟨proof⟩

lemma *widen-refT*: $\llbracket is\text{-}refT T; P \vdash U \leq T \rrbracket \implies is\text{-}refT U$
 ⟨proof⟩

lemma *refT-widen*: $\llbracket is\text{-}refT T; P \vdash T \leq U \rrbracket \implies is\text{-}refT U$
 ⟨proof⟩

inductive *is-lub* :: 'm prog \Rightarrow ty \Rightarrow ty \Rightarrow ty \Rightarrow bool ($\langle \vdash lub'((- / -)' \rangle = \rightarrow [51, 51, 51, 51] 50$)
for P :: 'm prog **and** U :: ty **and** V :: ty **and** T :: ty
where
 $\llbracket P \vdash U \leq T; P \vdash V \leq T; \bigwedge T'. \llbracket P \vdash U \leq T'; P \vdash V \leq T' \rrbracket \implies P \vdash T \leq T' \rrbracket$
 $\implies P \vdash lub(U, V) = T$

lemma *is-lub-upper*:
 $P \vdash lub(U, V) = T \implies P \vdash U \leq T \wedge P \vdash V \leq T$
 ⟨proof⟩

lemma *is-lub-least*:
 $\llbracket P \vdash lub(U, V) = T; P \vdash U \leq T'; P \vdash V \leq T' \rrbracket \implies P \vdash T \leq T'$
 ⟨proof⟩

lemma *is-lub-Void* [iff]:
 $P \vdash lub(Void, Void) = T \iff T = Void$
 ⟨proof⟩

lemma *is-lubI* [code-pred-intro]:
 $\llbracket P \vdash U \leq T; P \vdash V \leq T; \forall T'. P \vdash U \leq T' \longrightarrow P \vdash V \leq T' \longrightarrow P \vdash T \leq T' \rrbracket \implies P \vdash lub(U, V) = T$

<proof>

3.3.3 Method lookup

inductive *Methods* :: 'm prog \Rightarrow cname \Rightarrow (mname \rightarrow (ty list \times ty \times 'm option) \times cname) \Rightarrow bool
 ($\langle \vdash - \text{sees}'\text{-methods} \rightarrow [51,51,51] 50 \rangle$)

for *P* :: 'm prog

where

sees-methods-Object:

$\llbracket \text{class } P \text{ Object} = \text{Some}(D,fs,ms); Mm = \text{map-option } (\lambda m. (m, \text{Object})) \circ \text{map-of } ms \rrbracket$
 $\Longrightarrow P \vdash \text{Object sees-methods } Mm$

| *sees-methods-rec*:

$\llbracket \text{class } P \text{ C} = \text{Some}(D,fs,ms); C \neq \text{Object}; P \vdash D \text{ sees-methods } Mm;$
 $Mm' = Mm ++ (\text{map-option } (\lambda m. (m, C)) \circ \text{map-of } ms) \rrbracket$
 $\Longrightarrow P \vdash C \text{ sees-methods } Mm'$

lemma *sees-methods-fun*:

assumes $P \vdash C \text{ sees-methods } Mm$

shows $P \vdash C \text{ sees-methods } Mm' \Longrightarrow Mm' = Mm$

<proof>

lemma *visible-methods-exist*:

$P \vdash C \text{ sees-methods } Mm \Longrightarrow Mm \text{ M} = \text{Some}(m, D) \Longrightarrow$

$(\exists D' fs ms. \text{class } P \text{ D} = \text{Some}(D', fs, ms) \wedge \text{map-of } ms \text{ M} = \text{Some } m)$

<proof>

lemma *sees-methods-decl-above*:

assumes $P \vdash C \text{ sees-methods } Mm$

shows $Mm \text{ M} = \text{Some}(m, D) \Longrightarrow P \vdash C \preceq^* D$

<proof>

lemma *sees-methods-idemp*:

assumes $P \vdash C \text{ sees-methods } Mm$ **and** $Mm \text{ M} = \text{Some}(m, D)$

shows $\exists Mm'. (P \vdash D \text{ sees-methods } Mm') \wedge Mm' \text{ M} = \text{Some}(m, D)$

<proof>

lemma *sees-methods-decl-mono*:

assumes *sub*: $P \vdash C' \preceq^* C$ **and** $P \vdash C \text{ sees-methods } Mm$

shows $\exists Mm' Mm_2. P \vdash C' \text{ sees-methods } Mm' \wedge Mm' = Mm ++ Mm_2 \wedge (\forall M m D. Mm_2 \text{ M} = \text{Some}(m, D) \longrightarrow P \vdash D \preceq^* C)$

(**is** $\exists Mm' Mm_2. ?Q \ C' \ C \ Mm' \ Mm_2$)

<proof>

definition *Method* :: 'm prog \Rightarrow cname \Rightarrow mname \Rightarrow ty list \Rightarrow ty \Rightarrow 'm option \Rightarrow cname \Rightarrow bool

($\langle \vdash - \text{sees} \text{ :- } \text{-->-} = \text{- in } \rightarrow [51,51,51,51,51,51,51] 50 \rangle$)

where

$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \equiv$

$\exists Mm. P \vdash C \text{ sees-methods } Mm \wedge Mm \text{ M} = \text{Some}((Ts, T, m), D)$

Output translation to replace *None* with its notation *Native* when used as method body in *Method*.

abbreviation (output)

Method-native :: 'm prog \Rightarrow cname \Rightarrow mname \Rightarrow ty list \Rightarrow ty \Rightarrow cname \Rightarrow bool

($\langle \vdash - \text{sees} \text{ :- } \text{-->-} = \text{Native in } \rightarrow [51,51,51,51,51,51] 50 \rangle$)

where $\text{Method-native } P \ C \ M \ Ts \ T \ D \equiv \text{Method } P \ C \ M \ Ts \ T \ \text{Native } D$

definition $\text{has-method} :: 'm \ \text{prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{bool} \ (\langle \cdot \vdash \cdot \text{has} \rightarrow [51,0,51] \ 50)$

where

$P \vdash C \ \text{has} \ M \equiv \exists \ Ts \ T \ m \ D. \ P \vdash C \ \text{sees} \ M : Ts \rightarrow T = m \ \text{in} \ D$

lemma has-methodI :

$P \vdash C \ \text{sees} \ M : Ts \rightarrow T = m \ \text{in} \ D \Longrightarrow P \vdash C \ \text{has} \ M$
 $\langle \text{proof} \rangle$

lemma sees-method-fun :

$\llbracket P \vdash C \ \text{sees} \ M : TS \rightarrow T = m \ \text{in} \ D; \ P \vdash C \ \text{sees} \ M : TS' \rightarrow T' = m' \ \text{in} \ D' \rrbracket$
 $\Longrightarrow TS' = TS \wedge T' = T \wedge m' = m \wedge D' = D$
 $\langle \text{proof} \rangle$

lemma $\text{sees-method-decl-above}$:

$P \vdash C \ \text{sees} \ M : Ts \rightarrow T = m \ \text{in} \ D \Longrightarrow P \vdash C \preceq^* D$
 $\langle \text{proof} \rangle$

lemma $\text{visible-method-exists}$:

$P \vdash C \ \text{sees} \ M : Ts \rightarrow T = m \ \text{in} \ D \Longrightarrow$
 $\exists D' \ fs \ ms. \ \text{class } P \ D = \text{Some}(D', fs, ms) \wedge \text{map-of } ms \ M = \text{Some}(Ts, T, m) \langle \text{proof} \rangle$

lemma sees-method-idemp :

$P \vdash C \ \text{sees} \ M : Ts \rightarrow T = m \ \text{in} \ D \Longrightarrow P \vdash D \ \text{sees} \ M : Ts \rightarrow T = m \ \text{in} \ D$
 $\langle \text{proof} \rangle$

lemma $\text{sees-method-decl-mono}$:

$\llbracket P \vdash C' \preceq^* C; \ P \vdash C \ \text{sees} \ M : Ts \rightarrow T = m \ \text{in} \ D; \ P \vdash C' \ \text{sees} \ M : Ts' \rightarrow T' = m' \ \text{in} \ D' \rrbracket \Longrightarrow P \vdash D' \preceq^* D$
 $\langle \text{proof} \rangle$

lemma $\text{sees-method-is-class}$:

$P \vdash C \ \text{sees} \ M : Ts \rightarrow T = m \ \text{in} \ D \Longrightarrow \text{is-class } P \ C$
 $\langle \text{proof} \rangle$

3.3.4 Field lookup

inductive $\text{Fields} :: 'm \ \text{prog} \Rightarrow \text{cname} \Rightarrow ((\text{vname} \times \text{cname}) \times (\text{ty} \times \text{fmod})) \ \text{list} \Rightarrow \text{bool}$

$(\langle \cdot \vdash \cdot \text{has}'\text{-fields} \rightarrow [51,51,51] \ 50)$

for $P :: 'm \ \text{prog}$

where

has-fields-rec :

$\llbracket \text{class } P \ C = \text{Some}(D, fs, ms); \ C \neq \text{Object}; \ P \vdash D \ \text{has-fields} \ \text{FDTs}; \ \text{FDTs}' = \text{map} \ (\lambda(F, Tm). \ ((F, C), Tm)) \ fs \ @ \ \text{FDTs} \rrbracket$
 $\Longrightarrow P \vdash C \ \text{has-fields} \ \text{FDTs}'$

| has-fields-Object :

$\llbracket \text{class } P \ \text{Object} = \text{Some}(D, fs, ms); \ \text{FDTs} = \text{map} \ (\lambda(F, T). \ ((F, \text{Object}), T)) \ fs \rrbracket$
 $\Longrightarrow P \vdash \text{Object} \ \text{has-fields} \ \text{FDTs}$

lemma has-fields-fun :

assumes $P \vdash C \ \text{has-fields} \ \text{FDTs}$ **and** $P \vdash C \ \text{has-fields} \ \text{FDTs}'$

shows $\text{FDTs}' = \text{FDTs}$

$\langle \text{proof} \rangle$

lemma $\text{all-fields-in-has-fields}$:

assumes $P \vdash C$ *has-fields FDTs*
and $P \vdash C \preceq^* D$ *class* $P \ D = \text{Some}(D', fs, ms)$ $(F, Tm) \in \text{set } fs$
shows $((F, D), Tm) \in \text{set } FDTs$
 $\langle \text{proof} \rangle$

lemma *has-fields-decl-above*:
assumes $P \vdash C$ *has-fields FDTs* $((F, D), Tm) \in \text{set } FDTs$
shows $P \vdash C \preceq^* D$
 $\langle \text{proof} \rangle$

lemma *subcls-notin-has-fields*:
assumes $P \vdash C$ *has-fields FDTs* $((F, D), Tm) \in \text{set } FDTs$
shows $\neg (\text{subcls1 } P)^{++} D \ C$
 $\langle \text{proof} \rangle$

lemma *has-fields-mono-lem*:
assumes $P \vdash D \preceq^* C$ $P \vdash C$ *has-fields FDTs*
shows $\exists \text{pre. } P \vdash D$ *has-fields* $\text{pre}@FDTs \wedge \text{dom}(\text{map-of pre}) \cap \text{dom}(\text{map-of } FDTs) = \{\}$
 $\langle \text{proof} \rangle$

lemma *has-fields-is-class*:
 $P \vdash C$ *has-fields FDTs* \implies *is-class* $P \ C$
 $\langle \text{proof} \rangle$

lemma *Object-has-fields-Object*:
assumes $P \vdash \text{Object}$ *has-fields FDTs*
shows $\text{snd } 'fst \ ' \ \text{set } FDTs \subseteq \{\text{Object}\}$
 $\langle \text{proof} \rangle$

definition
 $\text{has-field} :: 'm \ \text{prog} \Rightarrow \text{cname} \Rightarrow \text{vname} \Rightarrow \text{ty} \Rightarrow \text{fmod} \Rightarrow \text{cname} \Rightarrow \text{bool}$
 $(\langle - \vdash - \text{has} \ - \ - \ '(-) \ \text{in} \ \rightarrow [51, 51, 51, 51, 51, 51] \ 50)$

where
 $P \vdash C$ *has* $F:T$ (fm) *in* $D \equiv$
 $\exists FDTs. P \vdash C$ *has-fields FDTs* $\wedge \text{map-of } FDTs (F, D) = \text{Some} (T, fm)$

lemma *has-field-mono*:
 $\llbracket P \vdash C$ *has* $F:T$ (fm) *in* $D; P \vdash C' \preceq^* C \rrbracket \implies P \vdash C'$ *has* $F:T$ (fm) *in* D
 $\langle \text{proof} \rangle$

lemma *has-field-is-class*:
 $P \vdash C$ *has* $M:T$ (fm) *in* $D \implies$ *is-class* $P \ C$
 $\langle \text{proof} \rangle$

lemma *has-field-decl-above*:
 $P \vdash C$ *has* $F:T$ (fm) *in* $D \implies P \vdash C \preceq^* D$
 $\langle \text{proof} \rangle$

lemma *has-field-fun*:
 $\llbracket P \vdash C$ *has* $F:T$ (fm) *in* $D; P \vdash C$ *has* $F:T'$ (fm') *in* $D \rrbracket \implies T' = T \wedge fm = fm'$
 $\langle \text{proof} \rangle$

definition
 $\text{sees-field} :: 'm \ \text{prog} \Rightarrow \text{cname} \Rightarrow \text{vname} \Rightarrow \text{ty} \Rightarrow \text{fmod} \Rightarrow \text{cname} \Rightarrow \text{bool}$

($\leftarrow \vdash - \text{sees } \text{-} \text{'(-) in } \rightarrow$ [51,51,51,51,51,51] 50)

where

$P \vdash C \text{ sees } F:T (fm) \text{ in } D \equiv$
 $\exists FDTs. P \vdash C \text{ has-fields } FDTs \wedge$
 $\text{map-of } (\text{map } (\lambda((F,D),Tm). (F,(D,Tm)))) FDTs F = \text{Some}(D,T,fm)$

lemma *map-of-remap-SomeD*:

$\text{map-of } (\text{map } (\lambda((k,k'),x). (k,(k',x))) t) k = \text{Some } (k',x) \implies \text{map-of } t (k, k') = \text{Some } x$
 $\langle \text{proof} \rangle$

lemma *has-visible-field*:

$P \vdash C \text{ sees } F:T (fm) \text{ in } D \implies P \vdash C \text{ has } F:T (fm) \text{ in } D$
 $\langle \text{proof} \rangle$

lemma *sees-field-fun*:

$\llbracket P \vdash C \text{ sees } F:T (fm) \text{ in } D; P \vdash C \text{ sees } F:T' (fm') \text{ in } D' \rrbracket \implies T' = T \wedge D' = D \wedge fm = fm'$
 $\langle \text{proof} \rangle$

lemma *sees-field-decl-above*:

$P \vdash C \text{ sees } F:T (fm) \text{ in } D \implies P \vdash C \preceq^* D$
 $\langle \text{proof} \rangle$

lemma *sees-field-idemp*:

assumes $P \vdash C \text{ sees } F:T (fm) \text{ in } D$
shows $P \vdash D \text{ sees } F:T (fm) \text{ in } D$
 $\langle \text{proof} \rangle$

3.3.5 Functional lookup

definition *method* $:: 'm \text{ prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{cname} \times \text{ty list} \times \text{ty} \times 'm \text{ option}$

where *method* $P C M \equiv \text{THE } (D, Ts, T, m). P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D$

definition *field* $:: 'm \text{ prog} \Rightarrow \text{cname} \Rightarrow \text{vname} \Rightarrow \text{cname} \times \text{ty} \times \text{fmod}$

where *field* $P C F \equiv \text{THE } (D, T, fm). P \vdash C \text{ sees } F:T (fm) \text{ in } D$

definition *fields* $:: 'm \text{ prog} \Rightarrow \text{cname} \Rightarrow ((\text{vname} \times \text{cname}) \times (\text{ty} \times \text{fmod})) \text{ list}$

where *fields* $P C \equiv \text{THE } FDTs. P \vdash C \text{ has-fields } FDTs$

lemma [simp]: $P \vdash C \text{ has-fields } FDTs \implies \text{fields } P C = FDTs \langle \text{proof} \rangle$

lemma *field-def2* [simp]: $P \vdash C \text{ sees } F:T (fm) \text{ in } D \implies \text{field } P C F = (D, T, fm) \langle \text{proof} \rangle$

lemma *method-def2* [simp]: $P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \implies \text{method } P C M = (D, Ts, T, m) \langle \text{proof} \rangle$

lemma *has-fields-b-fields*:

$P \vdash C \text{ has-fields } FDTs \implies \text{fields } P C = FDTs$
 $\langle \text{proof} \rangle$

lemma *has-field-map-of-fields* [simp]:

$P \vdash C \text{ has } F:T (fm) \text{ in } D \implies \text{map-of } (\text{fields } P C) (F, D) = \llbracket (T, fm) \rrbracket$
 $\langle \text{proof} \rangle$

3.3.6 Code generation

New introduction rules for subcls1

code-pred

— Disallow mode *i-o-o* to force *code-pred* in subsequent predicates not to use this inefficient mode
 (modes: $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$, $i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$)
subcls1
 ⟨proof⟩

Introduce proper constant *subcls'* for *subcls* and generate executable equation for *subcls'*
definition *subcls'* **where** *subcls' = subcls*

code-pred
 (modes: $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$, $i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$)
 [inductify]
subcls'
 ⟨proof⟩

lemma *subcls-conv-subcls'* [code-unfold]:
 (*subcls1 P*)^{^**} = *subcls' P*
 ⟨proof⟩

Change rule $?P \vdash ?A[] \leq \text{Class Object}$ such that predicate compiler tests on class *Object* first. Otherwise *widen-i-o-i* never terminates.

lemma *widen-array-object-code*:
 $C = \text{Object} \Longrightarrow P \vdash \text{Array } A \leq \text{Class } C$
 ⟨proof⟩

lemmas [code-pred-intro] =
widen-refl widen-subcls widen-null widen-null-array widen-array-object-code widen-array-array
code-pred
 (modes: $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$)
widen
 ⟨proof⟩

Readjust the code equations for *widen* such that *widen-i-i-i* is guaranteed to contain () at most once (even in the code representation!). This is important for the scheduler and the small-step semantics because of the weaker code equations for *the*.

A similar problem cannot hit the subclass relation because, for acyclic subclass hierarchies, the paths in the hierarchy are unique and cycle-free.

definition *widen-i-i-i'* **where** *widen-i-i-i' = widen-i-i-i*

declare *widen.equation* [code del]
lemmas *widen-i-i-i'-equation* [code] = *widen.equation*[folded *widen-i-i-i'-def*]

lemma *widen-i-i-i-code* [code]:
 $\text{widen-i-i-i } P \ T \ T' = (\text{if } P \vdash T \leq T' \text{ then } \text{Predicate.single } () \text{ else bot})$
 ⟨proof⟩

code-pred
 (modes: $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$, $i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$)
Methods
 ⟨proof⟩

code-pred
 (modes: $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$, $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow o \Rightarrow i \Rightarrow \text{bool}$)
 [inductify]

Method
 ⟨proof⟩

code-pred
 (*modes*: $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$)
 [*inductify*]
has-method
 ⟨proof⟩

declare *fun-upd-def* [*code-pred-inline*]

code-pred
 (*modes*: $i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$)
Fields
 ⟨proof⟩

code-pred
 (*modes*: $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow i \Rightarrow \text{bool}$)
 [*inductify*, *skip-proof*]
has-field
 ⟨proof⟩

code-pred
 (*modes*: $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$, $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow i \Rightarrow \text{bool}$)
 [*inductify*, *skip-proof*]
sees-field
 ⟨proof⟩

lemma *eval-Method-i-i-i-o-o-o-o-conv*:
Predicate.eval (*Method-i-i-i-o-o-o-o* $P C M$) = $(\lambda(Ts, T, m, D). P \vdash C \text{ sees } M:T \rightarrow T=m \text{ in } D)$
 ⟨proof⟩

lemma *method-code* [*code*]:
method $P C M =$
Predicate.the (*Predicate.bind* (*Method-i-i-i-o-o-o-o* $P C M$) $(\lambda(Ts, T, m, D). \text{Predicate.single } (D, Ts, T, m))$)
 ⟨proof⟩

lemma *eval-sees-field-i-i-i-o-o-o-conv*:
Predicate.eval (*sees-field-i-i-i-o-o-o* $P C F$) = $(\lambda(T, fm, D). P \vdash C \text{ sees } F:T (fm) \text{ in } D)$
 ⟨proof⟩

lemma *eval-sees-field-i-i-i-o-i-conv*:
Predicate.eval (*sees-field-i-i-i-o-o-i* $P C F D$) = $(\lambda(T, fm). P \vdash C \text{ sees } F:T (fm) \text{ in } D)$
 ⟨proof⟩

lemma *field-code* [*code*]:
field $P C F = \text{Predicate.the } (\text{Predicate.bind } (\text{sees-field-i-i-i-o-o-o} P C F) (\lambda(T, fm, D). \text{Predicate.single } (D, T, fm)))$
 ⟨proof⟩

lemma *eval-Fields-conv*:
Predicate.eval (*Fields-i-i-o* $P C$) = $(\lambda FDTs. P \vdash C \text{ has-fields } FDTs)$

<proof>

lemma *fields-code* [*code*]:

fields P C = Predicate.the (Fields-i-i-o P C)

<proof>

code-identifier

code-module *TypeRel* \rightarrow

(*SML*) *TypeRel* **and** (*Haskell*) *TypeRel* **and** (*OCaml*) *TypeRel*

| **code-module** *Decl* \rightarrow

(*SML*) *TypeRel* **and** (*Haskell*) *TypeRel* **and** (*OCaml*) *TypeRel*

end

3.4 Jinja Values

theory *Value*

imports

TypeRel

HOL-Library.Word

begin

unbundle *no floor-ceiling-syntax*

type-synonym *word32 = 32 word*

datatype *'addr val*

= *Unit* — dummy result value of void expressions

| *Null* — null reference

| *Bool bool* — Boolean value

| *Intg word32* — integer value

| *Addr 'addr* — addresses of objects, arrays and threads in the heap

primrec *default-val :: ty \Rightarrow 'addr val* — default value for all types

where

default-val Void = Unit

| *default-val Boolean = Bool False*

| *default-val Integer = Intg 0*

| *default-val NT = Null*

| *default-val (Class C) = Null*

| *default-val (Array A) = Null*

lemma *default-val-not-Addr: default-val T \neq Addr a*

<proof>

lemma *Addr-not-default-val: Addr a \neq default-val T*

<proof>

primrec *the-Intg :: 'addr val \Rightarrow word32*

where

the-Intg (Intg i) = i

primrec *the-Addr :: 'addr val \Rightarrow 'addr*

where

the-Addr (*Addr a*) = *a*

fun *is-Addr* :: 'addr val \Rightarrow bool

where

is-Addr (*Addr a*) = *True*

| *is-Addr* - = *False*

lemma *is-AddrE* [*elim!*]:

$\llbracket \text{is-Addr } v; \bigwedge a. v = \text{Addr } a \implies \text{thesis} \rrbracket \implies \text{thesis}$
 <proof>

fun *is-Intg* :: 'addr val \Rightarrow bool

where

is-Intg (*Intg i*) = *True*

| *is-Intg* - = *False*

lemma *is-IntgE* [*elim!*]:

$\llbracket \text{is-Intg } v; \bigwedge i. v = \text{Intg } i \implies \text{thesis} \rrbracket \implies \text{thesis}$
 <proof>

fun *is-Bool* :: 'addr val \Rightarrow bool

where

is-Bool (*Bool b*) = *True*

| *is-Bool* - = *False*

lemma *is-BoolE* [*elim!*]:

$\llbracket \text{is-Bool } v; \bigwedge a. v = \text{Bool } a \implies \text{thesis} \rrbracket \implies \text{thesis}$
 <proof>

definition *is-Ref* :: 'addr val \Rightarrow bool

where *is-Ref* *v* \equiv *v* = *Null* \vee *is-Addr* *v*

lemma *is-Ref-def2*:

is-Ref *v* = (*v* = *Null* \vee ($\exists a. v = \text{Addr } a$))

<proof>

lemma [*iff*]: *is-Ref* *Null* <proof>

definition *undefined-value* :: 'addr val **where** *undefined-value* = *Unit*

lemma *undefined-value-not-Addr*:

undefined-value \neq *Addr a* *Addr a* \neq *undefined-value*

<proof>

class *addr* =

fixes *hash-addr* :: 'a \Rightarrow int

and *monitor-funfun-to-list* :: ('a \Rightarrow f nat) \Rightarrow 'a list

assumes *set* (*monitor-funfun-to-list* *f*) = *Collect* ($\$$) (*funfun-dom* *f*)

locale *addr-base* =

fixes *addr2thread-id* :: 'addr \Rightarrow 'thread-id

and *thread-id2addr* :: 'thread-id \Rightarrow 'addr

end

3.5 Exceptions

theory *Exceptions*

imports

Value

begin

definition *NullPointer* :: *cname*

where [*code-unfold*]: *NullPointer* = *STR* "java/lang/NullPointerException"

definition *ClassCast* :: *cname*

where [*code-unfold*]: *ClassCast* = *STR* "java/lang/ClassCastException"

definition *OutOfMemory* :: *cname*

where [*code-unfold*]: *OutOfMemory* = *STR* "java/lang/OutOfMemoryError"

definition *ArrayIndexOutOfBounds* :: *cname*

where [*code-unfold*]: *ArrayIndexOutOfBounds* = *STR* "java/lang/ArrayIndexOutOfBoundsException"

definition *ArrayStore* :: *cname*

where [*code-unfold*]: *ArrayStore* = *STR* "java/lang/ArrayStoreException"

definition *NegativeArraySize* :: *cname*

where [*code-unfold*]: *NegativeArraySize* = *STR* "java/lang/NegativeArraySizeException"

definition *ArithmeticException* :: *cname*

where [*code-unfold*]: *ArithmeticException* = *STR* "java/lang/ArithmeticException"

definition *IllegalMonitorState* :: *cname*

where [*code-unfold*]: *IllegalMonitorState* = *STR* "java/lang/IllegalMonitorStateException"

definition *IllegalThreadState* :: *cname*

where [*code-unfold*]: *IllegalThreadState* = *STR* "java/lang/IllegalThreadStateException"

definition *InterruptedException* :: *cname*

where [*code-unfold*]: *InterruptedException* = *STR* "java/lang/InterruptedException"

definition *sys-xcpts-list* :: *cname list*

where

sys-xcpts-list =

[*NullPointer*, *ClassCast*, *OutOfMemory*, *ArrayIndexOutOfBounds*, *ArrayStore*, *NegativeArraySize*, *ArithmeticException*,

IllegalMonitorState, *IllegalThreadState*, *InterruptedException*]

definition *sys-xcpts* :: *cname set*

where [*code-unfold*]: *sys-xcpts* = *set sys-xcpts-list*

definition *wf-syscls* :: '*m prog* ⇒ *bool*

where *wf-syscls* *P* ≡ (∀ *C* ∈ {*Object*, *Throwable*, *Thread*}. *is-class* *P* *C*) ∧ (∀ *C* ∈ *sys-xcpts*. *P* ⊢ *C* ≤* *Throwable*)

3.5.1 System exceptions

lemma [simp]:

$NullPointerException \in sys\text{-}xcpts \wedge$
 $OutOfMemory \in sys\text{-}xcpts \wedge$
 $ClassCast \in sys\text{-}xcpts \wedge$
 $ArrayIndexOutOfBounds \in sys\text{-}xcpts \wedge$
 $ArrayStore \in sys\text{-}xcpts \wedge$
 $NegativeArraySize \in sys\text{-}xcpts \wedge$
 $IllegalMonitorState \in sys\text{-}xcpts \wedge$
 $IllegalThreadState \in sys\text{-}xcpts \wedge$
 $InterruptedException \in sys\text{-}xcpts \wedge$
 $ArithmeticException \in sys\text{-}xcpts$

$\langle proof \rangle$

lemma *sys-xcpts-cases* [consumes 1, cases set]:

$\llbracket C \in sys\text{-}xcpts; P \text{ } NullPointerException; P \text{ } OutOfMemory; P \text{ } ClassCast;$
 $P \text{ } ArrayIndexOutOfBounds; P \text{ } ArrayStore; P \text{ } NegativeArraySize;$
 $P \text{ } ArithmeticException;$
 $P \text{ } IllegalMonitorState; P \text{ } IllegalThreadState; P \text{ } InterruptedException \rrbracket$

$\implies P \ C$

$\langle proof \rangle$

lemma *OutOfMemory-not-Object*[simp]: $OutOfMemory \neq Object$

$\langle proof \rangle$

lemma *ClassCast-not-Object*[simp]: $ClassCast \neq Object$

$\langle proof \rangle$

lemma *NullPointerException-not-Object*[simp]: $NullPointerException \neq Object$

$\langle proof \rangle$

lemma *ArrayIndexOutOfBounds-not-Object*[simp]: $ArrayIndexOutOfBounds \neq Object$

$\langle proof \rangle$

lemma *ArrayStore-not-Object*[simp]: $ArrayStore \neq Object$

$\langle proof \rangle$

lemma *NegativeArraySize-not-Object*[simp]: $NegativeArraySize \neq Object$

$\langle proof \rangle$

lemma *ArithmeticException-not-Object*[simp]: $ArithmeticException \neq Object$

$\langle proof \rangle$

lemma *IllegalMonitorState-not-Object*[simp]: $IllegalMonitorState \neq Object$

$\langle proof \rangle$

lemma *IllegalThreadState-not-Object*[simp]: $IllegalThreadState \neq Object$

$\langle proof \rangle$

lemma *InterruptedException-not-Object*[simp]: $InterruptedException \neq Object$

$\langle proof \rangle$

lemma *sys-xcpts-neqs-aux*:

NullPointerException \neq *ClassCast* *NullPointerException* \neq *OutOfMemory* *NullPointerException* \neq *ArrayIndexOutOfBounds*
NullPointerException \neq *ArrayStore* *NullPointerException* \neq *NegativeArraySize* *NullPointerException* \neq *IllegalMonitorState*
NullPointerException \neq *IllegalThreadState* *NullPointerException* \neq *InterruptedException* *NullPointerException* \neq *ArithmeticException*
ClassCast \neq *OutOfMemory* *ClassCast* \neq *ArrayIndexOutOfBounds*
ClassCast \neq *ArrayStore* *ClassCast* \neq *NegativeArraySize* *ClassCast* \neq *IllegalMonitorState*
ClassCast \neq *IllegalThreadState* *ClassCast* \neq *InterruptedException* *ClassCast* \neq *ArithmeticException*
OutOfMemory \neq *ArrayIndexOutOfBounds*
OutOfMemory \neq *ArrayStore* *OutOfMemory* \neq *NegativeArraySize* *OutOfMemory* \neq *IllegalMonitorState*
OutOfMemory \neq *IllegalThreadState* *OutOfMemory* \neq *InterruptedException*
OutOfMemory \neq *ArithmeticException*
ArrayIndexOutOfBounds \neq *ArrayStore* *ArrayIndexOutOfBounds* \neq *NegativeArraySize* *ArrayIndexOutOfBounds* \neq *IllegalMonitorState*
ArrayIndexOutOfBounds \neq *IllegalThreadState* *ArrayIndexOutOfBounds* \neq *InterruptedException* *ArrayIndexOutOfBounds* \neq *ArithmeticException*
ArrayStore \neq *NegativeArraySize* *ArrayStore* \neq *IllegalMonitorState*
ArrayStore \neq *IllegalThreadState* *ArrayStore* \neq *InterruptedException*
ArrayStore \neq *ArithmeticException*
NegativeArraySize \neq *IllegalMonitorState*
NegativeArraySize \neq *IllegalThreadState* *NegativeArraySize* \neq *InterruptedException*
NegativeArraySize \neq *ArithmeticException*
IllegalMonitorState \neq *IllegalThreadState* *IllegalMonitorState* \neq *InterruptedException*
IllegalMonitorState \neq *ArithmeticException*
IllegalThreadState \neq *InterruptedException*
IllegalThreadState \neq *ArithmeticException*
InterruptedException \neq *ArithmeticException*
⟨proof⟩

lemmas *sys-xcpts-neqs* = *sys-xcpts-neqs-aux* *sys-xcpts-neqs-aux*[*symmetric*]

lemma *Thread-neq-sys-xcpts-aux*:

Thread \neq *NullPointerException*
Thread \neq *ClassCast*
Thread \neq *OutOfMemory*
Thread \neq *ArrayIndexOutOfBounds*
Thread \neq *ArrayStore*
Thread \neq *NegativeArraySize*
Thread \neq *ArithmeticException*
Thread \neq *IllegalMonitorState*
Thread \neq *IllegalThreadState*
Thread \neq *InterruptedException*

⟨proof⟩

lemmas *Thread-neq-sys-xcpts* = *Thread-neq-sys-xcpts-aux* *Thread-neq-sys-xcpts-aux*[*symmetric*]

3.5.2 Well-formedness for system classes and exceptions

lemma

assumes *wf-syscls* *P*

shows *wf-syscls-class-Object*: $\exists C fs ms. \text{class } P \text{ Object} = \text{Some } (C, fs, ms)$

and *wf-syscls-class-Thread*: $\exists C fs ms. \text{class } P \text{ Thread} = \text{Some } (C, fs, ms)$

⟨proof⟩

```

lemma [simp]:
  assumes wf-syscls P
  shows wf-syscls-is-class-Object: is-class P Object
  and wf-syscls-is-class-Thread: is-class P Thread
  ⟨proof⟩

lemma wf-syscls-xcpt-subcls-Throwable:
   $\llbracket C \in \text{sys-xcpts}; \text{wf-syscls } P \rrbracket \implies P \vdash C \preceq^* \text{Throwable}$ 
  ⟨proof⟩

lemma wf-syscls-is-class-Throwable:
   $\text{wf-syscls } P \implies \text{is-class } P \text{ Throwable}$ 
  ⟨proof⟩

lemma wf-syscls-is-class-sub-Throwable:
   $\llbracket \text{wf-syscls } P; P \vdash C \preceq^* \text{Throwable} \rrbracket \implies \text{is-class } P C$ 
  ⟨proof⟩

lemma wf-syscls-is-class-xcpt:
   $\llbracket C \in \text{sys-xcpts}; \text{wf-syscls } P \rrbracket \implies \text{is-class } P C$ 
  ⟨proof⟩

lemma wf-syscls-code [code]:
   $\text{wf-syscls } P \longleftrightarrow$ 
   $(\forall C \in \text{set } [\text{Object}, \text{Throwable}, \text{Thread}]. \text{is-class } P C) \wedge (\forall C \in \text{sys-xcpts}. P \vdash C \preceq^* \text{Throwable})$ 
  ⟨proof⟩

end

```

3.6 System Classes

```
theory SystemClasses
```

```
imports
```

```
  Exceptions
```

```
begin
```

This theory provides definitions for the *Object* class, and the system exceptions.

Inline SystemClasses definition because they are polymorphic values that violate ML's value restriction.

Object has actually superclass, but we set it to the empty string for code generation. Any other class name (like *undefined*) would do as well except for code generation.

```
definition ObjectC :: 'm cdecl
```

```
where [code-unfold]:
```

```

  ObjectC =
  (Object, (STR "", []),
   [(wait, [], Void, Native),
    (notify, [], Void, Native),
    (notifyAll, [], Void, Native),
    (hashCode, [], Integer, Native),
    (clone, [], Class Object, Native),
    (print, [Integer], Void, Native),
    (currentThread, [], Class Thread, Native),
    (interrupted, [], Boolean, Native),

```

```
(yield,[],Void,Native)
))
```

definition *ThrowableC* :: 'm cdecl
where [code-unfold]: *ThrowableC* ≡ (*Throwable*, (*Object*, [], []))

definition *NullPointerC* :: 'm cdecl
where [code-unfold]: *NullPointerC* ≡ (*NullPointer*, (*Throwable*, [], []))

definition *ClassCastC* :: 'm cdecl
where [code-unfold]: *ClassCastC* ≡ (*ClassCast*, (*Throwable*, [], []))

definition *OutOfMemoryC* :: 'm cdecl
where [code-unfold]: *OutOfMemoryC* ≡ (*OutOfMemory*, (*Throwable*, [], []))

definition *ArrayIndexOutOfBoundsC* :: 'm cdecl
where [code-unfold]: *ArrayIndexOutOfBoundsC* ≡ (*ArrayIndexOutOfBounds*, (*Throwable*, [], []))

definition *ArrayStoreC* :: 'm cdecl
where [code-unfold]: *ArrayStoreC* ≡ (*ArrayStore*, (*Throwable*, [], []))

definition *NegativeArraySizeC* :: 'm cdecl
where [code-unfold]: *NegativeArraySizeC* ≡ (*NegativeArraySize*, (*Throwable*, [], []))

definition *ArithmeticExceptionC* :: 'm cdecl
where [code-unfold]: *ArithmeticExceptionC* ≡ (*ArithmeticException*, (*Throwable*, [], []))

definition *IllegalMonitorStateC* :: 'm cdecl
where [code-unfold]: *IllegalMonitorStateC* ≡ (*IllegalMonitorState*, (*Throwable*, [], []))

definition *IllegalThreadStateC* :: 'm cdecl
where [code-unfold]: *IllegalThreadStateC* ≡ (*IllegalThreadState*, (*Throwable*, [], []))

definition *InterruptedExceptionC* :: 'm cdecl
where [code-unfold]: *InterruptedExceptionC* ≡ (*InterruptedException*, (*Throwable*, [], []))

definition *SystemClasses* :: 'm cdecl list
where [code-unfold]:
SystemClasses ≡
[*ObjectC*, *ThrowableC*, *NullPointerC*, *ClassCastC*, *OutOfMemoryC*,
ArrayIndexOutOfBoundsC, *ArrayStoreC*, *NegativeArraySizeC*,
ArithmeticExceptionC,
IllegalMonitorStateC, *IllegalThreadStateC*, *InterruptedExceptionC*]

end

3.7 An abstract heap model

```
theory Heap
imports
  Value
begin
```



```

primrec typeof :: 'addr val  $\rightarrow$  ty
where
  typeof Unit    = Some Void
| typeof Null    = Some NT
| typeof (Bool b) = Some Boolean
| typeof (Intg i) = Some Integer
| typeof (Addr a) = None

datatype addr-loc =
  CField cname vname
| ACell nat

lemma rec-addr-loc [simp]: rec-addr-loc = case-addr-loc
<proof>

primrec is-volatile :: 'm prog  $\Rightarrow$  addr-loc  $\Rightarrow$  bool
where
  is-volatile P (ACell n) = False
| is-volatile P (CField D F) = volatile (snd (snd (field P D F)))

locale heap-base =
  addr-base addr2thread-id thread-id2addr
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
  +
fixes spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
begin

fun typeof-h :: 'heap  $\Rightarrow$  'addr val  $\Rightarrow$  ty option (<typeof->)
where
  typeof_h (Addr a) = map-option ty-of-htype (typeof-addr h a)
| typeof_h v = typeof v

definition cname-of :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  cname
where cname-of h a = the-Class (ty-of-htype (the (typeof-addr h a)))

definition hext :: 'heap  $\Rightarrow$  'heap  $\Rightarrow$  bool (<-  $\triangleleft$  -> [51,51] 50)
where
  h  $\triangleleft$  h'  $\equiv$  typeof-addr h  $\subseteq_m$  typeof-addr h'

context
  notes [[inductive-internals]]
begin

inductive addr-loc-type :: 'm prog  $\Rightarrow$  'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  ty  $\Rightarrow$  bool
  (<-,-  $\vdash$  -@- : -> [50, 50, 50, 50, 50] 51)
for P :: 'm prog and h :: 'heap and a :: 'addr
where
  addr-loc-type-field:

```

$\llbracket \text{typeof-addr } h \ a = \lfloor U \rfloor; P \vdash \text{class-type-of } U \text{ has } F:T \text{ (fm) in } D \rrbracket$
 $\implies P, h \vdash a@CField \ D \ F : T$

| *addr-loc-type-cell*:
 $\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n' \rfloor; n < n' \rrbracket$
 $\implies P, h \vdash a@ACell \ n : T$

end

definition *typeof-addr-loc* :: 'm prog \Rightarrow 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow ty
where *typeof-addr-loc* $P \ h \ a \ al = (\text{THE } T. P, h \vdash a@al : T)$

definition *deterministic-heap-ops* :: bool

where

deterministic-heap-ops \longleftrightarrow
 $(\forall h \ ad \ al \ v \ v'. \text{heap-read } h \ ad \ al \ v \longrightarrow \text{heap-read } h \ ad \ al \ v' \longrightarrow v = v') \wedge$
 $(\forall h \ ad \ al \ v \ h' \ h''. \text{heap-write } h \ ad \ al \ v \ h' \longrightarrow \text{heap-write } h \ ad \ al \ v \ h'' \longrightarrow h' = h'') \wedge$
 $(\forall h \ hT \ h' \ a \ h'' \ a'. (h', a) \in \text{allocate } h \ hT \longrightarrow (h'', a') \in \text{allocate } h \ hT \longrightarrow h' = h'' \wedge a = a') \wedge$
 $\neg \text{spurious-wakeups}$

end

lemma *typeof-lit-eq-Boolean* [*simp*]: $(\text{typeof } v = \text{Some Boolean}) = (\exists b. v = \text{Bool } b)$
 $\langle \text{proof} \rangle$

lemma *typeof-lit-eq-Integer* [*simp*]: $(\text{typeof } v = \text{Some Integer}) = (\exists i. v = \text{Intg } i)$
 $\langle \text{proof} \rangle$

lemma *typeof-lit-eq-NT* [*simp*]: $(\text{typeof } v = \text{Some NT}) = (v = \text{Null})$
 $\langle \text{proof} \rangle$

lemma *typeof-lit-eq-Void* [*simp*]: $\text{typeof } v = \text{Some Void} \longleftrightarrow v = \text{Unit}$
 $\langle \text{proof} \rangle$

lemma *typeof-lit-neq-Class* [*simp*]: $\text{typeof } v \neq \text{Some } (\text{Class } C)$
 $\langle \text{proof} \rangle$

lemma *typeof-lit-neq-Array* [*simp*]: $\text{typeof } v \neq \text{Some } (\text{Array } T)$
 $\langle \text{proof} \rangle$

lemma *typeof-NoneD* [*simp, dest*]:
 $\text{typeof } v = \text{Some } x \implies \neg \text{is-Addr } v$
 $\langle \text{proof} \rangle$

lemma *typeof-lit-is-type*:
 $\text{typeof } v = \text{Some } T \implies \text{is-type } P \ T$
 $\langle \text{proof} \rangle$

context *heap-base* **begin**

lemma *typeof-h-eq-Boolean* [*simp*]: $(\text{typeof}_h \ v = \text{Some Boolean}) = (\exists b. v = \text{Bool } b)$
 $\langle \text{proof} \rangle$

lemma *typeof-h-eq-Integer* [*simp*]: $(\text{typeof}_h \ v = \text{Some Integer}) = (\exists i. v = \text{Intg } i)$

$\langle \text{proof} \rangle$

lemma *typeof-h-eq-NT* [simp]: $(\text{typeof}_h v = \text{Some } NT) = (v = \text{Null})$

$\langle \text{proof} \rangle$

lemma *hextI*:

$\llbracket \bigwedge a C. \text{typeof-addr } h a = \llbracket \text{Class-type } C \rrbracket \implies \text{typeof-addr } h' a = \llbracket \text{Class-type } C \rrbracket;$
 $\bigwedge a T n. \text{typeof-addr } h a = \llbracket \text{Array-type } T n \rrbracket \implies \text{typeof-addr } h' a = \llbracket \text{Array-type } T n \rrbracket \rrbracket$
 $\implies h \leq h'$

$\langle \text{proof} \rangle$

lemma *hext-objD*:

assumes $h \leq h'$

and $\text{typeof-addr } h a = \llbracket \text{Class-type } C \rrbracket$

shows $\text{typeof-addr } h' a = \llbracket \text{Class-type } C \rrbracket$

$\langle \text{proof} \rangle$

lemma *hext-arrD*:

assumes $h \leq h'$ $\text{typeof-addr } h a = \llbracket \text{Array-type } T n \rrbracket$

shows $\text{typeof-addr } h' a = \llbracket \text{Array-type } T n \rrbracket$

$\langle \text{proof} \rangle$

lemma *hext-refl* [iff]: $h \leq h$

$\langle \text{proof} \rangle$

lemma *hext-trans* [trans]: $\llbracket h \leq h'; h' \leq h'' \rrbracket \implies h \leq h''$

$\langle \text{proof} \rangle$

lemma *typeof-lit-typeof*:

$\text{typeof } v = \llbracket T \rrbracket \implies \text{typeof}_h v = \llbracket T \rrbracket$

$\langle \text{proof} \rangle$

lemma *addr-loc-type-fun*:

$\llbracket P, h \vdash a @ al : T; P, h \vdash a @ al : T' \rrbracket \implies T = T'$

$\langle \text{proof} \rangle$

lemma *THE-addr-loc-type*:

$P, h \vdash a @ al : T \implies (\text{THE } T. P, h \vdash a @ al : T) = T$

$\langle \text{proof} \rangle$

lemma *typeof-addr-locI* [simp]:

$P, h \vdash a @ al : T \implies \text{typeof-addr-loc } P h a al = T$

$\langle \text{proof} \rangle$

lemma *deterministic-heap-opsI*:

$\llbracket \bigwedge h ad al v v'. \llbracket \text{heap-read } h ad al v; \text{heap-read } h ad al v' \rrbracket \implies v = v';$
 $\bigwedge h ad al v h' h''. \llbracket \text{heap-write } h ad al v h'; \text{heap-write } h ad al v h'' \rrbracket \implies h' = h'';$
 $\bigwedge h hT h' a h'' a'. \llbracket (h', a) \in \text{allocate } h hT; (h'', a') \in \text{allocate } h hT \rrbracket \implies h' = h'' \wedge a = a';$
 $\neg \text{spurious-wakeups} \rrbracket$

$\implies \text{deterministic-heap-ops}$

$\langle \text{proof} \rangle$

lemma *deterministic-heap-ops-readD*:

$\llbracket \text{deterministic-heap-ops}; \text{heap-read } h \text{ ad al } v; \text{heap-read } h \text{ ad al } v' \rrbracket \Longrightarrow v = v'$
 ⟨proof⟩

lemma *deterministic-heap-ops-writeD*:

$\llbracket \text{deterministic-heap-ops}; \text{heap-write } h \text{ ad al } v \text{ h}'; \text{heap-write } h \text{ ad al } v \text{ h}'' \rrbracket \Longrightarrow h' = h''$
 ⟨proof⟩

lemma *deterministic-heap-ops-allocateD*:

$\llbracket \text{deterministic-heap-ops}; (h', a) \in \text{allocate } h \text{ hT}; (h'', a') \in \text{allocate } h \text{ hT} \rrbracket \Longrightarrow h' = h'' \wedge a = a'$
 ⟨proof⟩

lemma *deterministic-heap-ops-no-spurious-wakeups*:

$\text{deterministic-heap-ops} \Longrightarrow \neg \text{spurious-wakeups}$
 ⟨proof⟩

end

locale *addr-conv* =

heap-base

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate typeof-addr heap-read heap-write

+

prog P

for *addr2thread-id* :: ('addr :: addr) \Rightarrow 'thread-id

and *thread-id2addr* :: 'thread-id \Rightarrow 'addr

and *spurious-wakeups* :: bool

and *empty-heap* :: 'heap

and *allocate* :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) set

and *typeof-addr* :: 'heap \Rightarrow 'addr \rightarrow htype

and *heap-read* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow bool

and *heap-write* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow 'heap \Rightarrow bool

and *P* :: 'm prog

+

assumes *addr2thread-id-inverse*:

$\llbracket \text{typeof-addr } h \text{ a} = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket \Longrightarrow \text{thread-id2addr } (\text{addr2thread-id } a) = a$

begin

lemma *typeof-addr-thread-id2-addr-addr2thread-id [simp]*:

$\llbracket \text{typeof-addr } h \text{ a} = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket \Longrightarrow \text{typeof-addr } h (\text{thread-id2addr } (\text{addr2thread-id } a)) = \lfloor \text{Class-type } C \rfloor$

⟨proof⟩

end

locale *heap* =

addr-conv

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate typeof-addr heap-read heap-write

P

for *addr2thread-id* :: ('addr :: addr) \Rightarrow 'thread-id

and *thread-id2addr* :: 'thread-id \Rightarrow 'addr

and *spurious-wakeups* :: bool

and *empty-heap* :: 'heap
and *allocate* :: 'heap \Rightarrow *h*type \Rightarrow ('heap \times 'addr) set
and *typeof-addr* :: 'heap \Rightarrow 'addr \rightarrow *h*type
and *heap-read* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow bool
and *heap-write* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow 'heap \Rightarrow bool
and *P* :: 'm prog
 +
assumes *allocate-SomeD*: $\llbracket (h', a) \in \text{allocate } h \text{ } hT; \text{is-htype } P \text{ } hT \rrbracket \Longrightarrow \text{typeof-addr } h' \text{ } a = \text{Some } hT$

and *hext-allocate*: $\bigwedge a. (h', a) \in \text{allocate } h \text{ } hT \Longrightarrow h \trianglelefteq h'$

and *hext-heap-write*:
heap-write *h* *a* *al* *v* *h'* $\Longrightarrow h \trianglelefteq h'$

begin

lemmas *hext-heap-ops* = *hext-allocate* *hext-heap-write*

lemma *typeof-addr-hext-mono*:

$\llbracket h \trianglelefteq h'; \text{typeof-addr } h \text{ } a = [hT] \rrbracket \Longrightarrow \text{typeof-addr } h' \text{ } a = [hT]$
 $\langle \text{proof} \rangle$

lemma *hext-typeof-mono*:

$\llbracket h \trianglelefteq h'; \text{typeof}_h v = \text{Some } T \rrbracket \Longrightarrow \text{typeof}_{h'} v = \text{Some } T$
 $\langle \text{proof} \rangle$

lemma *addr-loc-type-hext-mono*:

$\llbracket P, h \vdash a@al : T; h \trianglelefteq h' \rrbracket \Longrightarrow P, h' \vdash a@al : T$
 $\langle \text{proof} \rangle$

lemma *type-of-hext-type-of*: — FIXME: What's this rule good for?

$\llbracket \text{typeof}_h w = [T]; \text{hext } h \text{ } h' \rrbracket \Longrightarrow \text{typeof}_{h'} w = [T]$
 $\langle \text{proof} \rangle$

lemma *hext-None*: $\llbracket h \trianglelefteq h'; \text{typeof-addr } h' \text{ } a = \text{None} \rrbracket \Longrightarrow \text{typeof-addr } h \text{ } a = \text{None}$

$\langle \text{proof} \rangle$

lemma *map-typeof-hext-mono*:

$\llbracket \text{map } \text{typeof}_h \text{ } vs = \text{map } \text{Some } Ts; h \trianglelefteq h' \rrbracket \Longrightarrow \text{map } \text{typeof}_{h'} \text{ } vs = \text{map } \text{Some } Ts$
 $\langle \text{proof} \rangle$

lemma *hext-typeof-addr-map-le*:

$h \trianglelefteq h' \Longrightarrow \text{typeof-addr } h \subseteq_m \text{typeof-addr } h'$
 $\langle \text{proof} \rangle$

lemma *hext-dom-typeof-addr-subset*:

$h \trianglelefteq h' \Longrightarrow \text{dom } (\text{typeof-addr } h) \subseteq \text{dom } (\text{typeof-addr } h')$
 $\langle \text{proof} \rangle$

end

declare *heap-base.typeof-h.simps* [code]

declare *heap-base.cname-of-def* [code]

end

3.8 Observable events in JinjaThreads

theory *Observable-Events*

imports

Heap

../Framework/FWState

begin

datatype (*'addr, 'thread-id*) *obs-event* =
 | *ExternalCall 'addr mname 'addr val list 'addr val*
 | *ReadMem 'addr addr-loc 'addr val*
 | *WriteMem 'addr addr-loc 'addr val*
 | *NewHeapElem 'addr htype*
 | *ThreadStart 'thread-id*
 | *ThreadJoin 'thread-id*
 | *SyncLock 'addr*
 | *SyncUnlock 'addr*
 | *ObsInterrupt 'thread-id*
 | *ObsInterrupted 'thread-id*

instance *obs-event* :: (*type, type*) *obs-action*

<proof>

type-synonym

(*'addr, 'thread-id, 'x, 'heap*) *Jinja-thread-action* =
 (*'addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event*) *thread-action*

<ML>

typ (*'addr, 'thread-id, 'x, 'heap*) *Jinja-thread-action*

lemma *range-ty-of-htype*: *range ty-of-htype* \subseteq *range Class* \cup *range Array*

<proof>

lemma *some-choice*: $(\exists a. \forall b. P b (a b)) \longleftrightarrow (\forall b. \exists a. P b a)$

<proof>

definition *convert-RA* :: *'addr released-locks* \Rightarrow (*'addr* :: *addr, 'thread-id*) *obs-event list*

where $\bigwedge ln. \text{convert-RA } ln = \text{concat } (\text{map } (\lambda ad. \text{replicate } (ln \$ ad) (\text{SyncLock } ad))) (\text{monitor-funfun-to-list } ln)$

lemma *set-convert-RA-not-New* [*simp*]:

$\bigwedge ln. \text{NewHeapElem } a \text{ CTn} \notin \text{set } (\text{convert-RA } ln)$

<proof>

lemma *set-convert-RA-not-Read* [*simp*]:

$\bigwedge ln. \text{ReadMem } ad \text{ al } v \notin \text{set } (\text{convert-RA } ln)$

<proof>

end

3.9 The initial configuration

theory *StartConfig*

imports

Exceptions

Observable-Events

begin

definition *initialization-list* :: *cname list*

where

initialization-list = *Thread # sys-xcpts-list*

context *heap-base* **begin**

definition *create-initial-object* :: *'heap × 'addr list × bool ⇒ cname ⇒ 'heap × 'addr list × bool*

where

create-initial-object =

$(\lambda(h, ads, b) C.$

if *b*

then let *HA* = *allocate h (Class-type C)*

in if *HA* = $\{\}$ *then* $(h, ads, False)$

else let (h', a') = *SOME* *ha*. *ha* ∈ *HA* *in* $(h', ads @ [a'], True)$

else $(h, ads, False)$)

definition *start-heap-data* :: *'heap × 'addr list × bool*

where

start-heap-data = *foldl create-initial-object (empty-heap, [], True) initialization-list*

definition *start-heap* :: *'heap*

where *start-heap* = *fst start-heap-data*

definition *start-heap-ok* :: *bool*

where *start-heap-ok* = *snd (snd (start-heap-data))*

definition *start-heap-obs* :: *('addr, 'thread-id) obs-event list*

where

start-heap-obs =

map $(\lambda(C, a). NewHeapElem a (Class-type C)) (zip\ initialization-list\ (fst\ (snd\ start-heap-data)))$

definition *start-addr* :: *'addr list*

where *start-addr* = *fst (snd start-heap-data)*

definition *addr-of-sys-xcpt* :: *cname ⇒ 'addr*

where *addr-of-sys-xcpt* *C* = *the (map-of (zip initialization-list start-addr) C)*

definition *start-tid* :: *'thread-id*

where *start-tid* = *addr2thread-id (hd start-addr)*

definition *start-state* :: $(cname \Rightarrow mname \Rightarrow ty\ list \Rightarrow ty \Rightarrow 'm \Rightarrow 'addr\ val\ list \Rightarrow 'x) \Rightarrow 'm\ prog \Rightarrow cname \Rightarrow mname \Rightarrow 'addr\ val\ list \Rightarrow ('addr, 'thread-id, 'x, 'heap, 'addr)\ state$

where

start-state *f P C M vs* ≡

let (D, Ts, T, m) = *method P C M*

in $(K\$ None, ([start-tid \mapsto (f\ D\ M\ Ts\ T\ (the\ m)\ vs, no-wait-locks)], start-heap), Map.empty, \{\})$

lemma *create-initial-object-simps*:

create-initial-object (h, ads, b) $C =$
 (if b
 then let $HA = \text{allocate } h \text{ (Class-type } C)$
 in if $HA = \{\}$ then ($h, ads, False$)
 else let $(h', a'') = \text{SOME } ha. ha \in HA$ in ($h', ads @ [a''], True$)
 else ($h, ads, False$))

$\langle \text{proof} \rangle$

lemma *create-initial-object-False* [*simp*]:

create-initial-object ($h, ads, False$) $C = (h, ads, False)$

$\langle \text{proof} \rangle$

lemma *foldl-create-initial-object-False* [*simp*]:

foldl create-initial-object ($h, ads, False$) $Cs = (h, ads, False)$

$\langle \text{proof} \rangle$

lemma *NewHeapElem-start-heap-obs-start-addrD*:

NewHeapElem a $CTn \in \text{set start-heap-obs} \implies a \in \text{set start-addr}$

$\langle \text{proof} \rangle$

lemma *shr-start-state*: *shr* (*start-state* f P C M vs) = *start-heap*

$\langle \text{proof} \rangle$

lemma *start-heap-obs-not-Read*:

ReadMem ad al $v \notin \text{set start-heap-obs}$

$\langle \text{proof} \rangle$

lemma *length-initialization-list-le-length-start-addr*:

length initialization-list \geq *length start-addr*

$\langle \text{proof} \rangle$

lemma (**in** $-$) *distinct-initialization-list*:

distinct initialization-list

$\langle \text{proof} \rangle$

lemma (**in** $-$) *wf-syscls-initialization-list-is-class*:

$\llbracket \text{wf-syscls } P; C \in \text{set initialization-list} \rrbracket \implies \text{is-class } P$

$\langle \text{proof} \rangle$

lemma *start-addr-NewHeapElem-start-heap-obsD*:

$a \in \text{set start-addr} \implies \exists CTn. \text{NewHeapElem } a \text{ } CTn \in \text{set start-heap-obs}$

$\langle \text{proof} \rangle$

lemma *in-set-start-addr-conv-NewHeapElem*:

$a \in \text{set start-addr} \iff (\exists CTn. \text{NewHeapElem } a \text{ } CTn \in \text{set start-heap-obs})$

$\langle \text{proof} \rangle$

3.9.1 preallocated

definition *preallocated* :: *'heap* \Rightarrow *bool*

where *preallocated* $h \equiv \forall C \in \text{sys-xcpts. typeof-addr } h \text{ (addr-of-sys-xcpt } C) = \lfloor \text{Class-type } C \rfloor$

lemma *typeof-addr-sys-xcp*:

$\llbracket \text{preallocated } h; C \in \text{sys-xcpts} \rrbracket \implies \text{typeof-addr } h \text{ (addr-of-sys-xcpt } C) = \lfloor \text{Class-type } C \rfloor$
 $\langle \text{proof} \rangle$

lemma *typeof-sys-xcp*:

$\llbracket \text{preallocated } h; C \in \text{sys-xcpts} \rrbracket \implies \text{typeof}_h \text{ (Addr (addr-of-sys-xcpt } C)) = \lfloor \text{Class } C \rfloor$
 $\langle \text{proof} \rangle$

lemma *addr-of-sys-xcpt-start-addr*:

$\llbracket \text{start-heap-ok}; C \in \text{sys-xcpts} \rrbracket \implies \text{addr-of-sys-xcpt } C \in \text{set start-addrs}$
 $\langle \text{proof} \rangle$

lemma [*simp*]:

assumes *preallocated h*

shows *typeof-ClassCast*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt ClassCast)} = \text{Some}(\text{Class-type ClassCast})$

and *typeof-OutOfMemory*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt OutOfMemory)} = \text{Some}(\text{Class-type OutOfMemory})$

and *typeof-NullPointer*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt NullPointer)} = \text{Some}(\text{Class-type NullPointer})$

and *typeof-ArrayIndexOutOfBounds*:

$\text{typeof-addr } h \text{ (addr-of-sys-xcpt ArrayIndexOutOfBounds)} = \text{Some}(\text{Class-type ArrayIndexOutOfBounds})$

and *typeof-ArrayStore*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt ArrayStore)} = \text{Some}(\text{Class-type ArrayStore})$

and *typeof-NegativeArraySize*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt NegativeArraySize)} = \text{Some}(\text{Class-type NegativeArraySize})$

and *typeof-ArithmeticException*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt ArithmeticException)} = \text{Some}(\text{Class-type ArithmeticException})$

and *typeof-IllegalMonitorState*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt IllegalMonitorState)} = \text{Some}(\text{Class-type IllegalMonitorState})$

and *typeof-IllegalThreadState*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt IllegalThreadState)} = \text{Some}(\text{Class-type IllegalThreadState})$

and *typeof-InterruptedException*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt InterruptedException)} = \text{Some}(\text{Class-type InterruptedException})$

$\langle \text{proof} \rangle$

lemma *cname-of-xcp* [*simp*]:

$\llbracket \text{preallocated } h; C \in \text{sys-xcpts} \rrbracket \implies \text{cname-of } h \text{ (addr-of-sys-xcpt } C) = C$
 $\langle \text{proof} \rangle$

lemma *preallocated-hext*:

$\llbracket \text{preallocated } h; h \trianglelefteq h' \rrbracket \implies \text{preallocated } h'$
 $\langle \text{proof} \rangle$

end

context *heap begin*

lemma *preallocated-heap-ops*:

assumes *preallocated h*

shows *preallocated-allocate*: $\bigwedge a. (h', a) \in \text{allocate } h \text{ hT} \implies \text{preallocated } h'$

and *preallocated-write-field*: $\text{heap-write } h \text{ a al v } h' \implies \text{preallocated } h'$

$\langle \text{proof} \rangle$

lemma *not-empty-pairE*: $\llbracket A \neq \{\}; \bigwedge a b. (a, b) \in A \implies \text{thesis} \rrbracket \implies \text{thesis}$

<proof>

lemma *allocate-not-emptyI*: $(h', a) \in \text{allocate } h \ hT \implies \text{allocate } h \ hT \neq \{\}$

<proof>

lemma *allocate-Eps*:

$\llbracket (h'', a'') \in \text{allocate } h \ hT; (\text{SOME } ha. ha \in \text{allocate } h \ hT) = (h', a') \rrbracket \implies (h', a') \in \text{allocate } h \ hT$

<proof>

lemma *preallocated-start-heap*:

$\llbracket \text{start-heap-ok}; \text{wf-syscls } P \rrbracket \implies \text{preallocated start-heap}$

<proof>

lemma *start-tid-start-addr*:

$\llbracket \text{wf-syscls } P; \text{start-heap-ok} \rrbracket \implies \text{thread-id2addr start-tid} \in \text{set start-addr}$

<proof>

lemma

assumes *wf-syscls* P

shows *dom-typeof-addr-start-heap*: $\text{set start-addr} \subseteq \text{dom } (\text{typeof-addr start-heap})$

and *distinct-start-addr*: *distinct start-addr*

<proof>

lemma *NewHeapElem-start-heap-obsD*:

assumes *wf-syscls* P

and *NewHeapElem* $a \ hT \in \text{set start-heap-obs}$

shows *typeof-addr start-heap* $a = \lfloor hT \rfloor$

<proof>

end

3.9.2 Code generation

definition *pick-addr* :: $(\text{'heap} \times \text{'addr}) \text{ set} \Rightarrow \text{'heap} \times \text{'addr}$

where *pick-addr* $HA = (\text{SOME } ha. ha \in HA)$

lemma *pick-addr-code* [*code*]:

pick-addr $(\text{set } \lfloor ha \rfloor) = ha$

<proof>

lemma (**in** *heap-base*) *start-heap-data-code*:

start-heap-data =

(*let*

$(h, \text{ads}, b) = \text{foldl}$

$(\lambda(h, \text{ads}, b) C.$

if b *then*

let $HA = \text{allocate } h \ (\text{Class-type } C)$

in if $HA = \{\}$ *then* $(h, \text{ads}, \text{False})$

else let $(h', a') = \text{pick-addr } HA$ *in* $(h', a' \# \text{ads}, \text{True})$

else $(h, \text{ads}, \text{False})$)

$(\text{empty-heap}, [], \text{True})$

initialization-list

in $(h, \text{rev ads}, b)$)

<proof>

```

lemmas [code] =
  heap-base.start-heap-data-code
  heap-base.start-heap-def
  heap-base.start-heap-ok-def
  heap-base.start-heap-obs-def
  heap-base.start-addr-def
  heap-base.addr-of-sys-xcpt-def
  heap-base.start-tid-def
  heap-base.start-state-def

```

```

end

```

3.10 Conformance Relations for Type Soundness Proofs

```

theory Conform

```

```

imports

```

```

  StartConfig

```

```

begin

```

```

context heap-base begin

```

```

definition conf :: 'm prog ⇒ 'heap ⇒ 'addr val ⇒ ty ⇒ bool (⟨-, -⟩ - :≤ -⟩ [51,51,51,51] 50)
where P, h ⊢ v :≤ T ≡ ∃ T'. typeofh v = Some T' ∧ P ⊢ T' ≤ T

```

```

definition lconf :: 'm prog ⇒ 'heap ⇒ (vname → 'addr val) ⇒ (vname → ty) ⇒ bool (⟨-, -⟩ - '(≤)')
  -> [51,51,51,51] 50)
where P, h ⊢ l ([:≤] E ≡ ∀ V v. l V = Some v ⟶ (∃ T. E V = Some T ∧ P, h ⊢ v :≤ T)

```

```

abbreviation confs :: 'm prog ⇒ 'heap ⇒ 'addr val list ⇒ ty list ⇒ bool (⟨-, -⟩ - [:≤] -> [51,51,51,51] 50)

```

```

where P, h ⊢ vs [:≤] Ts == list-all2 (conf P h) vs Ts

```

```

definition tconf :: 'm prog ⇒ 'heap ⇒ 'thread-id ⇒ bool (⟨-, -⟩ - √t⟩ [51,51,51] 50)

```

```

where P, h ⊢ t √t ≡ ∃ C. typeof-addr h (thread-id2addr t) = [Class-type C] ∧ P ⊢ C ≤* Thread

```

```

end

```

```

locale heap-conf-base =

```

```

  heap-base +

```

```

  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id

```

```

  and thread-id2addr :: 'thread-id ⇒ 'addr

```

```

  and spurious-wakeups :: bool

```

```

  and empty-heap :: 'heap

```

```

  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set

```

```

  and typeof-addr :: 'heap ⇒ 'addr → htype

```

```

  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool

```

```

  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool

```

```

  fixes hconf :: 'heap ⇒ bool

```

```

  and P :: 'm prog

```

```

sublocale heap-conf-base < prog P ⟨proof⟩

```

```

locale heap-conf =
  heap
    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate typeof-addr heap-read heap-write
  P
+
  heap-conf-base
    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate typeof-addr heap-read heap-write
  hconf P
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'm prog
+
assumes hconf-empty [iff]: hconf empty-heap
and typeof-addr-is-type:  $\llbracket$  typeof-addr h a =  $\lfloor$ hT $\rfloor$ ; hconf h  $\rrbracket \Longrightarrow$  is-type P (ty-of-htype hT)
and hconf-allocate-mono:  $\bigwedge a. \llbracket (h', a) \in$  allocate h hT; hconf h; is-htype P hT  $\rrbracket \Longrightarrow$  hconf h'
and hconf-heap-write-mono:
 $\bigwedge T. \llbracket$  heap-write h a al v h'; hconf h; P,h  $\vdash$  a@al : T; P,h  $\vdash$  v : $\leq$  T  $\rrbracket \Longrightarrow$  hconf h'

locale heap-progress =
  heap-conf
    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate typeof-addr heap-read heap-write
  hconf P
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'm prog
+
assumes heap-read-total:  $\llbracket$  hconf h; P,h  $\vdash$  a@al : T  $\rrbracket \Longrightarrow \exists v. \text{heap-read } h \ a \ al \ v \wedge P,h \vdash v : \leq T$ 
and heap-write-total:  $\llbracket$  hconf h; P,h  $\vdash$  a@al : T; P,h  $\vdash$  v : $\leq$  T  $\rrbracket \Longrightarrow \exists h'. \text{heap-write } h \ a \ al \ v \ h'$ 

locale heap-conf-read =
  heap-conf
    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate typeof-addr heap-read heap-write

```

```

  hconf P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'm prog
+
assumes heap-read-conf: [ heap-read h a al v; P, h ⊢ a@al : T; hconf h ] ⇒ P, h ⊢ v :≤ T

```

```

locale heap-typesafe =
  heap-conf-read +
  heap-progress +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'heap ⇒ 'addr → htype
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
  and hconf :: 'heap ⇒ bool
  and P :: 'm prog

```

context heap-conf **begin**

```

lemmas hconf-heap-ops-mono =
  hconf-allocate-mono
  hconf-heap-write-mono

```

end

3.10.1 Value conformance :≤

context heap-base **begin**

```

lemma conf-Null [simp]: P, h ⊢ Null :≤ T = P ⊢ NT ≤ T
⟨proof⟩

```

```

lemma typeof-conf [simp]: typeofh v = Some T ⇒ P, h ⊢ v :≤ T
⟨proof⟩

```

```

lemma typeof-lit-conf [simp]: typeof v = Some T ⇒ P, h ⊢ v :≤ T
⟨proof⟩

```

```

lemma defval-conf [simp]: P, h ⊢ default-val T :≤ T
⟨proof⟩

```

```

lemma conf-widen: P, h ⊢ v :≤ T ⇒ P ⊢ T ≤ T' ⇒ P, h ⊢ v :≤ T'
⟨proof⟩

```

lemma *conf-sys-xcpt*:

$\llbracket \text{preallocated } h; C \in \text{sys-xcpts} \rrbracket \Longrightarrow P, h \vdash \text{Addr } (\text{addr-of-sys-xcpt } C) \leq \text{Class } C$
 $\langle \text{proof} \rangle$

lemma *conf-NT* [iff]: $P, h \vdash v \leq NT = (v = \text{Null})$

$\langle \text{proof} \rangle$

lemma *is-IntgI*: $P, h \vdash v \leq \text{Integer} \Longrightarrow \text{is-Intg } v$

$\langle \text{proof} \rangle$

lemma *is-BoolI*: $P, h \vdash v \leq \text{Boolean} \Longrightarrow \text{is-Bool } v$

$\langle \text{proof} \rangle$

lemma *is-RefI*: $P, h \vdash v \leq T \Longrightarrow \text{is-refT } T \Longrightarrow \text{is-Ref } v$

$\langle \text{proof} \rangle$

lemma *non-npD*:

$\llbracket v \neq \text{Null}; P, h \vdash v \leq \text{Class } C; C \neq \text{Object} \rrbracket$
 $\Longrightarrow \exists a C'. v = \text{Addr } a \wedge \text{typeof-addr } h a = [\text{Class-type } C'] \wedge P \vdash C' \preceq^* C$
 $\langle \text{proof} \rangle$

lemma *non-npD2*:

$\llbracket v \neq \text{Null}; P, h \vdash v \leq \text{Class } C \rrbracket$
 $\Longrightarrow \exists a hT. v = \text{Addr } a \wedge \text{typeof-addr } h a = [hT] \wedge P \vdash \text{class-type-of } hT \preceq^* C$
 $\langle \text{proof} \rangle$

end

context *heap begin*

lemma *conf-heat*: $\llbracket h \sqsubseteq h'; P, h \vdash v \leq T \rrbracket \Longrightarrow P, h' \vdash v \leq T$

$\langle \text{proof} \rangle$

lemma *conf-heap-ops-mono*:

assumes $P, h \vdash v \leq T$

shows *conf-allocate-mono*: $(h', a) \in \text{allocate } h hT \Longrightarrow P, h' \vdash v \leq T$

and *conf-heap-write-mono*: $\text{heap-write } h a al v' h' \Longrightarrow P, h' \vdash v \leq T$

$\langle \text{proof} \rangle$

end

3.10.2 Value list conformance $[:\leq]$

context *heap-base begin*

lemma *confs-widens* [trans]: $\llbracket P, h \vdash vs [:\leq] Ts; P \vdash Ts [\leq] Ts' \rrbracket \Longrightarrow P, h \vdash vs [:\leq] Ts'$

$\langle \text{proof} \rangle$

lemma *confs-rev*: $P, h \vdash \text{rev } s [:\leq] t = (P, h \vdash s [:\leq] \text{rev } t)$

$\langle \text{proof} \rangle$

lemma *confs-conv-map*:

$P, h \vdash vs [:\leq] Ts' = (\exists Ts. \text{map } \text{typeof}_h vs = \text{map } \text{Some } Ts \wedge P \vdash Ts [\leq] Ts')$

$\langle proof \rangle$

lemma *confs-Cons2*: $P, h \vdash xs [:\leq] y \# ys = (\exists z zs. xs = z \# zs \wedge P, h \vdash z : \leq y \wedge P, h \vdash zs [:\leq] ys)$
 $\langle proof \rangle$

end

context *heap* **begin**

lemma *confs-heap*: $P, h \vdash vs [:\leq] Ts \implies h \leq h' \implies P, h' \vdash vs [:\leq] Ts$
 $\langle proof \rangle$

end

3.10.3 Local variable conformance

context *heap-base* **begin**

lemma *lconf-upd*:
 $\llbracket P, h \vdash l (:\leq) E; P, h \vdash v : \leq T; E \ V = \text{Some } T \rrbracket \implies P, h \vdash l(V \mapsto v) (:\leq) E$
 $\langle proof \rangle$

lemma *lconf-empty* [iff]: $P, h \vdash \text{Map.empty} (:\leq) E$
 $\langle proof \rangle$

lemma *lconf-upd2*: $\llbracket P, h \vdash l (:\leq) E; P, h \vdash v : \leq T \rrbracket \implies P, h \vdash l(V \mapsto v) (:\leq) E(V \mapsto T)$
 $\langle proof \rangle$

end

context *heap* **begin**

lemma *lconf-heap*: $\llbracket P, h \vdash l (:\leq) E; h \leq h' \rrbracket \implies P, h' \vdash l (:\leq) E$
 $\langle proof \rangle$

end

3.10.4 Thread object conformance

context *heap-base* **begin**

lemma *tconfI*: $\llbracket \text{typeof-addr } h (\text{thread-id2addr } t) = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket \implies P, h \vdash t \sqrt{t}$
 $\langle proof \rangle$

lemma *tconfD*: $P, h \vdash t \sqrt{t} \implies \exists C. \text{typeof-addr } h (\text{thread-id2addr } t) = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Thread}$
 $\langle proof \rangle$

end

context *heap* **begin**

lemma *tconf-heap-mono*: $\llbracket P, h \vdash t \sqrt{t}; h \leq h' \rrbracket \implies P, h' \vdash t \sqrt{t}$

<proof>

lemma *tconf-heap-ops-mono*:

assumes $P, h \vdash t \sqrt{t}$

shows *tconf-allocate-mono*: $(h', a) \in \text{allocate } h \ hT \implies P, h' \vdash t \sqrt{t}$

and *tconf-heap-write-mono*: $\text{heap-write } h \ a \ al \ v \ h' \implies P, h' \vdash t \sqrt{t}$

<proof>

lemma *tconf-start-heap-start-tid*:

$\llbracket \text{start-heap-ok}; \text{wf-syscls } P \rrbracket \implies P, \text{start-heap} \vdash \text{start-tid} \sqrt{t}$

<proof>

lemma *start-heap-write-typeable*:

assumes *WriteMem* $ad \ al \ v \in \text{set } \text{start-heap-obs}$

shows $\exists T. P, \text{start-heap} \vdash ad@al : T \wedge P, \text{start-heap} \vdash v : \leq T$

<proof>

end

3.10.5 Well-formed start state

context *heap-base begin*

inductive *wf-start-state* :: $'m \ \text{prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow 'addr \ \text{val} \ \text{list} \Rightarrow \text{bool}$

for $P :: 'm \ \text{prog}$ **and** $C :: \text{cname}$ **and** $M :: \text{mname}$ **and** $vs :: 'addr \ \text{val} \ \text{list}$

where

wf-start-state:

$\llbracket P \vdash C \ \text{sees } M : Ts \rightarrow T = \llbracket \text{meth} \rrbracket \ \text{in } D; \text{start-heap-ok}; P, \text{start-heap} \vdash vs \llbracket : \leq \rrbracket Ts \rrbracket$
 $\implies \text{wf-start-state } P \ C \ M \ vs$

end

end

3.11 Semantics of method calls that cannot be defined inside JinjaThreads

theory *ExternalCall*

imports

../Framework/FWSemantics

Conform

begin

type-synonym

$('addr, 'thread-id, 'heap) \ \text{external-thread-action} = ('addr, 'thread-id, \text{cname} \times \text{mname} \times 'addr, 'heap)$
Jinja-thread-action

<ML>

typ $('addr, 'thread-id, 'heap) \ \text{external-thread-action}$

3.11.1 Typing of external calls

inductive *external-WT-defs* :: *cname* \Rightarrow *mname* \Rightarrow *ty list* \Rightarrow *ty* \Rightarrow *bool* ($\langle \langle \dots'(-) \rangle \rangle :: \rightarrow [50, 0, 0, 50]$ 60)

where

Thread.start([]) :: *Void*
| *Thread.join*([]) :: *Void*
| *Thread.interrupt*([]) :: *Void*
| *Thread.isInterrupted*([]) :: *Boolean*
| *Object.wait*([]) :: *Void*
| *Object.notify*([]) :: *Void*
| *Object.notifyAll*([]) :: *Void*
| *Object.clone*([]) :: *Class Object*
| *Object.hashCode*([]) :: *Integer*
| *Object.print*([*Integer*]) :: *Void*
| *Object.currentThread*([]) :: *Class Thread*
| *Object.interrupted*([]) :: *Boolean*
| *Object.yield*([]) :: *Void*

inductive-cases *external-WT-defs-cases*:

a.start(*vs*) :: *T*
a.join(*vs*) :: *T*
a.interrupt(*vs*) :: *T*
a.isInterrupted(*vs*) :: *T*
a.wait(*vs*) :: *T*
a.notify(*vs*) :: *T*
a.notifyAll(*vs*) :: *T*
a.clone(*vs*) :: *T*
a.hashCode(*vs*) :: *T*
a.print(*vs*) :: *T*
a.currentThread(*vs*) :: *T*
a.interrupted([]) :: *T*
a.yield(*vs*) :: *T*

inductive *is-native* :: '*m prog* \Rightarrow *hType* \Rightarrow *mname* \Rightarrow *bool*

for *P* :: '*m prog* **and** *hT* :: *hType* **and** *M* :: *mname*

where $\llbracket P \vdash \text{class-type-of } hT \text{ sees } M:Ts \rightarrow T = \text{Native in } D; D \cdot M(Ts) :: T \rrbracket \Longrightarrow \text{is-native } P \ hT \ M$

lemma *is-nativeD*: *is-native* *P hT M* $\Longrightarrow \exists Ts \ T \ D. P \vdash \text{class-type-of } hT \text{ sees } M:Ts \rightarrow T = \text{Native in } D \wedge D \cdot M(Ts) :: T$

<proof>

inductive (**in** *heap-base*) *external-WT'* :: '*m prog* \Rightarrow '*heap* \Rightarrow '*addr* \Rightarrow *mname* \Rightarrow '*addr val list* \Rightarrow *ty* \Rightarrow *bool*

($\langle \langle \dots'(-) \rangle \rangle :: \rightarrow [50, 0, 0, 0, 50]$ 60)

for *P* :: '*m prog* **and** *h* :: '*heap* **and** *a* :: '*addr* **and** *M* :: *mname* **and** *vs* :: '*addr val list* **and** *U* :: *ty*

where

$\llbracket \text{typeof-addr } h \ a = [hT]; \text{map } \text{typeof}_h \ vs = \text{map } \text{Some } Ts; P \vdash \text{class-type-of } hT \text{ sees } M:Ts' \rightarrow U = \text{Native in } D;$

$P \vdash Ts \ [\leq] \ Ts'$

$\Longrightarrow P, h \vdash a \cdot M(vs) : U$

context *heap-base* **begin**

lemma *external-WT'-iff*:

$P, h \vdash a \cdot M(vs) : U \iff$
 $(\exists hT \ Ts \ Ts' \ D. \text{typeof-addr } h \ a = [hT] \wedge \text{map } \text{typeof}_h \ vs = \text{map } \text{Some } Ts \wedge P \vdash \text{class-type-of } hT$
sees $M : Ts' \rightarrow U = \text{Native in } D \wedge P \vdash Ts \leq Ts')$
 ⟨proof⟩

end

context *heap begin*

lemma *external-WT'-heqt-mono*:

$\llbracket P, h \vdash a \cdot M(vs) : T; h \leq h' \rrbracket \implies P, h' \vdash a \cdot M(vs) : T$
 ⟨proof⟩

end

3.11.2 Semantics of external calls

datatype *'addr extCallRet* =
RetVal 'addr val
 | *RetExc 'addr*
 | *RetStaySame*

lemma *rec-extCallRet [simp]*: *rec-extCallRet* = *case-extCallRet*
 ⟨proof⟩

context *heap-base begin*

abbreviation *RetEXC* :: *cname* \Rightarrow *'addr extCallRet*
where *RetEXC C* \equiv *RetExc (addr-of-sys-xcpt C)*

inductive *heap-copy-loc* :: *'addr* \Rightarrow *'addr* \Rightarrow *addr-loc* \Rightarrow *'heap* \Rightarrow (*'addr, 'thread-id*) *obs-event list* \Rightarrow *'heap* \Rightarrow *bool*

for *a* :: *'addr* **and** *a'* :: *'addr* **and** *al* :: *addr-loc* **and** *h* :: *'heap*

where

$\llbracket \text{heap-read } h \ a \ al \ v; \text{heap-write } h \ a' \ al \ v \ h' \rrbracket$
 $\implies \text{heap-copy-loc } a \ a' \ al \ h \ ([\text{ReadMem } a \ al \ v, \text{WriteMem } a' \ al \ v]) \ h'$

inductive *heap-copies* :: *'addr* \Rightarrow *'addr* \Rightarrow *addr-loc list* \Rightarrow *'heap* \Rightarrow (*'addr, 'thread-id*) *obs-event list* \Rightarrow *'heap* \Rightarrow *bool*

for *a* :: *'addr* **and** *a'* :: *'addr*

where

Nil: *heap-copies a a' [] h [] h*
 | *Cons*:
 $\llbracket \text{heap-copy-loc } a \ a' \ al \ h \ ob \ h'; \text{heap-copies } a \ a' \ als \ h' \ obs \ h'' \rrbracket$
 $\implies \text{heap-copies } a \ a' \ (al \ \# \ als) \ h \ (ob \ @ \ obs) \ h''$

inductive-cases *heap-copies-cases*:

heap-copies a a' [] h ops h'
heap-copies a a' (al#als) h ops h'

Contrary to Sun's JVM 1.6.0_07, cloning an interrupted thread does not yield an interrupted thread, because the interrupt flag is not stored inside the thread object. Starting a clone of a started thread with Sun JVM 1.6.0_07 raises an illegal thread state excep-

tion, we just start another thread. The thread at <http://mail.openjdk.java.net/pipermail/core-libs-dev/2010-August/004715.html> discusses the general problem of thread cloning and argues against that. The bug report http://bugs.sun.com/bugdatabase/view_bug.do?bug_id=6968584 changes the Thread class implementation such that `Object.clone()` can no longer be accessed for Thread and subclasses in Java 7.

Array cells are never volatile themselves.

inductive *heap-clone* :: 'm prog ⇒ 'heap ⇒ 'addr ⇒ 'heap ⇒ (('addr, 'thread-id) obs-event list × 'addr) option ⇒ bool

for *P* :: 'm prog **and** *h* :: 'heap **and** *a* :: 'addr

where

CloneFail:

$\llbracket \text{typeof-addr } h \ a = \lfloor hT \rfloor; \text{allocate } h \ hT = \{\} \rrbracket$

$\implies \text{heap-clone } P \ h \ a \ h \ \text{None}$

| *ObjClone*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; (h', a') \in \text{allocate } h \ (\text{Class-type } C);$

$P \vdash C \text{ has-fields } \text{FDTs}; \text{heap-copies } a \ a' \ (\text{map } (\lambda((F, D), \text{Tfm}). \text{CField } D \ F) \ \text{FDTs}) \ h' \ \text{obs } h'' \rrbracket$

$\implies \text{heap-clone } P \ h \ a \ h'' \ \lfloor (\text{NewHeapElem } a' \ (\text{Class-type } C) \ \# \ \text{obs}, a') \rfloor$

| *ArrClone*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor; (h', a') \in \text{allocate } h \ (\text{Array-type } T \ n); P \vdash \text{Object has-fields } \text{FDTs};$

$\text{heap-copies } a \ a' \ (\text{map } (\lambda((F, D), \text{Tfm}). \text{CField } D \ F) \ \text{FDTs} \ @ \ \text{map } \text{ACell } [0..<n]) \ h' \ \text{obs } h'' \rrbracket$

$\implies \text{heap-clone } P \ h \ a \ h'' \ \lfloor (\text{NewHeapElem } a' \ (\text{Array-type } T \ n) \ \# \ \text{obs}, a') \rfloor$

inductive *red-external* ::

'm prog ⇒ 'thread-id ⇒ 'heap ⇒ 'addr ⇒ mname ⇒ 'addr val list

⇒ ('addr, 'thread-id, 'heap) external-thread-action ⇒ 'addr extCallRet ⇒ 'heap ⇒ bool

and *red-external-syntax* ::

'm prog ⇒ 'thread-id ⇒ 'addr ⇒ mname ⇒ 'addr val list ⇒ 'heap

⇒ ('addr, 'thread-id, 'heap) external-thread-action ⇒ 'addr extCallRet ⇒ 'heap ⇒ bool

$\langle \cdot, \cdot \vdash ((\cdot \cdot'(-)'), /) \dashrightarrow \text{ext } ((\cdot, /(-))) \rangle [50, 0, 0, 0, 0, 0, 0, 0, 0] \ 51$

for *P* :: 'm prog **and** *t* :: 'thread-id **and** *h* :: 'heap **and** *a* :: 'addr

where

$P, t \vdash \langle a \cdot M(vs), h \rangle \dashrightarrow \text{ext } \langle va, h' \rangle \equiv \text{red-external } P \ t \ h \ a \ M \ vs \ ta \ va \ h'$

| *RedNewThread*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$

$\implies P, t \vdash \langle a \cdot \text{start}(\lfloor \rfloor), h \rangle \dashrightarrow \lfloor \text{NewThread } (\text{addr2thread-id } a) \ (C, \text{run}, a) \ h, \text{ThreadStart } (\text{addr2thread-id } a) \rfloor \dashrightarrow \text{ext } \langle \text{RetVal Unit}, h \rangle$

| *RedNewThreadFail*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$

$\implies P, t \vdash \langle a \cdot \text{start}(\lfloor \rfloor), h \rangle \dashrightarrow \lfloor \text{ThreadExists } (\text{addr2thread-id } a) \ \text{True} \rfloor \dashrightarrow \text{ext } \langle \text{RetEXC IllegalThreadState}, h \rangle$

| *RedJoin*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$

$\implies P, t \vdash \langle a \cdot \text{join}(\lfloor \rfloor), h \rangle \dashrightarrow \lfloor \text{Join } (\text{addr2thread-id } a), \text{IsInterrupted } t \ \text{False}, \text{ThreadJoin } (\text{addr2thread-id } a) \rfloor \dashrightarrow \text{ext } \langle \text{RetVal Unit}, h \rangle$

| *RedJoinInterrupt*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$

$\implies P, t \vdash \langle a \cdot \text{join}(\lfloor \rfloor), h \rangle \dashrightarrow \lfloor \text{IsInterrupted } t \ \text{True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t \rfloor \dashrightarrow \text{ext } \langle \text{RetEXC InterruptedException}, h \rangle$

— Interruption should produce inter-thread actions (JLS 17.4.4) for the synchronizes-with order. They should synchronize with the inter-thread actions that determine whether a thread has been interrupted. Hence, interruption generates an *ObsInterrupt* action.

Although *WakeUp* causes the interrupted thread to raise an *InterruptedException* independent of the interrupt status, the interrupt flag must be set with *Interrupt* such that other threads observe the interrupted thread as interrupted while it competes for the monitor lock again.

Interrupting a thread which has not yet been started does not set the interrupt flag (tested with Sun HotSpot JVM 1.6.0_07).

| *RedInterrupt*:

$$\begin{aligned} & \llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket \\ & \implies P, t \vdash \langle a \cdot \text{interrupt}(\lfloor \rfloor), h \rangle \\ & \quad - \{ \{ \text{ThreadExists } (\text{addr2thread-id } a) \ \text{True}, \text{WakeUp } (\text{addr2thread-id } a), \\ & \quad \quad \text{Interrupt } (\text{addr2thread-id } a), \text{ObsInterrupt } (\text{addr2thread-id } a) \} \} \rightarrow \text{ext} \\ & \quad \langle \text{RetVal Unit}, h \rangle \end{aligned}$$

| *RedInterruptInexist*:

$$\begin{aligned} & \llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket \\ & \implies P, t \vdash \langle a \cdot \text{interrupt}(\lfloor \rfloor), h \rangle \\ & \quad - \{ \{ \text{ThreadExists } (\text{addr2thread-id } a) \ \text{False} \} \} \rightarrow \text{ext} \\ & \quad \langle \text{RetVal Unit}, h \rangle \end{aligned}$$

| *RedIsInterruptedTrue*:

$$\begin{aligned} & \llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket \\ & \implies P, t \vdash \langle a \cdot \text{isInterrupted}(\lfloor \rfloor), h \rangle - \{ \{ \text{IsInterrupted } (\text{addr2thread-id } a) \ \text{True}, \text{ObsInterrupted } (\text{addr2thread-id } a) \} \} \rightarrow \text{ext} \\ & \quad \langle \text{RetVal } (\text{Bool True}), h \rangle \end{aligned}$$

| *RedIsInterruptedFalse*:

$$\begin{aligned} & \llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket \\ & \implies P, t \vdash \langle a \cdot \text{isInterrupted}(\lfloor \rfloor), h \rangle - \{ \{ \text{IsInterrupted } (\text{addr2thread-id } a) \ \text{False} \} \} \rightarrow \text{ext} \langle \text{RetVal } (\text{Bool False}), h \rangle \end{aligned}$$

— The JLS leaves unspecified whether *wait* first checks for the monitor state (whether the thread holds a lock on the monitor) or for the interrupt flag of the current thread. Sun Hotspot JVM 1.6.0_07 seems to check for the monitor state first, so we do it here, too.

| *RedWaitInterrupt*:

$$\begin{aligned} & P, t \vdash \langle a \cdot \text{wait}(\lfloor \rfloor), h \rangle - \{ \{ \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{IsInterrupted } t \ \text{True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t \} \} \rightarrow \text{ext} \\ & \quad \langle \text{RetEXC } \text{InterruptedException}, h \rangle \end{aligned}$$

| *RedWait*:

$$\begin{aligned} & P, t \vdash \langle a \cdot \text{wait}(\lfloor \rfloor), h \rangle - \{ \{ \text{Suspend } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \ \text{False}, \\ & \quad \text{SyncUnlock } a \} \} \rightarrow \text{ext} \\ & \quad \langle \text{RetStaySame}, h \rangle \end{aligned}$$

| *RedWaitFail*:

$$P, t \vdash \langle a \cdot \text{wait}(\lfloor \rfloor), h \rangle - \{ \{ \text{UnlockFail} \rightarrow a \} \} \rightarrow \text{ext} \langle \text{RetEXC } \text{IllegalMonitorState}, h \rangle$$

| *RedWaitNotified*:

$$P, t \vdash \langle a \cdot \text{wait}(\lfloor \rfloor), h \rangle - \{ \{ \text{Notified} \} \} \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$$

— This rule does NOT check that the interrupted flag is set, but still clears it. The semantics will

be that only the executing thread clears its interrupt.

| *RedWaitInterrupted*:

$P, t \vdash \langle a.\text{wait}(\square), h \rangle - \{\!\{ \text{WokenUp}, \text{ClearInterrupt } t, \text{ObsInterrupted } t \}\!\} \rightarrow \text{ext} \langle \text{RetEXC InterruptedException}, h \rangle$

— Calls to wait may decide to immediately wake up spuriously. This is indistinguishable from waking up spuriously any time before being notified or interrupted. Spurious wakeups are configured by the *spurious-wakeup* parameter of the *heap-base* locale.

| *RedWaitSpurious*:

$\text{spurious-wakeups} \implies$
 $P, t \vdash \langle a.\text{wait}(\square), h \rangle - \{\!\{ \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False}, \text{SyncUnlock } a \}\!\} \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

— *notify* and *notifyAll* do not perform synchronization inter-thread actions because they only tests whether the thread holds a lock, but do not change the lock state.

| *RedNotify*:

$P, t \vdash \langle a.\text{notify}(\square), h \rangle - \{\!\{ \text{Notify } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a \}\!\} \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

| *RedNotifyFail*:

$P, t \vdash \langle a.\text{notify}(\square), h \rangle - \{\!\{ \text{UnlockFail} \rightarrow a \}\!\} \rightarrow \text{ext} \langle \text{RetEXC IllegalMonitorState}, h \rangle$

| *RedNotifyAll*:

$P, t \vdash \langle a.\text{notifyAll}(\square), h \rangle - \{\!\{ \text{NotifyAll } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a \}\!\} \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

| *RedNotifyAllFail*:

$P, t \vdash \langle a.\text{notifyAll}(\square), h \rangle - \{\!\{ \text{UnlockFail} \rightarrow a \}\!\} \rightarrow \text{ext} \langle \text{RetEXC IllegalMonitorState}, h \rangle$

| *RedClone*:

$\text{heap-clone } P \ h \ a \ h' \ \lfloor (\text{obs}, a') \rfloor$
 $\implies P, t \vdash \langle a.\text{clone}(\square), h \rangle - (K\$ \square, \square, \square, \square, \square, \text{obs}) \rightarrow \text{ext} \langle \text{RetVal } (\text{Addr } a'), h \rangle$

| *RedCloneFail*:

$\text{heap-clone } P \ h \ a \ h' \ \text{None} \implies P, t \vdash \langle a.\text{clone}(\square), h \rangle - \varepsilon \rightarrow \text{ext} \langle \text{RetEXC OutOfMemory}, h \rangle$

| *RedHashCode*:

$P, t \vdash \langle a.\text{hashCode}(\square), h \rangle - \{\!\{ \}\!\} \rightarrow \text{ext} \langle \text{RetVal } (\text{Intg } (\text{word-of-int } (\text{hash-addr } a))), h \rangle$

| *RedPrint*:

$P, t \vdash \langle a.\text{print}(vs), h \rangle - \{\!\{ \text{ExternalCall } a \ \text{print } vs \ \text{Unit} \}\!\} \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

| *RedCurrentThread*:

$P, t \vdash \langle a.\text{currentThread}(\square), h \rangle - \{\!\{ \}\!\} \rightarrow \text{ext} \langle \text{RetVal } (\text{Addr } (\text{thread-id2addr } t)), h \rangle$

| *RedInterruptedTrue*:

$P, t \vdash \langle a.\text{interrupted}(\square), h \rangle - \{\!\{ \text{IsInterrupted } t \ \text{True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t \}\!\} \rightarrow \text{ext} \langle \text{RetVal } (\text{Bool True}), h \rangle$

| *RedInterruptedFalse*:

$P, t \vdash \langle a.\text{interrupted}(\square), h \rangle - \{\!\{ \text{IsInterrupted } t \ \text{False} \}\!\} \rightarrow \text{ext} \langle \text{RetVal } (\text{Bool False}), h \rangle$

| *RedYield*:

$P, t \vdash \langle a.\text{yield}(\square), h \rangle - \{\!\{ \text{Yield} \}\!\} \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

3.11.3 Aggressive formulation for external calls

definition *red-external-aggr* ::

'm prog \Rightarrow 'thread-id \Rightarrow 'addr \Rightarrow mname \Rightarrow 'addr val list \Rightarrow 'heap \Rightarrow
 (('addr, 'thread-id, 'heap) external-thread-action \times 'addr extCallRet \times 'heap) set

where

red-external-aggr P t a M vs h =
 (if M = wait then
 let ad-t = thread-id2addr t
 in {($\{\!\{$ Unlock \rightarrow a, Lock \rightarrow a, IsInterrupted t True, ClearInterrupt t, ObsInterrupted t $\}\!\}$, RetEXC InterruptedException, h),
 ($\{\!\{$ Suspend a, Unlock \rightarrow a, Lock \rightarrow a, ReleaseAcquire \rightarrow a, IsInterrupted t False, SyncUnlock a $\}\!\}$, RetStaySame, h),
 ($\{\!\{$ UnlockFail \rightarrow a $\}\!\}$, RetEXC IllegalMonitorState, h),
 ($\{\!\{$ Notified $\}\!\}$, RetVal Unit, h),
 ($\{\!\{$ WokenUp, ClearInterrupt t, ObsInterrupted t $\}\!\}$, RetEXC InterruptedException, h)} \cup
 (if spurious-wakeups then {($\{\!\{$ Unlock \rightarrow a, Lock \rightarrow a, ReleaseAcquire \rightarrow a, IsInterrupted t False, SyncUnlock a $\}\!\}$, RetVal Unit, h)} else {})
 else if M = notify then {($\{\!\{$ Notify a, Unlock \rightarrow a, Lock \rightarrow a $\}\!\}$, RetVal Unit, h),
 ($\{\!\{$ UnlockFail \rightarrow a $\}\!\}$, RetEXC IllegalMonitorState, h)}
 else if M = notifyAll then {($\{\!\{$ NotifyAll a, Unlock \rightarrow a, Lock \rightarrow a $\}\!\}$, RetVal Unit, h),
 ($\{\!\{$ UnlockFail \rightarrow a $\}\!\}$, RetEXC IllegalMonitorState, h)}
 else if M = clone then
 {((K\$ [], [], [], [], [], obs), RetVal (Addr a \wedge , h \wedge)|obs a' h'. heap-clone P h a h' [(obs, a')])
 \cup {($\{\!\{$ \}, RetEXC OutOfMemory, h \wedge)|h'. heap-clone P h a h' None}
 else if M = hashcode then {($\{\!\{$ \}, RetVal (Intg (word-of-int (hash-addr a))), h)}
 else if M = print then {($\{\!\{$ ExternalCall a M vs Unit $\}\!\}$, RetVal Unit, h)}
 else if M = currentThread then {($\{\!\{$ \}, RetVal (Addr (thread-id2addr t)), h)}
 else if M = interrupted then {($\{\!\{$ IsInterrupted t True, ClearInterrupt t, ObsInterrupted t $\}\!\}$, RetVal (Bool True), h),
 ($\{\!\{$ IsInterrupted t False $\}\!\}$, RetVal (Bool False), h)}
 else if M = yield then {($\{\!\{$ Yield $\}\!\}$, RetVal Unit, h)}
 else
 let hT = the (typeof-addr h a)
 in if P \vdash ty-of-hT \leq Class Thread then
 let t-a = addr2thread-id a
 in if M = start then
 {($\{\!\{$ NewThread t-a (the-Class (ty-of-hT), run, a) h, ThreadStart t-a $\}\!\}$, RetVal Unit, h),
 ($\{\!\{$ ThreadExists t-a True $\}\!\}$, RetEXC IllegalThreadState, h)}
 else if M = join then
 {($\{\!\{$ Join t-a, IsInterrupted t False, ThreadJoin t-a $\}\!\}$, RetVal Unit, h),
 ($\{\!\{$ IsInterrupted t True, ClearInterrupt t, ObsInterrupted t $\}\!\}$, RetEXC InterruptedException, h)}
 h)}
 else if M = interrupt then
 {($\{\!\{$ ThreadExists t-a True, WakeUp t-a, Interrupt t-a, ObsInterrupt t-a $\}\!\}$, RetVal Unit, h),
 ($\{\!\{$ ThreadExists t-a False $\}\!\}$, RetVal Unit, h)}
 else if M = isInterrupted then
 {($\{\!\{$ IsInterrupted t-a False $\}\!\}$, RetVal (Bool False), h),
 ($\{\!\{$ IsInterrupted t-a True, ObsInterrupted t-a $\}\!\}$, RetVal (Bool True), h)}
 else {($\{\!\{$ \}, undefined)}
 else {($\{\!\{$ \}, undefined)}

lemma *red-external-imp-red-external-aggr*:

$P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h^\wedge \rangle \implies (ta, va, h') \in red\text{-external-aggr } P \ t \ a \ M \ vs \ h$
 ⟨proof⟩

end

context heap begin

lemma *hext-heap-copy-loc*:

heap-copy-loc $a \ a' \ al \ h \ obs \ h' \implies h \leq h'$
 ⟨proof⟩

lemma *hext-heap-copies*:

assumes *heap-copies* $a \ a' \ als \ h \ obs \ h'$
shows $h \leq h'$
 ⟨proof⟩

lemma *hext-heap-clone*:

assumes *heap-clone* $P \ h \ a \ h' \ res$
shows $h \leq h'$
 ⟨proof⟩

theorem *red-external-hext*:

assumes $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h^\wedge \rangle$
shows *hext* $h \ h'$
 ⟨proof⟩

lemma *red-external-preserves-tconf*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h^\wedge \rangle; P, h \vdash t' \sqrt{t} \rrbracket \implies P, h' \vdash t' \sqrt{t}$
 ⟨proof⟩

end

context heap-conf begin

lemma *typeof-addr-heap-clone*:

assumes *heap-clone* $P \ h \ a \ h' \ [(obs, a')]$
and *hconf* h
shows *typeof-addr* $h' \ a' = \text{typeof-addr } h \ a$
 ⟨proof⟩

end

context heap-base begin

lemma *red-ext-new-thread-heap*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h^\wedge \rangle; NewThread \ t' \ ex \ h'' \in \text{set } \{ta\}_t \rrbracket \implies h'' = h'$
 ⟨proof⟩

lemma *red-ext-aggr-new-thread-heap*:

$\llbracket (ta, va, h') \in red\text{-external-aggr } P \ t \ a \ M \ vs \ h; NewThread \ t' \ ex \ h'' \in \text{set } \{ta\}_t \rrbracket \implies h'' = h'$
 ⟨proof⟩

end

context *addr-conv* **begin**

lemma *red-external-new-thread-exists-thread-object*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; NewThread t' x h'' \in set \{ta\}_t \rrbracket$
 $\implies \exists C. \text{typeof-addr } h' (\text{thread-id2addr } t') = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* Thread$
 $\langle proof \rangle$

lemma *red-external-aggr-new-thread-exists-thread-object*:

$\llbracket (ta, va, h') \in red\text{-external-aggr } P t a M vs h; \text{typeof-addr } h a \neq None;$
 $NewThread t' x h'' \in set \{ta\}_t \rrbracket$
 $\implies \exists C. \text{typeof-addr } h' (\text{thread-id2addr } t') = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* Thread$
 $\langle proof \rangle$

end

context *heap* **begin**

lemma *red-external-aggr-hext*:

$\llbracket (ta, va, h') \in red\text{-external-aggr } P t a M vs h; is\text{-native } P (\text{the } (\text{typeof-addr } h a)) M \rrbracket \implies h \leq h'$
 $\langle proof \rangle$

lemma *red-external-aggr-preserves-tconf*:

$\llbracket (ta, va, h') \in red\text{-external-aggr } P t a M vs h; is\text{-native } P (\text{the } (\text{typeof-addr } h a)) M; P, h \vdash t' \surd t \rrbracket$
 $\implies P, h' \vdash t' \surd t$
 $\langle proof \rangle$

end

context *heap-base* **begin**

lemma *red-external-Wakeup-no-Join-no-Lock-no-Interrupt*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; Notified \in set \{ta\}_w \vee WokenUp \in set \{ta\}_w \rrbracket \implies$
 $collect\text{-locks } \{ta\}_l = \{\} \wedge collect\text{-cond-actions } \{ta\}_c = \{\} \wedge collect\text{-interrupts } \{ta\}_i = \{\}$
 $\langle proof \rangle$

lemma *red-external-aggr-Wakeup-no-Join*:

$\llbracket (ta, va, h') \in red\text{-external-aggr } P t a M vs h;$
 $Notified \in set \{ta\}_w \vee WokenUp \in set \{ta\}_w \rrbracket$
 $\implies collect\text{-locks } \{ta\}_l = \{\} \wedge collect\text{-cond-actions } \{ta\}_c = \{\} \wedge collect\text{-interrupts } \{ta\}_i = \{\}$
 $\langle proof \rangle$

lemma *red-external-Suspend-StaySame*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; Suspend w \in set \{ta\}_w \rrbracket \implies va = RetStaySame$
 $\langle proof \rangle$

lemma *red-external-aggr-Suspend-StaySame*:

$\llbracket (ta, va, h') \in red\text{-external-aggr } P t a M vs h; Suspend w \in set \{ta\}_w \rrbracket \implies va = RetStaySame$
 $\langle proof \rangle$

lemma *red-external-Suspend-waitD*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; Suspend w \in set \{ta\}_w \rrbracket \implies M = wait$
 $\langle proof \rangle$

lemma *red-external-aggr-Suspend-waitD*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ \text{vs } h; \text{Suspend } w \in \text{set } \{ta\}_w \rrbracket \implies M = \text{wait}$
 ⟨proof⟩

lemma *red-external-new-thread-sub-thread*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; \text{NewThread } t' (C, M', a') \ h'' \in \text{set } \{ta\}_t \rrbracket$
 $\implies \text{typeof-addr } h' \ a' = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Thread} \wedge M' = \text{run}$
 ⟨proof⟩

lemma *red-external-aggr-new-thread-sub-thread*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ \text{vs } h; \text{typeof-addr } h \ a \neq \text{None};$
 $\text{NewThread } t' (C, M', a') \ h'' \in \text{set } \{ta\}_t \rrbracket$
 $\implies \text{typeof-addr } h' \ a' = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Thread} \wedge M' = \text{run}$
 ⟨proof⟩

lemma *heap-copy-loc-length*:

assumes *heap-copy-loc* $a \ a' \ \text{al } h \ \text{obs } h'$
shows $\text{length } \text{obs} = 2$
 ⟨proof⟩

lemma *heap-copies-length*:

assumes *heap-copies* $a \ a' \ \text{als } h \ \text{obs } h'$
shows $\text{length } \text{obs} = 2 * \text{length } \text{als}$
 ⟨proof⟩

end

3.11.4 τ -moves

inductive $\tau \text{external-defs} :: \text{cname} \Rightarrow \text{mname} \Rightarrow \text{bool}$

where

$\tau \text{external-defs}$ *Object* *hashcode*
 | $\tau \text{external-defs}$ *Object* *currentThread*

definition $\tau \text{external} :: 'm \ \text{prog} \Rightarrow \text{hType} \Rightarrow \text{mname} \Rightarrow \text{bool}$

where $\tau \text{external } P \ hT \ M \longleftrightarrow (\exists \text{Ts } \text{Tr } D. P \vdash \text{class-type-of } hT \ \text{sees } M: \text{Ts} \rightarrow \text{Tr} = \text{Native in } D \wedge \tau \text{external-defs } D \ M)$

context *heap-base* **begin**

definition $\tau \text{external}' :: 'm \ \text{prog} \Rightarrow 'heap \Rightarrow 'addr \Rightarrow \text{mname} \Rightarrow \text{bool}$

where $\tau \text{external}' P \ h \ a \ M \longleftrightarrow (\exists hT. \text{typeof-addr } h \ a = \text{Some } hT \wedge \tau \text{external } P \ hT \ M)$

lemma *$\tau \text{external}'$ -red-external-heap-unchanged*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; \tau \text{external}' P \ h \ a \ M \rrbracket \implies h' = h$
 ⟨proof⟩

lemma *$\tau \text{external}'$ -red-external-aggr-heap-unchanged*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ \text{vs } h; \tau \text{external}' P \ h \ a \ M \rrbracket \implies h' = h$
 ⟨proof⟩

lemma *$\tau \text{external}'$ -red-external-TA-empty*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; \tau \text{external}' P \ h \ a \ M \rrbracket \implies ta = \varepsilon$
 ⟨proof⟩

lemma τ external'-red-external-aggr-TA-empty:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ \text{vs } h; \tau\text{external}' \ P \ h \ a \ M \rrbracket \implies ta = \varepsilon$
 $\langle \text{proof} \rangle$

lemma red-external-new-thread-addr-conf:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext} \langle va, h' \rangle; \text{NewThread } t \ (C, M, a') \ h'' \in \text{set } \{ta\}_t \rrbracket$
 $\implies P, h' \vdash \text{Addr } a \ : \leq \ \text{Class } \text{Thread}$
 $\langle \text{proof} \rangle$

lemma τ external-red-external-aggr-heap-unchanged:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ \text{vs } h; \tau\text{external} \ P \ (\text{the } (\text{typeof-addr } h \ a)) \ M \rrbracket \implies h' = h$
 $\langle \text{proof} \rangle$

lemma τ external-red-external-aggr-TA-empty:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ \text{vs } h; \tau\text{external} \ P \ (\text{the } (\text{typeof-addr } h \ a)) \ M \rrbracket \implies ta = \varepsilon$
 $\langle \text{proof} \rangle$

end

3.11.5 Code generation

code-pred

(modes:
 $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$,
 $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$,
 $i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$,
 $o \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$)
external-WT-defs
 $\langle \text{proof} \rangle$

code-pred

(modes: $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$)
[*inductify*, *skip-proof*]
is-native
 $\langle \text{proof} \rangle$

declare heap-base.heap-copy-loc.intros[code-pred-intro]

code-pred

(modes: $(i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$)
heap-base.heap-copy-loc
 $\langle \text{proof} \rangle$

declare heap-base.heap-copies.intros [code-pred-intro]

code-pred

(modes: $(i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$)
heap-base.heap-copies
 $\langle \text{proof} \rangle$

declare heap-base.heap-clone.intros [*folded Predicate-Compile.contains-def*, code-pred-intro]

code-pred

(modes: $i \Rightarrow i \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$)

heap-base.heap-clone

$\langle \text{proof} \rangle$

`code_pred` in Isabelle2012 cannot handle boolean parameters as premises properly, so this replacement rule explicitly tests for *True*

lemma (in *heap-base*) *RedWaitSpurious-Code*:

spurious-wakeups = True \implies

$P, t \vdash \langle a \cdot \text{wait}(\square), h \rangle - \{\!| \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False}, \text{SyncUnlock } a \!\} \rightarrow \text{ext} \langle \text{RetVal } \text{Unit}, h \rangle$

$\langle \text{proof} \rangle$

lemmas [*code-pred-intro*] =

heap-base.RedNewThread heap-base.RedNewThreadFail

heap-base.RedJoin heap-base.RedJoinInterrupt

heap-base.RedInterrupt heap-base.RedInterruptInexist heap-base.RedIsInterruptedTrue heap-base.RedIsInterruptedFalse

heap-base.RedWaitInterrupt heap-base.RedWait heap-base.RedWaitFail heap-base.RedWaitNotified heap-base.RedWaitInterrupt

declare *heap-base.RedWaitSpurious-Code* [*code-pred-intro RedWaitSpurious*]

lemmas [*code-pred-intro*] =

heap-base.RedNotify heap-base.RedNotifyFail heap-base.RedNotifyAll heap-base.RedNotifyAllFail

heap-base.RedClone heap-base.RedCloneFail

heap-base.RedHashcode heap-base.RedPrint heap-base.RedCurrentThread

heap-base.RedInterruptedTrue heap-base.RedInterruptedFalse

heap-base.RedYield

code-pred

(modes: $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$)

heap-base.red-external

$\langle \text{proof} \rangle$

end

3.12 Generic Well-formedness of programs

theory *WellForm*

imports

SystemClasses

ExternalCall

begin

This theory defines global well-formedness conditions for programs but does not look inside method bodies. Hence it works for both Jinja and JVM programs. Well-typing of expressions is defined elsewhere (in theory *WellType*).

Because JinjaThreads does not have method overloading, its policy for method overriding is the classical one: *covariant in the result type but contravariant in the argument types*. This means the result type of the overriding method becomes more specific, the argument types become more general.

type-synonym $'m \text{ wf-mdecl-test} = 'm \text{ prog} \Rightarrow \text{cname} \Rightarrow 'm \text{ mdecl} \Rightarrow \text{bool}$

definition $wf\text{-}fdecl :: 'm \text{ prog} \Rightarrow fdecl \Rightarrow bool$
where $wf\text{-}fdecl P \equiv \lambda(F, T, fm). \text{ is-type } P T$

definition $wf\text{-}mdecl :: 'm \text{ wf-mdecl-test} \Rightarrow 'm \text{ prog} \Rightarrow \text{cname} \Rightarrow 'm \text{ mdecl}' \Rightarrow bool$ **where**
 $wf\text{-}mdecl \text{ wf-md } P C \equiv$
 $\lambda(M, Ts, T, m). (\forall T \in \text{set } Ts. \text{ is-type } P T) \wedge \text{ is-type } P T \wedge$
 $(\text{case } m \text{ of Native} \Rightarrow C \cdot M(Ts) :: T \mid [mb] \Rightarrow wf\text{-}md P C (M, Ts, T, mb))$

fun $wf\text{-}overriding :: 'm \text{ prog} \Rightarrow \text{cname} \Rightarrow 'm \text{ mdecl}' \Rightarrow bool$
where
 $wf\text{-}overriding P D (M, Ts, T, m) =$
 $(\forall D' Ts' T' m'. P \vdash D \text{ sees } M: Ts' \rightarrow T' = m' \text{ in } D' \longrightarrow P \vdash Ts' [\leq] Ts \wedge P \vdash T \leq T')$

definition $wf\text{-}cdecl :: 'm \text{ wf-mdecl-test} \Rightarrow 'm \text{ prog} \Rightarrow 'm \text{ cdecl} \Rightarrow bool$
where
 $wf\text{-}cdecl \text{ wf-md } P \equiv \lambda(C, (D, fs, ms)).$
 $(\forall f \in \text{set } fs. wf\text{-}fdecl P f) \wedge \text{ distinct-fst } fs \wedge$
 $(\forall m \in \text{set } ms. wf\text{-}mdecl \text{ wf-md } P C m) \wedge$
 $\text{ distinct-fst } ms \wedge$
 $(C \neq \text{Object} \longrightarrow$
 $\text{ is-class } P D \wedge \neg P \vdash D \preceq^* C \wedge$
 $(\forall m \in \text{set } ms. wf\text{-}overriding P D m)) \wedge$
 $(C = \text{Thread} \longrightarrow (\exists m. (\text{run}, [], \text{Void}, m) \in \text{set } ms))$

definition $wf\text{-}prog :: 'm \text{ wf-mdecl-test} \Rightarrow 'm \text{ prog} \Rightarrow bool$
where
 $wf\text{-}prog \text{ wf-md } P \longleftrightarrow wf\text{-}syscls P \wedge \text{ distinct-fst } (\text{classes } P) \wedge (\forall c \in \text{set } (\text{classes } P). wf\text{-}cdecl \text{ wf-md } P c)$

lemma $wf\text{-}prog\text{-}def2:$
 $wf\text{-}prog \text{ wf-md } P \longleftrightarrow wf\text{-}syscls P \wedge (\forall C \text{ rest. class } P C = [rest] \longrightarrow wf\text{-}cdecl \text{ wf-md } P (C, \text{rest}))$
 $\wedge \text{ distinct-fst } (\text{classes } P)$
 $\langle \text{proof} \rangle$

3.12.1 Well-formedness lemmas

lemma $wf\text{-}prog\text{-}wf\text{-}syscls: wf\text{-}prog \text{ wf-md } P \Longrightarrow wf\text{-}syscls P$
 $\langle \text{proof} \rangle$

lemma $\text{class-wf}:$
 $\llbracket \text{class } P C = \text{Some } c; wf\text{-}prog \text{ wf-md } P \rrbracket \Longrightarrow wf\text{-}cdecl \text{ wf-md } P (C, c)$
 $\langle \text{proof} \rangle$

lemma $[simp]:$
assumes $wf\text{-}prog \text{ wf-md } P$
shows $\text{class-Object}: \exists C fs ms. \text{ class } P \text{ Object} = \text{Some } (C, fs, ms)$
and $\text{class-Thread}: \exists C fs ms. \text{ class } P \text{ Thread} = \text{Some } (C, fs, ms)$
 $\langle \text{proof} \rangle$

lemma $[simp]:$
assumes $wf\text{-}prog \text{ wf-md } P$
shows $\text{is-class-Object}: \text{ is-class } P \text{ Object}$
and $\text{is-class-Thread}: \text{ is-class } P \text{ Thread}$
 $\langle \text{proof} \rangle$

lemma *xcpt-subcls-Throwable*:

$\llbracket C \in \text{sys-xcpts}; \text{wf-prog wf-md } P \rrbracket \Longrightarrow P \vdash C \preceq^* \text{Throwable}$
 $\langle \text{proof} \rangle$

lemma *is-class-Throwable*:

$\text{wf-prog wf-md } P \Longrightarrow \text{is-class } P \text{ Throwable}$
 $\langle \text{proof} \rangle$

lemma *is-class-sub-Throwable*:

$\llbracket \text{wf-prog wf-md } P; P \vdash C \preceq^* \text{Throwable} \rrbracket \Longrightarrow \text{is-class } P \ C$
 $\langle \text{proof} \rangle$

lemma *is-class-xcpt*:

$\llbracket C \in \text{sys-xcpts}; \text{wf-prog wf-md } P \rrbracket \Longrightarrow \text{is-class } P \ C$
 $\langle \text{proof} \rangle$

context *heap-base* **begin**

lemma *wf-preallocatedE*:

assumes *wf-prog wf-md* P
and *preallocated* h
and $C \in \text{sys-xcpts}$
obtains *typeof-addr* h (*addr-of-sys-xcpt* C) = $\lfloor \text{Class-type } C \rfloor$ $P \vdash C \preceq^* \text{Throwable}$
 $\langle \text{proof} \rangle$

lemma *wf-preallocatedD*:

assumes *wf-prog wf-md* P
and *preallocated* h
and $C \in \text{sys-xcpts}$
shows *typeof-addr* h (*addr-of-sys-xcpt* C) = $\lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Throwable}$
 $\langle \text{proof} \rangle$

end

lemma (**in** *heap-conf*) *hconf-start-heap*:

$\text{wf-prog wf-md } P \Longrightarrow \text{hconf start-heap}$
 $\langle \text{proof} \rangle$

lemma *subcls1-wfD*:

$\llbracket P \vdash C \prec^1 D; \text{wf-prog wf-md } P \rrbracket \Longrightarrow D \neq C \wedge \neg (\text{subcls1 } P)^{++} D \ C$
 $\langle \text{proof} \rangle$

lemma *wf-cdecl-supD*:

$\llbracket \text{wf-cdecl wf-md } P \ (C, D, r); C \neq \text{Object} \rrbracket \Longrightarrow \text{is-class } P \ D \langle \text{proof} \rangle$

lemma *subcls-asym*:

$\llbracket \text{wf-prog wf-md } P; (\text{subcls1 } P)^{++} C \ D \rrbracket \Longrightarrow \neg (\text{subcls1 } P)^{++} D \ C \langle \text{proof} \rangle$

lemma *subcls-irrefl*:

$\llbracket \text{wf-prog wf-md } P; (\text{subcls1 } P)^{++} C \ D \rrbracket \Longrightarrow C \neq D \langle \text{proof} \rangle$

lemma *acyclicP-def*:

$\text{acyclicP } r \longleftrightarrow (\forall x. \neg r^{++} x \ x)$

$\langle \text{proof} \rangle$

lemma *acyclic-subcls1*:

$wf\text{-prog } wf\text{-md } P \implies acyclicP (subcls1 P)$
 $\langle proof \rangle$

lemma *finite-conversep*: $finite \{(x, y). r^{-1-1} x y\} = finite \{(x, y). r x y\}$

$\langle proof \rangle$

lemma *acyclicP-wf-subcls1*:

$acyclicP (subcls1 P) \implies wfP ((subcls1 P)^{-1-1})$
 $\langle proof \rangle$

lemma *wf-subcls1*:

$wf\text{-prog } wf\text{-md } P \implies wfP ((subcls1 P)^{-1-1})$
 $\langle proof \rangle$

lemma *single-valued-subcls1*:

$wf\text{-prog } wf\text{-md } G \implies single\text{-valuedp } (subcls1 G) \langle proof \rangle$

lemma *subcls-induct*:

$\llbracket wf\text{-prog } wf\text{-md } P; \bigwedge C. \forall D. (subcls1 P)^{++} C D \longrightarrow Q D \implies Q C \rrbracket \implies Q C \langle proof \rangle$

lemma *subcls1-induct-aux*:

$\llbracket is\text{-class } P C; wf\text{-prog } wf\text{-md } P; Q \text{ Object};$
 $\bigwedge C D fs ms.$
 $\llbracket C \neq \text{Object}; is\text{-class } P C; class P C = Some (D,fs,ms) \wedge$
 $wf\text{-cdecl } wf\text{-md } P (C,D,fs,ms) \wedge P \vdash C \prec^1 D \wedge is\text{-class } P D \wedge Q D \rrbracket \implies Q C \rrbracket$
 $\implies Q C \langle proof \rangle$

lemma *subcls1-induct [consumes 2, case-names Object Subcls]*:

$\llbracket wf\text{-prog } wf\text{-md } P; is\text{-class } P C; Q \text{ Object};$
 $\bigwedge C D. \llbracket C \neq \text{Object}; P \vdash C \prec^1 D; is\text{-class } P D; Q D \rrbracket \implies Q C \rrbracket$
 $\implies Q C \langle proof \rangle$

lemma *subcls-C-Object*:

$\llbracket is\text{-class } P C; wf\text{-prog } wf\text{-md } P \rrbracket \implies P \vdash C \preceq^* \text{Object} \langle proof \rangle$

lemma *converse-subcls-is-class*:

assumes $wf: wf\text{-prog } wf\text{-md } P$
shows $\llbracket P \vdash C \preceq^* D; is\text{-class } P C \rrbracket \implies is\text{-class } P D$
 $\langle proof \rangle$

lemma *is-class-is-subcls*:

$wf\text{-prog } m P \implies is\text{-class } P C = P \vdash C \preceq^* \text{Object} \langle proof \rangle$

lemma *subcls-antisym*:

$\llbracket wf\text{-prog } m P; P \vdash C \preceq^* D; P \vdash D \preceq^* C \rrbracket \implies C = D$
 $\langle proof \rangle$

lemma *is-type-pTs*:

$\llbracket wf\text{-prog } wf\text{-md } P; class P C = \llbracket (S,fs,ms) \rrbracket; (M,Ts,T,m) \in set ms \rrbracket \implies set Ts \subseteq types P$
 $\langle proof \rangle$

lemma *widen-asym-1*:

assumes $wfP: wf\text{-prog } wf\text{-md } P$
shows $P \vdash C \leq D \implies C = D \vee \neg (P \vdash D \leq C)$
 $\langle proof \rangle$

lemma *widen-asy*: $\llbracket \text{wf-prog wf-md } P; P \vdash C \leq D; C \neq D \rrbracket \Longrightarrow \neg (P \vdash D \leq C)$
 ⟨proof⟩

lemma *widen-antisym*:
 $\llbracket \text{wf-prog } m \text{ } P; P \vdash T \leq U; P \vdash U \leq T \rrbracket \Longrightarrow T = U$
 ⟨proof⟩

lemma *widen-C-Object*: $\llbracket \text{wf-prog wf-md } P; \text{is-class } P \ C \rrbracket \Longrightarrow P \vdash \text{Class } C \leq \text{Class Object}$
 ⟨proof⟩

lemma *is-refType-widen-Object*:
assumes *wfP*: *wf-prog wfmc P*
shows $\llbracket \text{is-type } P \ A; \text{is-refT } A \rrbracket \Longrightarrow P \vdash A \leq \text{Class Object}$
 ⟨proof⟩

lemma *is-lub-unique*:
assumes *wf*: *wf-prog wf-md P*
shows $\llbracket P \vdash \text{lub}(U, V) = T; P \vdash \text{lub}(U, V) = T' \rrbracket \Longrightarrow T = T'$
 ⟨proof⟩

3.12.2 Well-formedness and method lookup

lemma *sees-wf-mdecl*:
 $\llbracket \text{wf-prog wf-md } P; P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D \rrbracket \Longrightarrow \text{wf-mdecl wf-md } P \ D \ (M, Ts, T, m)$ ⟨proof⟩

lemma *sees-method-mono* [*rule-format (no-asm)*]:
 $\llbracket P \vdash C' \leq^* C; \text{wf-prog wf-md } P \rrbracket \Longrightarrow$
 $\forall D \ Ts \ T \ m. P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D \longrightarrow$
 $(\exists D' \ Ts' \ T' \ m'. P \vdash C' \text{ sees } M:Ts' \rightarrow T' = m' \text{ in } D' \wedge P \vdash Ts \leq Ts' \wedge P \vdash T' \leq T)$
 ⟨proof⟩

lemma *sees-method-mono2*:
 $\llbracket P \vdash C' \leq^* C; \text{wf-prog wf-md } P;$
 $P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D; P \vdash C' \text{ sees } M:Ts' \rightarrow T' = m' \text{ in } D' \rrbracket$
 $\Longrightarrow P \vdash Ts \leq Ts' \wedge P \vdash T' \leq T$ ⟨proof⟩

lemma *mdecls-visible*:
assumes *wf*: *wf-prog wf-md P* **and** *class*: *is-class P C*
shows $\bigwedge D \ fs \ ms. \text{class } P \ C = \text{Some}(D, fs, ms)$
 $\Longrightarrow \exists Mm. P \vdash C \text{ sees-methods } Mm \wedge (\forall (M, Ts, T, m) \in \text{set } ms. Mm \ M = \text{Some}((Ts, T, m), C))$
 ⟨proof⟩

lemma *mdecl-visible*:
assumes *wf*: *wf-prog wf-md P* **and** *C*: *class P C = [(S, fs, ms)]* **and** *m*: $(M, Ts, T, m) \in \text{set } ms$
shows $P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } C$
 ⟨proof⟩

lemma *sees-wf-native*:
 $\llbracket \text{wf-prog wf-md } P; P \vdash C \text{ sees } M:Ts \rightarrow T = \text{Native in } D \rrbracket \Longrightarrow D \cdot M(Ts) :: T$
 ⟨proof⟩

lemma *Call-lemma*:

$\llbracket P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D; P \vdash C' \preceq^* C; \text{ wf-prog wf-md } P \rrbracket$
 $\implies \exists D' Ts' T' m'.$
 $P \vdash C' \text{ sees } M:Ts' \rightarrow T' = m' \text{ in } D' \wedge P \vdash Ts \llbracket \leq \rrbracket Ts' \wedge P \vdash T' \leq T \wedge P \vdash C' \preceq^* D'$
 $\wedge \text{ is-type } P T' \wedge (\forall T \in \text{set } Ts'. \text{ is-type } P T) \wedge (m' \neq \text{Native} \implies \text{wf-md } P D' (M, Ts', T', \text{the } m'))$
 <proof>

lemma *sub-Thread-sees-run*:

assumes *wf*: wf-prog wf-md *P*

and *PCThread*: $P \vdash C \preceq^* \text{Thread}$

shows $\exists D \text{ mthd}. P \vdash C \text{ sees run}: [] \rightarrow \text{Void} = \llbracket \text{mthd} \rrbracket \text{ in } D$

<proof>

lemma *wf-prog-lift*:

assumes *wf*: wf-prog $(\lambda P C \text{ bd}. A P C \text{ bd}) P$

and *rule*:

$\wedge \text{wf-md } C M Ts C T m.$

$\llbracket \text{wf-prog wf-md } P; P \vdash C \text{ sees } M:Ts \rightarrow T = \llbracket m \rrbracket \text{ in } C; \text{ is-class } P C; \text{ set } Ts \subseteq \text{types } P; A P C$
 $(M, Ts, T, m) \rrbracket$

$\implies B P C (M, Ts, T, m)$

shows wf-prog $(\lambda P C \text{ bd}. B P C \text{ bd}) P$

<proof>

3.12.3 Well-formedness and field lookup

lemma *wf-Fields-Ex*:

$\llbracket \text{wf-prog wf-md } P; \text{ is-class } P C \rrbracket \implies \exists \text{FDTs}. P \vdash C \text{ has-fields } \text{FDTs}$ <proof>

lemma *has-fields-types*:

$\llbracket P \vdash C \text{ has-fields } \text{FDTs}; (FD, T, fm) \in \text{set } \text{FDTs}; \text{wf-prog wf-md } P \rrbracket \implies \text{is-type } P T$ <proof>

lemma *sees-field-is-type*:

$\llbracket P \vdash C \text{ sees } F:T (fm) \text{ in } D; \text{wf-prog wf-md } P \rrbracket \implies \text{is-type } P T$

<proof>

lemma *wf-has-field-mono2*:

assumes *wf*: wf-prog wf-md *P*

and *has*: $P \vdash C \text{ has } F:T (fm) \text{ in } E$

shows $\llbracket P \vdash C \preceq^* D; P \vdash D \preceq^* E \rrbracket \implies P \vdash D \text{ has } F:T (fm) \text{ in } E$

<proof>

lemma *wf-has-field-idemp*:

$\llbracket \text{wf-prog wf-md } P; P \vdash C \text{ has } F:T (fm) \text{ in } D \rrbracket \implies P \vdash D \text{ has } F:T (fm) \text{ in } D$

<proof>

lemma *map-of-remap-conv*:

$\llbracket \text{distinct-fst } fs; \text{map-of } (\text{map } (\lambda(F, y). ((F, D), y)) fs) (F, D) = \llbracket T \rrbracket \rrbracket$

$\implies \text{map-of } (\text{map } (\lambda((F, D), T). (F, D, T)) (\text{map } (\lambda(F, y). ((F, D), y)) fs)) F = \llbracket (D, T) \rrbracket$

<proof>

lemma *has-field-idemp-sees-field*:

assumes *wf*: wf-prog wf-md *P*

and *has*: $P \vdash D \text{ has } F:T (fm) \text{ in } D$

shows $P \vdash D \text{ sees } F:T (fm) \text{ in } D$

<proof>

lemma *has-fields-distinct*:

assumes *wf*: *wf-prog wf-md P*

and $P \vdash C$ *has-fields FDTs*

shows *distinct (map fst FDTs)*

\langle *proof* \rangle

3.12.4 Code generation

code-pred

(*modes*: $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$)

[*inductify*]

wf-overriding

\langle *proof* \rangle

Separate subclass acyclicity from class declaration check. Otherwise, cyclic class hierarchies might lead to non-termination as *Methods* recurses over the class hierarchy.

definition *acyclic-class-hierarchy* :: $'m \text{ prog} \Rightarrow \text{bool}$

where

acyclic-class-hierarchy P \longleftrightarrow

$(\forall (C, D, fs, ml) \in \text{set (classes P)}. C \neq \text{Object} \longrightarrow \neg P \vdash D \preceq^* C)$

definition *wf-cdecl'* :: $'m \text{ wf-mdecl-test} \Rightarrow 'm \text{ prog} \Rightarrow 'm \text{ cdecl} \Rightarrow \text{bool}$

where

wf-cdecl' wf-md P = $(\lambda(C, (D, fs, ms)).$

$(\forall f \in \text{set } fs. \text{wf-fdecl } P f) \wedge \text{distinct-fst } fs \wedge$

$(\forall m \in \text{set } ms. \text{wf-mdecl } wf-md P C m) \wedge$

distinct-fst ms \wedge

$(C \neq \text{Object} \longrightarrow \text{is-class } P D \wedge (\forall m \in \text{set } ms. \text{wf-overriding } P D m)) \wedge$

$(C = \text{Thread} \longrightarrow (\exists m. (\text{run}, [], \text{Void}, m) \in \text{set } ms)))$

lemma *acyclic-class-hierarchy-code* [*code*]:

acyclic-class-hierarchy P $\longleftrightarrow (\forall (C, D, fs, ml) \in \text{set (classes P)}. C \neq \text{Object} \longrightarrow \neg \text{subcls}' P D C)$

\langle *proof* \rangle

lemma *wf-cdecl'-code* [*code*]:

wf-cdecl' wf-md P = $(\lambda(C, (D, fs, ms)).$

$(\forall f \in \text{set } fs. \text{wf-fdecl } P f) \wedge \text{distinct-fst } fs \wedge$

$(\forall m \in \text{set } ms. \text{wf-mdecl } wf-md P C m) \wedge$

distinct-fst ms \wedge

$(C \neq \text{Object} \longrightarrow \text{is-class } P D \wedge (\forall m \in \text{set } ms. \text{wf-overriding } P D m)) \wedge$

$(C = \text{Thread} \longrightarrow ((\text{run}, [], \text{Void}) \in \text{set (map } (\lambda(M, Ts, T, b). (M, Ts, T)) ms))))$

\langle *proof* \rangle

declare *set-append* [*symmetric, code-unfold*]

lemma *wf-prog-code* [*code*]:

wf-prog wf-md P \longleftrightarrow

acyclic-class-hierarchy P \wedge

wf-syscls P $\wedge \text{distinct-fst (classes P)}$ \wedge

$(\forall c \in \text{set (classes P)}. \text{wf-cdecl}' wf-md P c)$

\langle *proof* \rangle

end

3.13 Properties of external calls in well-formed programs

theory *ExternalCallWF*

imports

WellForm

../Framework/FWSemantics

begin

lemma *external-WT-defs-is-type*:

assumes *wf-prog wf-md P and C.M(Ts) :: T*

shows *is-class P C and is-type P T set Ts ⊆ types P*

<proof>

context *heap-base* **begin**

lemma *WT-red-external-aggr-imp-red-external*:

$\llbracket \text{wf-prog wf-md } P; (ta, va, h^\wedge) \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h; P, h \vdash a.M(vs) : U; P, h \vdash t \ \sqrt{t} \rrbracket$
 $\implies P, t \vdash \langle a.M(vs), h \rangle -ta \rightarrow \text{ext} \langle va, h^\wedge \rangle$

<proof>

lemma *WT-red-external-list-conv*:

$\llbracket \text{wf-prog wf-md } P; P, h \vdash a.M(vs) : U; P, h \vdash t \ \sqrt{t} \rrbracket$

$\implies P, t \vdash \langle a.M(vs), h \rangle -ta \rightarrow \text{ext} \langle va, h^\wedge \rangle \longleftrightarrow (ta, va, h^\wedge) \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h$

<proof>

lemma *red-external-new-thread-sees*:

$\llbracket \text{wf-prog wf-md } P; P, t \vdash \langle a.M(vs), h \rangle -ta \rightarrow \text{ext} \langle va, h^\wedge \rangle; \text{NewThread } t' (C, M', a') \ h'' \in \text{set } \{ta\}_t \rrbracket$
 $\implies \text{typeof-addr } h' \ a' = \lfloor \text{Class-type } C \rfloor \wedge (\exists T \ \text{meth } D. P \vdash C \ \text{sees } M' : \lfloor \rightarrow T = \lfloor \text{meth} \rfloor \ \text{in } D)$

<proof>

end

3.13.1 Preservation of heap conformance

context *heap-conf-read* **begin**

lemma *hconf-heap-copy-loc-mono*:

assumes *heap-copy-loc a a' al h obs h'*

and *hconf h*

and $P, h \vdash a@al : T \ P, h \vdash a'@al : T$

shows *hconf h'*

<proof>

lemma *hconf-heap-copies-mono*:

assumes *heap-copies a a' als h obs h'*

and *hconf h*

and *list-all2* $(\lambda al \ T. P, h \vdash a@al : T) \ als \ Ts$

and *list-all2* $(\lambda al \ T. P, h \vdash a'@al : T) \ als \ Ts$

shows *hconf h'*

<proof>

lemma *hconf-heap-clone-mono*:

assumes *heap-clone P h a h' res*

and *hconf h*

shows $hconf\ h'$
 $\langle proof \rangle$

theorem *external-call-hconf*:

assumes *major*: $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$
and *minor*: $P, h \vdash a \cdot M(vs) : U\ hconf\ h$
shows $hconf\ h'$
 $\langle proof \rangle$

end

context *heap-base* **begin**

primrec *conf-extRet* :: $'m\ prog \Rightarrow 'heap \Rightarrow 'addr\ extCallRet \Rightarrow ty \Rightarrow bool$ **where**
 $conf_extRet\ P\ h\ (RetVal\ v)\ T = (P, h \vdash v : \leq T)$
 $| conf_extRet\ P\ h\ (RetExc\ a)\ T = (P, h \vdash Addr\ a : \leq Class\ Throwable)$
 $| conf_extRet\ P\ h\ RetStaySame\ T = True$

end

context *heap-conf* **begin**

lemma *red-external-conf-extRet*:

assumes *wf*: $wf_prog\ wf_md\ P$
shows $\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; P, h \vdash a \cdot M(vs) : U; hconf\ h; preallocated\ h; P, h \vdash t \sqrt{t} \rrbracket$
 $\implies conf_extRet\ P\ h'\ va\ U$
 $\langle proof \rangle$

end

3.13.2 Progress theorems for external calls

context *heap-progress* **begin**

lemma *heap-copy-loc-progress*:

assumes *hconf*: $hconf\ h$
and *alconfa*: $P, h \vdash a @ al : T$
and *alconfa'*: $P, h \vdash a' @ al : T$
shows $\exists v\ h'. heap_copy_loc\ a\ a'\ al\ h\ ([ReadMem\ a\ al\ v, WriteMem\ a'\ al\ v])\ h' \wedge P, h \vdash v : \leq T \wedge hconf\ h'$
 $\langle proof \rangle$

lemma *heap-copies-progress*:

assumes *hconf*: $hconf\ h$
and *list-all2*: $(\lambda al\ T. P, h \vdash a @ al : T)\ als\ Ts$
and *list-all2'*: $(\lambda al\ T. P, h \vdash a' @ al : T)\ als\ Ts$
shows $\exists vs\ h'. heap_copies\ a\ a'\ als\ h\ (concat\ (map\ (\lambda(al, v). [ReadMem\ a\ al\ v, WriteMem\ a'\ al\ v])\ (zip\ als\ vs)))\ h' \wedge hconf\ h'$
 $\langle proof \rangle$

lemma *heap-clone-progress*:

assumes *wf*: $wf_prog\ wf_md\ P$
and *typea*: $typeof_addr\ h\ a = [hT]$

and $hconf: hconf\ h$
shows $\exists h' \text{ res. heap-clone } P\ h\ a\ h' \text{ res}$
 $\langle proof \rangle$

theorem *external-call-progress*:
assumes $wf: wf\text{-prog}\ wf\text{-md}\ P$
and $wt: P, h \vdash a \cdot M(vs) : U$
and $hconf: hconf\ h$
shows $\exists ta\ va\ h'. P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$
 $\langle proof \rangle$

end

3.13.3 Lemmas for preservation of deadlocked threads

context *heap-progress* **begin**

lemma *red-external-wt-hconf-hext*:
assumes $wf: wf\text{-prog}\ wf\text{-md}\ P$
and $red: P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$
and $hext: h'' \trianglelefteq h$
and $wt: P, h'' \vdash a \cdot M(vs) : U$
and $tconf: P, h'' \vdash t \sqrt{t}$
and $hconf: hconf\ h''$
shows $\exists ta'\ va'\ h'''. P, t \vdash \langle a \cdot M(vs), h'' \rangle -ta' \rightarrow ext \langle va', h''' \rangle \wedge$
 $collect\ locks\ \{ta\}_l = collect\ locks\ \{ta'\}_l \wedge$
 $collect\ cond\ actions\ \{ta\}_c = collect\ cond\ actions\ \{ta'\}_c \wedge$
 $collect\ interrupts\ \{ta\}_i = collect\ interrupts\ \{ta'\}_i$
 $\langle proof \rangle$

lemma *red-external-wf-red*:
assumes $wf: wf\text{-prog}\ wf\text{-md}\ P$
and $red: P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$
and $tconf: P, h \vdash t \sqrt{t}$
and $hconf: hconf\ h$
and $wst: wset\ s\ t = None \vee (M = wait \wedge (\exists w. wset\ s\ t = \lfloor PostWS\ w \rfloor))$
obtains $ta'\ va'\ h''$
where $P, t \vdash \langle a \cdot M(vs), h \rangle -ta' \rightarrow ext \langle va', h'' \rangle$
and $final\ thread.\ actions\ ok\ final\ s\ t\ ta' \vee final\ thread.\ actions\ ok'\ s\ t\ ta' \wedge final\ thread.\ actions\ subset$
 $ta'\ ta$
 $\langle proof \rangle$

end

context *heap-base* **begin**

lemma *red-external-ta-satisfiable*:
fixes $final$
assumes $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$
shows $\exists s. final\ thread.\ actions\ ok\ final\ s\ t\ ta$
 $\langle proof \rangle$

lemma *red-external-aggr-ta-satisfiable*:
fixes $final$

```

assumes  $(ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h$ 
shows  $\exists s. \text{final-thread.actions-ok final } s \ t \ ta$ 
<proof>

```

end

3.13.4 Determinism

context *heap-base* **begin**

lemma *heap-copy-loc-deterministic*:

```

assumes det: deterministic-heap-ops
and copy: heap-copy-loc a a' al h ops h' heap-copy-loc a a' al h ops' h''
shows  $ops = ops' \wedge h' = h''$ 
<proof>

```

lemma *heap-copies-deterministic*:

```

assumes det: deterministic-heap-ops
and copy: heap-copies a a' als h ops h' heap-copies a a' als h ops' h''
shows  $ops = ops' \wedge h' = h''$ 
<proof>

```

lemma *heap-clone-deterministic*:

```

assumes det: deterministic-heap-ops
and clone: heap-clone P h a h' obs heap-clone P h a h'' obs'
shows  $h' = h'' \wedge obs = obs'$ 
<proof>

```

lemma *red-external-deterministic*:

```

fixes final
assumes det: deterministic-heap-ops
and red: P, t \vdash \langle a \cdot M(vs), (shr s) \rangle -ta \rightarrow ext \langle va, h' \rangle \ P, t \vdash \langle a \cdot M(vs), (shr s) \rangle -ta' \rightarrow ext \langle va', h'' \rangle
and aok: final-thread.actions-ok final s t ta final-thread.actions-ok final s t ta'
shows  $ta = ta' \wedge va = va' \wedge h' = h''$ 
<proof>

```

end

end

3.14 Conformance for threads

theory *ConformThreaded*

imports

```

  ../Framework/FWLifting
  ../Framework/FWWellform
  Conform

```

begin

Every thread must be represented as an object whose address is its thread ID

context *heap-base* **begin**

```

abbreviation thread-conf ::  $'m \ \text{prog} \Rightarrow ('addr, 'thread-id, 'x) \ \text{thread-info} \Rightarrow 'heap \Rightarrow \text{bool}$ 
where  $\text{thread-conf } P \equiv \text{ts-ok } (\lambda t \ x \ m. \ P, m \vdash t \ \sqrt{t})$ 

```

lemma *thread-confI*:

$(\bigwedge t \ xln. \ ts \ t = [xln] \implies P, h \vdash t \ \surd t) \implies \text{thread-conf } P \ ts \ h$
 $\langle \text{proof} \rangle$

lemma *thread-confD*:

assumes *thread-conf* $P \ ts \ h \ ts \ t = [xln]$
shows $P, h \vdash t \ \surd t$
 $\langle \text{proof} \rangle$

lemma *thread-conf-ts-upd-eq* [*simp*]:

assumes *tst*: $ts \ t = [xln]$
shows $\text{thread-conf } P \ (ts(t \mapsto xln')) \ h \longleftrightarrow \text{thread-conf } P \ ts \ h$
 $\langle \text{proof} \rangle$

end

context *heap* **begin**

lemma *thread-conf-hext*:

$\llbracket \text{thread-conf } P \ ts \ h; h \trianglelefteq h' \rrbracket \implies \text{thread-conf } P \ ts \ h'$
 $\langle \text{proof} \rangle$

lemma *thread-conf-start-state*:

$\llbracket \text{start-heap-ok}; \text{wf-syscls } P \rrbracket \implies \text{thread-conf } P \ (\text{thr } (\text{start-state } f \ P \ C \ M \ vs)) \ (\text{shr } (\text{start-state } f \ P \ C \ M \ vs))$
 $\langle \text{proof} \rangle$

end

context *heap-base* **begin**

lemma *lock-thread-ok-start-state*:

$\text{lock-thread-ok } (\text{locks } (\text{start-state } f \ P \ C \ M \ vs)) \ (\text{thr } (\text{start-state } f \ P \ C \ M \ vs))$
 $\langle \text{proof} \rangle$

lemma *wset-thread-ok-start-state*:

$\text{wset-thread-ok } (\text{wset } (\text{start-state } f \ P \ C \ M \ vs)) \ (\text{thr } (\text{start-state } f \ P \ C \ M \ vs))$
 $\langle \text{proof} \rangle$

lemma *wset-final-ok-start-state*:

$\text{final-thread.wset-final-ok } \text{final } (\text{wset } (\text{start-state } f \ P \ C \ M \ vs)) \ (\text{thr } (\text{start-state } f \ P \ C \ M \ vs))$
 $\langle \text{proof} \rangle$

end

end

3.15 Binary Operators

theory *BinOp*

imports

WellForm Word-Lib.Bit-Shifts-Infix-Syntax

begin

datatype *bop* = — names of binary operations

```

  Eq
| NotEq
| LessThan
| LessOrEqual
| GreaterThan
| GreaterOrEqual
| Add
| Subtract
| Mult
| Div
| Mod
| BinAnd
| BinOr
| BinXor
| ShiftLeft
| ShiftRightZeros
| ShiftRightSigned

```

3.15.1 The semantics of binary operators

context

includes *bit-operations-syntax*

begin

type-synonym *'addr binop-ret* = *'addr val* + *'addr* — a value or the address of an exception

fun *binop-LessThan* :: *'addr val* \Rightarrow *'addr val* \Rightarrow *'addr binop-ret option*

where

```

  binop-LessThan (Intg i1) (Intg i2) = Some (Inl (Bool (i1 <s i2)))
| binop-LessThan v1 v2 = None

```

fun *binop-LessOrEqual* :: *'addr val* \Rightarrow *'addr val* \Rightarrow *'addr binop-ret option*

where

```

  binop-LessOrEqual (Intg i1) (Intg i2) = Some (Inl (Bool (i1 <=s i2)))
| binop-LessOrEqual v1 v2 = None

```

fun *binop-GreaterThan* :: *'addr val* \Rightarrow *'addr val* \Rightarrow *'addr binop-ret option*

where

```

  binop-GreaterThan (Intg i1) (Intg i2) = Some (Inl (Bool (i2 <s i1)))
| binop-GreaterThan v1 v2 = None

```

fun *binop-GreaterOrEqual* :: *'addr val* \Rightarrow *'addr val* \Rightarrow *'addr binop-ret option*

where

```

  binop-GreaterOrEqual (Intg i1) (Intg i2) = Some (Inl (Bool (i2 <=s i1)))
| binop-GreaterOrEqual v1 v2 = None

```

fun *binop-Add* :: *'addr val* \Rightarrow *'addr val* \Rightarrow *'addr binop-ret option*

where

```

  binop-Add (Intg i1) (Intg i2) = Some (Inl (Intg (i1 + i2)))
| binop-Add v1 v2 = None

```

```

fun binop-Subtract :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option
where
  binop-Subtract (Intg i1) (Intg i2) = Some (Inl (Intg (i1 - i2)))
| binop-Subtract v1 v2 = None

```

```

fun binop-Mult :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option
where
  binop-Mult (Intg i1) (Intg i2) = Some (Inl (Intg (i1 * i2)))
| binop-Mult v1 v2 = None

```

```

fun binop-BinAnd :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option
where
  binop-BinAnd (Intg i1) (Intg i2) = Some (Inl (Intg (i1 AND i2)))
| binop-BinAnd (Bool b1) (Bool b2) = Some (Inl (Bool (b1 ∧ b2)))
| binop-BinAnd v1 v2 = None

```

```

fun binop-BinOr :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option
where
  binop-BinOr (Intg i1) (Intg i2) = Some (Inl (Intg (i1 OR i2)))
| binop-BinOr (Bool b1) (Bool b2) = Some (Inl (Bool (b1 ∨ b2)))
| binop-BinOr v1 v2 = None

```

```

fun binop-BinXor :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option
where
  binop-BinXor (Intg i1) (Intg i2) = Some (Inl (Intg (i1 XOR i2)))
| binop-BinXor (Bool b1) (Bool b2) = Some (Inl (Bool (b1 ≠ b2)))
| binop-BinXor v1 v2 = None

```

```

fun binop-ShiftLeft :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option
where
  binop-ShiftLeft (Intg i1) (Intg i2) = Some (Inl (Intg (i1 << unat (i2 AND 0x1f))))
| binop-ShiftLeft v1 v2 = None

```

```

fun binop-ShiftRightZeros :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option
where
  binop-ShiftRightZeros (Intg i1) (Intg i2) = Some (Inl (Intg (i1 >> unat (i2 AND 0x1f))))
| binop-ShiftRightZeros v1 v2 = None

```

```

fun binop-ShiftRightSigned :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option
where
  binop-ShiftRightSigned (Intg i1) (Intg i2) = Some (Inl (Intg (i1 >>> unat (i2 AND 0x1f))))
| binop-ShiftRightSigned v1 v2 = None

```

Division on *'a word* is unsigned, but JLS specifies signed division.

```

definition word-sdiv :: 'a :: len word ⇒ 'a word ⇒ 'a word (infixl ‹sdiv› 70)

```

```

where [code]:

```

```

  x sdiv y =
    (let x' = sint x; y' = sint y;
      negative = (x' < 0) ≠ (y' < 0);
      result = abs x' div abs y'
      in word-of-int (if negative then -result else result))

```

```

definition word-smod :: 'a :: len word ⇒ 'a word ⇒ 'a word (infixl ‹smod› 70)

```

```

where [code]:

```



```

x smod y =
  (let x' = sint x; y' = sint y;
   negative = (x' < 0);
   result = abs x' mod abs y'
   in word-of-int (if negative then -result else result))
```

```
declare word-sdiv-def [simp] word-smod-def [simp]
```

```
lemma sdiv-smod-id: (a sdiv b) * b + (a smod b) = a
<proof>
```

```
end
```

```
notepad begin
```

```
<proof>
```

```
end
```

```
context heap-base
```

```
begin
```

```
fun binop-Mod :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option
```

```
where
```

```
  binop-Mod (Intg i1) (Intg i2) =
```

```
    Some (if i2 = 0 then Inr (addr-of-sys-xcpt ArithmeticException) else Inl (Intg (i1 smod i2)))
```

```
| binop-Mod v1 v2 = None
```

```
fun binop-Div :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option
```

```
where
```

```
  binop-Div (Intg i1) (Intg i2) =
```

```
    Some (if i2 = 0 then Inr (addr-of-sys-xcpt ArithmeticException) else Inl (Intg (i1 sdiv i2)))
```

```
| binop-Div v1 v2 = None
```

```
primrec binop :: binop ⇒ 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option
```

```
where
```

```
  binop Eq v1 v2 = Some (Inl (Bool (v1 = v2)))
```

```
| binop NotEq v1 v2 = Some (Inl (Bool (v1 ≠ v2)))
```

```
| binop LessThan = binop-LessThan
```

```
| binop LessOrEqual = binop-LessOrEqual
```

```
| binop GreaterThan = binop-GreaterThan
```

```
| binop GreaterOrEqual = binop-GreaterOrEqual
```

```
| binop Add = binop-Add
```

```
| binop Subtract = binop-Subtract
```

```
| binop Mult = binop-Mult
```

```
| binop Mod = binop-Mod
```

```
| binop Div = binop-Div
```

```
| binop BinAnd = binop-BinAnd
```

```
| binop BinOr = binop-BinOr
```

```
| binop BinXor = binop-BinXor
```

```
| binop ShiftLeft = binop-ShiftLeft
```

```
| binop ShiftRightZeros = binop-ShiftRightZeros
```

```
| binop ShiftRightSigned = binop-ShiftRightSigned
```

```
end
```

context

includes *bit-operations-syntax*

begin

lemma [*simp*]:

$(\text{binop-LessThan } v1 \ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists i1 \ i2. \ v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Bool } (i1 <_s i2)))$
 <proof>

lemma [*simp*]:

$(\text{binop-LessOrEqual } v1 \ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists i1 \ i2. \ v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Bool } (i1 <=s i2)))$
 <proof>

lemma [*simp*]:

$(\text{binop-GreaterThan } v1 \ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists i1 \ i2. \ v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Bool } (i2 <_s i1)))$
 <proof>

lemma [*simp*]:

$(\text{binop-GreaterOrEqual } v1 \ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists i1 \ i2. \ v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Bool } (i2 <=s i1)))$
 <proof>

lemma [*simp*]:

$(\text{binop-Add } v1 \ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists i1 \ i2. \ v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 + i2)))$
 <proof>

lemma [*simp*]:

$(\text{binop-Subtract } v1 \ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists i1 \ i2. \ v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 - i2)))$
 <proof>

lemma [*simp*]:

$(\text{binop-Mult } v1 \ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists i1 \ i2. \ v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 * i2)))$
 <proof>

lemma [*simp*]:

$(\text{binop-BinAnd } v1 \ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists b1 \ b2. \ v1 = \text{Bool } b1 \wedge v2 = \text{Bool } b2 \wedge va = \text{Inl } (\text{Bool } (b1 \wedge b2))) \vee$
 $(\exists i1 \ i2. \ v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \text{ AND } i2)))$
 <proof>

lemma [*simp*]:

$(\text{binop-BinOr } v1 \ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists b1 \ b2. \ v1 = \text{Bool } b1 \wedge v2 = \text{Bool } b2 \wedge va = \text{Inl } (\text{Bool } (b1 \vee b2))) \vee$
 $(\exists i1 \ i2. \ v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \text{ OR } i2)))$
 <proof>

lemma [*simp*]:

$(\text{binop-BinXor } v1 \ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists b1 \ b2. \ v1 = \text{Bool } b1 \wedge v2 = \text{Bool } b2 \wedge va = \text{Inl } (\text{Bool } (b1 \neq b2))) \vee$

($\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \text{ XOR } i2))$)
 $\langle \text{proof} \rangle$

lemma [simp]:
 ($\text{binop-ShiftLeft } v1\ v2 = \text{Some } va$) \longleftrightarrow
 ($\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \ll \text{unat } (i2 \text{ AND } 0x1f)))$)
 $\langle \text{proof} \rangle$

lemma [simp]:
 ($\text{binop-ShiftRightZeros } v1\ v2 = \text{Some } va$) \longleftrightarrow
 ($\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \gg \text{unat } (i2 \text{ AND } 0x1f)))$)
 $\langle \text{proof} \rangle$

lemma [simp]:
 ($\text{binop-ShiftRightSigned } v1\ v2 = \text{Some } va$) \longleftrightarrow
 ($\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \gg\gg \text{unat } (i2 \text{ AND } 0x1f)))$)
 $\langle \text{proof} \rangle$

end

context heap-base

begin

lemma [simp]:
 ($\text{binop-Mod } v1\ v2 = \text{Some } va$) \longleftrightarrow
 ($\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge$
 $va = (\text{if } i2 = 0 \text{ then } \text{Inr } (\text{addr-of-sys-xcpt } \text{ArithmeticException}) \text{ else } \text{Inl } (\text{Intg}(i1 \text{ smod } i2)))$)
 $\langle \text{proof} \rangle$

lemma [simp]:
 ($\text{binop-Div } v1\ v2 = \text{Some } va$) \longleftrightarrow
 ($\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge$
 $va = (\text{if } i2 = 0 \text{ then } \text{Inr } (\text{addr-of-sys-xcpt } \text{ArithmeticException}) \text{ else } \text{Inl } (\text{Intg}(i1 \text{ sdiv } i2)))$)
 $\langle \text{proof} \rangle$

end

3.15.2 Typing for binary operators

inductive WT-binop :: 'm prog \Rightarrow ty \Rightarrow bop \Rightarrow ty \Rightarrow ty \Rightarrow bool ($\langle \cdot \vdash \text{-}\langle \cdot \rangle \text{-} \cdot \rangle$ \rightarrow [51,0,0,0,51] 50)

where

WT-binop-Eq:

$P \vdash T1 \leq T2 \vee P \vdash T2 \leq T1 \implies P \vdash T1 \langle \text{Eq} \rangle T2 \text{ :: Boolean}$

| WT-binop-NotEq:

$P \vdash T1 \leq T2 \vee P \vdash T2 \leq T1 \implies P \vdash T1 \langle \text{NotEq} \rangle T2 \text{ :: Boolean}$

| WT-binop-LessThan:

$P \vdash \text{Integer} \langle \text{LessThan} \rangle \text{Integer} \text{ :: Boolean}$

| WT-binop-LessOrEqual:

$P \vdash \text{Integer} \langle \text{LessOrEqual} \rangle \text{Integer} \text{ :: Boolean}$

| WT-binop-GreaterThan:

$P \vdash \text{Integer} \langle \text{GreaterThan} \rangle \text{Integer} :: \text{Boolean}$

| *WT-binop-GreaterOrEqual*:
 $P \vdash \text{Integer} \langle \text{GreaterOrEqual} \rangle \text{Integer} :: \text{Boolean}$

| *WT-binop-Add*:
 $P \vdash \text{Integer} \langle \text{Add} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-Subtract*:
 $P \vdash \text{Integer} \langle \text{Subtract} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-Mult*:
 $P \vdash \text{Integer} \langle \text{Mult} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-Div*:
 $P \vdash \text{Integer} \langle \text{Div} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-Mod*:
 $P \vdash \text{Integer} \langle \text{Mod} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-BinAnd-Bool*:
 $P \vdash \text{Boolean} \langle \text{BinAnd} \rangle \text{Boolean} :: \text{Boolean}$

| *WT-binop-BinAnd-Int*:
 $P \vdash \text{Integer} \langle \text{BinAnd} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-BinOr-Bool*:
 $P \vdash \text{Boolean} \langle \text{BinOr} \rangle \text{Boolean} :: \text{Boolean}$

| *WT-binop-BinOr-Int*:
 $P \vdash \text{Integer} \langle \text{BinOr} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-BinXor-Bool*:
 $P \vdash \text{Boolean} \langle \text{BinXor} \rangle \text{Boolean} :: \text{Boolean}$

| *WT-binop-BinXor-Int*:
 $P \vdash \text{Integer} \langle \text{BinXor} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-ShiftLeft*:
 $P \vdash \text{Integer} \langle \text{ShiftLeft} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-ShiftRightZeros*:
 $P \vdash \text{Integer} \langle \text{ShiftRightZeros} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-ShiftRightSigned*:
 $P \vdash \text{Integer} \langle \text{ShiftRightSigned} \rangle \text{Integer} :: \text{Integer}$

lemma *WT-binopI* [*intro*]:

$P \vdash T1 \leq T2 \vee P \vdash T2 \leq T1 \implies P \vdash T1 \langle \text{Eq} \rangle T2 :: \text{Boolean}$

$P \vdash T1 \leq T2 \vee P \vdash T2 \leq T1 \implies P \vdash T1 \langle \text{NotEq} \rangle T2 :: \text{Boolean}$

$\text{bop} = \text{Add} \vee \text{bop} = \text{Subtract} \vee \text{bop} = \text{Mult} \vee \text{bop} = \text{Mod} \vee \text{bop} = \text{Div} \vee \text{bop} = \text{BinAnd} \vee \text{bop} =$
 $\text{BinOr} \vee \text{bop} = \text{BinXor} \vee$

$\text{bop} = \text{ShiftLeft} \vee \text{bop} = \text{ShiftRightZeros} \vee \text{bop} = \text{ShiftRightSigned}$

$\implies P \vdash \text{Integer} \langle \text{bop} \rangle \text{Integer} :: \text{Integer}$

$bop = LessThan \vee bop = LessOrEqual \vee bop = GreaterThan \vee bop = GreaterOrEqual \implies P \vdash Integer \langle bop \rangle Integer :: Boolean$

$bop = BinAnd \vee bop = BinOr \vee bop = BinXor \implies P \vdash Boolean \langle bop \rangle Boolean :: Boolean$
 ⟨proof⟩

inductive-cases [elim]:

$P \vdash T1 \langle Eq \rangle T2 :: T$

$P \vdash T1 \langle NotEq \rangle T2 :: T$

$P \vdash T1 \langle LessThan \rangle T2 :: T$

$P \vdash T1 \langle LessOrEqual \rangle T2 :: T$

$P \vdash T1 \langle GreaterThan \rangle T2 :: T$

$P \vdash T1 \langle GreaterOrEqual \rangle T2 :: T$

$P \vdash T1 \langle Add \rangle T2 :: T$

$P \vdash T1 \langle Subtract \rangle T2 :: T$

$P \vdash T1 \langle Mult \rangle T2 :: T$

$P \vdash T1 \langle Div \rangle T2 :: T$

$P \vdash T1 \langle Mod \rangle T2 :: T$

$P \vdash T1 \langle BinAnd \rangle T2 :: T$

$P \vdash T1 \langle BinOr \rangle T2 :: T$

$P \vdash T1 \langle BinXor \rangle T2 :: T$

$P \vdash T1 \langle ShiftLeft \rangle T2 :: T$

$P \vdash T1 \langle ShiftRightZeros \rangle T2 :: T$

$P \vdash T1 \langle ShiftRightSigned \rangle T2 :: T$

lemma *WT-binop-fun*: $\llbracket P \vdash T1 \langle bop \rangle T2 :: T; P \vdash T1 \langle bop \rangle T2 :: T' \rrbracket \implies T = T'$
 ⟨proof⟩

lemma *WT-binop-is-type*:

$\llbracket P \vdash T1 \langle bop \rangle T2 :: T; \text{is-type } P \ T1; \text{is-type } P \ T2 \rrbracket \implies \text{is-type } P \ T$
 ⟨proof⟩

inductive *WTrt-binop* :: 'm prog \Rightarrow ty \Rightarrow bop \Rightarrow ty \Rightarrow ty \Rightarrow bool ($\langle - \vdash - \langle - \rangle - : - \rangle$ [51,0,0,0,51] 50)

where

WTrt-binop-Eq:

$P \vdash T1 \langle Eq \rangle T2 : Boolean$

| *WTrt-binop-NotEq*:

$P \vdash T1 \langle NotEq \rangle T2 : Boolean$

| *WTrt-binop-LessThan*:

$P \vdash Integer \langle LessThan \rangle Integer : Boolean$

| *WTrt-binop-LessOrEqual*:

$P \vdash Integer \langle LessOrEqual \rangle Integer : Boolean$

| *WTrt-binop-GreaterThan*:

$P \vdash Integer \langle GreaterThan \rangle Integer : Boolean$

| *WTrt-binop-GreaterOrEqual*:

$P \vdash Integer \langle GreaterOrEqual \rangle Integer : Boolean$

| *WTrt-binop-Add*:

$P \vdash Integer \langle Add \rangle Integer : Integer$

- | *WTrt-binop-Subtract*:
 $P \vdash \text{Integer} \langle \text{Subtract} \rangle \text{Integer} : \text{Integer}$
- | *WTrt-binop-Mult*:
 $P \vdash \text{Integer} \langle \text{Mult} \rangle \text{Integer} : \text{Integer}$
- | *WTrt-binop-Div*:
 $P \vdash \text{Integer} \langle \text{Div} \rangle \text{Integer} : \text{Integer}$
- | *WTrt-binop-Mod*:
 $P \vdash \text{Integer} \langle \text{Mod} \rangle \text{Integer} : \text{Integer}$
- | *WTrt-binop-BinAnd-Bool*:
 $P \vdash \text{Boolean} \langle \text{BinAnd} \rangle \text{Boolean} : \text{Boolean}$
- | *WTrt-binop-BinAnd-Int*:
 $P \vdash \text{Integer} \langle \text{BinAnd} \rangle \text{Integer} : \text{Integer}$
- | *WTrt-binop-BinOr-Bool*:
 $P \vdash \text{Boolean} \langle \text{BinOr} \rangle \text{Boolean} : \text{Boolean}$
- | *WTrt-binop-BinOr-Int*:
 $P \vdash \text{Integer} \langle \text{BinOr} \rangle \text{Integer} : \text{Integer}$
- | *WTrt-binop-BinXor-Bool*:
 $P \vdash \text{Boolean} \langle \text{BinXor} \rangle \text{Boolean} : \text{Boolean}$
- | *WTrt-binop-BinXor-Int*:
 $P \vdash \text{Integer} \langle \text{BinXor} \rangle \text{Integer} : \text{Integer}$
- | *WTrt-binop-ShiftLeft*:
 $P \vdash \text{Integer} \langle \text{ShiftLeft} \rangle \text{Integer} : \text{Integer}$
- | *WTrt-binop-ShiftRightZeros*:
 $P \vdash \text{Integer} \langle \text{ShiftRightZeros} \rangle \text{Integer} : \text{Integer}$
- | *WTrt-binop-ShiftRightSigned*:
 $P \vdash \text{Integer} \langle \text{ShiftRightSigned} \rangle \text{Integer} : \text{Integer}$

lemma *WTrt-binopI* [intro]:

- $P \vdash T1 \langle \text{Eq} \rangle T2 : \text{Boolean}$
- $P \vdash T1 \langle \text{NotEq} \rangle T2 : \text{Boolean}$
- $\text{bop} = \text{Add} \vee \text{bop} = \text{Subtract} \vee \text{bop} = \text{Mult} \vee \text{bop} = \text{Div} \vee \text{bop} = \text{Mod} \vee \text{bop} = \text{BinAnd} \vee \text{bop} = \text{BinOr} \vee \text{bop} = \text{BinXor} \vee$
- $\text{bop} = \text{ShiftLeft} \vee \text{bop} = \text{ShiftRightZeros} \vee \text{bop} = \text{ShiftRightSigned}$
- $\implies P \vdash \text{Integer} \langle \text{bop} \rangle \text{Integer} : \text{Integer}$
- $\text{bop} = \text{LessThan} \vee \text{bop} = \text{LessOrEqual} \vee \text{bop} = \text{GreaterThan} \vee \text{bop} = \text{GreaterOrEqual} \implies P \vdash$
- $\text{Integer} \langle \text{bop} \rangle \text{Integer} : \text{Boolean}$
- $\text{bop} = \text{BinAnd} \vee \text{bop} = \text{BinOr} \vee \text{bop} = \text{BinXor} \implies P \vdash \text{Boolean} \langle \text{bop} \rangle \text{Boolean} : \text{Boolean}$
- <proof>*

inductive-cases *WTrt-binop-cases* [elim]:

- $P \vdash T1 \langle \text{Eq} \rangle T2 : T$
- $P \vdash T1 \langle \text{NotEq} \rangle T2 : T$

$P \vdash T1 \llbracket \text{LessThan} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{LessOrEqual} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{GreaterThan} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{GreaterOrEqual} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{Add} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{Subtract} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{Mult} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{Div} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{Mod} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{BinAnd} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{BinOr} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{BinXor} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{ShiftLeft} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{ShiftRightZeros} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{ShiftRightSigned} \rrbracket T2 : T$

inductive-simps *WTrt-binop-simps* [*simp*]:

$P \vdash T1 \llbracket \text{Eq} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{NotEq} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{LessThan} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{LessOrEqual} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{GreaterThan} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{GreaterOrEqual} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{Add} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{Subtract} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{Mult} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{Div} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{Mod} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{BinAnd} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{BinOr} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{BinXor} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{ShiftLeft} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{ShiftRightZeros} \rrbracket T2 : T$
 $P \vdash T1 \llbracket \text{ShiftRightSigned} \rrbracket T2 : T$

fun *binop-relevant-class* :: *bop* \Rightarrow 'm prog \Rightarrow *cname* \Rightarrow *bool*

where

$\text{binop-relevant-class } \text{Div} = (\lambda P C. P \vdash \text{ArithmeticException} \preceq^* C)$
 $\text{binop-relevant-class } \text{Mod} = (\lambda P C. P \vdash \text{ArithmeticException} \preceq^* C)$
 $\text{binop-relevant-class } - = (\lambda P C. \text{False})$

lemma *WT-binop-WTrt-binop*:

$P \vdash T1 \llbracket \text{bop} \rrbracket T2 :: T \Longrightarrow P \vdash T1 \llbracket \text{bop} \rrbracket T2 : T$
 <proof>

context *heap begin*

lemma *binop-progress*:

$\llbracket \text{typeof}_h v1 = \llbracket T1 \rrbracket; \text{typeof}_h v2 = \llbracket T2 \rrbracket; P \vdash T1 \llbracket \text{bop} \rrbracket T2 : T \rrbracket$
 $\Longrightarrow \exists va. \text{binop } \text{bop } v1 v2 = \llbracket va \rrbracket$
 <proof>

lemma *binop-type*:

assumes *wf*: *wf-prog wf-md P*

and *pre*: *preallocated h*
and *type*: $\text{typeof}_h v1 = [T1] \text{typeof}_h v2 = [T2] P \vdash T1 \ll bop \gg T2 : T$
shows $\text{binop } bop \ v1 \ v2 = [Inl \ v] \implies P, h \vdash v : \leq T$
and $\text{binop } bop \ v1 \ v2 = [Inr \ a] \implies P, h \vdash \text{Addr } a : \leq \text{Class Throwable}$
 ⟨*proof*⟩

lemma *binop-relevant-class*:
assumes *wf*: *wf-prog wf-md P*
and *pre*: *preallocated h*
and *bop*: $\text{binop } bop \ v1 \ v2 = [Inr \ a]$
and *sup*: $P \vdash \text{cname-of } h \ a \ \preceq^* \ C$
shows *binop-relevant-class bop P C*
 ⟨*proof*⟩

end

lemma *WTrt-binop-fun*: $\llbracket P \vdash T1 \ll bop \gg T2 : T; P \vdash T1 \ll bop \gg T2 : T' \rrbracket \implies T = T'$
 ⟨*proof*⟩

lemma *WTrt-binop-THE [simp]*: $P \vdash T1 \ll bop \gg T2 : T \implies \text{The } (WTrt\text{-binop } P \ T1 \ bop \ T2) = T$
 ⟨*proof*⟩

lemma *WTrt-binop-widen-mono*:
 $\llbracket P \vdash T1 \ll bop \gg T2 : T; P \vdash T1' \leq T1; P \vdash T2' \leq T2 \rrbracket \implies \exists T'. P \vdash T1' \ll bop \gg T2' : T' \wedge P \vdash T' \leq T$
 ⟨*proof*⟩

lemma *WTrt-binop-is-type*:
 $\llbracket P \vdash T1 \ll bop \gg T2 : T; \text{is-type } P \ T1; \text{is-type } P \ T2 \rrbracket \implies \text{is-type } P \ T$
 ⟨*proof*⟩

3.15.3 Code generator setup

lemmas [*code*] =
heap-base.binop-Div.simps
heap-base.binop-Mod.simps
heap-base.binop.simps

code-pred
 (*modes*: $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}, i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$)
WT-binop
 ⟨*proof*⟩

code-pred
 (*modes*: $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}, i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$)
WTrt-binop
 ⟨*proof*⟩

lemma *eval-WTrt-binop-i-i-i-i-o*:
 $\text{Predicate.eval } (WTrt\text{-binop-i-i-i-i-o } P \ T1 \ bop \ T2) \ T \longleftrightarrow P \vdash T1 \ll bop \gg T2 : T$
 ⟨*proof*⟩

lemma *the-WTrt-binop-code*:
 $(\text{THE } T. P \vdash T1 \ll bop \gg T2 : T) = \text{Predicate.the } (WTrt\text{-binop-i-i-i-i-o } P \ T1 \ bop \ T2)$

<proof>

end

3.16 The Jinja Type System as a Semilattice

theory *SemiType*

imports

WellForm

../DFA/Semilattices

begin

inductive-set

widen1 :: 'a prog \Rightarrow (ty \times ty) set

and *widen1-syntax* :: 'a prog \Rightarrow ty \Rightarrow ty \Rightarrow bool ($\langle \cdot \vdash \cdot \rangle^{<1} \rightarrow [71,71,71]$ 70)

for *P* :: 'a prog

where

$P \vdash C <^1 D \equiv (C, D) \in \text{widen1 } P$

| *widen1-Array-Object*:

$P \vdash \text{Array } (\text{Class Object}) <^1 \text{Class Object}$

| *widen1-Array-Integer*:

$P \vdash \text{Array Integer} <^1 \text{Class Object}$

| *widen1-Array-Boolean*:

$P \vdash \text{Array Boolean} <^1 \text{Class Object}$

| *widen1-Array-Void*:

$P \vdash \text{Array Void} <^1 \text{Class Object}$

| *widen1-Class*:

$P \vdash C <^1 D \implies P \vdash \text{Class } C <^1 \text{Class } D$

| *widen1-Array-Array*:

$\llbracket P \vdash T <^1 U; \neg \text{is-NT-Array } T \rrbracket \implies P \vdash \text{Array } T <^1 \text{Array } U$

abbreviation *widen1-trancl* :: 'a prog \Rightarrow ty \Rightarrow ty \Rightarrow bool ($\langle \cdot \vdash \cdot \rangle^{<+} \rightarrow [71,71,71]$ 70) **where**

$P \vdash T <^+ U \equiv (T, U) \in \text{trancl } (\text{widen1 } P)$

abbreviation *widen1-rtrancl* :: 'a prog \Rightarrow ty \Rightarrow ty \Rightarrow bool ($\langle \cdot \vdash \cdot \rangle^{<^*} \rightarrow [71,71,71]$ 70) **where**

$P \vdash T <^* U \equiv (T, U) \in \text{rtrancl } (\text{widen1 } P)$

inductive-simps *widen1-simps1* [*simp*]:

$P \vdash \text{Integer} <^1 T$

$P \vdash \text{Boolean} <^1 T$

$P \vdash \text{Void} <^1 T$

$P \vdash \text{Class Object} <^1 T$

$P \vdash \text{NT} <^1 U$

inductive-simps *widen1-simps* [*simp*]:

$P \vdash \text{Array } (\text{Class Object}) <^1 T$

$P \vdash \text{Array Integer} <^1 T$

$P \vdash \text{Array Boolean} <^1 T$
 $P \vdash \text{Array Void} <^1 T$
 $P \vdash \text{Class } C <^1 T$
 $P \vdash T <^1 \text{Array } U$

lemma *is-type-widen1*:
assumes *icO*: *is-class P Object*
shows $P \vdash T <^1 U \implies \text{is-type } P T$
 <proof>

lemma *widen1-NT-Array*:
assumes *is-NT-Array T*
shows $\neg P \vdash T[] <^1 U$
 <proof>

lemma *widen1-is-type*:
assumes *wfP*: *wf-prog wfmd P*
shows $(A, B) \in \text{widen1 } P \implies \text{is-type } P B$
 <proof>

lemma *widen1-trancl-is-type*:
assumes *wfP*: *wf-prog wfmd P*
shows $(A, B) \in (\text{widen1 } P)^{\wedge+} \implies \text{is-type } P B$
 <proof>

lemma *single-valued-widen1*:
assumes *wf*: *wf-prog wf-md P*
shows *single-valued (widen1 P)*
 <proof>

function *inheritance-level* :: 'a prog \Rightarrow cname \Rightarrow nat **where**
inheritance-level P C =
 (if *acyclicP (subcls1 P) \wedge is-class P C \wedge C \neq Object*
 then *Suc (inheritance-level P (fst (the (class P C))))*
 else 0)

<proof>

termination

<proof>

fun *subtype-measure* :: 'a prog \Rightarrow ty \Rightarrow nat **where**
subtype-measure P (Class C) = *inheritance-level P C*
 | *subtype-measure P (Array T)* = 1 + *subtype-measure P T*
 | *subtype-measure P T* = 0

lemma *subtype-measure-measure*:
assumes *acyclic*: *acyclicP (subcls1 P)*
and *widen1*: $P \vdash x <^1 y$
shows *subtype-measure P y* < *subtype-measure P x*
 <proof>

lemma *wf-converse-widen1*:
assumes *wfP*: *wf-prog wfmc P*
shows *wf ((widen1 P) $^{\wedge-1}$)*
 <proof>

lemma *subcls-into-widen1-rtrancl*:

$$P \vdash C \preceq^* D \implies P \vdash \text{Class } C <^* \text{Class } D$$

<proof>

lemma *not-widen1-NT-Array*:

$$P \vdash U <^1 T \implies \neg \text{is-NT-Array } T$$

<proof>

lemma *widen1-trancl-into-Array-widen1-trancl*:

$$\llbracket P \vdash A <^+ B; \neg \text{is-NT-Array } A \rrbracket \implies P \vdash A[] <^+ B[]$$

<proof>

lemma *widen1-rtrancl-into-Array-widen1-rtrancl*:

$$\llbracket P \vdash A <^* B; \neg \text{is-NT-Array } A \rrbracket \implies P \vdash A[] <^* B[]$$

<proof>

lemma *Array-Object-widen1-trancl*:

assumes *wf*: *wf-prog wmdc P*

and *itA*: *is-type P (A[])*

shows $P \vdash A[] <^+ \text{Class Object}$

<proof>

lemma *widen-into-widen1-trancl*:

assumes *wf*: *wf-prog wfmd P*

shows $\llbracket P \vdash A \leq B; A \neq B; A \neq \text{NT}; \text{is-type } P A \rrbracket \implies P \vdash A <^+ B$

<proof>

lemma *wf-prog-impl-acc-widen*:

assumes *wfP*: *wf-prog wfmd P*

shows *acc (types P) (widen P)*

<proof>

lemmas *wf-widen-acc = wf-prog-impl-acc-widen*

declare *wf-widen-acc [intro, simp]*

lemma *acyclic-widen1*:

$$\text{wf-prog wfmc } P \implies \text{acyclic (widen1 } P)$$

<proof>

lemma *widen1-into-widen*:

$$(A, B) \in \text{widen1 } P \implies P \vdash A \leq B$$

<proof>

lemma *widen1-rtrancl-into-widen*:

$$P \vdash A <^* B \implies P \vdash A \leq B$$

<proof>

lemma *widen-eq-widen1-trancl*:

$$\llbracket \text{wf-prog wf-md } P; T \neq \text{NT}; T \neq U; \text{is-type } P T \rrbracket \implies P \vdash T \leq U \iff P \vdash T <^+ U$$

<proof>

lemma *sup-is-type*:

assumes *wf*: *wf-prog wf-md P*

and itA : $is\text{-}type\ P\ A$
and itB : $is\text{-}type\ P\ B$
and sup : $sup\ P\ A\ B = OK\ T$
shows $is\text{-}type\ P\ T$
 $\langle proof \rangle$

lemma $closed\text{-}err\text{-}types$:
assumes wfP : $wf\text{-}prog\ wf\text{-}mb\ P$
shows $closed\ (err\ (types\ P))\ (lift2\ (sup\ P))$
 $\langle proof \rangle$

lemma $widen\text{-}into\text{-}widen1\text{-}rtrancl$:
 $\llbracket wf\text{-}prog\ wfmd\ P; widen\ P\ A\ B; A \neq NT; is\text{-}type\ P\ A \rrbracket \implies (A, B) \in (widen1\ P)^*$
 $\langle proof \rangle$

lemma $sup\text{-}widen\text{-}greater$:
assumes wfP : $wf\text{-}prog\ wf\text{-}mb\ P$
and $it1$: $is\text{-}type\ P\ t1$
and $it2$: $is\text{-}type\ P\ t2$
and sup : $sup\ P\ t1\ t2 = OK\ s$
shows $widen\ P\ t1\ s \wedge widen\ P\ t2\ s$
 $\langle proof \rangle$

lemma $sup\text{-}widen\text{-}smallest$:
assumes wfP : $wf\text{-}prog\ wf\text{-}mb\ P$
and itT : $is\text{-}type\ P\ T$
and itU : $is\text{-}type\ P\ U$
and TwV : $P \vdash T \leq V$
and UwV : $P \vdash U \leq V$
and sup : $sup\ P\ T\ U = OK\ W$
shows $widen\ P\ W\ V$
 $\langle proof \rangle$

lemma $sup\text{-}exists$:
 $\llbracket widen\ P\ a\ c; widen\ P\ b\ c \rrbracket \implies \exists T. sup\ P\ a\ b = OK\ T$
 $\langle proof \rangle$

lemma $err\text{-}semilat\text{-}JType\text{-}esl$:
assumes $wf\text{-}prog$: $wf\text{-}prog\ wf\text{-}mb\ P$
shows $err\text{-}semilat\ (esl\ P)$
 $\langle proof \rangle$

3.16.1 Relation between $SemiType.sup\ P\ T\ U = OK\ V$ and $P \vdash lub(T, U) = V$

lemma $sup\text{-}is\text{-}lubI$:
assumes wf : $wf\text{-}prog\ wf\text{-}md\ P$
and it : $is\text{-}type\ P\ T\ is\text{-}type\ P\ U$
and sup : $sup\ P\ T\ U = OK\ V$
shows $P \vdash lub(T, U) = V$
 $\langle proof \rangle$

lemma $is\text{-}lub\text{-}subD$:
assumes wf : $wf\text{-}prog\ wf\text{-}md\ P$

and *it*: *is-type* P T *is-type* P U
and *lub*: $P \vdash \text{lub}(T, U) = V$
shows *sup* P T $U = OK$ V
 <proof>

lemma *is-lub-is-type*:

$\llbracket \text{wf-prog } \text{wf-md } P; \text{is-type } P \ T; \text{is-type } P \ U; P \vdash \text{lub}(T, U) = V \rrbracket \implies \text{is-type } P \ V$
 <proof>

3.16.2 Code generator setup

code-pred *widen1p* <proof>

lemmas [*code*] = *widen1-def*

lemma *eval-widen1p-i-i-o-conv*:

$\text{Predicate.eval } (\text{widen1p-i-i-o } P \ T) = (\lambda U. P \vdash T <^1 U)$
 <proof>

lemma *rtrancl-widen1-code* [*code-unfold*]:

$(\text{widen1 } P)^{\hat{*}} = \{(a, b). \text{Predicate.holds } (\text{rtrancl-tab-FioB-i-i-i } (\text{widen1p-i-i-o } P) \ [] \ a \ b)\}$
 <proof>

declare *exec-lub-def* [*code-unfold*]

end

theory *Common-Main*

imports

../Basic/Auxiliary
../Framework/FWProgress
../Framework/FWBisimDeadlock
../Framework/FWBisimLift
../DFA/Abstract-BV
ExternalCallWF
ConformThreaded
BinOp
SemiType

begin

end

Chapter 4

JinjaThreads source language

4.1 Program State

```
theory State
imports
  ../Common/Heap
begin

type-synonym
  'addr locals = vname  $\rightarrow$  'addr val    — local vars, incl. params and “this”
type-synonym
  ('addr, 'heap) Jstate = 'heap  $\times$  'addr locals    — the heap and the local vars

definition hp :: 'heap  $\times$  'x  $\Rightarrow$  'heap where hp  $\equiv$  fst

definition lcl :: 'heap  $\times$  'x  $\Rightarrow$  'x where lcl  $\equiv$  snd

lemma hp-conv [simp]: hp (h, l) = h
<proof>

lemma lcl-conv [simp]: lcl (h, l) = l
<proof>

end
```

4.2 Expressions

```
theory Expr
imports
  ../Common/BinOp
begin

datatype (dead 'a, dead 'b, dead 'addr) exp
  = new cname    — class instance creation
  | newArray ty ('a,'b,'addr) exp (⟨newA -[-]⟩ [99,0] 90)    — array instance creation: type, size in
  outermost dimension
  | Cast ty ('a,'b,'addr) exp    — type cast
  | InstanceOf ('a,'b,'addr) exp ty (⟨instanceof -> [99, 99] 90) — instance of
```

| *Val* 'addr val — value
 | *BinOp* ('a,'b,'addr) exp bop ('a,'b,'addr) exp (⟨- «-» -> [80,0,81] 80) — binary operation
 | *Var* 'a — local variable (incl. parameter)
 | *LAss* 'a ('a,'b,'addr) exp (⟨-:=> [90,90]90) — local assignment
 | *AAcc* ('a,'b,'addr) exp ('a,'b,'addr) exp (⟨-[-]> [99,0] 90) — array cell read
 | *AAss* ('a,'b,'addr) exp ('a,'b,'addr) exp ('a,'b,'addr) exp (⟨-[-] := -> [10,99,90] 90) — array cell assignment
 | *ALen* ('a,'b,'addr) exp (⟨-length> [10] 90) — array length
 | *FAcc* ('a,'b,'addr) exp vname cname (⟨--{-}> [10,90,99]90) — field access
 | *FAss* ('a,'b,'addr) exp vname cname ('a,'b,'addr) exp (⟨--{-} := -> [10,90,99,90]90) — field assignment
 | *CompareAndSwap* ('a,'b,'addr) exp cname vname ('a,'b,'addr) exp ('a,'b,'addr) exp (⟨-compareAndSwap('(--, -, -)⟩ [10,90,90,90,90] 90) — compare and swap
 | *Call* ('a,'b,'addr) exp mname ('a,'b,'addr) exp list (⟨--'(-)⟩ [90,99,0] 90) — method call
 | *Block* 'a ty 'addr val option ('a,'b,'addr) exp (⟨{-:-;- -}>)
 | *Synchronized* 'b ('a,'b,'addr) exp ('a,'b,'addr) exp (⟨sync- '(-) -> [99,99,90] 90)
 | *InSynchronized* 'b 'addr ('a,'b,'addr) exp (⟨insync- '(-) -> [99,99,90] 90)
 | *Seq* ('a,'b,'addr) exp ('a,'b,'addr) exp (⟨-;;/- -> [61,60]60)
 | *Cond* ('a,'b,'addr) exp ('a,'b,'addr) exp ('a,'b,'addr) exp (⟨if '(-) -/ else -> [80,79,79]70)
 | *While* ('a,'b,'addr) exp ('a,'b,'addr) exp (⟨while '(-) -> [80,79]70)
 | *throw* ('a,'b,'addr) exp
 | *TryCatch* ('a,'b,'addr) exp cname 'a ('a,'b,'addr) exp (⟨try -/ catch'(- -) -> [0,99,80,79] 70)

type-synonym

'addr expr = (vname, unit, 'addr) exp — Jinja expression

type-synonym

'addr J-mb = vname list × 'addr expr — Jinja method body: parameter names and expression

type-synonym

'addr J-prog = 'addr J-mb prog — Jinja program

translations

(type) 'addr expr <= (type) (String.literal, unit, 'addr) exp

(type) 'addr J-prog <= (type) (String.literal list × 'addr expr) prog

4.2.1 Syntactic sugar

abbreviation unit :: ('a,'b,'addr) exp

where unit ≡ Val Unit

abbreviation null :: ('a,'b,'addr) exp

where null ≡ Val Null

abbreviation addr :: 'addr ⇒ ('a,'b,'addr) exp

where addr a == Val (Addr a)

abbreviation true :: ('a,'b,'addr) exp

where true == Val (Bool True)

abbreviation false :: ('a,'b,'addr) exp

where false == Val (Bool False)

abbreviation Throw :: 'addr ⇒ ('a,'b,'addr) exp

where Throw a == throw (Val (Addr a))

abbreviation (in *heap-base*) *THROW* :: *cname* \Rightarrow ('a,'b,'addr) *exp*
where *THROW* *xc* == *Throw* (*addr-of-sys-xcpt* *xc*)

abbreviation *sync-unit-syntax* :: ('a,unit,'addr) *exp* \Rightarrow ('a,unit,'addr) *exp* \Rightarrow ('a,unit,'addr) *exp*
(*sync*'(-) -> [99,90] 90)
where *sync*(*e1*) *e2* \equiv *sync*₍₎ (*e1*) *e2*

abbreviation *insync-unit-syntax* :: 'addr \Rightarrow ('a,unit,'addr) *exp* \Rightarrow ('a,unit,'addr) *exp* (*insync*'(-) -> [99,90] 90)
where *insync*(*a*) *e2* \equiv *insync*₍₎ (*a*) *e2*

Java syntax for binary operators

abbreviation *BinOp-Eq* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«==»* -> [80,81] 80)
where *e* *«==»* *e'* \equiv *e* *«Eq»* *e'*

abbreviation *BinOp-NotEq* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«!=»* -> [80,81] 80)
where *e* *«!=»* *e'* \equiv *e* *«NotEq»* *e'*

abbreviation *BinOp-LessThan* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«<»* -> [80,81] 80)
where *e* *«<»* *e'* \equiv *e* *«LessThan»* *e'*

abbreviation *BinOp-LessOrEqual* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«<=»* -> [80,81] 80)
where *e* *«<=»* *e'* \equiv *e* *«LessOrEqual»* *e'*

abbreviation *BinOp-GreaterThan* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«>»* -> [80,81] 80)
where *e* *«>»* *e'* \equiv *e* *«GreaterThan»* *e'*

abbreviation *BinOp-GreaterOrEqual* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«>=»* -> [80,81] 80)
where *e* *«>=»* *e'* \equiv *e* *«GreaterOrEqual»* *e'*

abbreviation *BinOp-Add* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«+»* -> [80,81] 80)
where *e* *«+»* *e'* \equiv *e* *«Add»* *e'*

abbreviation *BinOp-Subtract* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«-»* -> [80,81] 80)
where *e* *«-»* *e'* \equiv *e* *«Subtract»* *e'*

abbreviation *BinOp-Mult* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«*»* -> [80,81] 80)
where *e* *«*»* *e'* \equiv *e* *«Mult»* *e'*

abbreviation *BinOp-Div* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«/»* -> [80,81] 80)
where *e* *«/»* *e'* \equiv *e* *«Div»* *e'*

abbreviation *BinOp-Mod* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*

($\leftarrow \langle \% \rangle \rightarrow [80,81] 80$)
where $e \langle \% \rangle e' \equiv e \langle \text{Mod} \rangle e'$

abbreviation *BinOp-BinAnd* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle \& \rangle \rightarrow [80,81] 80$)
where $e \langle \& \rangle e' \equiv e \langle \text{BinAnd} \rangle e'$

abbreviation *BinOp-BinOr* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle | \rangle \rightarrow [80,81] 80$)
where $e \langle | \rangle e' \equiv e \langle \text{BinOr} \rangle e'$

abbreviation *BinOp-BinXor* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle \wedge \rangle \rightarrow [80,81] 80$)
where $e \langle \wedge \rangle e' \equiv e \langle \text{BinXor} \rangle e'$

abbreviation *BinOp-ShiftLeft* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle \ll \rangle \rightarrow [80,81] 80$)
where $e \langle \ll \rangle e' \equiv e \langle \text{ShiftLeft} \rangle e'$

abbreviation *BinOp-ShiftRightZeros* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle \gg \rangle \rightarrow [80,81] 80$)
where $e \langle \gg \rangle e' \equiv e \langle \text{ShiftRightZeros} \rangle e'$

abbreviation *BinOp-ShiftRightSigned* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle \gg \rangle \rightarrow [80,81] 80$)
where $e \langle \gg \rangle e' \equiv e \langle \text{ShiftRightSigned} \rangle e'$

abbreviation *BinOp-CondAnd* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle \&\& \rangle \rightarrow [80,81] 80$)
where $e \langle \&\& \rangle e' \equiv \text{if } (e) e' \text{ else false}$

abbreviation *BinOp-CondOr* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle || \rangle \rightarrow [80,81] 80$)
where $e \langle || \rangle e' \equiv \text{if } (e) \text{ true else } e'$

lemma *inj-Val* [*simp*]: *inj Val*
 $\langle \text{proof} \rangle$

lemma *expr-ineqs* [*simp*]: *Val v* ;; $e \neq e$ *if* ($e1$) e *else* $e2 \neq e$ *if* ($e1$) $e2$ *else* $e \neq e$
 $\langle \text{proof} \rangle$

4.2.2 Free Variables

primrec *fv* :: ($'a, 'b, 'addr$) *exp* \Rightarrow $'a$ *set*
and *fvs* :: ($'a, 'b, 'addr$) *exp list* \Rightarrow $'a$ *set*
where

$fv(\text{new } C) = \{\}$
 $| fv(\text{newA } T[e]) = fv\ e$
 $| fv(\text{Cast } C\ e) = fv\ e$
 $| fv(e\ \text{instanceof } T) = fv\ e$
 $| fv(\text{Val } v) = \{\}$
 $| fv(e_1 \langle \text{bop} \rangle e_2) = fv\ e_1 \cup fv\ e_2$
 $| fv(\text{Var } V) = \{V\}$
 $| fv(a[i]) = fv\ a \cup fv\ i$

$| \text{fv}(AAss\ a\ i\ e) = \text{fv}\ a \cup \text{fv}\ i \cup \text{fv}\ e$
 $| \text{fv}(a.\text{length}) = \text{fv}\ a$
 $| \text{fv}(LAss\ V\ e) = \{V\} \cup \text{fv}\ e$
 $| \text{fv}(e.F\{D\}) = \text{fv}\ e$
 $| \text{fv}(FAss\ e_1\ F\ D\ e_2) = \text{fv}\ e_1 \cup \text{fv}\ e_2$
 $| \text{fv}(e_1.\text{compareAndSwap}(D.F, e_2, e_3)) = \text{fv}\ e_1 \cup \text{fv}\ e_2 \cup \text{fv}\ e_3$
 $| \text{fv}(e.M(es)) = \text{fv}\ e \cup \text{fv}\ es$
 $| \text{fv}(\{V:T=vo; e\}) = \text{fv}\ e - \{V\}$
 $| \text{fv}(\text{sync}_V(h)\ e) = \text{fv}\ h \cup \text{fv}\ e$
 $| \text{fv}(\text{insync}_V(a)\ e) = \text{fv}\ e$
 $| \text{fv}(e_1;;e_2) = \text{fv}\ e_1 \cup \text{fv}\ e_2$
 $| \text{fv}(\text{if}(b)\ e_1\ \text{else}\ e_2) = \text{fv}\ b \cup \text{fv}\ e_1 \cup \text{fv}\ e_2$
 $| \text{fv}(\text{while}(b)\ e) = \text{fv}\ b \cup \text{fv}\ e$
 $| \text{fv}(\text{throw}\ e) = \text{fv}\ e$
 $| \text{fv}(\text{try}\ e_1\ \text{catch}(C\ V)\ e_2) = \text{fv}\ e_1 \cup (\text{fv}\ e_2 - \{V\})$

$| \text{fvs}(\[]) = \{\}$
 $| \text{fvs}(e\#es) = \text{fv}\ e \cup \text{fvs}\ es$

lemma [simp]: $\text{fvs}(es\ @\ es') = \text{fvs}\ es \cup \text{fvs}\ es'$
 <proof>

lemma [simp]: $\text{fvs}(\text{map}\ Val\ vs) = \{\}$
 <proof>

4.2.3 Locks and addresses

primrec $\text{expr-locks} :: ('a, 'b, 'addr)\ \text{exp} \Rightarrow 'addr \Rightarrow \text{nat}$
and $\text{expr-lockss} :: ('a, 'b, 'addr)\ \text{exp}\ \text{list} \Rightarrow 'addr \Rightarrow \text{nat}$

where

$\text{expr-locks}\ (\text{new}\ C) = (\lambda ad.\ 0)$
 $| \text{expr-locks}\ (\text{newA}\ T[e]) = \text{expr-locks}\ e$
 $| \text{expr-locks}\ (\text{Cast}\ T\ e) = \text{expr-locks}\ e$
 $| \text{expr-locks}\ (e\ \text{instanceof}\ T) = \text{expr-locks}\ e$
 $| \text{expr-locks}\ (\text{Val}\ v) = (\lambda ad.\ 0)$
 $| \text{expr-locks}\ (\text{Var}\ v) = (\lambda ad.\ 0)$
 $| \text{expr-locks}\ (e\ \ll\ \text{bop}\ \gg\ e') = (\lambda ad.\ \text{expr-locks}\ e\ ad + \text{expr-locks}\ e'\ ad)$
 $| \text{expr-locks}\ (V := e) = \text{expr-locks}\ e$
 $| \text{expr-locks}\ (a[i]) = (\lambda ad.\ \text{expr-locks}\ a\ ad + \text{expr-locks}\ i\ ad)$
 $| \text{expr-locks}\ (AAss\ a\ i\ e) = (\lambda ad.\ \text{expr-locks}\ a\ ad + \text{expr-locks}\ i\ ad + \text{expr-locks}\ e\ ad)$
 $| \text{expr-locks}\ (a.\text{length}) = \text{expr-locks}\ a$
 $| \text{expr-locks}\ (e.F\{D\}) = \text{expr-locks}\ e$
 $| \text{expr-locks}\ (FAss\ e\ F\ D\ e') = (\lambda ad.\ \text{expr-locks}\ e\ ad + \text{expr-locks}\ e'\ ad)$
 $| \text{expr-locks}\ (e.\text{compareAndSwap}(D.F, e', e'')) = (\lambda ad.\ \text{expr-locks}\ e\ ad + \text{expr-locks}\ e'\ ad + \text{expr-locks}\ e''\ ad)$
 $| \text{expr-locks}\ (e.m(ps)) = (\lambda ad.\ \text{expr-locks}\ e\ ad + \text{expr-lockss}\ ps\ ad)$
 $| \text{expr-locks}\ (\{V : T=vo; e\}) = \text{expr-locks}\ e$
 $| \text{expr-locks}\ (\text{sync}_V(o')\ e) = (\lambda ad.\ \text{expr-locks}\ o'\ ad + \text{expr-locks}\ e\ ad)$
 $| \text{expr-locks}\ (\text{insync}_V(a)\ e) = (\lambda ad.\ \text{if}\ (a = ad)\ \text{then}\ \text{Suc}\ (\text{expr-locks}\ e\ ad)\ \text{else}\ \text{expr-locks}\ e\ ad)$
 $| \text{expr-locks}\ (e;;e') = (\lambda ad.\ \text{expr-locks}\ e\ ad + \text{expr-locks}\ e'\ ad)$
 $| \text{expr-locks}\ (\text{if}(b)\ e\ \text{else}\ e') = (\lambda ad.\ \text{expr-locks}\ b\ ad + \text{expr-locks}\ e\ ad + \text{expr-locks}\ e'\ ad)$
 $| \text{expr-locks}\ (\text{while}(b)\ e) = (\lambda ad.\ \text{expr-locks}\ b\ ad + \text{expr-locks}\ e\ ad)$
 $| \text{expr-locks}\ (\text{throw}\ e) = \text{expr-locks}\ e$
 $| \text{expr-locks}\ (\text{try}\ e\ \text{catch}(C\ v)\ e') = (\lambda ad.\ \text{expr-locks}\ e\ ad + \text{expr-locks}\ e'\ ad)$

| $\text{expr-lockss } [] = (\lambda a. 0)$
 | $\text{expr-lockss } (x \# xs) = (\lambda ad. \text{expr-locks } x \text{ ad} + \text{expr-lockss } xs \text{ ad})$

lemma $\text{expr-lockss-append}$ [simp]:

$\text{expr-lockss } (es @ es') = (\lambda ad. \text{expr-lockss } es \text{ ad} + \text{expr-lockss } es' \text{ ad})$
 <proof>

lemma $\text{expr-lockss-map-Val}$ [simp]: $\text{expr-lockss } (\text{map Val } vs) = (\lambda ad. 0)$

<proof>

primrec $\text{contains-insync} :: ('a, 'b, 'addr) \text{exp} \Rightarrow \text{bool}$
and $\text{contains-insyncs} :: ('a, 'b, 'addr) \text{exp list} \Rightarrow \text{bool}$

where

$\text{contains-insync } (\text{new } C) = \text{False}$
 | $\text{contains-insync } (\text{newA } T [i]) = \text{contains-insync } i$
 | $\text{contains-insync } (\text{Cast } T \ e) = \text{contains-insync } e$
 | $\text{contains-insync } (e \text{ instanceof } T) = \text{contains-insync } e$
 | $\text{contains-insync } (\text{Val } v) = \text{False}$
 | $\text{contains-insync } (\text{Var } v) = \text{False}$
 | $\text{contains-insync } (e \llcorner e') = (\text{contains-insync } e \vee \text{contains-insync } e')$
 | $\text{contains-insync } (V := e) = \text{contains-insync } e$
 | $\text{contains-insync } (a [i]) = (\text{contains-insync } a \vee \text{contains-insync } i)$
 | $\text{contains-insync } (AAss \ a \ i \ e) = (\text{contains-insync } a \vee \text{contains-insync } i \vee \text{contains-insync } e)$
 | $\text{contains-insync } (a \cdot \text{length}) = \text{contains-insync } a$
 | $\text{contains-insync } (e \cdot F \{D\}) = \text{contains-insync } e$
 | $\text{contains-insync } (FAss \ e \ F \ D \ e') = (\text{contains-insync } e \vee \text{contains-insync } e')$
 | $\text{contains-insync } (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) = (\text{contains-insync } e \vee \text{contains-insync } e' \vee \text{contains-insync } e'')$
 | $\text{contains-insync } (e \cdot m(pns)) = (\text{contains-insync } e \vee \text{contains-insyncs } pns)$
 | $\text{contains-insync } (\{V : T = v_0; e\}) = \text{contains-insync } e$
 | $\text{contains-insync } (\text{sync}_V (o') \ e) = (\text{contains-insync } o' \vee \text{contains-insync } e)$
 | $\text{contains-insync } (\text{insync}_V (a) \ e) = \text{True}$
 | $\text{contains-insync } (e;; e') = (\text{contains-insync } e \vee \text{contains-insync } e')$
 | $\text{contains-insync } (\text{if } (b) \ e \ \text{else } e') = (\text{contains-insync } b \vee \text{contains-insync } e \vee \text{contains-insync } e')$
 | $\text{contains-insync } (\text{while } (b) \ e) = (\text{contains-insync } b \vee \text{contains-insync } e)$
 | $\text{contains-insync } (\text{throw } e) = \text{contains-insync } e$
 | $\text{contains-insync } (\text{try } e \ \text{catch}(C \ v) \ e') = (\text{contains-insync } e \vee \text{contains-insync } e')$

| $\text{contains-insyncs } [] = \text{False}$

| $\text{contains-insyncs } (x \# xs) = (\text{contains-insync } x \vee \text{contains-insyncs } xs)$

lemma $\text{contains-insyncs-append}$ [simp]:

$\text{contains-insyncs } (es @ es') \longleftrightarrow \text{contains-insyncs } es \vee \text{contains-insyncs } es'$
 <proof>

lemma $\text{fixes } e :: ('a, 'b, 'addr) \text{exp}$

and $es :: ('a, 'b, 'addr) \text{exp list}$

shows $\text{contains-insync-conv}: (\text{contains-insync } e \longleftrightarrow (\exists ad. \text{expr-locks } e \text{ ad} > 0))$

and $\text{contains-insyncs-conv}: (\text{contains-insyncs } es \longleftrightarrow (\exists ad. \text{expr-lockss } es \text{ ad} > 0))$

<proof>

lemma $\text{contains-insyncs-map-Val}$ [simp]: $\neg \text{contains-insyncs } (\text{map Val } vs)$

<proof>

4.2.4 Value expressions

inductive *is-val* :: ('a,'b,'addr) exp ⇒ bool **where**
is-val (Val v)

declare *is-val.intros* [*simp*]
declare *is-val.cases* [*elim!*]

lemma *is-val-iff*: *is-val* e ⇔ (∃ v. e = Val v)
 ⟨*proof*⟩

code-pred *is-val* ⟨*proof*⟩

fun *is-vals* :: ('a,'b,'addr) exp list ⇒ bool **where**
is-vals [] = True
 | *is-vals* (e#es) = (*is-val* e ∧ *is-vals* es)

lemma *is-vals-append* [*simp*]: *is-vals* (es @ es') ⇔ *is-vals* es ∧ *is-vals* es'
 ⟨*proof*⟩

lemma *is-vals-conv*: *is-vals* es = (∃ vs. es = map Val vs)
 ⟨*proof*⟩

lemma *is-vals-map-Vals* [*simp*]: *is-vals* (map Val vs) = True
 ⟨*proof*⟩

inductive *is-addr* :: ('a,'b,'addr) exp ⇒ bool
where *is-addr* (addr a)

declare *is-addr.intros*[*intro!*]
declare *is-addr.cases*[*elim!*]

lemma [*simp*]: (*is-addr* e) ⇔ (∃ a. e = addr a)
 ⟨*proof*⟩

primrec *the-Val* :: ('a, 'b, 'addr) exp ⇒ 'addr val
where
the-Val (Val v) = v

inductive *is-Throws* :: ('a, 'b, 'addr) exp list ⇒ bool
where

is-Throws (Throw a # es)
 | *is-Throws* es ⇒ *is-Throws* (Val v # es)

inductive-simps *is-Throws-simps*:

is-Throws []
is-Throws (e # es)

code-pred *is-Throws* ⟨*proof*⟩

lemma *is-Throws-conv*: *is-Throws* es ⇔ (∃ vs a es'. es = map Val vs @ Throw a # es')
 (is ?lhs ⇔ ?rhs)
 ⟨*proof*⟩

4.2.5 blocks

fun *blocks* :: 'a list \Rightarrow ty list \Rightarrow 'addr val list \Rightarrow ('a,'b,'addr) exp \Rightarrow ('a,'b,'addr) exp
where
blocks (V # Vs) (T # Ts) (v # vs) e = { V:T=[v]; *blocks* Vs Ts vs e }
| *blocks* [] [] [] e = e

lemma [*simp*]:
 $\llbracket \text{size } vs = \text{size } Vs; \text{size } Ts = \text{size } Vs \rrbracket \Longrightarrow \text{fv } (\text{blocks } Vs \ Ts \ vs \ e) = \text{fv } e - \text{set } Vs$
 $\langle \text{proof} \rangle$

lemma *expr-locks-blocks*:
 $\llbracket \text{length } vs = \text{length } pns; \text{length } Ts = \text{length } pns \rrbracket$
 $\Longrightarrow \text{expr-locks } (\text{blocks } pns \ Ts \ vs \ e) = \text{expr-locks } e$
 $\langle \text{proof} \rangle$

4.2.6 Final expressions

inductive *final* :: ('a,'b,'addr) exp \Rightarrow bool **where**
final (Val v)
| *final* (Throw a)

declare *final.cases* [*elim*]
declare *final.intros*[*simp*]

lemmas *finalE*[*consumes 1, case-names Val Throw*] = *final.cases*

lemma *final-iff*: *final* e $\longleftrightarrow (\exists v. e = \text{Val } v) \vee (\exists a. e = \text{Throw } a)$
 $\langle \text{proof} \rangle$

lemma *final-locks*: *final* e $\Longrightarrow \text{expr-locks } e \ l = 0$
 $\langle \text{proof} \rangle$

inductive *finals* :: ('a,'b,'addr) exp list \Rightarrow bool
where
finals []
| *finals* (Throw a # es)
| *finals* es $\Longrightarrow \text{finals } (\text{Val } v \# \text{es})$

inductive-simps *finals-simps*:
finals (e # es)

lemma [*iff*]: *finals* []
 $\langle \text{proof} \rangle$

lemma [*iff*]: *finals* (Val v # es) = *finals* es
 $\langle \text{proof} \rangle$

lemma *finals-app-map* [*iff*]: *finals* (map Val vs @ es) = *finals* es
 $\langle \text{proof} \rangle$

lemma [*iff*]: *finals* (throw e # es) = $(\exists a. e = \text{addr } a)$
 $\langle \text{proof} \rangle$

lemma *not-finals-ConsI*: $\neg \text{final } e \Longrightarrow \neg \text{finals } (e \# \text{es})$

⟨proof⟩

lemma *finals-iff*: $\text{finals } es \longleftrightarrow (\exists vs. es = \text{map Val } vs) \vee (\exists vs a es'. es = \text{map Val } vs @ \text{Throw } a \# es')$

(**is** ?lhs \longleftrightarrow ?rhs)

⟨proof⟩

code-pred *final* ⟨proof⟩

4.2.7 converting results from external calls

primrec *extRet2J* :: ('a, 'b, 'addr) exp \Rightarrow 'addr extCallRet \Rightarrow ('a, 'b, 'addr) exp

where

| *extRet2J* e (RetVal v) = Val v
 | *extRet2J* e (RetExc a) = Throw a
 | *extRet2J* e RetStaySame = e

lemma *fv-extRet2J [simp]*: $\text{fv } (\text{extRet2J } e \text{ va}) \subseteq \text{fv } e$

⟨proof⟩

4.2.8 expressions at a call

primrec *call* :: ('a, 'b, 'addr) exp \Rightarrow ('addr \times mname \times 'addr val list) option

and *calls* :: ('a, 'b, 'addr) exp list \Rightarrow ('addr \times mname \times 'addr val list) option

where

| *call* (new C) = None
 | *call* (newA T[e]) = *call* e
 | *call* (Cast C e) = *call* e
 | *call* (e instanceof T) = *call* e
 | *call* (Val v) = None
 | *call* (Var V) = None
 | *call* (V:=e) = *call* e
 | *call* (e «bop» e') = (if is-val e then *call* e' else *call* e)
 | *call* (a[i]) = (if is-val a then *call* i else *call* a)
 | *call* (AAss a i e) = (if is-val a then (if is-val i then *call* e else *call* i) else *call* a)
 | *call* (a.length) = *call* a
 | *call* (e.F{D}) = *call* e
 | *call* (FAss e F D e') = (if is-val e then *call* e' else *call* e)
 | *call* (e.compareAndSwap(D.F, e', e'')) = (if is-val e then if is-val e' then *call* e'' else *call* e' else *call* e)
 | *call* (e.M(es)) = (if is-val e then
 (if is-vals es \wedge is-addr e then [(THE a. e = addr a, M, THE vs. es = map Val vs)]
 else *calls* es)
 else *call* e)
 | *call* ({V:T=vo; e}) = *call* e
 | *call* (sync_V (o') e) = *call* o'
 | *call* (insync_V (a) e) = *call* e
 | *call* (e;;e') = *call* e
 | *call* (if (e) e1 else e2) = *call* e
 | *call* (while(b) e) = None
 | *call* (throw e) = *call* e
 | *call* (try e1 catch(C V) e2) = *call* e1

| *calls* [] = None

| $\text{calls } (e\#es) = (\text{if is-val } e \text{ then calls } es \text{ else call } e)$

lemma *calls-append* [*simp*]:

$\text{calls } (es @ es') = (\text{if calls } es = \text{None} \wedge \text{is-vals } es \text{ then calls } es' \text{ else calls } es)$
 <proof>

lemma *call-callE* [*consumes 1, case-names CallObj CallParams Call*]:

$\llbracket \text{call } (\text{obj} \cdot M(pns)) = \llbracket (a, M', vs) \rrbracket;$
 $\text{call obj} = \llbracket (a, M', vs) \rrbracket \implies \text{thesis};$
 $\bigwedge v. \llbracket \text{obj} = \text{Val } v; \text{calls } pns = \llbracket (a, M', vs) \rrbracket \rrbracket \implies \text{thesis};$
 $\llbracket \text{obj} = \text{addr } a; pns = \text{map Val } vs; M = M' \rrbracket \implies \text{thesis} \rrbracket \implies \text{thesis}$
 <proof>

lemma *calls-map-Val* [*simp*]:

$\text{calls } (\text{map Val } vs) = \text{None}$
 <proof>

lemma *call-not-is-val* [*dest*]: $\text{call } e = \llbracket aMvs \rrbracket \implies \neg \text{is-val } e$

<proof>

lemma *is-calls-not-is-vals* [*dest*]: $\text{calls } es = \llbracket aMvs \rrbracket \implies \neg \text{is-vals } es$

<proof>

end

4.3 Abstract heap locales for source code programs

theory *JHeap*

imports

../Common/Conform

Expr

begin

locale *J-heap-base* = *heap-base* +

constrains *addr2thread-id* :: ('addr :: addr) \Rightarrow 'thread-id
and *thread-id2addr* :: 'thread-id \Rightarrow 'addr
and *spurious-wakeups* :: bool
and *empty-heap* :: 'heap
and *allocate* :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) set
and *typeof-addr* :: 'heap \Rightarrow 'addr \rightarrow htype
and *heap-read* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow bool
and *heap-write* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow 'heap \Rightarrow bool

locale *J-heap* = *heap* +

constrains *addr2thread-id* :: ('addr :: addr) \Rightarrow 'thread-id
and *thread-id2addr* :: 'thread-id \Rightarrow 'addr
and *spurious-wakeups* :: bool
and *empty-heap* :: 'heap
and *allocate* :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) set
and *typeof-addr* :: 'heap \Rightarrow 'addr \rightarrow htype
and *heap-read* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow bool
and *heap-write* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow 'heap \Rightarrow bool
and *P* :: 'addr *J-prog*

sublocale $J\text{-heap} < J\text{-heap-base}$ $\langle \text{proof} \rangle$

locale $J\text{-heap-conf-base} = \text{heap-conf-base} +$
constrains $\text{addr2thread-id} :: ('addr :: \text{addr}) \Rightarrow 'thread\text{-id}$
and $\text{thread-id2addr} :: 'thread\text{-id} \Rightarrow 'addr$
and $\text{spurious-wakeups} :: \text{bool}$
and $\text{empty-heap} :: 'heap$
and $\text{allocate} :: 'heap \Rightarrow \text{htype} \Rightarrow ('heap \times 'addr) \text{ set}$
and $\text{typeof-addr} :: 'heap \Rightarrow 'addr \rightarrow \text{htype}$
and $\text{heap-read} :: 'heap \Rightarrow 'addr \Rightarrow \text{addr-loc} \Rightarrow 'addr \text{ val} \Rightarrow \text{bool}$
and $\text{heap-write} :: 'heap \Rightarrow 'addr \Rightarrow \text{addr-loc} \Rightarrow 'addr \text{ val} \Rightarrow 'heap \Rightarrow \text{bool}$
and $\text{hconf} :: 'heap \Rightarrow \text{bool}$
and $P :: 'addr \text{ J-prog}$

sublocale $J\text{-heap-conf-base} < J\text{-heap-base}$ $\langle \text{proof} \rangle$

locale $J\text{-heap-conf} =$
 $J\text{-heap-conf-base} +$
 $\text{heap-conf} +$
constrains $\text{addr2thread-id} :: ('addr :: \text{addr}) \Rightarrow 'thread\text{-id}$
and $\text{thread-id2addr} :: 'thread\text{-id} \Rightarrow 'addr$
and $\text{spurious-wakeups} :: \text{bool}$
and $\text{empty-heap} :: 'heap$
and $\text{allocate} :: 'heap \Rightarrow \text{htype} \Rightarrow ('heap \times 'addr) \text{ set}$
and $\text{typeof-addr} :: 'heap \Rightarrow 'addr \rightarrow \text{htype}$
and $\text{heap-read} :: 'heap \Rightarrow 'addr \Rightarrow \text{addr-loc} \Rightarrow 'addr \text{ val} \Rightarrow \text{bool}$
and $\text{heap-write} :: 'heap \Rightarrow 'addr \Rightarrow \text{addr-loc} \Rightarrow 'addr \text{ val} \Rightarrow 'heap \Rightarrow \text{bool}$
and $\text{hconf} :: 'heap \Rightarrow \text{bool}$
and $P :: 'addr \text{ J-prog}$

sublocale $J\text{-heap-conf} < J\text{-heap}$
 $\langle \text{proof} \rangle$

locale $J\text{-progress} =$
 $\text{heap-progress} +$
 $J\text{-heap-conf-base} +$
constrains $\text{addr2thread-id} :: ('addr :: \text{addr}) \Rightarrow 'thread\text{-id}$
and $\text{thread-id2addr} :: 'thread\text{-id} \Rightarrow 'addr$
and $\text{spurious-wakeups} :: \text{bool}$
and $\text{empty-heap} :: 'heap$
and $\text{allocate} :: 'heap \Rightarrow \text{htype} \Rightarrow ('heap \times 'addr) \text{ set}$
and $\text{typeof-addr} :: 'heap \Rightarrow 'addr \rightarrow \text{htype}$
and $\text{heap-read} :: 'heap \Rightarrow 'addr \Rightarrow \text{addr-loc} \Rightarrow 'addr \text{ val} \Rightarrow \text{bool}$
and $\text{heap-write} :: 'heap \Rightarrow 'addr \Rightarrow \text{addr-loc} \Rightarrow 'addr \text{ val} \Rightarrow 'heap \Rightarrow \text{bool}$
and $\text{hconf} :: 'heap \Rightarrow \text{bool}$
and $P :: 'addr \text{ J-prog}$

sublocale $J\text{-progress} < J\text{-heap}$ $\langle \text{proof} \rangle$

locale $J\text{-conf-read} =$
 $\text{heap-conf-read} +$
 $J\text{-heap-conf} +$
constrains $\text{addr2thread-id} :: ('addr :: \text{addr}) \Rightarrow 'thread\text{-id}$

```

and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr J-prog

```

```

sublocale J-conf-read < J-heap ⟨proof⟩

```

```

locale J-typesafe =
  heap-typesafe +
  J-conf-read +
  J-progress +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr J-prog

```

```

end

```

4.4 Small Step Semantics

```

theory SmallStep

```

```

imports

```

```

  Expr

```

```

  State

```

```

  JHeap

```

```

begin

```

```

type-synonym

```

```

  ('addr, 'thread-id, 'heap) J-thread-action =

```

```

  ('addr, 'thread-id, 'addr expr × 'addr locals, 'heap) Jinja-thread-action

```

```

type-synonym

```

```

  ('addr, 'thread-id, 'heap) J-state =

```

```

  ('addr, 'thread-id, 'addr expr × 'addr locals, 'heap, 'addr) state

```

```

⟨ML⟩

```

```

typ ('addr, 'thread-id, 'heap) J-thread-action

```

```

⟨ML⟩

```

```

typ ('addr, 'thread-id, 'heap) J-state

```

definition $extNTA2J :: 'addr J\text{-prog} \Rightarrow (cname \times mname \times 'addr) \Rightarrow 'addr\ expr \times 'addr\ locals$
where $extNTA2J P = (\lambda(C, M, a). \text{let } (D, Ts, T, meth) = \text{method } P\ C\ M; (pns, body) = \text{the meth}$
 $\text{in } (\{this:Class\ D=[Addr\ a];\ body\}, Map.empty))$

abbreviation $J\text{-local-start} ::$

$cname \Rightarrow mname \Rightarrow ty\ list \Rightarrow ty \Rightarrow 'addr\ J\text{-mb} \Rightarrow 'addr\ val\ list$
 $\Rightarrow 'addr\ expr \times 'addr\ locals$

where

$J\text{-local-start} \equiv$
 $\lambda C\ M\ Ts\ T\ (pns, body)\ \text{vs.}$
 $(\text{blocks } (this\ \# pns)\ (Class\ C\ \# Ts)\ (Null\ \# vs)\ body, Map.empty)$

abbreviation (in $J\text{-heap-base}$)

$J\text{-start-state} :: 'addr\ J\text{-prog} \Rightarrow cname \Rightarrow mname \Rightarrow 'addr\ val\ list \Rightarrow ('addr, 'thread\text{-id}, 'heap)\ J\text{-state}$

where

$J\text{-start-state} \equiv \text{start-state } J\text{-local-start}$

lemma $extNTA2J\text{-iff}$ [simp]:

$extNTA2J\ P\ (C, M, a) = (\{this:Class\ (fst\ (method\ P\ C\ M))=[Addr\ a];\ snd\ (the\ (snd\ (snd\ (snd\ (method\ P\ C\ M))))\}, Map.empty)$
 $\langle proof \rangle$

abbreviation $extTA2J ::$

$'addr\ J\text{-prog} \Rightarrow ('addr, 'thread\text{-id}, 'heap)\ \text{external-thread-action} \Rightarrow ('addr, 'thread\text{-id}, 'heap)\ J\text{-thread-action}$

where $extTA2J\ P \equiv \text{convert-extTA } (extNTA2J\ P)$

lemma $extTA2J\text{-}\varepsilon$: $extTA2J\ P\ \varepsilon = \varepsilon$

$\langle proof \rangle$

Locking mechanism: The expression on which the thread is synchronized is evaluated first to a value. If this expression evaluates to null, a null pointer expression is thrown. If this expression evaluates to an address, a lock must be obtained on this address, the sync expression is rewritten to insync. For insync expressions, the body expression may be evaluated. If the body expression is only a value or a thrown exception, the lock is released and the synchronized expression reduces to the body's expression. This is the normal Java semantics, not the one as presented in LNCS 1523, Cenciarelli/Knapp/Reus/Wirsing. There the expression on which the thread synchronized is evaluated except for the last step. If the thread can obtain the lock on the object immediately after the last evaluation step, the evaluation is done and the lock acquired. If the lock cannot be obtained, the evaluation step is discarded. If another thread changes the evaluation result of this last step, the thread then will try to synchronize on the new object.

context $J\text{-heap-base}$ **begin**

inductive $red ::$

$(('addr, 'thread\text{-id}, 'heap)\ \text{external-thread-action} \Rightarrow ('addr, 'thread\text{-id}, 'x, 'heap)\ \text{Jinja-thread-action})$
 $\Rightarrow 'addr\ J\text{-prog} \Rightarrow 'thread\text{-id}$
 $\Rightarrow 'addr\ expr \Rightarrow ('addr, 'heap)\ J\text{state}$
 $\Rightarrow ('addr, 'thread\text{-id}, 'x, 'heap)\ \text{Jinja-thread-action}$
 $\Rightarrow 'addr\ expr \Rightarrow ('addr, 'heap)\ J\text{state} \Rightarrow bool$
 $(\langle -, - \vdash ((1\langle -, - \rangle) \dashrightarrow / (1\langle -, - \rangle)) \rangle [51, 51, 0, 0, 0, 0, 0] 81)$

and $reds ::$

$((\text{'addr}, \text{'thread-id}, \text{'heap}) \text{ external-thread-action} \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'x}, \text{'heap}) \text{ Jinja-thread-action})$
 $\Rightarrow \text{'addr J-prog} \Rightarrow \text{'thread-id}$
 $\Rightarrow \text{'addr expr list} \Rightarrow (\text{'addr}, \text{'heap}) \text{ Jstate}$
 $\Rightarrow (\text{'addr}, \text{'thread-id}, \text{'x}, \text{'heap}) \text{ Jinja-thread-action}$
 $\Rightarrow \text{'addr expr list} \Rightarrow (\text{'addr}, \text{'heap}) \text{ Jstate} \Rightarrow \text{bool}$
 $(\langle -, -, - \vdash ((1 \langle -, - \rangle) [- \dashrightarrow] / (1 \langle -, - \rangle)) \rangle [51, 51, 0, 0, 0, 0, 0, 0] 81)$
for $\text{extTA} :: (\text{'addr}, \text{'thread-id}, \text{'heap}) \text{ external-thread-action} \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'x}, \text{'heap}) \text{ Jinja-thread-action}$
and $P :: \text{'addr J-prog}$ **and** $t :: \text{'thread-id}$
where
RedNew:
 $(h', a) \in \text{allocate } h \text{ (Class-type } C)$
 $\Longrightarrow \text{extTA}, P, t \vdash \langle \text{new } C, (h, l) \rangle - \{\text{NewHeapElem } a \text{ (Class-type } C)\} \rightarrow \langle \text{addr } a, (h', l) \rangle$
| *RedNewFail:*
 $\text{allocate } h \text{ (Class-type } C) = \{\}$
 $\Longrightarrow \text{extTA}, P, t \vdash \langle \text{new } C, (h, l) \rangle - \varepsilon \rightarrow \langle \text{THROW OutOfMemory}, (h, l) \rangle$
| *NewArrayRed:*
 $\text{extTA}, P, t \vdash \langle e, s \rangle - \text{ta} \rightarrow \langle e', s' \rangle \Longrightarrow \text{extTA}, P, t \vdash \langle \text{newA } T[e], s \rangle - \text{ta} \rightarrow \langle \text{newA } T[e'], s' \rangle$
| *RedNewArray:*
 $\llbracket 0 \leq s \ i; (h', a) \in \text{allocate } h \text{ (Array-type } T \text{ (nat (sint } i)) \rrbracket$
 $\Longrightarrow \text{extTA}, P, t \vdash \langle \text{newA } T[\text{Val (Intg } i)], (h, l) \rangle - \{\text{NewHeapElem } a \text{ (Array-type } T \text{ (nat (sint } i))\} \rightarrow$
 $\langle \text{addr } a, (h', l) \rangle$
| *RedNewArrayNegative:*
 $i < s \ 0 \Longrightarrow \text{extTA}, P, t \vdash \langle \text{newA } T[\text{Val (Intg } i)], s \rangle - \varepsilon \rightarrow \langle \text{THROW NegativeArraySize}, s \rangle$
| *RedNewArrayFail:*
 $\llbracket 0 \leq s \ i; \text{allocate } h \text{ (Array-type } T \text{ (nat (sint } i)) = \{\} \rrbracket$
 $\Longrightarrow \text{extTA}, P, t \vdash \langle \text{newA } T[\text{Val (Intg } i)], (h, l) \rangle - \varepsilon \rightarrow \langle \text{THROW OutOfMemory}, (h, l) \rangle$
| *CastRed:*
 $\text{extTA}, P, t \vdash \langle e, s \rangle - \text{ta} \rightarrow \langle e', s' \rangle \Longrightarrow \text{extTA}, P, t \vdash \langle \text{Cast } C \ e, s \rangle - \text{ta} \rightarrow \langle \text{Cast } C \ e', s' \rangle$
| *RedCast:*
 $\llbracket \text{typeof}_{hp} \ s \ v = \lfloor U \rfloor; P \vdash U \leq T \rrbracket$
 $\Longrightarrow \text{extTA}, P, t \vdash \langle \text{Cast } T \text{ (Val } v), s \rangle - \varepsilon \rightarrow \langle \text{Val } v, s \rangle$
| *RedCastFail:*
 $\llbracket \text{typeof}_{hp} \ s \ v = \lfloor U \rfloor; \neg P \vdash U \leq T \rrbracket$
 $\Longrightarrow \text{extTA}, P, t \vdash \langle \text{Cast } T \text{ (Val } v), s \rangle - \varepsilon \rightarrow \langle \text{THROW ClassCast}, s \rangle$
| *InstanceOfRed:*
 $\text{extTA}, P, t \vdash \langle e, s \rangle - \text{ta} \rightarrow \langle e', s' \rangle \Longrightarrow \text{extTA}, P, t \vdash \langle e \text{ instanceof } T, s \rangle - \text{ta} \rightarrow \langle e' \text{ instanceof } T, s' \rangle$
| *RedInstanceOf:*
 $\llbracket \text{typeof}_{hp} \ s \ v = \lfloor U \rfloor; b \longleftrightarrow v \neq \text{Null} \wedge P \vdash U \leq T \rrbracket$
 $\Longrightarrow \text{extTA}, P, t \vdash \langle (\text{Val } v) \text{ instanceof } T, s \rangle - \varepsilon \rightarrow \langle \text{Val (Bool } b), s \rangle$
| *BinOpRed1:*
 $\text{extTA}, P, t \vdash \langle e, s \rangle - \text{ta} \rightarrow \langle e', s' \rangle \Longrightarrow \text{extTA}, P, t \vdash \langle e \ \langle \text{bop} \rangle \ e2, s \rangle - \text{ta} \rightarrow \langle e' \ \langle \text{bop} \rangle \ e2, s' \rangle$
| *BinOpRed2:*

$$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle (Val v) \llbracket bop \rrbracket e, s \rangle -ta \rightarrow \langle (Val v) \llbracket bop \rrbracket e', s' \rangle$$

| *RedBinOp*:

$$\begin{aligned} & \text{binop } bop \ v1 \ v2 = \text{Some } (Inl \ v) \implies \\ & \text{extTA}, P, t \vdash \langle (Val \ v1) \llbracket bop \rrbracket (Val \ v2), s \rangle -\varepsilon \rightarrow \langle Val \ v, s \rangle \end{aligned}$$

| *RedBinOpFail*:

$$\begin{aligned} & \text{binop } bop \ v1 \ v2 = \text{Some } (Inr \ a) \implies \\ & \text{extTA}, P, t \vdash \langle (Val \ v1) \llbracket bop \rrbracket (Val \ v2), s \rangle -\varepsilon \rightarrow \langle Throw \ a, s \rangle \end{aligned}$$

| *RedVar*:

$$\begin{aligned} & \text{lcl } s \ V = \text{Some } v \implies \\ & \text{extTA}, P, t \vdash \langle Var \ V, s \rangle -\varepsilon \rightarrow \langle Val \ v, s \rangle \end{aligned}$$

| *LAssRed*:

$$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle V := e, s \rangle -ta \rightarrow \langle V := e', s' \rangle$$

| *RedLAss*:

$$\text{extTA}, P, t \vdash \langle V := (Val \ v), (h, l) \rangle -\varepsilon \rightarrow \langle \text{unit}, (h, l(V \mapsto v)) \rangle$$

| *AAccRed1*:

$$\text{extTA}, P, t \vdash \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \implies \text{extTA}, P, t \vdash \langle a[i], s \rangle -ta \rightarrow \langle a'[i], s' \rangle$$

| *AAccRed2*:

$$\text{extTA}, P, t \vdash \langle i, s \rangle -ta \rightarrow \langle i', s' \rangle \implies \text{extTA}, P, t \vdash \langle (Val \ a)[i], s \rangle -ta \rightarrow \langle (Val \ a)[i'], s' \rangle$$

| *RedAAccNull*:

$$\text{extTA}, P, t \vdash \langle \text{null}[Val \ i], s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, s \rangle$$

| *RedAAccBounds*:

$$\begin{aligned} & \llbracket \text{typeof-addr } (hp \ s) \ a = \llbracket \text{Array-type } T \ n \rrbracket; \ i < s \ 0 \vee \text{sint } i \geq \text{int } n \rrbracket \\ & \implies \text{extTA}, P, t \vdash \langle (\text{addr } a)[Val \ (Intg \ i)], s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{ArrayIndexOutOfBounds}, s \rangle \end{aligned}$$

| *RedAAcc*:

$$\begin{aligned} & \llbracket \text{typeof-addr } h \ a = \llbracket \text{Array-type } T \ n \rrbracket; \ 0 \leq s \ i; \ \text{sint } i < \text{int } n; \\ & \quad \text{heap-read } h \ a \ (ACell \ (\text{nat } (\text{sint } i))) \ v \rrbracket \\ & \implies \text{extTA}, P, t \vdash \langle (\text{addr } a)[Val \ (Intg \ i)], (h, l) \rangle -\{\text{ReadMem } a \ (ACell \ (\text{nat } (\text{sint } i))) \ v\} \rightarrow \langle Val \ v, \\ & (h, l) \rangle \end{aligned}$$

| *AAssRed1*:

$$\text{extTA}, P, t \vdash \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \implies \text{extTA}, P, t \vdash \langle a[i] := e, s \rangle -ta \rightarrow \langle a'[i] := e, s' \rangle$$

| *AAssRed2*:

$$\text{extTA}, P, t \vdash \langle i, s \rangle -ta \rightarrow \langle i', s' \rangle \implies \text{extTA}, P, t \vdash \langle (Val \ a)[i] := e, s \rangle -ta \rightarrow \langle (Val \ a)[i'] := e, s' \rangle$$

| *AAssRed3*:

$$\text{extTA}, P, t \vdash \langle (e::'\text{addr } \text{expr}), s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle (Val \ a)[Val \ i] := e, s \rangle -ta \rightarrow \langle (Val \ a)[Val \ i] := e', s' \rangle$$

| *RedAAssNull*:

$$\text{extTA}, P, t \vdash \langle \text{null}[Val \ i] := (Val \ e::'\text{addr } \text{expr}), s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, s \rangle$$

| *RedAAssBounds*:

$$\llbracket \text{typeof-addr } (hp \ s) \ a = \llbracket \text{Array-type } T \ n \rrbracket; \ i < s \ 0 \vee \text{sint } i \geq \text{int } n \rrbracket$$

$\implies \text{extTA}, P, t \vdash \langle (\text{addr } a)[\text{Val } (\text{Intg } i)] := (\text{Val } e::'\text{addr } \text{expr}), s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{ArrayIndexOutOfBounds}, s \rangle$

| *RedAssStore*:

$\llbracket \text{typeof-addr } (hp \ s) \ a = \llbracket \text{Array-type } T \ n \rrbracket; 0 \leq s \ i; \text{sint } i < \text{int } n;$
 $\text{typeof}_{hp \ s} \ w = \llbracket U \rrbracket; \neg (P \vdash U \leq T) \rrbracket$

$\implies \text{extTA}, P, t \vdash \langle (\text{addr } a)[\text{Val } (\text{Intg } i)] := (\text{Val } w::'\text{addr } \text{expr}), s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{ArrayStore}, s \rangle$

| *RedAss*:

$\llbracket \text{typeof-addr } h \ a = \llbracket \text{Array-type } T \ n \rrbracket; 0 \leq s \ i; \text{sint } i < \text{int } n; \text{typeof}_h \ w = \text{Some } U; P \vdash U \leq T;$
 $\text{heap-write } h \ a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ w \ h' \rrbracket$

$\implies \text{extTA}, P, t \vdash \langle (\text{addr } a)[\text{Val } (\text{Intg } i)] := \text{Val } w::'\text{addr } \text{expr}, (h, l) \rangle -\{\!\!\{ \text{WriteMem } a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ w \}\!\!\} \rightarrow \langle \text{unit}, (h', l) \rangle$

| *ALengthRed*:

$\text{extTA}, P, t \vdash \langle a, s \rangle -\text{ta} \rightarrow \langle a', s' \rangle \implies \text{extTA}, P, t \vdash \langle a \cdot \text{length}, s \rangle -\text{ta} \rightarrow \langle a' \cdot \text{length}, s' \rangle$

| *RedALength*:

$\text{typeof-addr } h \ a = \llbracket \text{Array-type } T \ n \rrbracket$

$\implies \text{extTA}, P, t \vdash \langle \text{addr } a \cdot \text{length}, (h, l) \rangle -\varepsilon \rightarrow \langle \text{Val } (\text{Intg } (\text{word-of-nat } n)), (h, l) \rangle$

| *RedALengthNull*:

$\text{extTA}, P, t \vdash \langle \text{null} \cdot \text{length}, s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, s \rangle$

| *FAccRed*:

$\text{extTA}, P, t \vdash \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle e \cdot F\{D\}, s \rangle -\text{ta} \rightarrow \langle e' \cdot F\{D\}, s' \rangle$

| *RedFAcc*:

$\text{heap-read } h \ a \ (\text{CField } D \ F) \ v$

$\implies \text{extTA}, P, t \vdash \langle (\text{addr } a) \cdot F\{D\}, (h, l) \rangle -\{\!\!\{ \text{ReadMem } a \ (\text{CField } D \ F) \ v \}\!\!\} \rightarrow \langle \text{Val } v, (h, l) \rangle$

| *RedFAccNull*:

$\text{extTA}, P, t \vdash \langle \text{null} \cdot F\{D\}, s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, s \rangle$

| *FAssRed1*:

$\text{extTA}, P, t \vdash \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle e \cdot F\{D\} := e2, s \rangle -\text{ta} \rightarrow \langle e' \cdot F\{D\} := e2, s' \rangle$

| *FAssRed2*:

$\text{extTA}, P, t \vdash \langle (e::'\text{addr } \text{expr}), s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle \text{Val } v \cdot F\{D\} := e, s \rangle -\text{ta} \rightarrow \langle \text{Val } v \cdot F\{D\} := e', s' \rangle$

| *RedFAss*:

$\text{heap-write } h \ a \ (\text{CField } D \ F) \ v \ h' \implies$

$\text{extTA}, P, t \vdash \langle (\text{addr } a) \cdot F\{D\} := \text{Val } v, (h, l) \rangle -\{\!\!\{ \text{WriteMem } a \ (\text{CField } D \ F) \ v \}\!\!\} \rightarrow \langle \text{unit}, (h', l) \rangle$

| *RedFAssNull*:

$\text{extTA}, P, t \vdash \langle \text{null} \cdot F\{D\} := \text{Val } v::'\text{addr } \text{expr}, s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, s \rangle$

| *CASRed1*:

$\text{extTA}, P, t \vdash \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies$

$\text{extTA}, P, t \vdash \langle e \cdot \text{compareAndSwap}(D \cdot F, e2, e3), s \rangle -\text{ta} \rightarrow \langle e' \cdot \text{compareAndSwap}(D \cdot F, e2, e3), s' \rangle$

| *CASRed2*:

$\text{extTA}, P, t \vdash \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies$

$extTA, P, t \vdash \langle Val\ v \cdot compareAndSwap(D \cdot F, e, e\exists), s \rangle -ta \rightarrow \langle Val\ v \cdot compareAndSwap(D \cdot F, e', e\exists), s \rangle$

| *CASRed3*:

$extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s \rangle \implies$
 $extTA, P, t \vdash \langle Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e), s \rangle -ta \rightarrow \langle Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e'), s \rangle$

| *CASNull*:

$extTA, P, t \vdash \langle null \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'), s \rangle -\varepsilon \rightarrow \langle THROW\ NullPointer, s \rangle$

| *RedCASSucceed*:

$\llbracket heap-read\ h\ a\ (CField\ D\ F)\ v; heap-write\ h\ a\ (CField\ D\ F)\ v'\ h' \rrbracket \implies$
 $extTA, P, t \vdash \langle addr\ a \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'), (h, l) \rangle$
 $-\{\!\{ReadMem\ a\ (CField\ D\ F)\ v, WriteMem\ a\ (CField\ D\ F)\ v'\}\!\} \rightarrow$
 $\langle true, (h', l) \rangle$

| *RedCASFail*:

$\llbracket heap-read\ h\ a\ (CField\ D\ F)\ v''; v \neq v'' \rrbracket \implies$
 $extTA, P, t \vdash \langle addr\ a \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'), (h, l) \rangle$
 $-\{\!\{ReadMem\ a\ (CField\ D\ F)\ v''\}\!\} \rightarrow$
 $\langle false, (h, l) \rangle$

| *CallObj*:

$extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s \rangle \implies extTA, P, t \vdash \langle e \cdot M(es), s \rangle -ta \rightarrow \langle e' \cdot M(es), s \rangle$

| *CallParams*:

$extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s \rangle \implies$
 $extTA, P, t \vdash \langle (Val\ v) \cdot M(es), s \rangle -ta \rightarrow \langle (Val\ v) \cdot M(es'), s \rangle$

| *RedCall*:

$\llbracket typeof-addr\ (hp\ s)\ a = \lfloor hU \rfloor; P \vdash class-type-of\ hU\ sees\ M:Ts \rightarrow T = \lfloor (pns, body) \rfloor\ in\ D;$
 $size\ vs = size\ pns; size\ Ts = size\ pns \rrbracket$
 $\implies extTA, P, t \vdash \langle (addr\ a) \cdot M(map\ Val\ vs), s \rangle -\varepsilon \rightarrow \langle blocks\ (this\ \# \ pns)\ (Class\ D\ \# \ Ts)\ (Addr\ a\ \# \ vs)\ body, s \rangle$

| *RedCallExternal*:

$\llbracket typeof-addr\ (hp\ s)\ a = \lfloor hU \rfloor; P \vdash class-type-of\ hU\ sees\ M:Ts \rightarrow T = Native\ in\ D;$
 $P, t \vdash \langle a \cdot M(vs), hp\ s \rangle -ta \rightarrow ext\ \langle va, h \rangle;$
 $ta' = extTA\ ta; e' = extRet2J\ ((addr\ a) \cdot M(map\ Val\ vs))\ va; s' = (h', lcl\ s) \rrbracket$
 $\implies extTA, P, t \vdash \langle (addr\ a) \cdot M(map\ Val\ vs), s \rangle -ta' \rightarrow \langle e', s \rangle$

| *RedCallNull*:

$extTA, P, t \vdash \langle null \cdot M(map\ Val\ vs), s \rangle -\varepsilon \rightarrow \langle THROW\ NullPointer, s \rangle$

| *BlockRed*:

$extTA, P, t \vdash \langle e, (h, l(V := vo)) \rangle -ta \rightarrow \langle e', (h', l') \rangle$
 $\implies extTA, P, t \vdash \langle \{V:T=vo; e\}, (h, l) \rangle -ta \rightarrow \langle \{V:T=l' V; e'\}, (h', l'(V := l V)) \rangle$

| *RedBlock*:

$extTA, P, t \vdash \langle \{V:T=vo; Val\ u\}, s \rangle -\varepsilon \rightarrow \langle Val\ u, s \rangle$

| *SynchronizedRed1*:

$extTA, P, t \vdash \langle o', s \rangle -ta \rightarrow \langle o'', s \rangle \implies extTA, P, t \vdash \langle sync(o')\ e, s \rangle -ta \rightarrow \langle sync(o'')\ e, s \rangle$

- | *SynchronizedNull*:
 $extTA, P, t \vdash \langle sync(null) e, s \rangle -\varepsilon \rightarrow \langle THROW \text{NullPointer}, s \rangle$
- | *LockSynchronized*:
 $extTA, P, t \vdash \langle sync(addr\ a) e, s \rangle -\{\!\{Lock \rightarrow a, SyncLock\ a}\!\} \rightarrow \langle insync(a) e, s \rangle$
- | *SynchronizedRed2*:
 $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle insync(a) e, s \rangle -ta \rightarrow \langle insync(a) e', s' \rangle$
- | *UnlockSynchronized*:
 $extTA, P, t \vdash \langle insync(a) (Val\ v), s \rangle -\{\!\{Unlock \rightarrow a, SyncUnlock\ a}\!\} \rightarrow \langle Val\ v, s \rangle$
- | *SeqRed*:
 $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle e;;e2, s \rangle -ta \rightarrow \langle e';;e2, s' \rangle$
- | *RedSeq*:
 $extTA, P, t \vdash \langle (Val\ v);;e, s \rangle -\varepsilon \rightarrow \langle e, s \rangle$
- | *CondRed*:
 $extTA, P, t \vdash \langle b, s \rangle -ta \rightarrow \langle b', s' \rangle \implies extTA, P, t \vdash \langle if\ (b)\ e1\ else\ e2, s \rangle -ta \rightarrow \langle if\ (b')\ e1\ else\ e2, s' \rangle$
- | *RedCondT*:
 $extTA, P, t \vdash \langle if\ (true)\ e1\ else\ e2, s \rangle -\varepsilon \rightarrow \langle e1, s \rangle$
- | *RedCondF*:
 $extTA, P, t \vdash \langle if\ (false)\ e1\ else\ e2, s \rangle -\varepsilon \rightarrow \langle e2, s \rangle$
- | *RedWhile*:
 $extTA, P, t \vdash \langle while(b)\ c, s \rangle -\varepsilon \rightarrow \langle if\ (b)\ (c;;while(b)\ c)\ else\ unit, s \rangle$
- | *ThrowRed*:
 $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle throw\ e, s \rangle -ta \rightarrow \langle throw\ e', s' \rangle$
- | *RedThrowNull*:
 $extTA, P, t \vdash \langle throw\ null, s \rangle -\varepsilon \rightarrow \langle THROW \text{NullPointer}, s \rangle$
- | *TryRed*:
 $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle try\ e\ catch(C\ V)\ e2, s \rangle -ta \rightarrow \langle try\ e'\ catch(C\ V)\ e2, s' \rangle$
- | *RedTry*:
 $extTA, P, t \vdash \langle try\ (Val\ v)\ catch(C\ V)\ e2, s \rangle -\varepsilon \rightarrow \langle Val\ v, s \rangle$
- | *RedTryCatch*:
 $\llbracket \text{typeof-addr } (hp\ s)\ a = \lfloor \text{Class-type } D \rfloor; P \vdash D \preceq^* C \rrbracket$
 $\implies extTA, P, t \vdash \langle try\ (Throw\ a)\ catch(C\ V)\ e2, s \rangle -\varepsilon \rightarrow \langle \{V:\text{Class } C = \lfloor \text{Addr } a \rfloor; e2\}, s \rangle$
- | *RedTryFail*:
 $\llbracket \text{typeof-addr } (hp\ s)\ a = \lfloor \text{Class-type } D \rfloor; \neg P \vdash D \preceq^* C \rrbracket$
 $\implies extTA, P, t \vdash \langle try\ (Throw\ a)\ catch(C\ V)\ e2, s \rangle -\varepsilon \rightarrow \langle Throw\ a, s \rangle$
- | *ListRed1*:
 $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies$

$$\text{extTA}, P, t \vdash \langle e \# es, s \rangle [-ta \rightarrow] \langle e' \# es, s' \rangle$$

| *ListRed2*:

$$\begin{aligned} \text{extTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle &\implies \\ \text{extTA}, P, t \vdash \langle \text{Val } v \# es, s \rangle [-ta \rightarrow] \langle \text{Val } v \# es', s' \rangle \end{aligned}$$

— Exception propagation

$$\begin{aligned} &| \text{NewArrayThrow: } \text{extTA}, P, t \vdash \langle \text{newA } T[\text{Throw } a], s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{CastThrow: } \text{extTA}, P, t \vdash \langle \text{Cast } C (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{InstanceOfThrow: } \text{extTA}, P, t \vdash \langle (\text{Throw } a) \text{ instanceof } T, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{BinOpThrow1: } \text{extTA}, P, t \vdash \langle (\text{Throw } a) \ll \text{bop} \gg e_2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{BinOpThrow2: } \text{extTA}, P, t \vdash \langle (\text{Val } v_1) \ll \text{bop} \gg (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{LAssThrow: } \text{extTA}, P, t \vdash \langle V := (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{AAccThrow1: } \text{extTA}, P, t \vdash \langle (\text{Throw } a)[i], s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{AAccThrow2: } \text{extTA}, P, t \vdash \langle (\text{Val } v)[\text{Throw } a], s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{AAssThrow1: } \text{extTA}, P, t \vdash \langle (\text{Throw } a)[i] := e, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{AAssThrow2: } \text{extTA}, P, t \vdash \langle (\text{Val } v)[\text{Throw } a] := e, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{AAssThrow3: } \text{extTA}, P, t \vdash \langle (\text{Val } v)[\text{Val } i] := \text{Throw } a :: \text{'addr expr'}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{ALengthThrow: } \text{extTA}, P, t \vdash \langle (\text{Throw } a) \cdot \text{length}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{FAccThrow: } \text{extTA}, P, t \vdash \langle (\text{Throw } a) \cdot F\{D\}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{FAssThrow1: } \text{extTA}, P, t \vdash \langle (\text{Throw } a) \cdot F\{D\} := e_2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{FAssThrow2: } \text{extTA}, P, t \vdash \langle \text{Val } v \cdot F\{D\} := (\text{Throw } a :: \text{'addr expr'}), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{CASThrow: } \text{extTA}, P, t \vdash \langle \text{Throw } a \cdot \text{compareAndSwap}(D \cdot F, e_2, e_3), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{CASThrow2: } \text{extTA}, P, t \vdash \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } a, e_3), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{CASThrow3: } \text{extTA}, P, t \vdash \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', \text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{CallThrowObj: } \text{extTA}, P, t \vdash \langle (\text{Throw } a) \cdot M(es), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{CallThrowParams: } \llbracket es = \text{map Val } vs @ \text{Throw } a \# es' \rrbracket \implies \text{extTA}, P, t \vdash \langle (\text{Val } v) \cdot M(es), s \rangle -\varepsilon \rightarrow \\ &\langle \text{Throw } a, s \rangle \\ &| \text{BlockThrow: } \text{extTA}, P, t \vdash \langle \{V:T=vo; \text{Throw } a\}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{SynchronizedThrow1: } \text{extTA}, P, t \vdash \langle \text{sync}(\text{Throw } a) e, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{SynchronizedThrow2: } \text{extTA}, P, t \vdash \langle \text{insync}(a) \text{ Throw } ad, s \rangle -\{\text{Unlock} \rightarrow a, \text{SyncUnlock } a\} \rightarrow \langle \text{Throw } \\ &ad, s \rangle \\ &| \text{SeqThrow: } \text{extTA}, P, t \vdash \langle (\text{Throw } a); e_2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{CondThrow: } \text{extTA}, P, t \vdash \langle \text{if } (\text{Throw } a) e_1 \text{ else } e_2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \\ &| \text{ThrowThrow: } \text{extTA}, P, t \vdash \langle \text{throw}(\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle \end{aligned}$$

inductive-cases *red-cases*:

$$\begin{aligned} \text{extTA}, P, t \vdash \langle \text{new } C, s \rangle -ta \rightarrow \langle e', s' \rangle \\ \text{extTA}, P, t \vdash \langle \text{newA } T[e], s \rangle -ta \rightarrow \langle e', s' \rangle \\ \text{extTA}, P, t \vdash \langle \text{Cast } T e, s \rangle -ta \rightarrow \langle e', s' \rangle \\ \text{extTA}, P, t \vdash \langle e \text{ instanceof } T, s \rangle -ta \rightarrow \langle e', s' \rangle \\ \text{extTA}, P, t \vdash \langle e \ll \text{bop} \gg e', s \rangle -ta \rightarrow \langle e'', s' \rangle \\ \text{extTA}, P, t \vdash \langle \text{Var } V, s \rangle -ta \rightarrow \langle e', s' \rangle \\ \text{extTA}, P, t \vdash \langle V := e, s \rangle -ta \rightarrow \langle e', s' \rangle \\ \text{extTA}, P, t \vdash \langle a[i], s \rangle -ta \rightarrow \langle e', s' \rangle \\ \text{extTA}, P, t \vdash \langle a[i] := e, s \rangle -ta \rightarrow \langle e', s' \rangle \\ \text{extTA}, P, t \vdash \langle a \cdot \text{length}, s \rangle -ta \rightarrow \langle e', s' \rangle \\ \text{extTA}, P, t \vdash \langle e \cdot F\{D\}, s \rangle -ta \rightarrow \langle e', s' \rangle \\ \text{extTA}, P, t \vdash \langle e \cdot F\{D\} := e', s \rangle -ta \rightarrow \langle e'', s' \rangle \\ \text{extTA}, P, t \vdash \langle e \cdot \text{compareAndSwap}(D \cdot F, e', e''), s \rangle -ta \rightarrow \langle e''', s' \rangle \\ \text{extTA}, P, t \vdash \langle e \cdot M(es), s \rangle -ta \rightarrow \langle e', s' \rangle \\ \text{extTA}, P, t \vdash \langle \{V:T=vo; e\}, s \rangle -ta \rightarrow \langle e', s' \rangle \\ \text{extTA}, P, t \vdash \langle \text{sync}(o') e, s \rangle -ta \rightarrow \langle e', s' \rangle \end{aligned}$$

$$\begin{aligned}
& \text{extTA},P,t \vdash \langle \text{insync}(a) \ e, s \rangle -ta \rightarrow \langle e', s' \rangle \\
& \text{extTA},P,t \vdash \langle e;;e', s \rangle -ta \rightarrow \langle e'', s' \rangle \\
& \text{extTA},P,t \vdash \langle \text{if } (b) \ e1 \ \text{else } e2, s \rangle -ta \rightarrow \langle e', s' \rangle \\
& \text{extTA},P,t \vdash \langle \text{while } (b) \ e, s \rangle -ta \rightarrow \langle e', s' \rangle \\
& \text{extTA},P,t \vdash \langle \text{throw } e, s \rangle -ta \rightarrow \langle e', s' \rangle \\
& \text{extTA},P,t \vdash \langle \text{try } e \ \text{catch}(C \ V) \ e', s \rangle -ta \rightarrow \langle e'', s' \rangle
\end{aligned}$$

inductive-cases *reds-cases*:

$$\text{extTA},P,t \vdash \langle e \# \ es, s \rangle [-ta \rightarrow] \langle es', s' \rangle$$

abbreviation *red'* ::

$$\begin{aligned}
& 'addr \ J\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'addr \ \text{expr} \Rightarrow ('heap \times 'addr \ \text{locals}) \\
& \Rightarrow ('addr, 'thread\text{-id}, 'heap) \ J\text{-thread-action} \Rightarrow 'addr \ \text{expr} \Rightarrow ('heap \times 'addr \ \text{locals}) \Rightarrow \text{bool} \\
& (\langle -, - \vdash ((1 \langle -, - \rangle) \dashrightarrow / (1 \langle -, - \rangle)) \rangle [51, 0, 0, 0, 0, 0, 0] \ 81)
\end{aligned}$$

where $\text{red}' \ P \equiv \text{red} \ (\text{extTA}2J \ P) \ P$

abbreviation *reds'* ::

$$\begin{aligned}
& 'addr \ J\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'addr \ \text{expr list} \Rightarrow ('heap \times 'addr \ \text{locals}) \\
& \Rightarrow ('addr, 'thread\text{-id}, 'heap) \ J\text{-thread-action} \Rightarrow 'addr \ \text{expr list} \Rightarrow ('heap \times 'addr \ \text{locals}) \Rightarrow \text{bool} \\
& (\langle -, - \vdash ((1 \langle -, - \rangle) [-\dashrightarrow] / (1 \langle -, - \rangle)) \rangle [51, 0, 0, 0, 0, 0, 0] \ 81)
\end{aligned}$$

where $\text{reds}' \ P \equiv \text{reds} \ (\text{extTA}2J \ P) \ P$

4.4.1 Some easy lemmas

lemma [*iff*]:

$$\neg \text{extTA},P,t \vdash \langle \text{Val } v, s \rangle -ta \rightarrow \langle e', s' \rangle$$

<proof>

lemma *red-no-val* [*dest*]:

$$\llbracket \text{extTA},P,t \vdash \langle e, s \rangle -tas \rightarrow \langle e', s' \rangle; \text{is-val } e \rrbracket \Longrightarrow \text{False}$$

<proof>

lemma [*iff*]: $\neg \text{extTA},P,t \vdash \langle \text{Throw } a, s \rangle -ta \rightarrow \langle e', s' \rangle$

<proof>

lemma *reds-map-Val-Throw*:

$$\text{extTA},P,t \vdash \langle \text{map Val vs @ Throw } a \ \# \ es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \longleftrightarrow \text{False}$$

<proof>

lemma *reds-preserves-len*:

$$\text{extTA},P,t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \Longrightarrow \text{length } es' = \text{length } es$$

<proof>

lemma *red-lcl-incr*: $\text{extTA},P,t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \Longrightarrow \text{dom } (lcl \ s) \subseteq \text{dom } (lcl \ s')$

and *reds-lcl-incr*: $\text{extTA},P,t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \Longrightarrow \text{dom } (lcl \ s) \subseteq \text{dom } (lcl \ s')$

<proof>

lemma *red-lcl-add-aux*:

$$\text{extTA},P,t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \Longrightarrow \text{extTA},P,t \vdash \langle e, (hp \ s, l0 \ ++ \ lcl \ s) \rangle -ta \rightarrow \langle e', (hp \ s', l0 \ ++ \ lcl \ s') \rangle$$

and *reds-lcl-add-aux*:

$$\text{extTA},P,t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \Longrightarrow \text{extTA},P,t \vdash \langle es, (hp \ s, l0 \ ++ \ lcl \ s) \rangle [-ta \rightarrow] \langle es', (hp \ s', l0 \ ++ \ lcl \ s') \rangle$$

<proof>

lemma *red-lcl-add*: $extTA, P, t \vdash \langle e, (h, l) \rangle -ta \rightarrow \langle e', (h', l') \rangle \implies extTA, P, t \vdash \langle e, (h, l0 ++ l) \rangle -ta \rightarrow \langle e', (h', l0 ++ l') \rangle$

and *reds-lcl-add*: $extTA, P, t \vdash \langle es, (h, l) \rangle [-ta \rightarrow] \langle es', (h', l') \rangle \implies extTA, P, t \vdash \langle es, (h, l0 ++ l) \rangle [-ta \rightarrow] \langle es', (h', l0 ++ l') \rangle$

$\langle proof \rangle$

lemma *reds-no-val* [*dest*]:

$\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; is\text{-vals } es \rrbracket \implies False$

$\langle proof \rangle$

lemma *red-no-Throw* [*dest!*]:

$extTA, P, t \vdash \langle Throw\ a, s \rangle -ta \rightarrow \langle e', s' \rangle \implies False$

$\langle proof \rangle$

lemma *red-lcl-sub*:

$\llbracket extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; fv\ e \subseteq W \rrbracket$

$\implies extTA, P, t \vdash \langle e, (hp\ s, (lcl\ s)|'W) \rangle -ta \rightarrow \langle e', (hp\ s', (lcl\ s')|'W) \rangle$

and *reds-lcl-sub*:

$\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; fvs\ es \subseteq W \rrbracket$

$\implies extTA, P, t \vdash \langle es, (hp\ s, (lcl\ s)|'W) \rangle [-ta \rightarrow] \langle es', (hp\ s', (lcl\ s')|'W) \rangle$

$\langle proof \rangle$

lemma *red-notfree-unchanged*: $\llbracket extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; V \notin fv\ e \rrbracket \implies lcl\ s'\ V = lcl\ s\ V$

and *reds-notfree-unchanged*: $\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; V \notin fvs\ es \rrbracket \implies lcl\ s'\ V = lcl\ s\ V$

$\langle proof \rangle$

lemma *red-dom-lcl*: $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies dom\ (lcl\ s') \subseteq dom\ (lcl\ s) \cup fv\ e$

and *reds-dom-lcl*: $extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies dom\ (lcl\ s') \subseteq dom\ (lcl\ s) \cup fvs\ es$

$\langle proof \rangle$

lemma *red-Suspend-is-call*:

$\llbracket convert\text{-}extTA\ extNTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; Suspend\ w \in set\ \{ta\}_w \rrbracket$

$\implies \exists a\ vs\ hT\ Ts\ Tr\ D. call\ e' = \llbracket (a, wait, vs) \rrbracket \wedge typeof\text{-}addr\ (hp\ s)\ a = \llbracket hT \rrbracket \wedge P \vdash class\text{-}type\text{-}of\ hT\ sees\ wait: Ts \rightarrow Tr = Native\ in\ D$

and *reds-Suspend-is-calls*:

$\llbracket convert\text{-}extTA\ extNTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; Suspend\ w \in set\ \{ta\}_w \rrbracket$

$\implies \exists a\ vs\ hT\ Ts\ Tr\ D. calls\ es' = \llbracket (a, wait, vs) \rrbracket \wedge typeof\text{-}addr\ (hp\ s)\ a = \llbracket hT \rrbracket \wedge P \vdash class\text{-}type\text{-}of\ hT\ sees\ wait: Ts \rightarrow Tr = Native\ in\ D$

$\langle proof \rangle$

end

context *J-heap begin*

lemma *red-heat-incr*: $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies hp\ s \trianglelefteq hp\ s'$

and *reds-heat-incr*: $extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies hp\ s \trianglelefteq hp\ s'$

$\langle proof \rangle$

lemma *red-preserves-tconf*: $\llbracket extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; P, hp\ s \vdash t\ \sqrt{t} \rrbracket \implies P, hp\ s' \vdash t\ \sqrt{t}$

$\langle proof \rangle$

lemma *reds-preserves-tconf*: $\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; P, hp \ s \vdash t \ \checkmark \ \langle proof \rangle \rrbracket \implies P, hp \ s' \vdash t \ \checkmark$

end

4.4.2 Code generation

context *J-heap-base* **begin**

lemma *RedCall-code*:

$\llbracket \text{is-vals } es; \text{typeof-addr } (hp \ s) \ a = [hU]; P \vdash \text{class-type-of } hU \ \text{sees } M:Ts \rightarrow T = [(pns, body)] \ \text{in } D; \text{size } es = \text{size } pns; \text{size } Ts = \text{size } pns \rrbracket$
 $\implies \text{extTA}, P, t \vdash \langle (addr \ a) \cdot M(es), s \rangle -\varepsilon \rightarrow \langle \text{blocks } (this \ \# \ pns) \ (\text{Class } D \ \# \ Ts) \ (\text{Addr } a \ \# \ \text{map the-Val } es) \ \text{body}, s \rangle$

and *RedCallExternal-code*:

$\llbracket \text{is-vals } es; \text{typeof-addr } (hp \ s) \ a = [hU]; P \vdash \text{class-type-of } hU \ \text{sees } M:Ts \rightarrow T = \text{Native in } D; P, t \vdash \langle a \cdot M(\text{map the-Val } es), hp \ s \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle \rrbracket$
 $\implies \text{extTA}, P, t \vdash \langle (addr \ a) \cdot M(es), s \rangle -\text{extTA } ta \rightarrow \langle \text{extRet2J } ((addr \ a) \cdot M(es)) \ va, (h', \text{lcl } s) \rangle$

and *RedCallNull-code*:

$\text{is-vals } es \implies \text{extTA}, P, t \vdash \langle \text{null} \cdot M(es), s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, s \rangle$

and *CallThrowParams-code*:

$\text{is-Throws } es \implies \text{extTA}, P, t \vdash \langle (\text{Val } v) \cdot M(es), s \rangle -\varepsilon \rightarrow \langle \text{hd } (\text{dropWhile } \text{is-val } es), s \rangle$

$\langle proof \rangle$

end

lemmas [*code-pred-intro*] =

J-heap-base.RedNew[*folded Predicate-Compile.contains-def*] *J-heap-base.RedNewFail* *J-heap-base.NewArrayRed*
J-heap-base.RedNewArray[*folded Predicate-Compile.contains-def*]
J-heap-base.RedNewArrayNegative *J-heap-base.RedNewArrayFail*
J-heap-base.CastRed *J-heap-base.RedCast* *J-heap-base.RedCastFail* *J-heap-base.InstanceOfRed*
J-heap-base.RedInstanceOf *J-heap-base.BinOpRed1* *J-heap-base.BinOpRed2* *J-heap-base.RedBinOp*
J-heap-base.RedBinOpFail
J-heap-base.RedVar *J-heap-base.LAssRed* *J-heap-base.RedLAss*
J-heap-base.AAccRed1 *J-heap-base.AAccRed2* *J-heap-base.RedAAccNull*
J-heap-base.RedAAccBounds *J-heap-base.RedAAcc* *J-heap-base.AAssRed1* *J-heap-base.AAssRed2* *J-heap-base.AAss*
J-heap-base.RedAAssNull *J-heap-base.RedAAssBounds* *J-heap-base.RedAAssStore* *J-heap-base.RedAAss*
J-heap-base.ALengthRed
J-heap-base.RedALength *J-heap-base.RedALengthNull* *J-heap-base.FAccRed* *J-heap-base.RedFAcc* *J-heap-base.RedF*
J-heap-base.FAssRed1 *J-heap-base.FAssRed2* *J-heap-base.RedFAss* *J-heap-base.RedFAssNull*
J-heap-base.CASRed1 *J-heap-base.CASRed2* *J-heap-base.CASRed3* *J-heap-base.CASNull* *J-heap-base.RedCASSucc*
J-heap-base.RedCASFail
J-heap-base.CallObj *J-heap-base.CallParams*

declare

J-heap-base.RedCall-code[*code-pred-intro RedCall-code*]
J-heap-base.RedCallExternal-code[*code-pred-intro RedCallExternal-code*]
J-heap-base.RedCallNull-code[*code-pred-intro RedCallNull-code*]

```

lemmas [code-pred-intro] =
  J-heap-base.BlockRed J-heap-base.RedBlock J-heap-base.SynchronizedRed1 J-heap-base.SynchronizedNull
  J-heap-base.LockSynchronized J-heap-base.SynchronizedRed2 J-heap-base.UnlockSynchronized
  J-heap-base.SeqRed J-heap-base.RedSeq J-heap-base.CondRed J-heap-base.RedCondT J-heap-base.RedCondF
  J-heap-base.RedWhile
  J-heap-base.ThrowRed

declare
  J-heap-base.RedThrowNull[code-pred-intro RedThrowNull]

lemmas [code-pred-intro] =
  J-heap-base.TryRed J-heap-base.RedTry J-heap-base.RedTryCatch
  J-heap-base.RedTryFail J-heap-base.ListRed1 J-heap-base.ListRed2
  J-heap-base.NewArrayThrow J-heap-base.CastThrow J-heap-base.InstanceOfThrow J-heap-base.BinOpThrow1
  J-heap-base.BinOpThrow2
  J-heap-base.LAssThrow J-heap-base.AAccThrow1 J-heap-base.AAccThrow2 J-heap-base.AAssThrow1
  J-heap-base.AAssThrow2
  J-heap-base.AAssThrow3 J-heap-base.ALengthThrow J-heap-base.FAccThrow J-heap-base.FAssThrow1
  J-heap-base.FAssThrow2
  J-heap-base.CASThrow J-heap-base.CASThrow2 J-heap-base.CASThrow3
  J-heap-base.CallThrowObj

declare
  J-heap-base.CallThrowParams-code[code-pred-intro CallThrowParams-code]

lemmas [code-pred-intro] =
  J-heap-base.BlockThrow J-heap-base.SynchronizedThrow1 J-heap-base.SynchronizedThrow2 J-heap-base.SeqThrow
  J-heap-base.CondThrow

declare
  J-heap-base.ThrowThrow[code-pred-intro ThrowThrow]

code-pred
  (modes:
    J-heap-base.red:  $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$ 
    and
    J-heap-base.reds:  $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$ 
    [detect-switches, skip-proof] — proofs are possible, but take veerry long
    J-heap-base.red
  <proof>
end

```

4.5 Weak well-formedness of Jinja programs

```

theory WWellForm
imports
  ../Common/WellForm
  Expr
begin

```

definition

$wf\text{-}J\text{-}mdecl :: 'addr\ J\text{-}prog \Rightarrow cname \Rightarrow 'addr\ J\text{-}mb\ mdecl \Rightarrow bool$

where

$wf\text{-}J\text{-}mdecl\ P\ C \equiv \lambda(M, Ts, T, (pns, body)).$

$length\ Ts = length\ pns \wedge distinct\ pns \wedge this \notin set\ pns \wedge fv\ body \subseteq \{this\} \cup set\ pns$

lemma $wf\text{-}J\text{-}mdecl[simp]$:

$wf\text{-}J\text{-}mdecl\ P\ C\ (M, Ts, T, pns, body) =$

$(length\ Ts = length\ pns \wedge distinct\ pns \wedge this \notin set\ pns \wedge fv\ body \subseteq \{this\} \cup set\ pns)\langle proof \rangle$

abbreviation $wf\text{-}J\text{-}prog :: 'addr\ J\text{-}prog \Rightarrow bool$

where $wf\text{-}J\text{-}prog == wf\text{-}prog\ wf\text{-}J\text{-}mdecl$

end

4.6 Well-typedness of Jinja expressions

theory $WellType$ **imports**

$Expr$

$State$

$../Common/ExternalCallWF$

$../Common/WellForm$

$../Common/SemiType$

begin

declare $Listn.\text{lesub-list-impl-same-size}[simp\ del]$

declare $listE\text{-length}\ [simp\ del]$

type-synonym

$env = vname \rightarrow ty$

inductive

$WT :: (ty \Rightarrow ty \Rightarrow ty \Rightarrow bool) \Rightarrow 'addr\ J\text{-}prog \Rightarrow env \Rightarrow 'addr\ expr \Rightarrow ty \Rightarrow bool\ (\langle -, -, - \vdash - :: - \rangle$
 $[51, 51, 51, 51] 50)$

and $WTs :: (ty \Rightarrow ty \Rightarrow ty \Rightarrow bool) \Rightarrow 'addr\ J\text{-}prog \Rightarrow env \Rightarrow 'addr\ expr\ list \Rightarrow ty\ list \Rightarrow bool$
 $(\langle -, -, - \vdash - :: - \rangle [51, 51, 51, 51] 50)$

for $is\text{-lub} :: ty \Rightarrow ty \Rightarrow ty \Rightarrow bool\ (\langle \vdash\ lub'((- / -)') = - \rangle [51, 51, 51] 50)$

and $P :: 'addr\ J\text{-}prog$

where

$WTNew:$

$is\text{-class}\ P\ C \Longrightarrow$

$is\text{-lub}, P, E \vdash new\ C :: Class\ C$

| $WTNewArray:$

$\llbracket is\text{-lub}, P, E \vdash e :: Integer; is\text{-type}\ P\ (T[]) \rrbracket \Longrightarrow$

$is\text{-lub}, P, E \vdash newA\ T[e] :: T[]$

| $WTCast:$

$\llbracket is\text{-lub}, P, E \vdash e :: T; P \vdash U \leq T \vee P \vdash T \leq U; is\text{-type}\ P\ U \rrbracket$

$\Longrightarrow is\text{-lub}, P, E \vdash Cast\ U\ e :: U$

- | *WTInstanceOf*:
 $\llbracket is-lub, P, E \vdash e :: T; P \vdash U \leq T \vee P \vdash T \leq U; is-type\ P\ U; is-refT\ U \rrbracket$
 $\implies is-lub, P, E \vdash e\ instanceof\ U :: Boolean$
- | *WTVal*:
 $typeof\ v = Some\ T \implies$
 $is-lub, P, E \vdash Val\ v :: T$
- | *WTVar*:
 $E\ V = Some\ T \implies$
 $is-lub, P, E \vdash Var\ V :: T$
- | *WTBinOp*:
 $\llbracket is-lub, P, E \vdash e1 :: T1; is-lub, P, E \vdash e2 :: T2; P \vdash T1 \llbracket bop \rrbracket T2 :: T \rrbracket$
 $\implies is-lub, P, E \vdash e1 \llbracket bop \rrbracket e2 :: T$
- | *WTLAss*:
 $\llbracket E\ V = Some\ T; is-lub, P, E \vdash e :: T'; P \vdash T' \leq T; V \neq this \rrbracket$
 $\implies is-lub, P, E \vdash V := e :: Void$
- | *WTAAcc*:
 $\llbracket is-lub, P, E \vdash a :: T[]; is-lub, P, E \vdash i :: Integer \rrbracket$
 $\implies is-lub, P, E \vdash a[i] :: T$
- | *WTAAss*:
 $\llbracket is-lub, P, E \vdash a :: T[]; is-lub, P, E \vdash i :: Integer; is-lub, P, E \vdash e :: T'; P \vdash T' \leq T \rrbracket$
 $\implies is-lub, P, E \vdash a[i] := e :: Void$
- | *WTALength*:
 $is-lub, P, E \vdash a :: T[] \implies is-lub, P, E \vdash a.length :: Integer$
- | *WTFAcc*:
 $\llbracket is-lub, P, E \vdash e :: U; class-type-of'\ U = [C]; P \vdash C\ sees\ F:T\ (fm)\ in\ D \rrbracket$
 $\implies is-lub, P, E \vdash e.F\{D\} :: T$
- | *WTFAss*:
 $\llbracket is-lub, P, E \vdash e_1 :: U; class-type-of'\ U = [C]; P \vdash C\ sees\ F:T\ (fm)\ in\ D; is-lub, P, E \vdash e_2 :: T'; P \vdash T' \leq T \rrbracket$
 $\implies is-lub, P, E \vdash e_1.F\{D\} := e_2 :: Void$
- | *WTCAS*:
 $\llbracket is-lub, P, E \vdash e1 :: U; class-type-of'\ U = [C]; P \vdash C\ sees\ F:T\ (fm)\ in\ D; volatile\ fm;$
 $is-lub, P, E \vdash e2 :: T'; P \vdash T' \leq T; is-lub, P, E \vdash e3 :: T''; P \vdash T'' \leq T \rrbracket$
 $\implies is-lub, P, E \vdash e1.compareAndSwap(D.F, e2, e3) :: Boolean$
- | *WTCall*:
 $\llbracket is-lub, P, E \vdash e :: U; class-type-of'\ U = [C]; P \vdash C\ sees\ M:Ts \rightarrow T = meth\ in\ D;$
 $is-lub, P, E \vdash es [::] Ts'; P \vdash Ts' [\leq] Ts \rrbracket$
 $\implies is-lub, P, E \vdash e.M(es) :: T$
- | *WTBlock*:
 $\llbracket is-type\ P\ T; is-lub, P, E(V \mapsto T) \vdash e :: T'; case\ vo\ of\ None \Rightarrow True \mid [v] \Rightarrow \exists\ T'.\ typeof\ v = [T']$
 $\wedge\ P \vdash T' \leq T \rrbracket$
 $\implies is-lub, P, E \vdash \{V:T=vo; e\} :: T'$

| *WTSynchronized*:
 $\llbracket is-lub, P, E \vdash o' :: T; is-refT\ T; T \neq NT; is-lub, P, E \vdash e :: T' \rrbracket$
 $\implies is-lub, P, E \vdash sync(o')\ e :: T'$

— Note that *insync* is not statically typable.

| *WTSeq*:
 $\llbracket is-lub, P, E \vdash e_1 :: T_1; is-lub, P, E \vdash e_2 :: T_2 \rrbracket$
 $\implies is-lub, P, E \vdash e_1;;e_2 :: T_2$

| *WTCond*:
 $\llbracket is-lub, P, E \vdash e :: Boolean; is-lub, P, E \vdash e_1 :: T_1; is-lub, P, E \vdash e_2 :: T_2; \vdash lub(T_1, T_2) = T \rrbracket$
 $\implies is-lub, P, E \vdash if\ (e)\ e_1\ else\ e_2 :: T$

| *WTWhile*:
 $\llbracket is-lub, P, E \vdash e :: Boolean; is-lub, P, E \vdash c :: T \rrbracket$
 $\implies is-lub, P, E \vdash while\ (e)\ c :: Void$

| *WTThrow*:
 $\llbracket is-lub, P, E \vdash e :: Class\ C; P \vdash C \preceq^* Throwable \rrbracket \implies$
 $is-lub, P, E \vdash throw\ e :: Void$

| *WTTry*:
 $\llbracket is-lub, P, E \vdash e_1 :: T; is-lub, P, E(V \mapsto Class\ C) \vdash e_2 :: T; P \vdash C \preceq^* Throwable \rrbracket$
 $\implies is-lub, P, E \vdash try\ e_1\ catch(C\ V)\ e_2 :: T$

| *WTNil*: $is-lub, P, E \vdash []\ [::]\ []$

| *WTCons*: $\llbracket is-lub, P, E \vdash e :: T; is-lub, P, E \vdash es [::]\ Ts \rrbracket \implies is-lub, P, E \vdash e\#\#es [::]\ T\#\#Ts$

abbreviation $WT' :: 'addr\ J-prog \Rightarrow env \Rightarrow 'addr\ expr \Rightarrow ty \Rightarrow bool\ (\langle -, - \vdash - :: \rangle \rightarrow [51, 51, 51]\ 50)$
where $WT'\ P \equiv WT\ (TypeRel.is-lub\ P)\ P$

abbreviation $WTs' :: 'addr\ J-prog \Rightarrow env \Rightarrow 'addr\ expr\ list \Rightarrow ty\ list \Rightarrow bool\ (\langle -, - \vdash - [::]\ \rangle \rightarrow [51, 51, 51]\ 50)$

where $WTs'\ P \equiv WTs\ (TypeRel.is-lub\ P)\ P$

declare $WT-WTs.intros[intro!]$

inductive-simps $WTs-iffs\ [iff]$:
 $is-lub', P, E \vdash []\ [::]\ Ts$
 $is-lub', P, E \vdash e\#\#es [::]\ T\#\#Ts$
 $is-lub', P, E \vdash e\#\#es [::]\ Ts$

lemma $WTs-conv-list-all2$:

fixes $is-lub$

shows $is-lub, P, E \vdash es [::]\ Ts = list-all2\ (WT\ is-lub\ P\ E)\ es\ Ts$

<proof>

lemma $WTs-append\ [iff]$: $\bigwedge is-lub\ Ts.\ (is-lub, P, E \vdash es_1 @ es_2 [::]\ Ts) =$
 $(\exists Ts_1\ Ts_2.\ Ts = Ts_1 @ Ts_2 \wedge is-lub, P, E \vdash es_1 [::]\ Ts_1 \wedge is-lub, P, E \vdash es_2 [::]\ Ts_2)$

<proof>

inductive-simps *WT-iffs* [iff]:

$is-lub', P, E \vdash Val\ v :: T$
 $is-lub', P, E \vdash Var\ V :: T$
 $is-lub', P, E \vdash e_1;;e_2 :: T_2$
 $is-lub', P, E \vdash \{V:T=vo; e\} :: T'$

inductive-cases *WT-elim-cases*[elim]:

$is-lub', P, E \vdash V := e :: T$
 $is-lub', P, E \vdash sync(o')\ e :: T$
 $is-lub', P, E \vdash if\ (e)\ e_1\ else\ e_2 :: T$
 $is-lub', P, E \vdash while\ (e)\ c :: T$
 $is-lub', P, E \vdash throw\ e :: T$
 $is-lub', P, E \vdash try\ e_1\ catch(C\ V)\ e_2 :: T$
 $is-lub', P, E \vdash Cast\ D\ e :: T$
 $is-lub', P, E \vdash e\ instanceof\ U :: T$
 $is-lub', P, E \vdash a.F\{D\} :: T$
 $is-lub', P, E \vdash a.F\{D\} := v :: T$
 $is-lub', P, E \vdash e.compareAndSwap(D.F, e', e'') :: T$
 $is-lub', P, E \vdash e_1 \ll bop \gg e_2 :: T$
 $is-lub', P, E \vdash new\ C :: T$
 $is-lub', P, E \vdash newA\ T[e] :: T'$
 $is-lub', P, E \vdash a[i] := e :: T$
 $is-lub', P, E \vdash a[i] :: T$
 $is-lub', P, E \vdash a.length :: T$
 $is-lub', P, E \vdash e.M(ps) :: T$
 $is-lub', P, E \vdash sync(o')\ e :: T$
 $is-lub', P, E \vdash insync(a)\ e :: T$

lemma *fixes is-lub* :: $ty \Rightarrow ty \Rightarrow ty \Rightarrow bool\ (\llub'((- / -)') = \rightarrow [51,51,51]\ 50)$

assumes *is-lub-unique*: $\bigwedge T1\ T2\ T3\ T4. \llbracket \vdash lub(T1, T2) = T3; \vdash lub(T1, T2) = T4 \rrbracket \Longrightarrow T3 = T4$

shows *WT-unique*: $\llbracket is-lub, P, E \vdash e :: T; is-lub, P, E \vdash e :: T' \rrbracket \Longrightarrow T = T'$

and *WTs-unique*: $\llbracket is-lub, P, E \vdash es [::] Ts; is-lub, P, E \vdash es [::] Ts' \rrbracket \Longrightarrow Ts = Ts'$

<proof>

lemma *fixes is-lub*

shows *wt-env-mono*: $is-lub, P, E \vdash e :: T \Longrightarrow (\bigwedge E'. E \subseteq_m E' \Longrightarrow is-lub, P, E' \vdash e :: T)$

and *wts-env-mono*: $is-lub, P, E \vdash es [::] Ts \Longrightarrow (\bigwedge E'. E \subseteq_m E' \Longrightarrow is-lub, P, E' \vdash es [::] Ts)$

<proof>

lemma *fixes is-lub*

shows *WT-fv*: $is-lub, P, E \vdash e :: T \Longrightarrow fv\ e \subseteq dom\ E$

and *WT-fvs*: $is-lub, P, E \vdash es [::] Ts \Longrightarrow fvs\ es \subseteq dom\ E$

<proof>

lemma *fixes is-lub*

shows *WT-expr-locks*: $is-lub, P, E \vdash e :: T \Longrightarrow expr-locks\ e = (\lambda ad. 0)$

and *WTs-expr-locks*: $is-lub, P, E \vdash es [::] Ts \Longrightarrow expr-lockss\ es = (\lambda ad. 0)$

<proof>

lemma

fixes *is-lub* :: $ty \Rightarrow ty \Rightarrow ty \Rightarrow bool\ (\llub'((- / -)') = \rightarrow [51,51,51]\ 50)$

assumes *is-lub-is-type*: $\bigwedge T1\ T2\ T3. \llbracket \vdash lub(T1, T2) = T3; is-type\ P\ T1; is-type\ P\ T2 \rrbracket \Longrightarrow is-type\ P\ T3$

and *wf*: *wf-prog wf-md P*
shows *WT-is-type*: $\llbracket is-lub, P, E \vdash e :: T; ran\ E \subseteq types\ P \rrbracket \Longrightarrow is-type\ P\ T$
and *WTs-is-type*: $\llbracket is-lub, P, E \vdash es\ [::]\ Ts; ran\ E \subseteq types\ P \rrbracket \Longrightarrow set\ Ts \subseteq types\ P$
 $\langle proof \rangle$

lemma

fixes *is-lub1* :: *ty* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *bool* ($\langle \vdash 1\ lub'((-,\ /\ -)^{\wedge}) = \rightarrow [51, 51, 51]\ 50 \rangle$)
and *is-lub2* :: *ty* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *bool* ($\langle \vdash 2\ lub'((-,\ /\ -)^{\wedge}) = \rightarrow [51, 51, 51]\ 50 \rangle$)
assumes *wf*: *wf-prog wf-md P*
and *is-lub1-into-is-lub2*: $\bigwedge T1\ T2\ T3. \llbracket \vdash 1\ lub(T1, T2) = T3; is-type\ P\ T1; is-type\ P\ T2 \rrbracket \Longrightarrow \vdash 2\ lub(T1, T2) = T3$
and *is-lub2-is-type*: $\bigwedge T1\ T2\ T3. \llbracket \vdash 2\ lub(T1, T2) = T3; is-type\ P\ T1; is-type\ P\ T2 \rrbracket \Longrightarrow is-type\ P\ T3$
shows *WT-change-is-lub*: $\llbracket is-lub1, P, E \vdash e :: T; ran\ E \subseteq types\ P \rrbracket \Longrightarrow is-lub2, P, E \vdash e :: T$
and *WTs-change-is-lub*: $\llbracket is-lub1, P, E \vdash es\ [::]\ Ts; ran\ E \subseteq types\ P \rrbracket \Longrightarrow is-lub2, P, E \vdash es\ [::]\ Ts$
 $\langle proof \rangle$

4.6.1 Code generator setup

lemma *WTBlock-code*:

$\bigwedge is-lub. \llbracket is-type\ P\ T; is-lub, P, E(V \mapsto T) \vdash e :: T' \rrbracket$
 $case\ vo\ of\ None \Rightarrow True \mid [v] \Rightarrow case\ typeof\ v\ of\ None \Rightarrow False \mid Some\ T' \Rightarrow P \vdash T' \leq T \rrbracket$
 $\Longrightarrow is-lub, P, E \vdash \{V:T=vo; e\} :: T'$
 $\langle proof \rangle$

lemmas [*code-pred-intro*] =

WTNew WTNewArray WTCast WTInstanceOf WTVal WTVar WTBinOp WTLAss WTAAcc WTAAss
WTALength WTFAcc WTFAss WTCAS WTCall

declare

WTBlock-code [*code-pred-intro WTBlock'*]

lemmas [*code-pred-intro*] =

WTSynchronized WTSeq WTCond WTWhile WTThrow WTTry
WTNil WTCons

code-pred

(*modes*:

$(i \Rightarrow i \Rightarrow o \Rightarrow bool) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool,$
 $(i \Rightarrow i \Rightarrow o \Rightarrow bool) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow bool)$

[*detect-switches, skip-proof*]

WT

$\langle proof \rangle$

inductive *is-lub-sup* :: '*m prog* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *bool*

for *P T1 T2 T3*

where

$sup\ P\ T1\ T2 = OK\ T3 \Longrightarrow is-lub-sup\ P\ T1\ T2\ T3$

code-pred

(*modes*: $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool, i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow bool$)

is-lub-sup

$\langle proof \rangle$

definition *WT-code* :: '*addr J-prog* \Rightarrow *env* \Rightarrow '*addr expr* \Rightarrow *ty* \Rightarrow *bool* ($\langle \langle -, - \vdash - :: '' \rangle \rightarrow [51, 51, 51]\ 50 \rangle$)

where *WT-code* *P* \equiv *WT* (*is-lub-sup* *P*) *P*

definition $WTs\text{-code} :: 'addr\ J\text{-prog} \Rightarrow env \Rightarrow 'addr\ expr\ list \Rightarrow ty\ list \Rightarrow bool\ (\langle -, - \vdash - \rangle [::']) \rightarrow [51,51,51]\ 50)$

where $WTs\text{-code}\ P \equiv WTs\ (is\text{-lub-sup}\ P)\ P$

lemma assumes $wf: wf\text{-prog}\ wf\text{-md}\ P$

shows $WT\text{-code-into-WT}$:

$\llbracket P, E \vdash e ::' T; ran\ E \subseteq types\ P \rrbracket \Longrightarrow P, E \vdash e :: T$

and $WTs\text{-code-into-WTs}$:

$\llbracket P, E \vdash es [::'] Ts; ran\ E \subseteq types\ P \rrbracket \Longrightarrow P, E \vdash es [::] Ts$
 $\langle proof \rangle$

lemma assumes $wf: wf\text{-prog}\ wf\text{-md}\ P$

shows $WT\text{-into-WT-code}$:

$\llbracket P, E \vdash e :: T; ran\ E \subseteq types\ P \rrbracket \Longrightarrow P, E \vdash e ::' T$

and $WT\text{-into-WTs-code-OK}$:

$\llbracket P, E \vdash es [::] Ts; ran\ E \subseteq types\ P \rrbracket \Longrightarrow P, E \vdash es [::'] Ts$
 $\langle proof \rangle$

theorem $WT\text{-eq-WT-code}$:

assumes $wf\text{-prog}\ wf\text{-md}\ P$

and $ran\ E \subseteq types\ P$

shows $P, E \vdash e :: T \longleftrightarrow P, E \vdash e ::' T$

$\langle proof \rangle$

code-pred

$(modes: i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow bool, i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool)$

$[inductify]$

$WT\text{-code}$

$\langle proof \rangle$

code-pred

$(modes: i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow bool, i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool)$

$[inductify]$

$WTs\text{-code}$

$\langle proof \rangle$

end

4.7 Definite assignment

theory $DefAss$

imports

$Expr$

begin

4.7.1 Hypersets

type-synonym $'a\ hyperset = 'a\ set\ option$

definition $hyperUn :: 'a\ hyperset \Rightarrow 'a\ hyperset \Rightarrow 'a\ hyperset\ (\mathbf{infixl}\ \langle \sqcup \rangle\ 65)$

where

$$A \sqcup B \equiv \text{case } A \text{ of } \text{None} \Rightarrow \text{None} \\ | [A] \Rightarrow (\text{case } B \text{ of } \text{None} \Rightarrow \text{None} | [B] \Rightarrow [A \cup B])$$

definition *hyperInt* :: 'a hyperset \Rightarrow 'a hyperset \Rightarrow 'a hyperset (infixl $\langle \sqcap \rangle$ 70)
where

$$A \sqcap B \equiv \text{case } A \text{ of } \text{None} \Rightarrow B \\ | [A] \Rightarrow (\text{case } B \text{ of } \text{None} \Rightarrow [A] | [B] \Rightarrow [A \cap B])$$

definition *hyperDiff1* :: 'a hyperset \Rightarrow 'a \Rightarrow 'a hyperset (infixl $\langle \ominus \rangle$ 65)
where

$$A \ominus a \equiv \text{case } A \text{ of } \text{None} \Rightarrow \text{None} | [A] \Rightarrow [A - \{a\}]$$

definition *hyper-isin* :: 'a \Rightarrow 'a hyperset \Rightarrow bool (infix $\langle \in \in \rangle$ 50)
where

$$a \in \in A \equiv \text{case } A \text{ of } \text{None} \Rightarrow \text{True} | [A] \Rightarrow a \in A$$

definition *hyper-subset* :: 'a hyperset \Rightarrow 'a hyperset \Rightarrow bool (infix $\langle \sqsubseteq \rangle$ 50)
where

$$A \sqsubseteq B \equiv \text{case } B \text{ of } \text{None} \Rightarrow \text{True} \\ | [B] \Rightarrow (\text{case } A \text{ of } \text{None} \Rightarrow \text{False} | [A] \Rightarrow A \subseteq B)$$

lemmas *hyperset-defs* =

hyperUn-def hyperInt-def hyperDiff1-def hyper-isin-def hyper-subset-def

lemma [*simp*]: $[\{\}] \sqcup A = A \wedge A \sqcup [\{\}] = A$ $\langle \text{proof} \rangle$

lemma [*simp*]: $[A] \sqcup [B] = [A \cup B] \wedge [A] \ominus a = [A - \{a\}]$ $\langle \text{proof} \rangle$

lemma [*simp*]: $\text{None} \sqcup A = \text{None} \wedge A \sqcup \text{None} = \text{None}$ $\langle \text{proof} \rangle$

lemma [*simp*]: $a \in \in \text{None} \wedge \text{None} \ominus a = \text{None}$ $\langle \text{proof} \rangle$

lemma *hyperUn-assoc*: $(A \sqcup B) \sqcup C = A \sqcup (B \sqcup C)$ $\langle \text{proof} \rangle$

lemma *hyper-insert-comm*: $A \sqcup [\{a\}] = [\{a\}] \sqcup A \wedge A \sqcup ([\{a\}] \sqcup B) = [\{a\}] \sqcup (A \sqcup B)$ $\langle \text{proof} \rangle$

lemma *sqSub-mem-lem* [*elim*]: $\llbracket A \sqsubseteq A'; a \in \in A \rrbracket \Longrightarrow a \in \in A'$ $\langle \text{proof} \rangle$

lemma [*iff*]: $A \sqsubseteq \text{None}$ $\langle \text{proof} \rangle$

lemma [*simp*]: $A \sqsubseteq A$ $\langle \text{proof} \rangle$

lemma [*iff*]: $[A] \sqsubseteq [B] \longleftrightarrow A \subseteq B$ $\langle \text{proof} \rangle$

lemma *sqUn-lem2*: $A \sqsubseteq A' \Longrightarrow B \sqcup A \sqsubseteq B \sqcup A'$ $\langle \text{proof} \rangle$

lemma *sqSub-trans* [*trans*, *intro*]: $\llbracket A \sqsubseteq B; B \sqsubseteq C \rrbracket \Longrightarrow A \sqsubseteq C$ $\langle \text{proof} \rangle$

lemma *hyperUn-comm*: $A \sqcup B = B \sqcup A$ $\langle \text{proof} \rangle$

lemma *hyperUn-leftComm*: $A \sqcup (B \sqcup C) = B \sqcup (A \sqcup C)$ $\langle \text{proof} \rangle$

lemmas *hyperUn-ac = hyperUn-comm hyperUn-leftComm hyperUn-assoc*

lemma [*simp*]: $[\{\}] \sqcup B = B$
 ⟨*proof*⟩

lemma [*simp*]: $[\{\}] \sqsubseteq A$
 ⟨*proof*⟩

lemma *sqInt-lem*: $A \sqsubseteq A' \implies A \sqcap B \sqsubseteq A' \sqcap B$
 ⟨*proof*⟩

4.7.2 Definite assignment

primrec $\mathcal{A} :: ('a, 'b, 'addr) \text{exp} \Rightarrow 'a \text{hyperset}$
and $\mathcal{A}s :: ('a, 'b, 'addr) \text{exp list} \Rightarrow 'a \text{hyperset}$
where

$\mathcal{A} (\text{new } C) = [\{\}]$
 $\mathcal{A} (\text{newA } T[e]) = \mathcal{A} e$
 $\mathcal{A} (\text{Cast } C e) = \mathcal{A} e$
 $\mathcal{A} (e \text{ instanceof } T) = \mathcal{A} e$
 $\mathcal{A} (\text{Val } v) = [\{\}]$
 $\mathcal{A} (e_1 \ll\text{bop}\gg e_2) = \mathcal{A} e_1 \sqcup \mathcal{A} e_2$
 $\mathcal{A} (\text{Var } V) = [\{\}]$
 $\mathcal{A} (\text{LAss } V e) = [\{V\}] \sqcup \mathcal{A} e$
 $\mathcal{A} (a[i]) = \mathcal{A} a \sqcup \mathcal{A} i$
 $\mathcal{A} (a[i] := e) = \mathcal{A} a \sqcup \mathcal{A} i \sqcup \mathcal{A} e$
 $\mathcal{A} (a \cdot \text{length}) = \mathcal{A} a$
 $\mathcal{A} (e \cdot F\{D\}) = \mathcal{A} e$
 $\mathcal{A} (e_1 \cdot F\{D\} := e_2) = \mathcal{A} e_1 \sqcup \mathcal{A} e_2$
 $\mathcal{A} (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) = \mathcal{A} e1 \sqcup \mathcal{A} e2 \sqcup \mathcal{A} e3$
 $\mathcal{A} (e \cdot M(es)) = \mathcal{A} e \sqcup \mathcal{A}s es$
 $\mathcal{A} (\{V:T=vo; e\}) = \mathcal{A} e \ominus V$
 $\mathcal{A} (\text{sync}_V(o') e) = \mathcal{A} o' \sqcup \mathcal{A} e$
 $\mathcal{A} (\text{insync}_V(a) e) = \mathcal{A} e$
 $\mathcal{A} (e_1;;e_2) = \mathcal{A} e_1 \sqcup \mathcal{A} e_2$
 $\mathcal{A} (\text{if } (e) e_1 \text{ else } e_2) = \mathcal{A} e \sqcup (\mathcal{A} e_1 \sqcap \mathcal{A} e_2)$
 $\mathcal{A} (\text{while } (b) e) = \mathcal{A} b$
 $\mathcal{A} (\text{throw } e) = \text{None}$
 $\mathcal{A} (\text{try } e_1 \text{ catch}(C V) e_2) = \mathcal{A} e_1 \sqcap (\mathcal{A} e_2 \ominus V)$

$\mathcal{A}s ([\]) = [\{\}]$
 $\mathcal{A}s (e\#es) = \mathcal{A} e \sqcup \mathcal{A}s es$

primrec $\mathcal{D} :: ('a, 'b, 'addr) \text{exp} \Rightarrow 'a \text{hyperset} \Rightarrow \text{bool}$
and $\mathcal{D}s :: ('a, 'b, 'addr) \text{exp list} \Rightarrow 'a \text{hyperset} \Rightarrow \text{bool}$
where

$\mathcal{D} (\text{new } C) A = \text{True}$
 $\mathcal{D} (\text{newA } T[e]) A = \mathcal{D} e A$
 $\mathcal{D} (\text{Cast } C e) A = \mathcal{D} e A$
 $\mathcal{D} (e \text{ instanceof } T) = \mathcal{D} e$
 $\mathcal{D} (\text{Val } v) A = \text{True}$
 $\mathcal{D} (e_1 \ll\text{bop}\gg e_2) A = (\mathcal{D} e_1 A \wedge \mathcal{D} e_2 (A \sqcup \mathcal{A} e_1))$
 $\mathcal{D} (\text{Var } V) A = (V \in \in A)$
 $\mathcal{D} (\text{LAss } V e) A = \mathcal{D} e A$

```

|  $\mathcal{D} (a[i]) A = (\mathcal{D} a A \wedge \mathcal{D} i (A \sqcup \mathcal{A} a))$ 
|  $\mathcal{D} (a[i] := e) A = (\mathcal{D} a A \wedge \mathcal{D} i (A \sqcup \mathcal{A} a) \wedge \mathcal{D} e (A \sqcup \mathcal{A} a \sqcup \mathcal{A} i))$ 
|  $\mathcal{D} (a.length) A = \mathcal{D} a A$ 
|  $\mathcal{D} (e.F\{D\}) A = \mathcal{D} e A$ 
|  $\mathcal{D} (e_1.F\{D\}:=e_2) A = (\mathcal{D} e_1 A \wedge \mathcal{D} e_2 (A \sqcup \mathcal{A} e_1))$ 
|  $\mathcal{D} (e_1.compareAndSwap(D.F, e_2, e_3)) A = (\mathcal{D} e_1 A \wedge \mathcal{D} e_2 (A \sqcup \mathcal{A} e_1) \wedge \mathcal{D} e_3 (A \sqcup \mathcal{A} e_1 \sqcup \mathcal{A} e_2))$ 
|  $\mathcal{D} (e.M(es)) A = (\mathcal{D} e A \wedge \mathcal{D} s es (A \sqcup \mathcal{A} e))$ 
|  $\mathcal{D} (\{V:T=vo; e\}) A = (\text{if } vo = \text{None then } \mathcal{D} e (A \ominus V) \text{ else } \mathcal{D} e (A \sqcup [\{V\}]))$ 
|  $\mathcal{D} (sync_V (o') e) A = (\mathcal{D} o' A \wedge \mathcal{D} e (A \sqcup \mathcal{A} o'))$ 
|  $\mathcal{D} (insync_V (a) e) A = \mathcal{D} e A$ 
|  $\mathcal{D} (e_1;;e_2) A = (\mathcal{D} e_1 A \wedge \mathcal{D} e_2 (A \sqcup \mathcal{A} e_1))$ 
|  $\mathcal{D} (\text{if } (e) e_1 \text{ else } e_2) A = (\mathcal{D} e A \wedge \mathcal{D} e_1 (A \sqcup \mathcal{A} e) \wedge \mathcal{D} e_2 (A \sqcup \mathcal{A} e))$ 
|  $\mathcal{D} (\text{while } (e) c) A = (\mathcal{D} e A \wedge \mathcal{D} c (A \sqcup \mathcal{A} e))$ 
|  $\mathcal{D} (\text{throw } e) A = \mathcal{D} e A$ 
|  $\mathcal{D} (\text{try } e_1 \text{ catch}(C V) e_2) A = (\mathcal{D} e_1 A \wedge \mathcal{D} e_2 (A \sqcup [\{V\}]))$ 

```

```

|  $\mathcal{D} s (\[]) A = \text{True}$ 
|  $\mathcal{D} s (e\#es) A = (\mathcal{D} e A \wedge \mathcal{D} s es (A \sqcup \mathcal{A} e))$ 

```

lemma *As-map-Val*[simp]: $\mathcal{A} s (\text{map Val } vs) = [\{\}] \langle \text{proof} \rangle$

lemma *As-append* [simp]: $\mathcal{A} s (xs @ ys) = (\mathcal{A} s xs) \sqcup (\mathcal{A} s ys)$
 $\langle \text{proof} \rangle$

lemma *Ds-map-Val*[simp]: $\mathcal{D} s (\text{map Val } vs) A \langle \text{proof} \rangle$

lemma *D-append*[iff]: $\bigwedge A. \mathcal{D} s (es @ es') A = (\mathcal{D} s es A \wedge \mathcal{D} s es' (A \sqcup \mathcal{A} es)) \langle \text{proof} \rangle$

lemma *fixes* $e :: ('a, 'b, 'addr) \text{exp}$ **and** $es :: ('a, 'b, 'addr) \text{exp list}$

shows *A-fv*: $\bigwedge A. \mathcal{A} e = [A] \implies A \subseteq \text{fv } e$

and $\bigwedge A. \mathcal{A} s es = [A] \implies A \subseteq \text{fvs } es$

$\langle \text{proof} \rangle$

lemma *sqUn-lem*: $A \sqsubseteq A' \implies A \sqcup B \sqsubseteq A' \sqcup B \langle \text{proof} \rangle$

lemma *diff-lem*: $A \sqsubseteq A' \implies A \ominus b \sqsubseteq A' \ominus b \langle \text{proof} \rangle$

lemma *fixes* $e :: ('a, 'b, 'addr) \text{exp}$ **and** $es :: ('a, 'b, 'addr) \text{exp list}$

shows *D-mono*: $\bigwedge A A'. A \sqsubseteq A' \implies \mathcal{D} e A \implies \mathcal{D} e A'$

and *Ds-mono*: $\bigwedge A A'. A \sqsubseteq A' \implies \mathcal{D} s es A \implies \mathcal{D} s es A' \langle \text{proof} \rangle$

lemma *D-mono'*: $\mathcal{D} e A \implies A \sqsubseteq A' \implies \mathcal{D} e A'$

and *Ds-mono'*: $\mathcal{D} s es A \implies A \sqsubseteq A' \implies \mathcal{D} s es A' \langle \text{proof} \rangle$

declare *hyperUn-comm* [simp]

declare *hyperUn-leftComm* [simp]

end

4.8 Well-formedness Constraints

theory *JWellForm*

imports

WWellForm

WellType

DefAss
begin

definition *wf-J-mdecl* :: 'addr J-prog ⇒ cname ⇒ 'addr J-mb mdecl ⇒ bool
where
wf-J-mdecl P C ≡ λ(*M, Ts, T, (pns, body)*).
length Ts = *length pns* ∧
distinct pns ∧
this ∉ *set pns* ∧
(∃ *T'*. *P, [this ↦ Class C, pns ↦ Ts]* ⊢ *body* :: *T' ∧ P ⊢ T' ≤ T*) ∧
 \mathcal{D} *body* [{ *this* } ∪ *set pns*]

lemma *wf-J-mdecl[simp]*:
wf-J-mdecl P C (M, Ts, T, pns, body) ≡
(*length Ts* = *length pns* ∧
distinct pns ∧
this ∉ *set pns* ∧
(∃ *T'*. *P, [this ↦ Class C, pns ↦ Ts]* ⊢ *body* :: *T' ∧ P ⊢ T' ≤ T*) ∧
 \mathcal{D} *body* [{ *this* } ∪ *set pns*])⟨*proof*⟩

abbreviation *wf-J-prog* :: 'addr J-prog ⇒ bool
where *wf-J-prog* == *wf-prog wf-J-mdecl*

lemma *wf-mdecl-wwf-mdecl*: *wf-J-mdecl P C Md* ⇒ *wwf-J-mdecl P C Md*⟨*proof*⟩⟨*proof*⟩⟨*proof*⟩⟨*proof*⟩⟨*proof*⟩⟨*proof*⟩⟨*proof*⟩⟨*ML*⟩

4.9 The source language as an instance of the framework

theory *Threaded*

imports

SmallStep

JWellForm

../Common/ConformThreaded

../Common/ExternalCallWF

../Framework/FWLiftingSem

../Framework/FWProgressAux

begin

context *heap-base* **begin** — Move to ?? - also used in BV

lemma *wset-Suspend-ok-start-state*:

fixes *final r convert-RA*

assumes *start-state f P C M vs* ∈ *I*

shows *start-state f P C M vs* ∈ *multithreaded-base.wset-Suspend-ok final r convert-RA I*
⟨*proof*⟩

end

abbreviation *final-expr* :: 'addr expr × 'addr locals ⇒ bool **where**

final-expr ≡ λ(*e, x*). *final e*

lemma *final-locks*: *final e* ⇒ *expr-locks e l* = 0

⟨*proof*⟩

context *J-heap-base* **begin**

abbreviation *mred*

$:: 'addr\ J\text{-prog} \Rightarrow ('addr, 'thread\text{-id}, 'addr\ expr \times 'addr\ locals, 'heap, 'addr, ('addr, 'thread\text{-id})\ obs\text{-event})\ semantics$

where

$mred\ P\ t \equiv (\lambda((e, l), h)\ ta\ ((e', l'), h').\ P, t \vdash \langle e, (h, l) \rangle -ta \rightarrow \langle e', (h', l') \rangle)$

lemma *red-new-thread-heap*:

$\llbracket convert\text{-ext}TA\ extNTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; NewThread\ t''\ ex''\ h'' \in set\ \{\{ta\}_t\} \rrbracket \Longrightarrow h'' = hp\ s'$

and *reds-new-thread-heap*:

$\llbracket convert\text{-ext}TA\ extNTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; NewThread\ t''\ ex''\ h'' \in set\ \{\{ta\}_t\} \rrbracket \Longrightarrow h'' = hp\ s'$
 $\langle proof \rangle$

lemma *red-ta-Wakeup-no-Join-no-Lock-no-Interrupt*:

$\llbracket convert\text{-ext}TA\ extNTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; Notified \in set\ \{\{ta\}_w\} \vee WokenUp \in set\ \{\{ta\}_w\} \rrbracket \Longrightarrow collect\text{-locks}\ \{\{ta\}_l\} = \{\} \wedge collect\text{-cond}\text{-actions}\ \{\{ta\}_c\} = \{\} \wedge collect\text{-interrupts}\ \{\{ta\}_i\} = \{\}$

and *reds-ta-Wakeup-no-Join-no-Lock-no-Interrupt*:

$\llbracket convert\text{-ext}TA\ extNTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; Notified \in set\ \{\{ta\}_w\} \vee WokenUp \in set\ \{\{ta\}_w\} \rrbracket \Longrightarrow collect\text{-locks}\ \{\{ta\}_l\} = \{\} \wedge collect\text{-cond}\text{-actions}\ \{\{ta\}_c\} = \{\} \wedge collect\text{-interrupts}\ \{\{ta\}_i\} = \{\}$
 $\langle proof \rangle$

lemma *final-no-red*:

$final\ e \Longrightarrow \neg P, t \vdash \langle e, (h, l) \rangle -ta \rightarrow \langle e', (h', l') \rangle$
 $\langle proof \rangle$

lemma *red-mthr: multithreaded final-expr* (*mred P*)

$\langle proof \rangle$

end

sublocale *J-heap-base* < *red-mthr: multithreaded*

final-expr

mred P

convert-RA

for *P*

$\langle proof \rangle$

context *J-heap-base* **begin**

abbreviation

mredT $::$

$'addr\ J\text{-prog} \Rightarrow ('addr, 'thread\text{-id}, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state$

$\Rightarrow ('thread\text{-id} \times ('addr, 'thread\text{-id}, 'addr\ expr \times 'addr\ locals, 'heap)\ Jinja\text{-thread}\text{-action})$

$\Rightarrow ('addr, 'thread\text{-id}, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state \Rightarrow bool$

where

$mredT\ P \equiv red\text{-mthr}.redT\ P$

abbreviation

$mredT\text{-syntax1} :: 'addr\ J\text{-prog} \Rightarrow ('addr, 'thread\text{-id}, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state$

$\Rightarrow 'thread\text{-id} \Rightarrow ('addr, 'thread\text{-id}, 'addr\ expr \times 'addr\ locals, 'heap)\ Jinja\text{-thread}\text{-action}$

$$\begin{aligned} &\Rightarrow ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state \Rightarrow bool \\ &(\langle \vdash - \dashv \rightarrow \rightarrow \rangle [50, 0, 0, 0, 50] 80) \end{aligned}$$

where

$$mredT\text{-syntax1}\ P\ s\ t\ ta\ s' \equiv mredT\ P\ s\ (t, ta)\ s'$$

abbreviation

$$\begin{aligned} &mRedT\text{-syntax1} :: \\ &'addr\ J\text{-prog} \\ &\Rightarrow ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state \\ &\Rightarrow ('thread-id \times ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap)\ Jinja\text{-thread-action})\ list \\ &\Rightarrow ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state \Rightarrow bool \\ &(\langle \vdash - \dashv \rightarrow * \rangle [50, 0, 0, 50] 80) \end{aligned}$$

where

$$P \vdash s \dashv \rightarrow * s' \equiv red\text{-mthr.RedT}\ P\ s\ ttas\ s'$$

end

context *J-heap* begin

lemma *redT-hext-incr*:

$$P \vdash s \dashv \rightarrow ta \rightarrow s' \Longrightarrow shr\ s \sqsubseteq shr\ s'$$

<proof>

lemma *RedT-hext-incr*:

$$\begin{aligned} &\text{assumes } P \vdash s \dashv \rightarrow * s' \\ &\text{shows } shr\ s \sqsubseteq shr\ s' \end{aligned}$$

<proof>

end

4.9.1 Lifting *tconf* to multithreaded states

context *J-heap* begin

lemma *red-NewThread-Thread-Object*:

$$\begin{aligned} &\llbracket \text{convert-extTA}\ extNTA, P, t \vdash \langle e, s \rangle \dashv \rightarrow \langle e', s' \rangle; \text{NewThread}\ t' x m \in \text{set } \{ta\}_t \rrbracket \\ &\Longrightarrow \exists C. \text{typeof-addr}\ (hp\ s')\ (\text{thread-id2addr}\ t') = \lfloor \text{Class-type}\ C \rfloor \wedge P \vdash C \preceq^* \text{Thread} \\ &\text{and } \text{reds-NewThread-Thread-Object}: \\ &\llbracket \text{convert-extTA}\ extNTA, P, t \vdash \langle es, s \rangle \dashv \rightarrow \langle es', s' \rangle; \text{NewThread}\ t' x m \in \text{set } \{ta\}_t \rrbracket \\ &\Longrightarrow \exists C. \text{typeof-addr}\ (hp\ s')\ (\text{thread-id2addr}\ t') = \lfloor \text{Class-type}\ C \rfloor \wedge P \vdash C \preceq^* \text{Thread} \end{aligned}$$

<proof>

lemma *lifting-wf-tconf*:

$$\text{lifting-wf}\ \text{final-expr}\ (mred\ P)\ (\lambda t\ ex\ h. P, h \vdash t \surd t)$$

<proof>

end

sublocale *J-heap* < *red-tconf*: *lifting-wf final-expr mred P convert-RA* $\lambda t\ ex\ h. P, h \vdash t \surd t$

<proof>

4.9.2 Towards agreement between the framework semantics' lock state and the locks stored in the expressions

primrec $\text{sync-ok} :: ('a, 'b, 'addr) \text{exp} \Rightarrow \text{bool}$
and $\text{sync-oks} :: ('a, 'b, 'addr) \text{exp list} \Rightarrow \text{bool}$
where
 $\text{sync-ok} (\text{new } C) = \text{True}$
 $\text{sync-ok} (\text{newA } T [i]) = \text{sync-ok } i$
 $\text{sync-ok} (\text{Cast } T e) = \text{sync-ok } e$
 $\text{sync-ok} (e \text{ instanceof } T) = \text{sync-ok } e$
 $\text{sync-ok} (\text{Val } v) = \text{True}$
 $\text{sync-ok} (\text{Var } v) = \text{True}$
 $\text{sync-ok} (e \ll \text{bop} \gg e') = (\text{sync-ok } e \wedge \text{sync-ok } e' \wedge (\text{contains-insync } e' \longrightarrow \text{is-val } e))$
 $\text{sync-ok} (V := e) = \text{sync-ok } e$
 $\text{sync-ok} (a [i]) = (\text{sync-ok } a \wedge \text{sync-ok } i \wedge (\text{contains-insync } i \longrightarrow \text{is-val } a))$
 $\text{sync-ok} (\text{AAss } a i e) = (\text{sync-ok } a \wedge \text{sync-ok } i \wedge \text{sync-ok } e \wedge (\text{contains-insync } i \longrightarrow \text{is-val } a) \wedge (\text{contains-insync } e \longrightarrow \text{is-val } a \wedge \text{is-val } i))$
 $\text{sync-ok} (a \cdot \text{length}) = \text{sync-ok } a$
 $\text{sync-ok} (e \cdot F \{D\}) = \text{sync-ok } e$
 $\text{sync-ok} (\text{FAss } e F D e') = (\text{sync-ok } e \wedge \text{sync-ok } e' \wedge (\text{contains-insync } e' \longrightarrow \text{is-val } e))$
 $\text{sync-ok} (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) = (\text{sync-ok } e \wedge \text{sync-ok } e' \wedge \text{sync-ok } e'' \wedge (\text{contains-insync } e' \longrightarrow \text{is-val } e) \wedge (\text{contains-insync } e'' \longrightarrow \text{is-val } e \wedge \text{is-val } e'))$
 $\text{sync-ok} (e \cdot m(\text{pns})) = (\text{sync-ok } e \wedge \text{sync-oks } \text{pns} \wedge (\text{contains-insyncs } \text{pns} \longrightarrow \text{is-val } e))$
 $\text{sync-ok} (\{V : T = \text{vo}; e\}) = \text{sync-ok } e$
 $\text{sync-ok} (\text{sync}_V (o') e) = (\text{sync-ok } o' \wedge \neg \text{contains-insync } e)$
 $\text{sync-ok} (\text{insync}_V (a) e) = \text{sync-ok } e$
 $\text{sync-ok} (e; e') = (\text{sync-ok } e \wedge \neg \text{contains-insync } e')$
 $\text{sync-ok} (\text{if } (b) e \text{ else } e') = (\text{sync-ok } b \wedge \neg \text{contains-insync } e \wedge \neg \text{contains-insync } e')$
 $\text{sync-ok} (\text{while } (b) e) = (\neg \text{contains-insync } b \wedge \neg \text{contains-insync } e)$
 $\text{sync-ok} (\text{throw } e) = \text{sync-ok } e$
 $\text{sync-ok} (\text{try } e \text{ catch } (C v) e') = (\text{sync-ok } e \wedge \neg \text{contains-insync } e')$
 $\text{sync-oks } [] = \text{True}$
 $\text{sync-oks } (x \# xs) = (\text{sync-ok } x \wedge \text{sync-oks } xs \wedge (\text{contains-insyncs } xs \longrightarrow \text{is-val } x))$

lemma sync-oks-append [simp]:

$\text{sync-oks } (xs @ ys) \longleftrightarrow \text{sync-oks } xs \wedge \text{sync-oks } ys \wedge (\text{contains-insyncs } ys \longrightarrow (\exists vs. xs = \text{map Val } vs))$
 <proof>

lemma $\text{fixes } e :: ('a, 'b, 'addr) \text{exp}$ **and** $es :: ('a, 'b, 'addr) \text{exp list}$

shows $\text{not-contains-insync-sync-ok}: \neg \text{contains-insync } e \Longrightarrow \text{sync-ok } e$
and $\text{not-contains-insyncs-sync-oks}: \neg \text{contains-insyncs } es \Longrightarrow \text{sync-oks } es$
 <proof>

lemma $\text{expr-locks-sync-ok}: (\bigwedge ad. \text{expr-locks } e \text{ ad} = 0) \Longrightarrow \text{sync-ok } e$

and $\text{expr-locks-sync-oks}: (\bigwedge ad. \text{expr-locks } es \text{ ad} = 0) \Longrightarrow \text{sync-oks } es$
 <proof>

lemma sync-ok-extRet2J [simp, intro!]: $\text{sync-ok } e \Longrightarrow \text{sync-ok } (\text{extRet2J } e \text{ va})$

<proof>

abbreviation

$\text{sync-es-ok} :: ('addr, 'thread-id, ('a, 'b, 'addr) \text{exp} \times 'c) \text{thread-info} \Rightarrow 'heap \Rightarrow \text{bool}$
where

$sync-es-ok \equiv ts-ok (\lambda t (e, x) m. sync-ok e)$

lemma *sync-es-ok-blocks* [simp]:

$\llbracket length\ pns = length\ Ts; length\ Ts = length\ vs \rrbracket \implies sync-ok (blocks\ pns\ Ts\ vs\ e) = sync-ok\ e$
 ⟨proof⟩

context *J-heap-base* **begin**

lemma **assumes** *wf*: *wf-J-prog* *P*

shows *red-preserve-sync-ok*: $\llbracket extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; sync-ok\ e \rrbracket \implies sync-ok\ e'$
and *reds-preserve-sync-oks*: $\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; sync-oks\ es \rrbracket \implies sync-oks\ es'$
 ⟨proof⟩

lemma **assumes** *wf*: *wf-J-prog* *P*

shows *expr-locks-new-thread*:
 $\llbracket P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; NewThread\ t'' (e'', x'')\ h \in set\ \{\{ta\}_t\} \rrbracket \implies expr-locks\ e'' = (\lambda ad. 0)$

and *expr-locks-new-thread'*:
 $\llbracket P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; NewThread\ t'' (e'', x'')\ h \in set\ \{\{ta\}_t\} \rrbracket \implies expr-locks\ e'' = (\lambda ad. 0)$
 ⟨proof⟩

lemma **assumes** *wf*: *wf-J-prog* *P*

shows *red-new-thread-sync-ok*: $\llbracket P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; NewThread\ t'' (e'', x'')\ h'' \in set\ \{\{ta\}_t\} \rrbracket \implies sync-ok\ e''$
and *reds-new-thread-sync-ok*: $\llbracket P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; NewThread\ t'' (e'', x'')\ h'' \in set\ \{\{ta\}_t\} \rrbracket \implies sync-ok\ e''$
 ⟨proof⟩

lemma *lifting-wf-sync-ok*: *wf-J-prog* *P* $\implies lifting-wf\ final-expr (mred\ P) (\lambda t (e, x) m. sync-ok\ e)$
 ⟨proof⟩

lemma *redT-preserve-sync-ok*:

assumes *red*: $P \vdash s -t \triangleright ta \rightarrow s'$
shows $\llbracket wf-J-prog\ P; sync-es-ok (thr\ s) (shr\ s) \rrbracket \implies sync-es-ok (thr\ s') (shr\ s')$
 ⟨proof⟩

lemma *RedT-preserves-sync-ok*:

$\llbracket wf-J-prog\ P; P \vdash s -\triangleright ttas \rightarrow * s'; sync-es-ok (thr\ s) (shr\ s) \rrbracket$
 $\implies sync-es-ok (thr\ s') (shr\ s')$
 ⟨proof⟩

lemma *sync-es-ok-J-start-state*:

$\llbracket wf-J-prog\ P; P \vdash C\ sees\ M: Ts \rightarrow T = [(pns, body)]\ in\ D; length\ Ts = length\ vs \rrbracket$
 $\implies sync-es-ok (thr\ (J-start-state\ P\ C\ M\ vs))\ m$
 ⟨proof⟩

end

Framework lock state agrees with locks stored in the expression

definition *lock-ok* :: $(\text{'addr}, \text{'thread-id})\ locks \Rightarrow (\text{'addr}, \text{'thread-id}, (\text{'a}, \text{'b}, \text{'addr})\ exp \times \text{'x})\ thread-info \Rightarrow bool$ **where**

$\bigwedge ln. lock-ok\ ls\ ts \equiv \forall t. (case\ (ts\ t)\ of\ None \Rightarrow (\forall l. has-locks\ (ls\ \$\ l)\ t = 0)$
 $\quad | \llbracket ((e, x), ln) \rrbracket \Rightarrow (\forall l. has-locks\ (ls\ \$\ l)\ t + ln\ \$\ l = expr-locks\ e\ l))$

lemma *lock-okI*:

$\llbracket \bigwedge t l. ts\ t = \text{None} \implies \text{has-locks } (ls\ \$\ l)\ t = 0; \bigwedge t e\ x\ ln\ l. ts\ t = \llbracket (e, x), ln \rrbracket \implies \text{has-locks } (ls\ \$\ l)\ t + ln\ \$\ l = \text{expr-locks } e\ l \rrbracket \implies \text{lock-ok } ls\ ts$
 $\langle \text{proof} \rangle$

lemma *lock-okE*:

$\llbracket \text{lock-ok } ls\ ts;$
 $\quad \forall t. ts\ t = \text{None} \longrightarrow (\forall l. \text{has-locks } (ls\ \$\ l)\ t = 0) \implies Q;$
 $\quad \forall t e\ x\ ln. ts\ t = \llbracket (e, x), ln \rrbracket \longrightarrow (\forall l. \text{has-locks } (ls\ \$\ l)\ t + ln\ \$\ l = \text{expr-locks } e\ l) \implies Q \rrbracket$
 $\implies Q$
 $\langle \text{proof} \rangle$

lemma *lock-okD1*:

$\llbracket \text{lock-ok } ls\ ts; ts\ t = \text{None} \rrbracket \implies \forall l. \text{has-locks } (ls\ \$\ l)\ t = 0$
 $\langle \text{proof} \rangle$

lemma *lock-okD2*:

$\bigwedge ln. \llbracket \text{lock-ok } ls\ ts; ts\ t = \llbracket (e, x), ln \rrbracket \rrbracket \implies \forall l. \text{has-locks } (ls\ \$\ l)\ t + ln\ \$\ l = \text{expr-locks } e\ l$
 $\langle \text{proof} \rangle$

lemma *lock-ok-lock-thread-ok*:

assumes *lock*: *lock-ok* *ls* *ts*
shows *lock-thread-ok* *ls* *ts*
 $\langle \text{proof} \rangle$

lemma (in *J-heap-base*) *lock-ok-J-start-state*:

$\llbracket \text{wf-J-prog } P; P \vdash C \text{ sees } M:Ts \rightarrow T = \llbracket (pns, body) \rrbracket \text{ in } D; \text{length } Ts = \text{length } vs \rrbracket$
 $\implies \text{lock-ok } (\text{locks } (J\text{-start-state } P\ C\ M\ vs))\ (\text{thr } (J\text{-start-state } P\ C\ M\ vs))$
 $\langle \text{proof} \rangle$

4.9.3 Preservation of lock state agreement

fun *upd-expr-lock-action* :: *int* \Rightarrow *lock-action* \Rightarrow *int*

where

upd-expr-lock-action *i* *Lock* = *i* + 1
| *upd-expr-lock-action* *i* *Unlock* = *i* - 1
| *upd-expr-lock-action* *i* *UnlockFail* = *i*
| *upd-expr-lock-action* *i* *ReleaseAcquire* = *i*

fun *upd-expr-lock-actions* :: *int* \Rightarrow *lock-action list* \Rightarrow *int* **where**

upd-expr-lock-actions *n* $\llbracket \rrbracket$ = *n*
| *upd-expr-lock-actions* *n* (*L* # *Ls*) = *upd-expr-lock-actions* (*upd-expr-lock-action* *n* *L*) *Ls*

lemma *upd-expr-lock-actions-append* [*simp*]:

upd-expr-lock-actions *n* (*Ls* @ *Ls'*) = *upd-expr-lock-actions* (*upd-expr-lock-actions* *n* *Ls*) *Ls'*
 $\langle \text{proof} \rangle$

definition *upd-expr-locks* :: (*l* \Rightarrow *int*) \Rightarrow *l lock-actions* \Rightarrow *l* \Rightarrow *int*

where *upd-expr-locks* *els* *las* \equiv $\lambda l. \text{upd-expr-lock-actions } (els\ l)\ (las\ \$\ l)$

lemma *upd-expr-locks-iff* [*simp*]:

upd-expr-locks *els* *las* *l* = *upd-expr-lock-actions* (*els* *l*) (*las* \$ *l*)
 $\langle \text{proof} \rangle$

lemma *upd-expr-lock-action-add* [simp]:

$upd\text{-}expr\text{-}lock\text{-}action\ (l + l')\ L = upd\text{-}expr\text{-}lock\text{-}action\ l\ L + l'$
 ⟨proof⟩

lemma *upd-expr-lock-actions-add* [simp]:

$upd\text{-}expr\text{-}lock\text{-}actions\ (l + l')\ Ls = upd\text{-}expr\text{-}lock\text{-}actions\ l\ Ls + l'$
 ⟨proof⟩

lemma *upd-expr-locks-add* [simp]:

$upd\text{-}expr\text{-}locks\ (\lambda a. x\ a + y\ a)\ las = (\lambda a. upd\text{-}expr\text{-}locks\ x\ las\ a + y\ a)$
 ⟨proof⟩

lemma *expr-locks-extRet2J* [simp, intro!]: $expr\text{-}locks\ e = (\lambda ad. 0) \implies expr\text{-}locks\ (extRet2J\ e\ va) =$
 $(\lambda ad. 0)$

⟨proof⟩

lemma (in *J-heap-base*)

assumes *wf*: *wf-J-prog P*

shows *red-update-expr-locks*:

$\llbracket convert\text{-}extTA\ extNTA, P, t \vdash \langle e, s \rangle \text{-}ta \rightarrow \langle e', s^\wedge \rangle; sync\text{-}ok\ e \rrbracket$
 $\implies upd\text{-}expr\text{-}locks\ (int\ o\ expr\text{-}locks\ e)\ \{\!|ta|\!\}_l = int\ o\ expr\text{-}locks\ e'$

and *reds-update-expr-locks*:

$\llbracket convert\text{-}extTA\ extNTA, P, t \vdash \langle es, s \rangle \text{-}ta \rightarrow \langle es', s^\wedge \rangle; sync\text{-}oks\ es \rrbracket$
 $\implies upd\text{-}expr\text{-}locks\ (int\ o\ expr\text{-}lockss\ es)\ \{\!|ta|\!\}_l = int\ o\ expr\text{-}lockss\ es'$

⟨proof⟩

definition *lock-expr-locks-ok* :: $'t\ FWState.lock \Rightarrow 't \Rightarrow nat \Rightarrow int \Rightarrow bool$ **where**

$lock\text{-}expr\text{-}locks\text{-}ok\ l\ t\ n\ i \equiv (i = int\ (has\text{-}locks\ l\ t) + int\ n) \wedge i \geq 0$

lemma *upd-lock-upd-expr-lock-action-preserve-lock-expr-locks-ok*:

assumes *lao*: *lock-action-ok l t L*

and *lelo*: *lock-expr-locks-ok l t n i*

shows *lock-expr-locks-ok* (*upd-lock l t L*) *t* (*upd-threadR n l t L*) (*upd-expr-lock-action i L*)

⟨proof⟩

lemma *upd-locks-upd-expr-lock-preserve-lock-expr-locks-ok*:

$\llbracket lock\text{-}actions\text{-}ok\ l\ t\ Ls; lock\text{-}expr\text{-}locks\text{-}ok\ l\ t\ n\ i \rrbracket$

$\implies lock\text{-}expr\text{-}locks\text{-}ok\ (upd\text{-}locks\ l\ t\ Ls)\ t\ (upd\text{-}threadRs\ n\ l\ t\ Ls)\ (upd\text{-}expr\text{-}lock\text{-}actions\ i\ Ls)$

⟨proof⟩

definition *ls-els-ok* :: $('addr, 'thread-id)\ locks \Rightarrow 'thread-id \Rightarrow ('addr \Rightarrow nat) \Rightarrow ('addr \Rightarrow int) \Rightarrow bool$
where

$\bigwedge ln. ls\text{-}els\text{-}ok\ ls\ t\ ln\ els \equiv \forall l. lock\text{-}expr\text{-}locks\text{-}ok\ (ls\ \$\ l)\ t\ (ln\ \$\ l)\ (els\ l)$

lemma *ls-els-okI*:

$\bigwedge ln. (\bigwedge l. lock\text{-}expr\text{-}locks\text{-}ok\ (ls\ \$\ l)\ t\ (ln\ \$\ l)\ (els\ l)) \implies ls\text{-}els\text{-}ok\ ls\ t\ ln\ els$
 ⟨proof⟩

lemma *ls-els-okE*:

$\bigwedge ln. \llbracket ls\text{-}els\text{-}ok\ ls\ t\ ln\ els; \forall l. lock\text{-}expr\text{-}locks\text{-}ok\ (ls\ \$\ l)\ t\ (ln\ \$\ l)\ (els\ l) \implies P \rrbracket \implies P$
 ⟨proof⟩

lemma *ls-els-okD*:

$\bigwedge ln. ls\text{-}els\text{-}ok\ ls\ t\ ln\ els \implies lock\text{-}expr\text{-}locks\text{-}ok\ (ls\ \$\ l)\ t\ (ln\ \$\ l)\ (els\ l)$
 $\langle proof \rangle$

lemma *redT-updLs-upd-expr-locks-preserves-ls-els-ok*:

$\bigwedge ln. \llbracket ls\text{-}els\text{-}ok\ ls\ t\ ln\ els; lock\text{-}ok\text{-}las\ ls\ t\ las \rrbracket$
 $\implies ls\text{-}els\text{-}ok\ (redT\text{-}updLs\ ls\ t\ las)\ t\ (redT\text{-}updLns\ ls\ t\ ln\ las)\ (upd\text{-}expr\text{-}locks\ els\ las)$
 $\langle proof \rangle$

lemma *sync-ok-redT-updT*:

assumes *sync-es-ok* $ts\ h$
and *nt*: $\bigwedge t\ e\ x\ h''. ta = NewThread\ t\ (e, x)\ h'' \implies sync\text{-}ok\ e$
shows *sync-es-ok* $(redT\text{-}updT\ ts\ ta)\ h'$
 $\langle proof \rangle$

lemma *sync-ok-redT-updT*s:

$\llbracket sync\text{-}es\text{-}ok\ ts\ h; \bigwedge t\ e\ x\ h. NewThread\ t\ (e, x)\ h \in set\ tas \implies sync\text{-}ok\ e \rrbracket$
 $\implies sync\text{-}es\text{-}ok\ (redT\text{-}updTs\ ts\ tas)\ h'$
 $\langle proof \rangle$

lemma *lock-ok-thr-updI*:

$\bigwedge ln. \llbracket lock\text{-}ok\ ls\ ts; ts\ t = \lfloor ((e, xs), ln) \rfloor; expr\text{-}locks\ e = expr\text{-}locks\ e' \rrbracket$
 $\implies lock\text{-}ok\ ls\ (ts(t \mapsto ((e', xs'), ln)))$
 $\langle proof \rangle$

context *J-heap-base* **begin**

lemma *redT-preserves-lock-ok*:

assumes *wf*: *wf-J-prog* P
and $P \vdash s \text{-}t \triangleright ta \rightarrow s'$
and *lock-ok* $(locks\ s)\ (thr\ s)$
and *sync-es-ok* $(thr\ s)\ (shr\ s)$
shows *lock-ok* $(locks\ s')\ (thr\ s')$
 $\langle proof \rangle$

lemma *invariant3p-sync-es-ok-lock-ok*:

assumes *wf*: *wf-J-prog* P
shows *invariant3p* $(mredT\ P)\ \{s. sync\text{-}es\text{-}ok\ (thr\ s)\ (shr\ s) \wedge lock\text{-}ok\ (locks\ s)\ (thr\ s)\}$
 $\langle proof \rangle$

lemma *RedT-preserves-lock-ok*:

assumes *wf*: *wf-J-prog* P
and *Red*: $P \vdash s \text{-}t \triangleright ttas \rightarrow^* s'$
and *ae*: *sync-es-ok* $(thr\ s)\ (shr\ s)$
and *loes*: *lock-ok* $(locks\ s)\ (thr\ s)$
shows *lock-ok* $(locks\ s')\ (thr\ s')$
 $\langle proof \rangle$

end

4.9.4 Determinism

context *J-heap-base* **begin**

lemma

fixes *final*

assumes *det: deterministic-heap-ops*

shows *red-deterministic:*

$\llbracket \text{convert-extTA } extTA, P, t \vdash \langle e, (shr\ s, xs) \rangle -ta \rightarrow \langle e', s' \rangle;$
 $\text{convert-extTA } extTA, P, t \vdash \langle e, (shr\ s, xs) \rangle -ta' \rightarrow \langle e'', s'' \rangle;$
 $final\text{-thread.actions-ok } final\ s\ t\ ta; final\text{-thread.actions-ok } final\ s\ t\ ta' \rrbracket$
 $\implies ta = ta' \wedge e' = e'' \wedge s' = s''$

and *reds-deterministic:*

$\llbracket \text{convert-extTA } extTA, P, t \vdash \langle es, (shr\ s, xs) \rangle [-ta \rightarrow] \langle es', s' \rangle;$
 $\text{convert-extTA } extTA, P, t \vdash \langle es, (shr\ s, xs) \rangle [-ta' \rightarrow] \langle es'', s'' \rangle;$
 $final\text{-thread.actions-ok } final\ s\ t\ ta; final\text{-thread.actions-ok } final\ s\ t\ ta' \rrbracket$
 $\implies ta = ta' \wedge es' = es'' \wedge s' = s''$

$\langle proof \rangle$

lemma *red-mthr-deterministic:*

assumes *det: deterministic-heap-ops*

shows *red-mthr.deterministic P UNIV*

$\langle proof \rangle$

end

end

4.10 Runtime Well-typedness

theory *WellTypeRT*

imports

WellType

JHeap

begin

context *J-heap-base* **begin**

inductive *WTrt* :: '*addr J-prog* \Rightarrow '*heap* \Rightarrow '*env* \Rightarrow '*addr expr* \Rightarrow '*ty* \Rightarrow *bool*

and *WTrts* :: '*addr J-prog* \Rightarrow '*heap* \Rightarrow '*env* \Rightarrow '*addr expr list* \Rightarrow '*ty list* \Rightarrow *bool*

for *P* :: '*addr J-prog* **and** *h* :: '*heap*

where

WTrtNew:

$is\text{-class } P\ C \implies WTrt\ P\ h\ E\ (new\ C)\ (Class\ C)$

| *WTrtNewArray:*

$\llbracket WTrt\ P\ h\ E\ e\ Integer; is\text{-type } P\ (T\ []) \rrbracket$

$\implies WTrt\ P\ h\ E\ (newA\ T\ [e])\ (T\ [])$

| *WTrtCast:*

$\llbracket WTrt\ P\ h\ E\ e\ T; is\text{-type } P\ U \rrbracket \implies WTrt\ P\ h\ E\ (Cast\ U\ e)\ U$

| *WTrtInstanceOf:*

$\llbracket WTrt\ P\ h\ E\ e\ T; is\text{-type } P\ U \rrbracket \implies WTrt\ P\ h\ E\ (e\ instanceof\ U)\ Boolean$

| *WTrtVal:*

$$\text{typeof}_h v = \text{Some } T \implies \text{WTrt } P \ h \ E \ (\text{Val } v) \ T$$

| *WTrtVar*:

$$E \ V = \text{Some } T \implies \text{WTrt } P \ h \ E \ (\text{Var } V) \ T$$

| *WTrtBinOp*:

$$\llbracket \text{WTrt } P \ h \ E \ e1 \ T1; \text{WTrt } P \ h \ E \ e2 \ T2; P \vdash T1 \llbracket \text{bop} \rrbracket T2 : T \rrbracket \\ \implies \text{WTrt } P \ h \ E \ (e1 \llbracket \text{bop} \rrbracket e2) \ T$$

| *WTrtLAss*:

$$\llbracket E \ V = \text{Some } T; \text{WTrt } P \ h \ E \ e \ T'; P \vdash T' \leq T \rrbracket \\ \implies \text{WTrt } P \ h \ E \ (V := e) \ \text{Void}$$

| *WTrtAAcc*:

$$\llbracket \text{WTrt } P \ h \ E \ a \ (T[_]); \text{WTrt } P \ h \ E \ i \ \text{Integer} \rrbracket \\ \implies \text{WTrt } P \ h \ E \ (a[i]) \ T$$

| *WTrtAAccNT*:

$$\llbracket \text{WTrt } P \ h \ E \ a \ \text{NT}; \text{WTrt } P \ h \ E \ i \ \text{Integer} \rrbracket \\ \implies \text{WTrt } P \ h \ E \ (a[i]) \ T$$

| *WTrtAAss*:

$$\llbracket \text{WTrt } P \ h \ E \ a \ (T[_]); \text{WTrt } P \ h \ E \ i \ \text{Integer}; \text{WTrt } P \ h \ E \ e \ T' \rrbracket \\ \implies \text{WTrt } P \ h \ E \ (a[i] := e) \ \text{Void}$$

| *WTrtAAssNT*:

$$\llbracket \text{WTrt } P \ h \ E \ a \ \text{NT}; \text{WTrt } P \ h \ E \ i \ \text{Integer}; \text{WTrt } P \ h \ E \ e \ T' \rrbracket \\ \implies \text{WTrt } P \ h \ E \ (a[i] := e) \ \text{Void}$$

| *WTrtALength*:

$$\text{WTrt } P \ h \ E \ a \ (T[_]) \implies \text{WTrt } P \ h \ E \ (a.\text{length}) \ \text{Integer}$$

| *WTrtALengthNT*:

$$\text{WTrt } P \ h \ E \ a \ \text{NT} \implies \text{WTrt } P \ h \ E \ (a.\text{length}) \ T$$

| *WTrtFAcc*:

$$\llbracket \text{WTrt } P \ h \ E \ e \ U; \text{class-type-of}' U = [C]; P \vdash C \text{ has } F:T \ (fm) \ \text{in } D \rrbracket \implies \\ \text{WTrt } P \ h \ E \ (e.F\{D\}) \ T$$

| *WTrtFAccNT*:

$$\text{WTrt } P \ h \ E \ e \ \text{NT} \implies \text{WTrt } P \ h \ E \ (e.F\{D\}) \ T$$

| *WTrtFAss*:

$$\llbracket \text{WTrt } P \ h \ E \ e1 \ U; \text{class-type-of}' U = [C]; P \vdash C \text{ has } F:T \ (fm) \ \text{in } D; \text{WTrt } P \ h \ E \ e2 \ T2; P \\ \vdash T2 \leq T \rrbracket \\ \implies \text{WTrt } P \ h \ E \ (e1.F\{D\} := e2) \ \text{Void}$$

| *WTrtFAssNT*:

$$\llbracket \text{WTrt } P \ h \ E \ e1 \ \text{NT}; \text{WTrt } P \ h \ E \ e2 \ T2 \rrbracket \\ \implies \text{WTrt } P \ h \ E \ (e1.F\{D\} := e2) \ \text{Void}$$

| *WTrtCAS*:

$$\llbracket \text{WTrt } P \ h \ E \ e1 \ U; \text{class-type-of}' U = [C]; P \vdash C \text{ has } F:T \ (fm) \ \text{in } D; \text{volatile } fm; \\ \text{WTrt } P \ h \ E \ e2 \ T2; P \vdash T2 \leq T; \text{WTrt } P \ h \ E \ e3 \ T3; P \vdash T3 \leq T \rrbracket$$

$\implies \text{WTrt } P \text{ h } E (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \text{ Boolean}$

| *WTrtCASNT*:

$\llbracket \text{WTrt } P \text{ h } E e1 \text{ NT}; \text{WTrt } P \text{ h } E e2 \text{ T2}; \text{WTrt } P \text{ h } E e3 \text{ T3} \rrbracket$
 $\implies \text{WTrt } P \text{ h } E (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \text{ Boolean}$

| *WTrtCall*:

$\llbracket \text{WTrt } P \text{ h } E e \text{ U}; \text{class-type-of}' U = \lfloor C \rfloor; P \vdash C \text{ sees } M:Ts \rightarrow T = \text{meth in } D;$
 $\text{WTrts } P \text{ h } E \text{ es } Ts'; P \vdash Ts' \llbracket \leq \rrbracket Ts \rrbracket$
 $\implies \text{WTrt } P \text{ h } E (e \cdot M(\text{es})) T$

| *WTrtCallNT*:

$\llbracket \text{WTrt } P \text{ h } E e \text{ NT}; \text{WTrts } P \text{ h } E \text{ es } Ts \rrbracket$
 $\implies \text{WTrt } P \text{ h } E (e \cdot M(\text{es})) T$

| *WTrtBlock*:

$\llbracket \text{WTrt } P \text{ h } (E(V \mapsto T)) e \text{ T}'; \text{case vo of None} \Rightarrow \text{True} \mid \lfloor v \rfloor \Rightarrow \exists T'. \text{typeof}_h v = \lfloor T' \rfloor \wedge P \vdash T' \leq T \rrbracket$
 $\implies \text{WTrt } P \text{ h } E \{V:T=vo; e\} T'$

| *WTrtSynchronized*:

$\llbracket \text{WTrt } P \text{ h } E o' \text{ T}; \text{is-refT } T; \text{WTrt } P \text{ h } E e \text{ T}' \rrbracket$
 $\implies \text{WTrt } P \text{ h } E (\text{sync}(o') e) T'$

| *WTrtInSynchronized*:

$\llbracket \text{WTrt } P \text{ h } E (\text{addr } a) \text{ T}; \text{WTrt } P \text{ h } E e \text{ T}' \rrbracket$
 $\implies \text{WTrt } P \text{ h } E (\text{insync}(a) e) T'$

| *WTrtSeq*:

$\llbracket \text{WTrt } P \text{ h } E e1 \text{ T1}; \text{WTrt } P \text{ h } E e2 \text{ T2} \rrbracket$
 $\implies \text{WTrt } P \text{ h } E (e1;;e2) T2$

| *WTrtCond*:

$\llbracket \text{WTrt } P \text{ h } E e \text{ Boolean}; \text{WTrt } P \text{ h } E e1 \text{ T1}; \text{WTrt } P \text{ h } E e2 \text{ T2}; P \vdash \text{lub}(T1, T2) = T \rrbracket$
 $\implies \text{WTrt } P \text{ h } E (\text{if } (e) e1 \text{ else } e2) T$

| *WTrtWhile*:

$\llbracket \text{WTrt } P \text{ h } E e \text{ Boolean}; \text{WTrt } P \text{ h } E c \text{ T} \rrbracket$
 $\implies \text{WTrt } P \text{ h } E (\text{while}(e) c) \text{ Void}$

| *WTrtThrow*:

$\llbracket \text{WTrt } P \text{ h } E e \text{ T}; P \vdash T \leq \text{Class Throwable} \rrbracket$
 $\implies \text{WTrt } P \text{ h } E (\text{throw } e) T'$

| *WTrtTry*:

$\llbracket \text{WTrt } P \text{ h } E e1 \text{ T1}; \text{WTrt } P \text{ h } (E(V \mapsto \text{Class } C)) e2 \text{ T2}; P \vdash T1 \leq T2 \rrbracket$
 $\implies \text{WTrt } P \text{ h } E (\text{try } e1 \text{ catch}(C V) e2) T2$

| *WTrtNil*: $\text{WTrts } P \text{ h } E \llbracket \rrbracket$

| *WTrtCons*: $\llbracket \text{WTrt } P \text{ h } E e \text{ T}; \text{WTrts } P \text{ h } E \text{ es } Ts \rrbracket \implies \text{WTrts } P \text{ h } E (e \# \text{es}) (T \# Ts)$

abbreviation

$\text{WTrt-syntax} :: 'addr \text{ J-prog} \Rightarrow \text{env} \Rightarrow 'heap \Rightarrow 'addr \text{ expr} \Rightarrow \text{ty} \Rightarrow \text{bool} (\langle -, -, - \vdash - : - \rangle \rightarrow [51, 51, 51] 50)$

where

$$P, E, h \vdash e : T \equiv \text{WTrt } P \ h \ E \ e \ T$$

abbreviation

$\text{WTrts-syntax} :: 'addr \ J\text{-prog} \Rightarrow \text{env} \Rightarrow 'heap \Rightarrow 'addr \ \text{expr list} \Rightarrow \text{ty list} \Rightarrow \text{bool} \ (\langle -, -, - \vdash - \rangle [\cdot] \rightarrow) [51, 51, 51] 50)$

where

$$P, E, h \vdash \text{es } [\cdot] \ Ts \equiv \text{WTrts } P \ h \ E \ \text{es} \ Ts$$

lemmas $[\text{intro!}] =$

$\text{WTrtNew } \text{WTrtNewArray } \text{WTrtCast } \text{WTrtInstanceOf } \text{WTrtVal } \text{WTrtVar } \text{WTrtBinOp } \text{WTrtLAss} \\ \text{WTrtBlock } \text{WTrtSynchronized } \text{WTrtInSynchronized } \text{WTrtSeq } \text{WTrtCond } \text{WTrtWhile} \\ \text{WTrtThrow } \text{WTrtTry } \text{WTrtNil } \text{WTrtCons}$

lemmas $[\text{intro}] =$

$\text{WTrtFAcc } \text{WTrtFAccNT } \text{WTrtFAss } \text{WTrtFAssNT } \text{WTrtCall } \text{WTrtCallNT} \\ \text{WTrtAAcc } \text{WTrtAAccNT } \text{WTrtAAss } \text{WTrtAAssNT } \text{WTrtALength } \text{WTrtALengthNT}$

4.10.1 Easy consequences

inductive-simps $\text{WTrts-iffs} [\text{iff}]$:

$$P, E, h \vdash [] [\cdot] \ Ts \\ P, E, h \vdash e \# \text{es } [\cdot] \ T \# \ Ts \\ P, E, h \vdash (e \# \text{es}) [\cdot] \ Ts$$

lemma $\text{WTrts-conv-list-all2}$: $P, E, h \vdash \text{es } [\cdot] \ Ts = \text{list-all2} \ (\text{WTrt } P \ h \ E) \ \text{es} \ Ts$
 $\langle \text{proof} \rangle$

lemma $[\text{simp}]$: $(P, E, h \vdash \text{es}_1 \ @ \ \text{es}_2 [\cdot] \ Ts) =$
 $(\exists \ Ts_1 \ \ Ts_2. \ Ts = \ Ts_1 \ @ \ \ Ts_2 \ \wedge \ P, E, h \vdash \text{es}_1 [\cdot] \ \ Ts_1 \ \& \ P, E, h \vdash \text{es}_2 [\cdot] \ \ Ts_2)$
 $\langle \text{proof} \rangle$

inductive-simps $\text{WTrt-iffs} [\text{iff}]$:

$$P, E, h \vdash \text{Val } v : T \\ P, E, h \vdash \text{Var } v : T \\ P, E, h \vdash e_1 ;; e_2 : T_2 \\ P, E, h \vdash \{ V : T = v_0; e \} : T'$$

inductive-cases $\text{WTrt-elim-cases}[\text{elim!}]$:

$$P, E, h \vdash \text{newA } T [i] : U \\ P, E, h \vdash v := e : T \\ P, E, h \vdash \text{if } (e) \ e_1 \ \text{else } e_2 : T \\ P, E, h \vdash \text{while}(e) \ c : T \\ P, E, h \vdash \text{throw } e : T \\ P, E, h \vdash \text{try } e_1 \ \text{catch}(C \ V) \ e_2 : T \\ P, E, h \vdash \text{Cast } D \ e : T \\ P, E, h \vdash e \ \text{instanceof } U : T \\ P, E, h \vdash a [i] : T \\ P, E, h \vdash a [i] := e : T \\ P, E, h \vdash a \cdot \text{length} : T \\ P, E, h \vdash e \cdot F \{D\} : T \\ P, E, h \vdash e \cdot F \{D\} := v : T \\ P, E, h \vdash e \cdot \text{compareAndSwap}(D \cdot F, \ e_2, \ e_3) : T \\ P, E, h \vdash e_1 \ \langle\langle \text{bop} \rangle\rangle \ e_2 : T$$

$P, E, h \vdash \text{new } C : T$
 $P, E, h \vdash e \cdot M(es) : T$
 $P, E, h \vdash \text{sync}(o') e : T$
 $P, E, h \vdash \text{insync}(a) e : T$

4.10.2 Some interesting lemmas

lemma *WTrts-Val[simp]*:

$P, E, h \vdash \text{map Val } vs [:] Ts \longleftrightarrow \text{map } (\text{typeof}_h) vs = \text{map Some } Ts$
 ⟨proof⟩

lemma *WTrt-env-mono*: $P, E, h \vdash e : T \implies (\bigwedge E'. E \subseteq_m E' \implies P, E', h \vdash e : T)$
and *WTrts-env-mono*: $P, E, h \vdash es [:] Ts \implies (\bigwedge E'. E \subseteq_m E' \implies P, E', h \vdash es [:] Ts)$
 ⟨proof⟩

lemma *WT-implies-WTrt*: $P, E \vdash e :: T \implies P, E, h \vdash e : T$
and *WTs-implies-WTrts*: $P, E \vdash es [::] Ts \implies P, E, h \vdash es [:] Ts$
 ⟨proof⟩

lemma *wt-blocks*:

$\bigwedge E. \llbracket \text{length } Vs = \text{length } Ts; \text{length } vs = \text{length } Ts \rrbracket \implies$
 $(P, E, h \vdash \text{blocks } Vs Ts vs e : T) =$
 $(P, E(Vs[\mapsto]Ts), h \vdash e : T \wedge (\exists Ts'. \text{map } (\text{typeof}_h) vs = \text{map Some } Ts' \wedge P \vdash Ts' [\leq] Ts))$
 ⟨proof⟩

end

context *J-heap begin*

lemma *WTrt-hext-mono*: $P, E, h \vdash e : T \implies h \trianglelefteq h' \implies P, E, h' \vdash e : T$
and *WTrts-hext-mono*: $P, E, h \vdash es [:] Ts \implies h \trianglelefteq h' \implies P, E, h' \vdash es [:] Ts$
 ⟨proof⟩

end

end

4.11 Progress of Small Step Semantics

theory *Progress*

imports

WellTypeRT

DefAss

SmallStep

../Common/ExternalCallWF

WWellForm

begin

context *J-heap begin*

lemma *final-addrE* [*consumes 3, case-names addr Throw*]:

$\llbracket P, E, h \vdash e : T; \text{class-type-of}' T = \lfloor U \rfloor; \text{final } e;$
 $\bigwedge a. e = \text{addr } a \implies R;$
 $\bigwedge a. e = \text{Throw } a \implies R \rrbracket \implies R$

<proof>

lemma *finalRefE* [*consumes 3, case-names null Class Array Throw*]:

[[$P, E, h \vdash e : T$; *is-refT* T ; *final* e ;
 $e = \text{null} \implies R$;
 $\bigwedge a C. \llbracket e = \text{addr } a; T = \text{Class } C \rrbracket \implies R$;
 $\bigwedge a U. \llbracket e = \text{addr } a; T = U[] \rrbracket \implies R$;
 $\bigwedge a. e = \text{Throw } a \implies R \rrbracket \implies R$

<proof>

end

theorem (*in J-progress*) *red-progress*:

assumes *wf*: *wf-J-prog* P **and** *hconf*: *hconf* h

shows *progress*: [[$P, E, h \vdash e : T$; $\mathcal{D} e \llbracket \text{dom } l \rrbracket$; $\neg \text{final } e$] $\implies \exists e' s' ta. \text{extTA}, P, t \vdash \langle e, (h, l) \rangle \text{-ta} \rightarrow \langle e', s' \rangle$

and *progresss*: [[$P, E, h \vdash es \llbracket : \rrbracket Ts$; $\mathcal{D} s es \llbracket \text{dom } l \rrbracket$; $\neg \text{finals } es$] $\implies \exists es' s' ta. \text{extTA}, P, t \vdash \langle es, (h, l) \rangle \text{-ta} \rightarrow \langle es', s' \rangle$

<proof>

end

4.12 Preservation of definite assignment

theory *DefAssPreservation*

imports

DefAss
JWellForm
SmallStep

begin

Preservation of definite assignment more complex and requires a few lemmas first.

lemma *D-extRetJ* [*intro!*]: $\mathcal{D} e A \implies \mathcal{D} (\text{extRet2J } e \text{ va}) A$

<proof>

lemma *blocks-defass* [*iff*]: $\bigwedge A. \llbracket \text{length } Vs = \text{length } Ts; \text{length } vs = \text{length } Ts \rrbracket \implies$

$\mathcal{D} (\text{blocks } Vs \ Ts \ vs \ e) A = \mathcal{D} e (A \sqcup \llbracket \text{set } Vs \rrbracket) \langle \text{proof} \rangle$

context *J-heap-base* **begin**

lemma *red-lA-incr*: $\text{extTA}, P, t \vdash \langle e, s \rangle \text{-ta} \rightarrow \langle e', s' \rangle \implies \llbracket \text{dom } (lcl \ s) \rrbracket \sqcup \mathcal{A} \ e \sqsubseteq \llbracket \text{dom } (lcl \ s') \rrbracket \sqcup \mathcal{A} \ e'$

and *reds-lA-incr*: $\text{extTA}, P, t \vdash \langle es, s \rangle \text{-ta} \rightarrow \langle es', s' \rangle \implies \llbracket \text{dom } (lcl \ s) \rrbracket \sqcup \mathcal{A} \ s \sqsubseteq \llbracket \text{dom } (lcl \ s') \rrbracket \sqcup \mathcal{A} \ s'$

<proof>

end

Now preservation of definite assignment.

declare *hyperUn-comm* [*simp del*]

declare *hyperUn-leftComm* [*simp del*]

context *J-heap-base* **begin**

lemma **assumes** *wf*: *wf-J-prog* P

shows *red-preserves-defass*: $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \mathcal{D} e \lfloor dom (lcl s) \rfloor \implies \mathcal{D} e' \lfloor dom (lcl s') \rfloor$
and *reds-preserves-defass*: $extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \mathcal{D} s es \lfloor dom (lcl s) \rfloor \implies \mathcal{D} s es' \lfloor dom (lcl s') \rfloor$
 $\langle proof \rangle$

end

end

4.13 Type Safety Proof

theory *TypeSafe*

imports

Progress

DefAssPreservation

begin

4.13.1 Basic preservation lemmas

First two easy preservation lemmas.

theorem (in *J-conf-read*)

shows *red-preserves-hconf*:

$\llbracket extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; P, E, hp s \vdash e : T; hconf (hp s) \rrbracket \implies hconf (hp s')$

and *reds-preserves-hconf*:

$\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; P, E, hp s \vdash es [:] Ts; hconf (hp s) \rrbracket \implies hconf (hp s')$

$\langle proof \rangle$

theorem (in *J-heap*) *red-preserves-lconf*:

$\llbracket extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; P, E, hp s \vdash e : T; P, hp s \vdash lcl s (: \leq) E \rrbracket \implies P, hp s' \vdash lcl s' (: \leq) E$

and *reds-preserves-lconf*:

$\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; P, E, hp s \vdash es [:] Ts; P, hp s \vdash lcl s (: \leq) E \rrbracket \implies P, hp s' \vdash lcl s' (: \leq) E$

$\langle proof \rangle$

Combining conformance of heap and local variables:

definition (in *J-heap-conf-base*) *sconf* :: $env \Rightarrow ('addr, 'heap) Jstate \Rightarrow bool$ ($\langle \cdot \vdash \cdot \sqrt{\cdot} \rangle$ [51,51]50)

where $E \vdash s \sqrt{\cdot} \equiv let (h, l) = s in hconf h \wedge P, h \vdash l (: \leq) E \wedge preallocated h$

context *J-conf-read* **begin**

lemma *red-preserves-sconf*:

$\llbracket extTA, P, t \vdash \langle e, s \rangle -tas \rightarrow \langle e', s' \rangle; P, E, hp s \vdash e : T; E \vdash s \sqrt{\cdot} \rrbracket \implies E \vdash s' \sqrt{\cdot}$

$\langle proof \rangle$

lemma *reds-preserves-sconf*:

$\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; P, E, hp s \vdash es [:] Ts; E \vdash s \sqrt{\cdot} \rrbracket \implies E \vdash s' \sqrt{\cdot}$

$\langle proof \rangle$

end

lemma (in *J-heap-base*) *wt-external-call*:

$\llbracket conf-extRet P h va T; P, E, h \vdash e : T \rrbracket \implies \exists T'. P, E, h \vdash extRet2J e va : T' \wedge P \vdash T' \leq T$

<proof>

4.13.2 Subject reduction

theorem (in *J-conf-read*) **assumes** *wf: wf-J-prog P*

shows *subject-reduction:*

$\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle \text{--ta}\rightarrow \langle e', s' \rangle; E \vdash s \checkmark; P, E, hp \ s \vdash e: T; P, hp \ s \vdash t \checkmark t \rrbracket$
 $\implies \exists T'. P, E, hp \ s' \vdash e': T' \wedge P \vdash T' \leq T$

and *subjects-reduction:*

$\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle \text{--ta}\rightarrow \langle e', s' \rangle; E \vdash s \checkmark; P, E, hp \ s \vdash \text{es}[:] Ts; P, hp \ s \vdash t \checkmark t \rrbracket$
 $\implies \exists Ts'. P, E, hp \ s' \vdash \text{es}'[:] Ts' \wedge P \vdash Ts' [\leq] Ts$

<proof>

end

4.14 Progress and type safety theorem for the multithreaded system

theory *ProgressThreaded*

imports

Threaded

TypeSafe

../Framework/FWProgress

begin

lemma *lock-ok-ls-Some-ex-ts-not-final:*

assumes *lock: lock-ok ls ts*

and *hl: has-lock (ls \$ l) t*

shows $\exists e \ x \ ln. \ ts \ t = \llbracket ((e, x), ln) \rrbracket \wedge \neg \text{final } e$

<proof>

4.14.1 Preservation lemmata

4.14.2 Definite assignment

abbreviation

def-ass-ts-ok :: ('addr, 'thread-id, 'addr expr \times 'addr locals) thread-info \Rightarrow 'heap \Rightarrow bool

where

def-ass-ts-ok \equiv *ts-ok* ($\lambda t \ (e, x) \ h. \ \mathcal{D} \ e \ \llbracket \text{dom } x \rrbracket$)

context *J-heap-base* **begin**

lemma **assumes** *wf: wf-J-prog P*

shows *red-def-ass-new-thread:*

$\llbracket P, t \vdash \langle e, s \rangle \text{--ta}\rightarrow \langle e', s' \rangle; \text{NewThread } t'' \ (e'', x'') \ c'' \in \text{set } \{\!| \text{ta} \!|_t \rrbracket \implies \mathcal{D} \ e'' \ \llbracket \text{dom } x'' \rrbracket$

and *reds-def-ass-new-thread:*

$\llbracket P, t \vdash \langle e, s \rangle \text{--ta}\rightarrow \langle e', s' \rangle; \text{NewThread } t'' \ (e'', x'') \ c'' \in \text{set } \{\!| \text{ta} \!|_t \rrbracket \implies \mathcal{D} \ e'' \ \llbracket \text{dom } x'' \rrbracket$

<proof>

lemma *lifting-wf-def-ass: wf-J-prog P \implies lifting-wf final-expr (mred P) ($\lambda t \ (e, x) \ m. \ \mathcal{D} \ e \ \llbracket \text{dom } x \rrbracket$)*

<proof>

lemma *def-ass-ts-ok-J-start-state:*

$\llbracket \text{wf-}J\text{-prog } P; P \vdash C \text{ sees } M:Ts \rightarrow T = \llbracket (pns, \text{body}) \rrbracket \text{ in } D; \text{length } vs = \text{length } Ts \rrbracket \implies$
 $\text{def-ass-ts-ok } (\text{thr } (J\text{-start-state } P \ C \ M \ vs)) \ h$
 $\langle \text{proof} \rangle$

end

4.14.3 typeability

context *J-heap-base* begin

definition *type-ok* :: 'addr *J-prog* \Rightarrow env \times ty \Rightarrow 'addr *expr* \Rightarrow 'heap \Rightarrow bool
where *type-ok* *P* \equiv ($\lambda(E, T) \ e \ c. (\exists T'. (P, E, c \vdash e : T' \wedge P \vdash T' \leq T))$)

definition *J-sconf-type-ET-start* :: 'm *prog* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow ('thread-id \rightarrow (env \times ty))
where

J-sconf-type-ET-start *P C M* \equiv
 let $(-, -, T, -) = \text{method } P \ C \ M$
 in ($[start\text{-tid} \mapsto (\text{Map.empty}, T)]$)

lemma *fixes E* :: env

assumes *wf*: *wf-J-prog P*

shows *red-type-newthread*:

$\llbracket P, t \vdash \langle e, s \rangle \text{--}ta \rightarrow \langle e', s' \rangle; P, E, hp \ s \vdash e : T; \text{NewThread } t'' (e'', x'') (hp \ s') \in \text{set } \{ta\}_t \rrbracket$
 $\implies \exists E \ T. P, E, hp \ s' \vdash e'' : T \wedge P, hp \ s' \vdash x'' (\leq) E$

and *reds-type-newthread*:

$\llbracket P, t \vdash \langle es, s \rangle \text{[-}ta \rightarrow \langle es', s' \rangle; \text{NewThread } t'' (e'', x'') (hp \ s') \in \text{set } \{ta\}_t; P, E, hp \ s \vdash es \text{[:] } Ts \rrbracket$
 $\implies \exists E \ T. P, E, hp \ s' \vdash e'' : T \wedge P, hp \ s' \vdash x'' (\leq) E$

$\langle \text{proof} \rangle$

end

context *J-heap-conf-base* begin

definition *sconf-type-ok* :: (env \times ty) \Rightarrow 'thread-id \Rightarrow 'addr *expr* \times 'addr *locals* \Rightarrow 'heap \Rightarrow bool
where

sconf-type-ok *ET t ex h* \equiv $\text{fst } ET \vdash (h, \text{snd } ex) \checkmark \wedge \text{type-ok } P \ ET \ (\text{fst } ex) \ h \wedge P, h \vdash t \checkmark/t$

abbreviation *sconf-type-ts-ok* ::

(*'thread-id* \rightarrow (env \times ty)) \Rightarrow ('addr, 'thread-id, 'addr *expr* \times 'addr *locals*) *thread-info* \Rightarrow 'heap \Rightarrow bool

where

sconf-type-ts-ok \equiv *ts-inv sconf-type-ok*

lemma *ts-inv-ok-J-sconf-type-ET-start*:

ts-inv-ok (*thr* (*J-start-state P C M vs*)) (*J-sconf-type-ET-start P C M*)

$\langle \text{proof} \rangle$

end

lemma (*in J-heap*) *red-preserve-welltype*:

$\llbracket \text{extTA}, P, t \vdash \langle e, (h, x) \rangle \text{--}ta \rightarrow \langle e', (h', x') \rangle; P, E, h \vdash e'' : T \rrbracket \implies P, E, h' \vdash e'' : T$
 $\langle \text{proof} \rangle$

context *J-heap-conf* begin

lemma *sconf-type-ts-ok-J-start-state*:

$\llbracket wf\text{-}J\text{-prog } P; wf\text{-start-state } P \ C \ M \ vs \rrbracket$
 $\implies sconf\text{-type-ts-ok } (J\text{-sconf-type-ET-start } P \ C \ M) \ (thr \ (J\text{-start-state } P \ C \ M \ vs)) \ (shr \ (J\text{-start-state } P \ C \ M \ vs))$
 $\langle proof \rangle$

lemma *J-start-state-sconf-type-ok*:

assumes $wf: wf\text{-}J\text{-prog } P$
and $ok: wf\text{-start-state } P \ C \ M \ vs$
shows $ts\text{-ok } (\lambda t \ x \ h. \exists ET. sconf\text{-type-ok } ET \ t \ x \ h) \ (thr \ (J\text{-start-state } P \ C \ M \ vs)) \ start\text{-heap}$
 $\langle proof \rangle$

end

context *J-conf-read* **begin**

lemma *red-preserves-type-ok*:

$\llbracket extTA, P, t \vdash \langle e, s \rangle \text{-}ta \rightarrow \langle e', s' \rangle; wf\text{-}J\text{-prog } P; E \vdash s \ \checkmark; type\text{-ok } P \ (E, T) \ e \ (hp \ s); P, hp \ s \vdash t \ \checkmark/t \rrbracket$
 $\implies type\text{-ok } P \ (E, T) \ e' \ (hp \ s')$
 $\langle proof \rangle$

lemma *lifting-inv-sconf-subject-ok*:

assumes $wf: wf\text{-}J\text{-prog } P$
shows $lifting\text{-inv } final\text{-expr } (mred \ P) \ sconf\text{-type-ok}$
 $\langle proof \rangle$

end

4.14.4 *wf-red*

context *J-progress* **begin**

context **begin**

declare $red\text{-mthr.actions-ok-iff}$ [*simp del*]
declare $red\text{-mthr.actions-ok.cases}$ [*rule del*]
declare $red\text{-mthr.actions-ok.intros}$ [*rule del*]

lemma **assumes** $wf: wf\text{-prog } wf\text{-md } P$

shows $red\text{-wf-red-aux}$:

$\llbracket P, t \vdash \langle e, s \rangle \text{-}ta \rightarrow \langle e', s' \rangle; \neg red\text{-mthr.actions-ok}' \ (ls, (ts, m), ws, is) \ t \ ta;$
 $sync\text{-ok } e; hconf \ (hp \ s); P, hp \ s \vdash t \ \checkmark/t;$
 $\forall l. has\text{-locks } (ls \ \$ \ l) \ t \geq expr\text{-locks } e \ l;$
 $ws \ t = None \vee$
 $(\exists a \ vs \ w \ T \ Ts \ Tr \ D. call \ e = \lfloor (a, wait, vs) \rfloor \wedge typeof\text{-addr } (hp \ s) \ a = \lfloor T \rfloor \wedge P \vdash class\text{-type-of } T$
 $sees \ wait: Ts \rightarrow Tr = Native \ in \ D \wedge ws \ t = \lfloor PostWS \ w \rfloor) \rrbracket$
 $\implies \exists e'' \ s'' \ ta'. P, t \vdash \langle e, s \rangle \text{-}ta' \rightarrow \langle e'', s'' \rangle \wedge$
 $(red\text{-mthr.actions-ok}' \ (ls, (ts, m), ws, is) \ t \ ta' \vee$
 $red\text{-mthr.actions-ok}' \ (ls, (ts, m), ws, is) \ t \ ta' \wedge red\text{-mthr.actions-subset } ta' \ ta)$
(is $\llbracket -; -; -; -; -; ?wakeup \ e \ s \rrbracket \implies ?concl \ e \ s \ ta)$
and $reds\text{-wf-red-aux}$:
 $\llbracket P, t \vdash \langle es, s \rangle \text{-}ta \rightarrow \langle es', s' \rangle; \neg red\text{-mthr.actions-ok}' \ (ls, (ts, m), ws, is) \ t \ ta;$
 $sync\text{-oks } es; hconf \ (hp \ s); P, hp \ s \vdash t \ \checkmark/t;$
 $\forall l. has\text{-locks } (ls \ \$ \ l) \ t \geq expr\text{-lockss } es \ l;$

$ws\ t = None \vee$
 $(\exists a\ vs\ w\ T\ Ts\ T\ Tr\ D.\ calls\ es = \lfloor (a, wait, vs) \rfloor \wedge typeof_addr\ (hp\ s)\ a = \lfloor T \rfloor \wedge P \vdash class_type_of$
 $T\ sees\ wait: Ts \rightarrow Tr = Native\ in\ D \wedge ws\ t = \lfloor PostWS\ w \rfloor \rfloor$
 $\implies \exists es''\ s''\ ta'. P, t \vdash \langle es, s \rangle [-ta' \rightarrow] \langle es'', s'' \rangle \wedge$
 $(red_mthr.actions-ok\ (ls, (ts, m), ws, is)\ t\ ta' \vee$
 $red_mthr.actions-ok'\ (ls, (ts, m), ws, is)\ t\ ta' \wedge red_mthr.actions-subset\ ta'\ ta)$
 $\langle proof \rangle$

end

end

context *J-heap-base* **begin**

lemma *shows red-ta-satisfiable:*

$P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \exists s. red_mthr.actions-ok\ s\ t\ ta$

and *reds-ta-satisfiable:*

$P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \exists s. red_mthr.actions-ok\ s\ t\ ta$

$\langle proof \rangle$

end

context *J-typesafe* **begin**

lemma *wf-progress:*

assumes *wf: wf-J-prog P*

shows *progress final-expr (mred P)*

$(red_mthr.wset-Suspend-ok\ P\ (\{s. sync-es-ok\ (thr\ s)\ (shr\ s) \wedge lock-ok\ (locks\ s)\ (thr\ s)\} \cap \{s.$

$\exists Es. sconf-type-ts-ok\ Es\ (thr\ s)\ (shr\ s)\} \cap \{s. def-ass-ts-ok\ (thr\ s)\ (shr\ s)\}))$

(is progress - - ?wf-state)

$\langle proof \rangle$

lemma *redT-progress-deadlock:*

assumes *wf: wf-J-prog P*

and *wf-start: wf-start-state P C M vs*

and *Red: P \vdash J-start-state P C M vs \multimap ttas \rightarrow^* s*

and *ndead: \neg red_mthr.deadlock P s*

shows $\exists t'\ ta'\ s'. P \vdash s -t' \triangleright ta' \rightarrow s'$

$\langle proof \rangle$

lemma *redT-progress-deadlocked:*

assumes *wf: wf-J-prog P*

and *wf-start: wf-start-state P C M vs*

and *Red: P \vdash J-start-state P C M vs \multimap ttas \rightarrow^* s*

and *ndead: red_mthr.not-final-thread s t \neg t \in red_mthr.deadlocked P s*

shows $\exists t'\ ta'\ s'. P \vdash s -t' \triangleright ta' \rightarrow s'$

$\langle proof \rangle$

4.14.5 Type safety proof

theorem *TypeSafetyT:*

fixes *C and M and ttas and Es*

defines $Es == J-sconf-type-ET-start\ P\ C\ M$

and $Es' == upd_invs\ Es\ sconf-type-ok\ (concat\ (map\ (thr-a \circ snd)\ ttas))$

assumes *wf*: *wf-J-prog P*
and *start-wf*: *wf-start-state P C M vs*
and *RedT*: $P \vdash J\text{-start-state } P \ C \ M \ vs \ \dashv\rightarrow\text{ttas}\dashv\rightarrow^* \ s'$
and *nored*: $\neg (\exists t \ ta \ s''. \ P \vdash \ s' \ \dashv\rightarrow\text{ta}\dashv\rightarrow \ s'')$
shows *thread-conf P (thr s') (shr s')*
and $\text{thr } s' \ t = \llbracket ((e', x'), \text{ln}') \rrbracket \implies$
 $(\exists v. \ e' = \text{Val } v \wedge (\exists E \ T. \ Es' \ t = \llbracket (E, T) \rrbracket \wedge P, \text{shr } s' \vdash v : \leq T) \wedge \text{ln}' = \text{no-wait-locks})$
 $\vee (\exists a \ C. \ e' = \text{Throw } a \wedge \text{typeof-addr } (\text{shr } s') \ a = \llbracket \text{Class-type } C \rrbracket \wedge P \vdash C \preceq^* \text{Throwable} \wedge \text{ln}'$
 $= \text{no-wait-locks})$
 $\vee (t \in \text{red-mthr.deadlocked } P \ s' \wedge (\exists E \ T. \ Es' \ t = \llbracket (E, T) \rrbracket \wedge (\exists T'. \ P, E, \text{shr } s' \vdash e' : T' \wedge P$
 $\vdash T' \leq T)))$
(is - \implies ?thesis2)
and $Es \subseteq_m Es'$
 $\langle \text{proof} \rangle$
end
end

4.15 Preservation of Deadlock

theory *Deadlocked*

imports

ProgressThreaded

begin

context *J-progress* **begin**

lemma *red-wt-hconf-hext*:

assumes *wf*: *wf-J-prog P*

and *hconf*: *hconf H*

and *tconf*: $P, H \vdash t \sqrt{t}$

shows $\llbracket \text{convert-extTA } \text{extNTA}, P, t \vdash \langle e, s \rangle \dashv\rightarrow \langle e', s' \rangle; P, E, H \vdash e : T; \text{hext } H \ (hp \ s) \rrbracket$

$\implies \exists ta' \ e' \ s'. \ \text{convert-extTA } \text{extNTA}, P, t \vdash \langle e, (H, \text{lcl } s) \rangle \dashv\rightarrow \langle e', s' \rangle \wedge$

$\text{collect-locks } \llbracket ta \rrbracket_l = \text{collect-locks } \llbracket ta' \rrbracket_l \wedge \text{collect-cond-actions } \llbracket ta \rrbracket_c = \text{collect-cond-actions } \llbracket ta' \rrbracket_c \wedge$

$\text{collect-interrupts } \llbracket ta \rrbracket_i = \text{collect-interrupts } \llbracket ta' \rrbracket_i$

and $\llbracket \text{convert-extTA } \text{extNTA}, P, t \vdash \langle es, s \rangle \dashv\rightarrow \langle es', s' \rangle; P, E, H \vdash es \ [:] \ Ts; \text{hext } H \ (hp \ s) \rrbracket$

$\implies \exists ta' \ es' \ s'. \ \text{convert-extTA } \text{extNTA}, P, t \vdash \langle es, (H, \text{lcl } s) \rangle \dashv\rightarrow \langle es', s' \rangle \wedge$

$\text{collect-locks } \llbracket ta \rrbracket_l = \text{collect-locks } \llbracket ta' \rrbracket_l \wedge \text{collect-cond-actions } \llbracket ta \rrbracket_c = \text{collect-cond-actions } \llbracket ta' \rrbracket_c \wedge$

$\text{collect-interrupts } \llbracket ta \rrbracket_i = \text{collect-interrupts } \llbracket ta' \rrbracket_i$

$\langle \text{proof} \rangle$

lemma *can-lock-devreserp*:

$\llbracket \text{wf-J-prog } P; \text{red-mthr.can-sync } P \ t \ (e, l) \ h' \ L; P, E, h \vdash e : T; P, h \vdash t \sqrt{t}; \text{hconf } h; h \leq h' \rrbracket$

$\implies \text{red-mthr.can-sync } P \ t \ (e, l) \ h \ L$

$\langle \text{proof} \rangle$

end

context *J-typesafe* **begin**

lemma *preserve-deadlocked*:
assumes *wf*: *wf-J-prog P*
shows *preserve-deadlocked final-expr (mred P) convert-RA* ($\{s. \text{sync-es-ok } (thr\ s) (shr\ s) \wedge \text{lock-ok } (locks\ s) (thr\ s)\} \cap \{s. \exists Es. \text{sconf-type-ts-ok } Es\ (thr\ s) (shr\ s)\} \cap \{s. \text{def-ass-ts-ok } (thr\ s) (shr\ s)\}$)
(is preserve-deadlocked - - - ?wf-state)
 $\langle proof \rangle$

end

end

4.16 Program annotation

theory *Annotate*

imports

WellType

begin

abbreviation (output)

unanFAcc :: *'addr expr* \Rightarrow *vname* \Rightarrow *'addr expr* ($\langle \langle \dots \rangle [10,10] 90$)

where

unanFAcc e F \equiv *F* *Acc e F* (*STR ""*)

abbreviation (output)

unanFAss :: *'addr expr* \Rightarrow *vname* \Rightarrow *'addr expr* \Rightarrow *'addr expr* ($\langle \langle \dots := \rangle [10,0,90] 90$)

where

unanFAss e F e' \equiv *F* *Ass e F* (*STR ""*) *e'*

definition *array-length-field-name* :: *vname*

where *array-length-field-name* = *STR "length"*

notation (output) *array-length-field-name* ($\langle length \rangle$)

definition *super* :: *vname*

where *super* = *STR "super"*

lemma *super-neq-this* [*simp*]: *super* \neq *this* *this* \neq *super*

$\langle proof \rangle$

inductive *Anno* :: (*ty* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *bool*) \Rightarrow *'addr J-prog* \Rightarrow *env* \Rightarrow *'addr expr* \Rightarrow *'addr expr* \Rightarrow *bool*

($\langle \langle \dots \vdash \dots \rightsquigarrow \dots \rangle [51,51,0,0,51] 50$)

and *Annos* :: (*ty* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *bool*) \Rightarrow *'addr J-prog* \Rightarrow *env* \Rightarrow *'addr expr list* \Rightarrow *'addr expr list* \Rightarrow *bool*

($\langle \langle \dots \vdash \dots \rightsquigarrow \dots \rangle [51,51,0,0,51] 50$)

for *is-lub* :: *ty* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *bool* **and** *P* :: *'addr J-prog*

where

AnnoNew: *is-lub, P, E* \vdash *new C* \rightsquigarrow *new C*

| *AnnoNewArray*: *is-lub, P, E* \vdash *i* \rightsquigarrow *i'* \Longrightarrow *is-lub, P, E* \vdash *newA T[i]* \rightsquigarrow *newA T[i']*

| *AnnoCast*: *is-lub, P, E* \vdash *e* \rightsquigarrow *e'* \Longrightarrow *is-lub, P, E* \vdash *Cast C e* \rightsquigarrow *Cast C e'*

| *AnnoInstanceOf*: *is-lub, P, E* \vdash *e* \rightsquigarrow *e'* \Longrightarrow *is-lub, P, E* \vdash *e instanceof T* \rightsquigarrow *e' instanceof T*

| *AnnoVal*: *is-lub, P, E* \vdash *Val v* \rightsquigarrow *Val v*

| *AnnoVarVar*: $\llbracket E\ V = \lfloor T \rfloor; V \neq \text{super} \rrbracket \Longrightarrow$ *is-lub, P, E* \vdash *Var V* \rightsquigarrow *Var V*

| *AnnoVarField*:
 — There is no need to handle access of array fields explicitly, because arrays do not implement methods, i.e. *this* is always of a *Class* type.

$$\llbracket E \ V = \text{None}; V \neq \text{super}; E \ \text{this} = \lfloor \text{Class } C \rfloor; P \vdash C \ \text{sees } V:T \ (fm) \ \text{in } D \rrbracket$$

$$\implies is-lub, P, E \vdash \text{Var } V \rightsquigarrow \text{Var } \text{this} \cdot V \{D\}$$

| *AnnoBinOp*:

$$\llbracket is-lub, P, E \vdash e1 \rightsquigarrow e1'; is-lub, P, E \vdash e2 \rightsquigarrow e2' \rrbracket$$

$$\implies is-lub, P, E \vdash e1 \llbracket bop \rrbracket e2 \rightsquigarrow e1' \llbracket bop \rrbracket e2'$$

| *AnnoLAssVar*:

$$\llbracket E \ V = \lfloor T \rfloor; V \neq \text{super}; is-lub, P, E \vdash e \rightsquigarrow e' \rrbracket \implies is-lub, P, E \vdash V := e \rightsquigarrow V := e'$$

| *AnnoLAssField*:

$$\llbracket E \ V = \text{None}; V \neq \text{super}; E \ \text{this} = \lfloor \text{Class } C \rfloor; P \vdash C \ \text{sees } V:T \ (fm) \ \text{in } D; is-lub, P, E \vdash e \rightsquigarrow e' \rrbracket$$

$$\implies is-lub, P, E \vdash V := e \rightsquigarrow \text{Var } \text{this} \cdot V \{D\} := e'$$

| *AnnoAAcc*:

$$\llbracket is-lub, P, E \vdash a \rightsquigarrow a'; is-lub, P, E \vdash i \rightsquigarrow i' \rrbracket \implies is-lub, P, E \vdash a[i] \rightsquigarrow a'[i']$$

| *AnnoAAss*:

$$\llbracket is-lub, P, E \vdash a \rightsquigarrow a'; is-lub, P, E \vdash i \rightsquigarrow i'; is-lub, P, E \vdash e \rightsquigarrow e' \rrbracket \implies is-lub, P, E \vdash a[i] := e \rightsquigarrow a'[i'] := e'$$

| *AnnoALength*:

$$is-lub, P, E \vdash a \rightsquigarrow a' \implies is-lub, P, E \vdash a.length \rightsquigarrow a'.length$$

| — All arrays implicitly declare a final field called *length* to store the array length, which hides a potential field of the same name in *Object* (cf. JLS 6.4.5). The last premise implements the hiding because field lookup does not model the implicit declaration.

| *AnnoFAcc*:

$$\llbracket is-lub, P, E \vdash e \rightsquigarrow e'; is-lub, P, E \vdash e' :: U; class\text{-type-of}' U = \lfloor C \rfloor; P \vdash C \ \text{sees } F:T \ (fm) \ \text{in } D;$$

$$is\text{-Array } U \longrightarrow F \neq \text{array-length-field-name} \rrbracket$$

$$\implies is-lub, P, E \vdash e \cdot F \{STR \ \text{''''}\} \rightsquigarrow e' \cdot F \{D\}$$

| *AnnoFAccALength*:

$$\llbracket is-lub, P, E \vdash e \rightsquigarrow e'; is-lub, P, E \vdash e' :: T \lfloor \rrbracket$$

$$\implies is-lub, P, E \vdash e \cdot \text{array-length-field-name} \{STR \ \text{''''}\} \rightsquigarrow e' \cdot \text{length}$$

| *AnnoFAccSuper*:
 — In class *C* with super class *D*, "super" is syntactic sugar for "*((D) this)*" (cf. JLS, 15.11.2)

$$\llbracket E \ \text{this} = \lfloor \text{Class } C \rfloor; C \neq \text{Object}; class \ P \ C = \lfloor (D, fs, ms) \rfloor;$$

$$P \vdash D \ \text{sees } F:T \ (fm) \ \text{in } D' \rrbracket$$

$$\implies is-lub, P, E \vdash \text{Var } \text{super} \cdot F \{STR \ \text{''''}\} \rightsquigarrow (\text{Cast } (\text{Class } D) \ (\text{Var } \text{this})) \cdot F \{D'\}$$

| *AnnoFAss*:

$$\llbracket is-lub, P, E \vdash e1 \rightsquigarrow e1'; is-lub, P, E \vdash e2 \rightsquigarrow e2';$$

$$is-lub, P, E \vdash e1' :: U; class\text{-type-of}' U = \lfloor C \rfloor; P \vdash C \ \text{sees } F:T \ (fm) \ \text{in } D;$$

$$is\text{-Array } U \longrightarrow F \neq \text{array-length-field-name} \rrbracket$$

$$\implies is-lub, P, E \vdash e1 \cdot F \{STR \ \text{''''}\} := e2 \rightsquigarrow e1' \cdot F \{D\} := e2'$$

| *AnnoFAssSuper*:

$$\llbracket E \ \text{this} = \lfloor \text{Class } C \rfloor; C \neq \text{Object}; class \ P \ C = \lfloor (D, fs, ms) \rfloor;$$

$$P \vdash D \ \text{sees } F:T \ (fm) \ \text{in } D'; is-lub, P, E \vdash e \rightsquigarrow e' \rrbracket$$

$$\implies is-lub, P, E \vdash \text{Var } \text{super} \cdot F \{STR \ \text{''''}\} := e \rightsquigarrow (\text{Cast } (\text{Class } D) \ (\text{Var } \text{this})) \cdot F \{D'\} := e'$$

| *AnnoCAS*:

$$\llbracket is-lub, P, E \vdash e1 \rightsquigarrow e1'; is-lub, P, E \vdash e2 \rightsquigarrow e2'; is-lub, P, E \vdash e3 \rightsquigarrow e3' \rrbracket$$

$$\implies is-lub, P, E \vdash e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3) \rightsquigarrow e1' \cdot \text{compareAndSwap}(D \cdot F, e2', e3')$$

| *AnnoCall*:

$$\llbracket is-lub, P, E \vdash e \rightsquigarrow e'; is-lub, P, E \vdash es \rightsquigarrow es' \rrbracket$$

$$\implies is-lub, P, E \vdash \text{Call } e \ M \ es \rightsquigarrow \text{Call } e' \ M \ es'$$

| *AnnoBlock*:

$$is-lub, P, E (V \mapsto T) \vdash e \rightsquigarrow e' \implies is-lub, P, E \vdash \{V:T=vo; e\} \rightsquigarrow \{V:T=vo; e'\}$$

| *AnnoSync*:

$$\llbracket is-lub, P, E \vdash e1 \rightsquigarrow e1'; is-lub, P, E \vdash e2 \rightsquigarrow e2' \rrbracket$$

$$\Longrightarrow is\text{-lub}, P, E \vdash \text{sync}(e1) e2 \rightsquigarrow \text{sync}(e1') e2'$$

| *AnnoComp*:

$$\llbracket is\text{-lub}, P, E \vdash e1 \rightsquigarrow e1'; is\text{-lub}, P, E \vdash e2 \rightsquigarrow e2' \rrbracket$$

$$\Longrightarrow is\text{-lub}, P, E \vdash e1;;e2 \rightsquigarrow e1';;e2'$$

| *AnnoCond*:

$$\llbracket is\text{-lub}, P, E \vdash e \rightsquigarrow e'; is\text{-lub}, P, E \vdash e1 \rightsquigarrow e1'; is\text{-lub}, P, E \vdash e2 \rightsquigarrow e2' \rrbracket$$

$$\Longrightarrow is\text{-lub}, P, E \vdash \text{if}(e) e1 \text{ else } e2 \rightsquigarrow \text{if}(e') e1' \text{ else } e2'$$

| *AnnoLoop*:

$$\llbracket is\text{-lub}, P, E \vdash e \rightsquigarrow e'; is\text{-lub}, P, E \vdash c \rightsquigarrow c' \rrbracket$$

$$\Longrightarrow is\text{-lub}, P, E \vdash \text{while}(e) c \rightsquigarrow \text{while}(e') c'$$

| *AnnoThrow*:

$$is\text{-lub}, P, E \vdash e \rightsquigarrow e' \Longrightarrow is\text{-lub}, P, E \vdash \text{throw } e \rightsquigarrow \text{throw } e'$$

| *AnnoTry*:

$$\llbracket is\text{-lub}, P, E \vdash e1 \rightsquigarrow e1'; is\text{-lub}, P, E(V \mapsto \text{Class } C) \vdash e2 \rightsquigarrow e2' \rrbracket$$

$$\Longrightarrow is\text{-lub}, P, E \vdash \text{try } e1 \text{ catch}(C V) e2 \rightsquigarrow \text{try } e1' \text{ catch}(C V) e2'$$

| *AnnoNil*:

$$is\text{-lub}, P, E \vdash [] [\rightsquigarrow] []$$

| *AnnoCons*:

$$\llbracket is\text{-lub}, P, E \vdash e \rightsquigarrow e'; is\text{-lub}, P, E \vdash es [\rightsquigarrow] es' \rrbracket \Longrightarrow is\text{-lub}, P, E \vdash e\#es [\rightsquigarrow] e'\#es'$$

inductive-cases *Anno-cases* [*elim!*]:

$$is\text{-lub}', P, E \vdash \text{new } C \rightsquigarrow e$$

$$is\text{-lub}', P, E \vdash \text{newA } T[e] \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash \text{Cast } T e \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash e \text{ instanceof } T \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash \text{Val } v \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash \text{Var } V \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash e1 \llcorner \text{bop} \lrcorner e2 \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash V := e \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash e1 [e2] \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash e1 [e2] := e3 \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash e \cdot \text{length} \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash e \cdot F\{D\} \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash e1 \cdot F\{D\} := e2 \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3) \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash e \cdot M(es) \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash \{V:T=vo; e\} \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash \text{sync}(e1) e2 \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash \text{insync}(a) e2 \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash e1;; e2 \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash \text{if}(e) e1 \text{ else } e2 \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash \text{while}(e1) e2 \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash \text{throw } e \rightsquigarrow e'$$

$$is\text{-lub}', P, E \vdash \text{try } e1 \text{ catch}(C V) e2 \rightsquigarrow e'$$

inductive-cases *Annos-cases* [*elim!*]:

$$is\text{-lub}', P, E \vdash [] [\rightsquigarrow] es'$$

$$is\text{-lub}', P, E \vdash e \# es [\rightsquigarrow] es'$$

abbreviation *Anno'* :: 'addr *J-prog* \Rightarrow env \Rightarrow 'addr *expr* \Rightarrow 'addr *expr* \Rightarrow bool ($\langle -, - \vdash - \rightsquigarrow - \rangle$)
[51,0,0,51]50)

where *Anno'* *P* \equiv *Anno* (*TypeRel.is-lub* *P*) *P*

abbreviation $Annos' :: 'addr J\text{-prog} \Rightarrow env \Rightarrow 'addr\ expr\ list \Rightarrow 'addr\ expr\ list \Rightarrow bool\ (\langle -, - \rangle \vdash - [\rightsquigarrow]) \rightarrow [51,0,0,51]50)$

where $Annos' P \equiv Annos\ (TypeRel.is\text{-}lub\ P)\ P$

definition $annotate :: 'addr\ J\text{-prog} \Rightarrow env \Rightarrow 'addr\ expr \Rightarrow 'addr\ expr$

where $annotate\ P\ E\ e = THE\text{-}default\ e\ (\lambda e'. P, E \vdash e \rightsquigarrow e')$

lemma fixes $is\text{-}lub :: ty \Rightarrow ty \Rightarrow ty \Rightarrow bool\ (\langle \vdash\ lub'((-,\ /\ -)' = \rightarrow [51,51,51] 50)$

assumes $is\text{-}lub\text{-}unique: \bigwedge T1\ T2\ T3\ T4. [\vdash\ lub(T1, T2) = T3; \vdash\ lub(T1, T2) = T4] \Longrightarrow T3 = T4$

shows $Anno\text{-}fun: [is\text{-}lub, P, E \vdash e \rightsquigarrow e'; is\text{-}lub, P, E \vdash e \rightsquigarrow e''] \Longrightarrow e' = e''$

and $Annos\text{-}fun: [is\text{-}lub, P, E \vdash es [\rightsquigarrow] es'; is\text{-}lub, P, E \vdash es [\rightsquigarrow] es''] \Longrightarrow es' = es''$

$\langle proof \rangle$

4.16.1 Code generation

definition $Anno\text{-}code :: 'addr\ J\text{-prog} \Rightarrow env \Rightarrow 'addr\ expr \Rightarrow 'addr\ expr \Rightarrow bool\ (\langle -, - \rangle \vdash - \rightsquigarrow'' \rightarrow [51,0,0,51]50)$

where $Anno\text{-}code\ P = Anno\ (is\text{-}lub\text{-}sup\ P)\ P$

definition $Annos\text{-}code :: 'addr\ J\text{-prog} \Rightarrow env \Rightarrow 'addr\ expr\ list \Rightarrow 'addr\ expr\ list \Rightarrow bool\ (\langle -, - \rangle \vdash - [\rightsquigarrow'] \rightarrow [51,0,0,51]50)$

where $Annos\text{-}code\ P = Annos\ (is\text{-}lub\text{-}sup\ P)\ P$

primrec $block\text{-}types :: ('a, 'b, 'addr)\ exp \Rightarrow ty\ list$

and $blocks\text{-}types :: ('a, 'b, 'addr)\ exp\ list \Rightarrow ty\ list$

where

$block\text{-}types\ (new\ C) = []$
 $| block\text{-}types\ (newA\ T[e]) = block\text{-}types\ e$
 $| block\text{-}types\ (Cast\ U\ e) = block\text{-}types\ e$
 $| block\text{-}types\ (e\ instanceof\ U) = block\text{-}types\ e$
 $| block\text{-}types\ (e1\ \langle bop \rangle\ e2) = block\text{-}types\ e1\ @\ block\text{-}types\ e2$
 $| block\text{-}types\ (Val\ v) = []$
 $| block\text{-}types\ (Var\ V) = []$
 $| block\text{-}types\ (V := e) = block\text{-}types\ e$
 $| block\text{-}types\ (a[i]) = block\text{-}types\ a\ @\ block\text{-}types\ i$
 $| block\text{-}types\ (a[i] := e) = block\text{-}types\ a\ @\ block\text{-}types\ i\ @\ block\text{-}types\ e$
 $| block\text{-}types\ (a.length) = block\text{-}types\ a$
 $| block\text{-}types\ (e.F\{D\}) = block\text{-}types\ e$
 $| block\text{-}types\ (e.F\{D\} := e') = block\text{-}types\ e\ @\ block\text{-}types\ e'$
 $| block\text{-}types\ (e.compareAndSwap(D.F, e', e'')) = block\text{-}types\ e\ @\ block\text{-}types\ e'\ @\ block\text{-}types\ e''$
 $| block\text{-}types\ (e.M(es)) = block\text{-}types\ e\ @\ blocks\text{-}types\ es$
 $| block\text{-}types\ \{V:T=vo; e\} = T\ \# block\text{-}types\ e$
 $| block\text{-}types\ (sync_V(e)\ e') = block\text{-}types\ e\ @\ block\text{-}types\ e'$
 $| block\text{-}types\ (insync_V(a)\ e) = block\text{-}types\ e$
 $| block\text{-}types\ (e;;e') = block\text{-}types\ e\ @\ block\text{-}types\ e'$
 $| block\text{-}types\ (if\ (e)\ e1\ else\ e2) = block\text{-}types\ e\ @\ block\text{-}types\ e1\ @\ block\text{-}types\ e2$
 $| block\text{-}types\ (while\ (b)\ c) = block\text{-}types\ b\ @\ block\text{-}types\ c$
 $| block\text{-}types\ (throw\ e) = block\text{-}types\ e$
 $| block\text{-}types\ (try\ e\ catch(C\ V)\ e') = block\text{-}types\ e\ @\ Class\ C\ \# block\text{-}types\ e'$

$| blocks\text{-}types\ [] = []$
 $| blocks\text{-}types\ (e\ #\ es) = block\text{-}types\ e\ @\ blocks\text{-}types\ es$

lemma fixes *is-lub1* :: $ty \Rightarrow ty \Rightarrow ty \Rightarrow bool$ ($\vdash 1$ *lub'*((-, / -)') = \rightarrow [51,51,51] 50)
and *is-lub2* :: $ty \Rightarrow ty \Rightarrow ty \Rightarrow bool$ ($\vdash 2$ *lub'*((-, / -)') = \rightarrow [51,51,51] 50)
assumes *wf*: *wf-prog wf-md P*
and *is-lub1-into-is-lub2*: $\bigwedge T1 T2 T3. \llbracket \vdash 1 \text{ lub}(T1, T2) = T3; \text{is-type } P T1; \text{is-type } P T2 \rrbracket \Longrightarrow \vdash 2$
lub(*T1*, *T2*) = *T3*
and *is-lub2-is-type*: $\bigwedge T1 T2 T3. \llbracket \vdash 2 \text{ lub}(T1, T2) = T3; \text{is-type } P T1; \text{is-type } P T2 \rrbracket \Longrightarrow \text{is-type}$
P T3
shows *Anno-change-is-lub*:
 $\llbracket \text{is-lub1}, P, E \vdash e \rightsquigarrow e'; \text{ran } E \cup \text{set}(\text{block-types } e) \subseteq \text{types } P \rrbracket \Longrightarrow \text{is-lub2}, P, E \vdash e \rightsquigarrow e'$
and *Annos-change-is-lub*:
 $\llbracket \text{is-lub1}, P, E \vdash es [\rightsquigarrow] es'; \text{ran } E \cup \text{set}(\text{blocks-types } es) \subseteq \text{types } P \rrbracket \Longrightarrow \text{is-lub2}, P, E \vdash es [\rightsquigarrow] es'$
<proof>

lemma assumes *wf*: *wf-prog wf-md P*
shows *Anno-into-Anno-code*: $\llbracket P, E \vdash e \rightsquigarrow e'; \text{ran } E \cup \text{set}(\text{block-types } e) \subseteq \text{types } P \rrbracket \Longrightarrow P, E \vdash e$
 $\rightsquigarrow' e'$
and *Annos-into-Annos-code*: $\llbracket P, E \vdash es [\rightsquigarrow] es'; \text{ran } E \cup \text{set}(\text{blocks-types } es) \subseteq \text{types } P \rrbracket \Longrightarrow P, E$
 $\vdash es [\rightsquigarrow'] es'$
<proof>

lemma assumes *wf*: *wf-prog wf-md P*
shows *Anno-code-into-Anno*: $\llbracket P, E \vdash e \rightsquigarrow' e'; \text{ran } E \cup \text{set}(\text{block-types } e) \subseteq \text{types } P \rrbracket \Longrightarrow P, E \vdash$
 $e \rightsquigarrow e'$
and *Annos-code-into-Annos*: $\llbracket P, E \vdash es [\rightsquigarrow'] es'; \text{ran } E \cup \text{set}(\text{blocks-types } es) \subseteq \text{types } P \rrbracket \Longrightarrow P, E$
 $\vdash es [\rightsquigarrow] es'$
<proof>

lemma fixes *is-lub*
assumes *wf*: *wf-prog wf-md P*
shows *WT-block-types-is-type*: $\text{is-lub}, P, E \vdash e :: T \Longrightarrow \text{set}(\text{block-types } e) \subseteq \text{types } P$
and *WTs-blocks-types-is-type*: $\text{is-lub}, P, E \vdash es [::] Ts \Longrightarrow \text{set}(\text{blocks-types } es) \subseteq \text{types } P$
<proof>

lemma fixes *is-lub*
shows *Anno-block-types*: $\text{is-lub}, P, E \vdash e \rightsquigarrow e' \Longrightarrow \text{block-types } e = \text{block-types } e'$
and *Annos-blocks-types*: $\text{is-lub}, P, E \vdash es [\rightsquigarrow] es' \Longrightarrow \text{blocks-types } es = \text{blocks-types } es'$
<proof>

code-pred
(modes: (i \Rightarrow i \Rightarrow o \Rightarrow bool) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool)
[detect-switches, skip-proof]
Anno
<proof>

definition *annotate-code* :: $'addr J\text{-prog} \Rightarrow env \Rightarrow 'addr \text{expr} \Rightarrow 'addr \text{expr}$
where *annotate-code* *P E e* = *THE-default e* ($\lambda e'. P, E \vdash e \rightsquigarrow' e'$)

code-pred
(modes: i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool)
[inductify]
Anno-code
<proof>

lemma *eval-Anno-i-i-i-o-conv*:

Predicate.eval (Anno-code-i-i-i-o P E e) = ($\lambda e'. P, E \vdash e \rightsquigarrow' e'$)
<proof>

lemma *annotate-code* [*code*]:

annotate-code P E e = Predicate.singleton ($\lambda-. Code.abort (STR "annotate") (\lambda-. e)$) (Anno-code-i-i-i-o P E e)

<proof>

end

theory *J-Main*

imports

State

Deadlocked

Annotate

begin

end

Chapter 5

Jinja Virtual Machine

5.1 State of the JVM

```
theory JVMState
imports
  ../Common/Observable-Events
begin
```

5.1.1 Frame Stack

```
type-synonym
  pc = nat
```

```
type-synonym
  'addr frame = 'addr val list × 'addr val list × cname × mname × pc
  — operand stack
  — registers (including this pointer, method parameters, and local variables)
  — name of class where current method is defined
  — parameter types
  — program counter within frame
```

```
⟨ML⟩
typ 'addr frame
```

5.1.2 Runtime State

```
type-synonym
  ('addr, 'heap) jvm-state = 'addr option × 'heap × 'addr frame list
  — exception flag, heap, frames
```

```
type-synonym
  'addr jvm-thread-state = 'addr option × 'addr frame list
  — exception flag, frames, thread lock state
```

```
type-synonym
  ('addr, 'thread-id, 'heap) jvm-thread-action = ('addr, 'thread-id, 'addr jvm-thread-state, 'heap) Jinja-thread-action
```

```
type-synonym
  ('addr, 'thread-id, 'heap) jvm-ta-state = ('addr, 'thread-id, 'heap) jvm-thread-action × ('addr, 'heap)
```

jvm-state

```

⟨ML⟩
typ ('addr, 'thread-id, 'heap) jvm-thread-action

end

```

5.2 Instructions of the JVM

theory *JVMInstructions*

imports

JVMState

../Common/BinOp

begin

datatype 'addr *instr*

<i>Load nat</i>	— load from local variable
<i>Store nat</i>	— store into local variable
<i>Push 'addr val</i>	— push a value (constant)
<i>New cname</i>	— create object
<i>NewArray ty</i>	— create array for elements of given type
<i>ALoad</i>	— Load array element from heap to stack
<i>AStore</i>	— Set element in array
<i>ALength</i>	— Return the length of the array
<i>Getfield vname cname</i>	— Fetch field from object
<i>Putfield vname cname</i>	— Set field in object
<i>CAS vname cname</i>	— Compare-and-swap instruction
<i>Checkcast ty</i>	— Check whether object is of given type
<i>Instanceof ty</i>	— instanceof test
<i>Invoke mname nat</i>	— inv. instance meth of an object
<i>Return</i>	— return from method
<i>Pop</i>	— pop top element from opstack
<i>Dup</i>	— duplicate top stack element
<i>Swap</i>	— swap top stack elements
<i>BinOpInstr bop</i>	— binary operator instruction
<i>Goto int</i>	— goto relative address
<i>IfFalse int</i>	— branch if top of stack false
<i>ThrowExc</i>	— throw top of stack as exception
<i>MEnter</i>	— enter the monitor of object on top of the stack
<i>MExit</i>	— exit the monitor of object on top of the stack

abbreviation *CmpEq* :: 'addr *instr*

where *CmpEq* \equiv *BinOpInstr Eq*

abbreviation *CmpLeq* :: 'addr *instr*

where *CmpLeq* \equiv *BinOpInstr LessOrEqual*

abbreviation *CmpGeq* :: 'addr *instr*

where *CmpGeq* \equiv *BinOpInstr GreaterOrEqual*

abbreviation *CmpLt* :: 'addr *instr*

where *CmpLt* \equiv *BinOpInstr LessThan*

abbreviation *CmpGt* :: 'addr instr
where *CmpGt* \equiv *BinOpInstr GreaterThan*

abbreviation *IAdd* :: 'addr instr
where *IAdd* \equiv *BinOpInstr Add*

abbreviation *ISub* :: 'addr instr
where *ISub* \equiv *BinOpInstr Subtract*

abbreviation *IMult* :: 'addr instr
where *IMult* \equiv *BinOpInstr Mult*

abbreviation *IDiv* :: 'addr instr
where *IDiv* \equiv *BinOpInstr Div*

abbreviation *IMod* :: 'addr instr
where *IMod* \equiv *BinOpInstr Mod*

abbreviation *IShl* :: 'addr instr
where *IShl* \equiv *BinOpInstr ShiftLeft*

abbreviation *IShr* :: 'addr instr
where *IShr* \equiv *BinOpInstr ShiftRightSigned*

abbreviation *IUShr* :: 'addr instr
where *IUShr* \equiv *BinOpInstr ShiftRightZeros*

abbreviation *IAnd* :: 'addr instr
where *IAnd* \equiv *BinOpInstr BinAnd*

abbreviation *IOr* :: 'addr instr
where *IOr* \equiv *BinOpInstr BinOr*

abbreviation *IXor* :: 'addr instr
where *IXor* \equiv *BinOpInstr BinXor*

type-synonym
'addr bytecode = *'addr instr list*

type-synonym
ex-entry = *pc* \times *pc* \times *cname option* \times *pc* \times *nat*
— start-pc, end-pc, exception type (None = Any), handler-pc, remaining stack depth

type-synonym
ex-table = *ex-entry list*

type-synonym
'addr jvm-method = *nat* \times *nat* \times *'addr bytecode* \times *ex-table*
— max stacksize
— number of local variables. Add 1 + no. of parameters to get no. of registers
— instruction sequence
— exception handler table

```

type-synonym
  'addr jvm-prog = 'addr jvm-method prog
end

```

5.3 Abstract heap locales for byte code programs

```

theory JVMHeap
imports
  ../Common/Conform
  JVMInstructions
begin

locale JVM-heap-base =
  heap-base +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'heap ⇒ 'addr → htype
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool

locale JVM-heap =
  JVM-heap-base +
  heap +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'heap ⇒ 'addr → htype
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
  and P :: 'addr jvm-prog

locale JVM-heap-conf-base =
  heap-conf-base +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'heap ⇒ 'addr → htype
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
  and hconf :: 'heap ⇒ bool
  and P :: 'addr jvm-prog

sublocale JVM-heap-conf-base < JVM-heap-base ⟨proof⟩

locale JVM-heap-conf-base' =

```

```

JVM-heap-conf-base +
heap +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr jvm-prog

```

```

sublocale JVM-heap-conf-base' < JVM-heap ⟨proof⟩

```

```

locale JVM-heap-conf =
  JVM-heap-conf-base' +
  heap-conf +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr jvm-prog

```

```

locale JVM-progress =
  heap-progress +
  JVM-heap-conf-base' +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr jvm-prog

```

```

locale JVM-conf-read =
  heap-conf-read +
  JVM-heap-conf +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool

```

```

and hconf :: 'heap ⇒ bool
and P :: 'addr jvm-prog

locale JVM-typesafe =
  heap-typesafe +
  JVM-conf-read +
  JVM-progress +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'heap ⇒ 'addr → htype
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
  and hconf :: 'heap ⇒ bool
  and P :: 'addr jvm-prog

end

```

5.4 JVM Instruction Semantics

theory *JVMExecInstr*

imports

JVMInstructions

JVMHeap

../Common/ExternalCall

begin

primrec *extRet2JVM* ::

nat ⇒ 'heap ⇒ 'addr val list ⇒ 'addr val list ⇒ *cname* ⇒ *mname* ⇒ *pc* ⇒ 'addr frame list
 ⇒ 'addr *extCallRet* ⇒ ('addr, 'heap) *jvm-state*

where

extRet2JVM *n h stk loc C M pc frs* (*RetVal* *v*) = (*None*, *h*, (*v* # *drop* (*Suc* *n*) *stk*, *loc*, *C*, *M*, *pc* + 1) # *frs*)

| *extRet2JVM* *n h stk loc C M pc frs* (*RetExc* *a*) = (*[a]*, *h*, (*stk*, *loc*, *C*, *M*, *pc*) # *frs*)

| *extRet2JVM* *n h stk loc C M pc frs* *RetStaySame* = (*None*, *h*, (*stk*, *loc*, *C*, *M*, *pc*) # *frs*)

lemma *eq-extRet2JVM-conv* [*simp*]:

(*xcp*, *h'*, *frs'*) = *extRet2JVM* *n h stk loc C M pc frs va* ↔

h' = *h* ∧ (*case* *va* of *RetVal* *v* ⇒ *xcp* = *None* ∧ *frs'* = (*v* # *drop* (*Suc* *n*) *stk*, *loc*, *C*, *M*, *pc* + 1) # *frs*)

| *RetExc* *a* ⇒ *xcp* = [*a*] ∧ *frs'* = (*stk*, *loc*, *C*, *M*, *pc*) # *frs*

| *RetStaySame* ⇒ *xcp* = *None* ∧ *frs'* = (*stk*, *loc*, *C*, *M*, *pc*) # *frs*)

⟨*proof*⟩

definition *extNTA2JVM* :: 'addr *jvm-prog* ⇒ (*cname* × *mname* × 'addr) ⇒ 'addr *jvm-thread-state*

where *extNTA2JVM* *P* ≡ (λ(*C*, *M*, *a*). *let* (*D*, *M'*, *Ts*, *meth*) = *method* *P* *C* *M*; (*mxs*, *mxl0*, *ins*, *xt*) = *the meth*

in (*None*, [(*[], Addr a* # *replicate mxl0 undefined-value*, *D*, *M*, 0)]))

abbreviation *extTA2JVM* ::

'addr *jvm-prog* ⇒ ('addr, 'thread-id, 'heap) *external-thread-action* ⇒ ('addr, 'thread-id, 'heap) *jvm-thread-action*

where $extTA2JVM P \equiv convert-extTA (extNTA2JVM P)$

context *JVM-heap-base* begin

primrec *exec-instr* ::

'addr instr \Rightarrow 'addr jvm-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow 'addr val list \Rightarrow 'addr val list
 \Rightarrow cname \Rightarrow mname \Rightarrow pc \Rightarrow 'addr frame list \Rightarrow
 (('addr, 'thread-id, 'heap) jvm-thread-action \times ('addr, 'heap) jvm-state) set

where

exec-instr-Load:

exec-instr (Load *n*) *P t h stk loc C₀ M₀ pc frs* =
 $\{(\varepsilon, (None, h, ((loc ! n) \# stk, loc, C_0, M_0, pc+1)\#frs))\}$

| *exec-instr* (Store *n*) *P t h stk loc C₀ M₀ pc frs* =
 $\{(\varepsilon, (None, h, (tl\ stk, loc[n:=hd\ stk], C_0, M_0, pc+1)\#frs))\}$

| *exec-instr-Push*:

exec-instr (Push *v*) *P t h stk loc C₀ M₀ pc frs* =
 $\{(\varepsilon, (None, h, (v \# stk, loc, C_0, M_0, pc+1)\#frs))\}$

| *exec-instr-New*:

exec-instr (New *C*) *P t h stk loc C₀ M₀ pc frs* =
 (let *HA* = allocate *h* (Class-type *C*)
 in if *HA* = {} then $\{(\varepsilon, \lfloor addr-of-sys-xcpt\ OutOfMemory \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)\}$
 else $(\lambda(h', a). (\lfloor NewHeapElem\ a\ (Class-type\ C) \rfloor, None, h', (Addr\ a \# stk, loc, C_0, M_0, pc + 1)\#frs))$ ' *HA*)

| *exec-instr-NewArray*:

exec-instr (NewArray *T*) *P t h stk loc C₀ M₀ pc frs* =
 (let *si* = the-Intg (hd *stk*);
 i = nat (sint *si*)
 in (if *si* < *s* 0
 then $\{(\varepsilon, \lfloor addr-of-sys-xcpt\ NegativeArraySize \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)\}$
 else let *HA* = allocate *h* (Array-type *T* *i*)
 in if *HA* = {} then $\{(\varepsilon, \lfloor addr-of-sys-xcpt\ OutOfMemory \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)\}$
 else $(\lambda(h', a). (\lfloor NewHeapElem\ a\ (Array-type\ T\ i) \rfloor, None, h', (Addr\ a \# tl\ stk, loc, C_0, M_0, pc + 1) \# frs))$ ' *HA*))

| *exec-instr-ALoad*:

exec-instr ALoad *P t h stk loc C₀ M₀ pc frs* =
 (let *i* = the-Intg (hd *stk*);
 va = hd (tl *stk*);
 a = the-Addr *va*;
 len = alen-of-htype (the (typeof-addr *h* *a*))
 in (if *va* = Null then $\{(\varepsilon, \lfloor addr-of-sys-xcpt\ NullPointer \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)\}$
 else if *i* < *s* 0 \vee int *len* \leq sint *i* then
 $\{(\varepsilon, \lfloor addr-of-sys-xcpt\ ArrayIndexOutOfBounds \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)\}$
 else $\{(\lfloor ReadMem\ a\ (ACell\ (nat\ (sint\ i)))\ v \rfloor, None, h, (v \# tl\ (tl\ stk), loc, C_0, M_0, pc + 1) \# frs) \mid v.$
 heap-read *h* *a* (ACell (nat (sint *i*)) *v*)})

| *exec-instr-ASStore*:

exec-instr ASStore *P t h stk loc C₀ M₀ pc frs* =
 (let *ve* = hd *stk*;

```

    vi = hd (tl stk);
    va = hd (tl (tl stk))
  in (if va = Null then {(\epsilon, \[addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)}
    else (let i = the-Intg vi;
          idx = nat (sint i);
          a = the-Addr va;
          hT = the (typeof-addr h a);
          T = ty-of-htype hT;
          len = alen-of-htype hT;
          U = the (typeofh ve)
        in (if i < s 0 \vee int len \leq sint i then
          {(\epsilon, \[addr-of-sys-xcpt ArrayIndexOutOfBounds], h, (stk, loc, C0, M0, pc) # frs)}
        else if P \vdash U \leq the-Array T then
          {(\{\!| WriteMem a (ACell idx) ve |\!\}, None, h', (tl (tl (tl stk)), loc, C0, M0, pc+1) # frs)
           | h'. heap-write h a (ACell idx) ve h'\}}
        else {(\epsilon, (\[addr-of-sys-xcpt ArrayStore], h, (stk, loc, C0, M0, pc) # frs))}))))))

| exec-instr-ALength:
  exec-instr ALength P t h stk loc C0 M0 pc frs =
  {(\epsilon, (let va = hd stk
    in if va = Null
      then (\[addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)
      else (None, h, (Intg (word-of-int (int (alen-of-htype (the (typeof-addr h (the-Addr va))))))) #
        tl stk, loc, C0, M0, pc+1) # frs))}

| exec-instr (Getfield F C) P t h stk loc C0 M0 pc frs =
  (let v = hd stk
  in if v = Null then {(\epsilon, \[addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)}
  else let a = the-Addr v
    in {(\{\!| ReadMem a (CField C F) v' |\!\}, None, h, (v' # (tl stk), loc, C0, M0, pc + 1) # frs) |
  v'.
    heap-read h a (CField C F) v'\}}

| exec-instr (Putfield F C) P t h stk loc C0 M0 pc frs =
  (let v = hd stk;
  r = hd (tl stk)
  in if r = Null then {(\epsilon, \[addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)}
  else let a = the-Addr r
    in {(\{\!| WriteMem a (CField C F) v |\!\}, None, h', (tl (tl stk), loc, C0, M0, pc + 1) # frs) | h'.
    heap-write h a (CField C F) v h'\}}

| exec-instr (CAS F C) P t h stk loc C0 M0 pc frs =
  (let v'' = hd stk; v' = hd (tl stk); v = hd (tl (tl stk))
  in if v = Null then {(\epsilon, \[addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)}
  else let a = the-Addr v
    in {(\{\!| ReadMem a (CField C F) v', WriteMem a (CField C F) v'' |\!\}, None, h', (Bool True #
    tl (tl (tl stk)), loc, C0, M0, pc + 1) # frs) | h'.
    heap-read h a (CField C F) v' \wedge heap-write h a (CField C F) v'' h'\} \cup
    {(\{\!| ReadMem a (CField C F) v'' |\!\}, None, h, (Bool False # tl (tl (tl stk)), loc, C0, M0, pc
    + 1) # frs) | v''.
    heap-read h a (CField C F) v'' \wedge v'' \neq v'\}}

| exec-instr (Checkcast T) P t h stk loc C0 M0 pc frs =
  {(\epsilon, let U = the (typeofh (hd stk))

```


in if $P \vdash U \leq T$ then $(None, h, (stk, loc, C_0, M_0, pc + 1) \# frs)$
 else $(\lfloor \text{addr-of-sys-xcpt } \text{ClassCast} \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)$

| *exec-instr* (*Instanceof* T) $P t h stk loc C_0 M_0 pc frs =$
 $\{(\varepsilon, None, h, (Bool (hd\ stk \neq\ Null \wedge P \vdash the\ (typeof_h\ (hd\ stk)) \leq T) \# tl\ stk, loc, C_0, M_0, pc + 1) \# frs)\}$

| *exec-instr-Invoke*:

exec-instr (*Invoke* $M n$) $P t h stk loc C_0 M_0 pc frs =$

(let $ps = rev\ (take\ n\ stk)$;

$r = stk ! n$;

$a = the\text{-Addr}\ r$;

$T = the\ (typeof\text{-addr}\ h\ a)$

in (if $r = Null$ then $\{(\varepsilon, \lfloor \text{addr-of-sys-xcpt } \text{NullPointer} \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)\}$

else

let $C = class\text{-type-of}\ T$;

$(D, M', Ts, meth) = method\ P\ C\ M$

in case *meth* of

Native \Rightarrow

$\{(extTA2JVM\ P\ ta, extRet2JVM\ n\ h'\ stk\ loc\ C_0\ M_0\ pc\ frs\ va) \mid ta\ va\ h'\}$

$(ta, va, h') \in red\text{-external-aggr}\ P\ t\ a\ M\ ps\ h\}$

| $\lfloor (m\ \&,\ m\ \&_0, ins, xt) \rfloor \Rightarrow$

let $f' = (\lfloor, [r] @ ps @ (replicate\ m\ \&_0\ undefined\text{-value}), D, M, 0)$

in $\{(\varepsilon, None, h, f' \# (stk, loc, C_0, M_0, pc) \# frs)\}$

| *exec-instr* *Return* $P t h stk_0 loc_0 C_0 M_0 pc frs =$

$\{(\varepsilon, (if\ frs = []\ then\ (None, h, [])\ else$

let $v = hd\ stk_0$;

$(stk, loc, C, m, pc) = hd\ frs$;

$n = length\ (fst\ (snd\ (method\ P\ C_0\ M_0)))$

in $(None, h, (v \# (drop\ (n+1)\ stk), loc, C, m, pc+1) \# tl\ frs)\}$

| *exec-instr* *Pop* $P t h stk loc C_0 M_0 pc frs =$

$\{(\varepsilon, (None, h, (tl\ stk, loc, C_0, M_0, pc+1) \# frs)\)}$

| *exec-instr* *Dup* $P t h stk loc C_0 M_0 pc frs =$

$\{(\varepsilon, (None, h, (hd\ stk \# stk, loc, C_0, M_0, pc+1) \# frs)\)}$

| *exec-instr* *Swap* $P t h stk loc C_0 M_0 pc frs =$

$\{(\varepsilon, (None, h, (hd\ (tl\ stk) \# hd\ stk \# tl\ (tl\ stk), loc, C_0, M_0, pc+1) \# frs)\)}$

| *exec-instr* (*BinOpInstr* *bop*) $P t h stk loc C_0 M_0 pc frs =$

$\{(\varepsilon,$

case *the* (*binop* *bop* (*hd* (*tl* *stk*)) (*hd* *stk*)) of

Inl $v \Rightarrow (None, h, (v \# tl\ (tl\ stk), loc, C_0, M_0, pc+1) \# frs)$

| *Inr* $a \Rightarrow (Some\ a, h, (stk, loc, C_0, M_0, pc) \# frs)\}$

| *exec-instr* (*IfFalse* i) $P t h stk loc C_0 M_0 pc frs =$

$\{(\varepsilon, (let\ pc' = if\ hd\ stk = Bool\ False\ then\ nat(int\ pc+i)\ else\ pc+1$

in $(None, h, (tl\ stk, loc, C_0, M_0, pc') \# frs)\)}$

| *exec-instr-Goto*:

exec-instr (*Goto* i) $P t h stk loc C_0 M_0 pc frs =$

$\{(\varepsilon, (None, h, (stk, loc, C_0, M_0, nat(int\ pc+i)) \# frs)\)}$

```

| exec-instr ThrowExc  $P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$ 
   $\{(\varepsilon, (let\ xp' = if\ hd\ stk = Null\ then\ \lfloor addr-of-sys-xcpt\ NullPointer \rfloor\ else\ \lfloor the-Addr(hd\ stk) \rfloor$ 
     $in\ (xp', h, (stk, loc, C_0, M_0, pc)\#frs))\ )\}$ 

| exec-instr-MEnter:
exec-instr MEnter  $P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$ 
   $\{let\ v = hd\ stk$ 
     $in\ if\ v = Null$ 
       $then\ (\varepsilon, \lfloor addr-of-sys-xcpt\ NullPointer \rfloor, h, (stk, loc, C_0, M_0, pc)\ \# frs)$ 
       $else\ (\{\!| Lock \rightarrow the-Addr\ v, SyncLock\ (the-Addr\ v) \!\}, None, h, (tl\ stk, loc, C_0, M_0, pc + 1)\ \# frs)\}$ 

| exec-instr-MExit:
exec-instr MExit  $P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$ 
   $(let\ v = hd\ stk$ 
     $in\ if\ v = Null$ 
       $then\ \{(\varepsilon, \lfloor addr-of-sys-xcpt\ NullPointer \rfloor, h, (stk, loc, C_0, M_0, pc)\#frs)\}$ 
       $else\ \{(\{\!| Unlock \rightarrow the-Addr\ v, SyncUnlock\ (the-Addr\ v) \!\}, None, h, (tl\ stk, loc, C_0, M_0, pc + 1)\ \# frs),$ 
         $(\{\!| UnlockFail \rightarrow the-Addr\ v \!\}, \lfloor addr-of-sys-xcpt\ IllegalMonitorState \rfloor, h, (stk, loc, C_0, M_0, pc)$ 
         $\# frs)\}\}$ 

end

end

```

5.5 Exception handling in the JVM

theory *JVMExceptions*

imports

JVMInstructions

begin

abbreviation *Any* :: *cname option*

where *Any* $\equiv None$

definition *matches-ex-entry* :: *'m prog* \Rightarrow *cname* \Rightarrow *pc* \Rightarrow *ex-entry* \Rightarrow *bool*

where

```

matches-ex-entry  $P\ C\ pc\ xcp \equiv$ 
   $let\ (s, e, C', h, d) = xcp\ in$ 
   $s \leq pc \wedge pc < e \wedge (case\ C'\ of\ None \Rightarrow True \mid \lfloor C'' \rfloor \Rightarrow P \vdash C \preceq^* C'')$ 

```

primrec

match-ex-table :: *'m prog* \Rightarrow *cname* \Rightarrow *pc* \Rightarrow *ex-table* \Rightarrow (*pc* \times *nat*) *option*

where

```

match-ex-table  $P\ C\ pc\ [] = None$ 
| match-ex-table  $P\ C\ pc\ (e\#es) = (if\ matches-ex-entry\ P\ C\ pc\ e$ 
   $then\ Some\ (snd(snd(snd\ e)))$ 
   $else\ match-ex-table\ P\ C\ pc\ es)$ 

```

abbreviation *ex-table-of* :: *'addr jvm-prog* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *ex-table*

where *ex-table-of* $P\ C\ M == snd\ (snd\ (snd\ (the\ (snd\ (snd\ (snd\ (method\ P\ C\ M)))))))))$

lemma *match-ex-table-SomeD*:

match-ex-table P C pc xt = Some (pc',d') \implies
 $\exists (f,t,D,h,d) \in \text{set } xt. \text{ matches-ex-entry } P C pc (f,t,D,h,d) \wedge h = pc' \wedge d=d'$
<proof>

end

5.6 Program Execution in the JVM

theory *JVMExec*

imports

JVMExecInstr
JVMExceptions
../Common/StartConfig

begin

abbreviation *instrs-of* :: *'addr jvm-prog \Rightarrow cname \Rightarrow mname \Rightarrow 'addr instr list*

where *instrs-of P C M == fst(snd(snd(the(snd(snd(snd(method P C M)))))))))*

5.6.1 single step execution

context *JVM-heap-base* **begin**

fun *exception-step* :: *'addr jvm-prog \Rightarrow 'addr \Rightarrow 'heap \Rightarrow 'addr frame \Rightarrow 'addr frame list \Rightarrow ('addr, 'heap) jvm-state*

where

exception-step P a h (stk, loc, C, M, pc) frs =
(case match-ex-table P (cname-of h a) pc (ex-table-of P C M) of
None \Rightarrow ($\lfloor a \rfloor$, h, frs)
| Some (pc', d) \Rightarrow (None, h, (Addr a # drop (size stk - d) stk, loc, C, M, pc') # frs))

lemma *exception-step-def-raw*:

exception-step =
 $(\lambda P a h (stk, loc, C, M, pc) frs.$
case match-ex-table P (cname-of h a) pc (ex-table-of P C M) of
None \Rightarrow ($\lfloor a \rfloor$, h, frs)
| Some (pc', d) \Rightarrow (None, h, (Addr a # drop (size stk - d) stk, loc, C, M, pc') # frs))
<proof>

fun *exec* :: *'addr jvm-prog \Rightarrow 'thread-id \Rightarrow ('addr, 'heap) jvm-state \Rightarrow ('addr, 'thread-id, 'heap)*

jvm-ta-state set **where**

exec P t (xcp, h, []) = {}
| exec P t (None, h, (stk, loc, C, M, pc) # frs) = exec-instr (instrs-of P C M ! pc) P t h stk loc C M pc frs
| exec P t ($\lfloor a \rfloor$, h, fr # frs) = { $(\varepsilon, \text{exception-step } P a h fr frs)$ }

5.6.2 relational view

inductive *exec-1* ::

'addr jvm-prog \Rightarrow 'thread-id \Rightarrow ('addr, 'heap) jvm-state
 \Rightarrow *('addr, 'thread-id, 'heap) jvm-thread-action \Rightarrow ('addr, 'heap) jvm-state \Rightarrow bool*
 $(\langle -, - \rangle / - \dashrightarrow \text{jvm} \rightarrow / \rightarrow [61, 0, 61, 0, 61] 60)$
for *P* :: *'addr jvm-prog* **and** *t* :: *'thread-id*

where

exec-1I:
 $(ta, \sigma') \in \text{exec } P \ t \ \sigma \implies P, t \vdash \sigma \text{ -ta-jvm} \rightarrow \sigma'$

lemma *exec-1-iff*:

$P, t \vdash \sigma \text{ -ta-jvm} \rightarrow \sigma' \iff (ta, \sigma') \in \text{exec } P \ t \ \sigma$
 ⟨proof⟩

end

The start configuration of the JVM: in the start heap, we call a method m of class C in program P with parameters vs . The *this* pointer of the frame is set to *Null* to simulate a static method invocation.

abbreviation *JVM-local-start* ::

$\text{cname} \Rightarrow \text{mname} \Rightarrow \text{ty list} \Rightarrow \text{ty} \Rightarrow \text{'addr jvm-method} \Rightarrow \text{'addr val list}$
 $\Rightarrow \text{'addr jvm-thread-state}$

where

$\text{JVM-local-start} \equiv$
 $\lambda C \ M \ Ts \ T \ (mxs, mxl0, b) \ vs.$
 $(\text{None}, [([], \text{Null} \ \# \ vs \ @ \ \text{replicate } mxl0 \ \text{undefined-value}, C, M, 0)])$

context *JVM-heap-base* **begin**

abbreviation *JVM-start-state* ::

$\text{'addr jvm-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{'addr val list} \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'addr jvm-thread-state}, \text{'heap}, \text{'addr})$
state

where

$\text{JVM-start-state} \equiv \text{start-state } \text{JVM-local-start}$

definition *JVM-start-state'* :: $\text{'addr jvm-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{'addr val list} \Rightarrow (\text{'addr}, \text{'heap})$
jvm-state

where

$\text{JVM-start-state}' \ P \ C \ M \ vs \equiv$
 $\text{let } (D, Ts, T, \text{meth}) = \text{method } P \ C \ M;$
 $(mxs, mxl0, ins, xt) = \text{the meth}$
 $\text{in } (\text{None}, \text{start-heap}, [([], \text{Null} \ \# \ vs \ @ \ \text{replicate } mxl0 \ \text{undefined-value}, D, M, 0)])$

end

end

5.7 A Defensive JVM

theory *JVMDefensive*

imports *JVMExec ../Common/ExternalCallWF*

begin

Extend the state space by one element indicating a type error (or other abnormal termination)

datatype $\text{'a type-error} = \text{TypeError} \mid \text{Normal 'a}$

context *JVM-heap-base* **begin**

definition *is-Array-ref* :: 'addr val \Rightarrow 'heap \Rightarrow bool **where**

is-Array-ref v h \equiv
is-Ref v \wedge
(v \neq Null \longrightarrow *typeof-addr* h (the-Addr v) \neq None \wedge *is-Array* (ty-of-htype (the (typeof-addr h (the-Addr v))))))

declare *is-Array-ref-def*[simp]

primrec *check-instr* :: ['addr instr, 'addr jvm-prog, 'heap, 'addr val list, 'addr val list, cname, mname, pc, 'addr frame list] \Rightarrow bool

where

check-instr-Load:

check-instr (Load n) P h stk loc C M₀ pc frs =
(n < length loc)

| *check-instr-Store*:

check-instr (Store n) P h stk loc C₀ M₀ pc frs =
(0 < length stk \wedge n < length loc)

| *check-instr-Push*:

check-instr (Push v) P h stk loc C₀ M₀ pc frs =
(\neg *is-Addr* v)

| *check-instr-New*:

check-instr (New C) P h stk loc C₀ M₀ pc frs =
is-class P C

| *check-instr-NewArray*:

check-instr (NewArray T) P h stk loc C₀ M₀ pc frs =
(*is-type* P (T[]) \wedge 0 < length stk \wedge *is-Intg* (hd stk))

| *check-instr-ALoad*:

check-instr ALoad P h stk loc C₀ M₀ pc frs =
(1 < length stk \wedge *is-Intg* (hd stk) \wedge *is-Array-ref* (hd (tl stk)) h)

| *check-instr-ASStore*:

check-instr ASStore P h stk loc C₀ M₀ pc frs =
(2 < length stk \wedge *is-Intg* (hd (tl stk)) \wedge *is-Array-ref* (hd (tl (tl stk))) h \wedge *typeof_h* (hd stk) \neq None)

| *check-instr-ALength*:

check-instr ALength P h stk loc C₀ M₀ pc frs =
(0 < length stk \wedge *is-Array-ref* (hd stk) h)

| *check-instr-Getfield*:

check-instr (Getfield F C) P h stk loc C₀ M₀ pc frs =
(0 < length stk \wedge (\exists C' T fm. P \vdash C sees F:T (fm) in C') \wedge
(let (C', T, fm) = field P C F; ref = hd stk in
C' = C \wedge *is-Ref* ref \wedge (ref \neq Null \longrightarrow
(\exists T. *typeof-addr* h (the-Addr ref) = [T] \wedge P \vdash class-type-of T \preceq^* C))))

| *check-instr-Putfield*:

check-instr (Putfield F C) P h stk loc C₀ M₀ pc frs =
(1 < length stk \wedge (\exists C' T fm. P \vdash C sees F:T (fm) in C') \wedge
(let (C', T, fm) = field P C F; v = hd stk; ref = hd (tl stk) in

$$C' = C \wedge \text{is-Ref } \text{ref} \wedge (\text{ref} \neq \text{Null} \longrightarrow \\ (\exists T'. \text{typeof-addr } h (\text{the-Addr } \text{ref}) = \lfloor T' \rfloor \wedge P \vdash \text{class-type-of } T' \preceq^* C \wedge P, h \vdash v : \leq T))))$$

| *check-instr-CAS*:

$$\text{check-instr } (\text{CAS } F \ C) \ P \ h \ \text{stk} \ \text{loc} \ C_0 \ M_0 \ \text{pc} \ \text{frs} = \\ (2 < \text{length } \text{stk} \wedge (\exists C' \ T \ \text{fm}. P \vdash C \ \text{sees } F:T \ (\text{fm}) \ \text{in } C') \wedge \\ (\text{let } (C', \ T, \ \text{fm}) = \text{field } P \ C \ F; \ v'' = \text{hd } \text{stk}; \ v' = \text{hd } (\text{tl } \text{stk}); \ v = \text{hd } (\text{tl } (\text{tl } \text{stk})) \ \text{in} \\ C' = C \wedge \text{is-Ref } v \wedge \text{volatile } \text{fm} \wedge (v \neq \text{Null} \longrightarrow \\ (\exists T'. \text{typeof-addr } h (\text{the-Addr } v) = \lfloor T' \rfloor \wedge P \vdash \text{class-type-of } T' \preceq^* C \wedge P, h \vdash v' : \leq T \wedge P, h \vdash \\ v'' : \leq T))))$$

| *check-instr-Checkcast*:

$$\text{check-instr } (\text{Checkcast } T) \ P \ h \ \text{stk} \ \text{loc} \ C_0 \ M_0 \ \text{pc} \ \text{frs} = \\ (0 < \text{length } \text{stk} \wedge \text{is-type } P \ T)$$

| *check-instr-Instanceof*:

$$\text{check-instr } (\text{Instanceof } T) \ P \ h \ \text{stk} \ \text{loc} \ C_0 \ M_0 \ \text{pc} \ \text{frs} = \\ (0 < \text{length } \text{stk} \wedge \text{is-type } P \ T \wedge \text{is-Ref } (\text{hd } \text{stk}))$$

| *check-instr-Invoke*:

$$\text{check-instr } (\text{Invoke } M \ n) \ P \ h \ \text{stk} \ \text{loc} \ C_0 \ M_0 \ \text{pc} \ \text{frs} = \\ (n < \text{length } \text{stk} \wedge \text{is-Ref } (\text{stk}!n) \wedge \\ (\text{stk}!n \neq \text{Null} \longrightarrow \\ (\text{let } a = \text{the-Addr } (\text{stk}!n); \\ T = \text{the } (\text{typeof-addr } h \ a); \\ C = \text{class-type-of } T; \\ (D, \ Ts, \ Tr, \ \text{meth}) = \text{method } P \ C \ M \\ \text{in } \text{typeof-addr } h \ a \neq \text{None} \wedge P \vdash C \ \text{has } M \wedge \\ P, h \vdash \text{rev } (\text{take } n \ \text{stk}) \ [:\leq] \ Ts \wedge \\ (\text{meth} = \text{None} \longrightarrow D \cdot M(Ts) :: Tr))))$$

| *check-instr-Return*:

$$\text{check-instr } \text{Return} \ P \ h \ \text{stk} \ \text{loc} \ C_0 \ M_0 \ \text{pc} \ \text{frs} = \\ (0 < \text{length } \text{stk} \wedge ((0 < \text{length } \text{frs}) \longrightarrow \\ (P \vdash C_0 \ \text{has } M_0) \wedge \\ (\text{let } v = \text{hd } \text{stk}; \\ T = \text{fst } (\text{snd } (\text{snd } (\text{method } P \ C_0 \ M_0)))) \\ \text{in } P, h \vdash v : \leq T)))$$

| *check-instr-Pop*:

$$\text{check-instr } \text{Pop} \ P \ h \ \text{stk} \ \text{loc} \ C_0 \ M_0 \ \text{pc} \ \text{frs} = \\ (0 < \text{length } \text{stk})$$

| *check-instr-Dup*:

$$\text{check-instr } \text{Dup} \ P \ h \ \text{stk} \ \text{loc} \ C_0 \ M_0 \ \text{pc} \ \text{frs} = \\ (0 < \text{length } \text{stk})$$

| *check-instr-Swap*:

$$\text{check-instr } \text{Swap} \ P \ h \ \text{stk} \ \text{loc} \ C_0 \ M_0 \ \text{pc} \ \text{frs} = \\ (1 < \text{length } \text{stk})$$

| *check-instr-BinOpInstr*:

$$\text{check-instr } (\text{BinOpInstr } \text{bop}) \ P \ h \ \text{stk} \ \text{loc} \ C_0 \ M_0 \ \text{pc} \ \text{frs} = \\ (1 < \text{length } \text{stk} \wedge (\exists T1 \ T2 \ T. \text{typeof}_h (\text{hd } \text{stk}) = \lfloor T2 \rfloor \wedge \text{typeof}_h (\text{hd } (\text{tl } \text{stk})) = \lfloor T1 \rfloor \wedge P \vdash$$

$T1 \ll bop \gg T2 : T)$

| *check-instr-IfFalse*:

$check_instr\ (IfFalse\ b)\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(0 < length\ stk \wedge is_Bool\ (hd\ stk) \wedge 0 \leq int\ pc + b)$

| *check-instr-Goto*:

$check_instr\ (Goto\ b)\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(0 \leq int\ pc + b)$

| *check-instr-Throw*:

$check_instr\ ThrowExc\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(0 < length\ stk \wedge is_Ref\ (hd\ stk) \wedge P \vdash the\ (typeof_h\ (hd\ stk)) \leq Class\ Throwable)$

| *check-instr-MEnter*:

$check_instr\ MEnter\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(0 < length\ stk \wedge is_Ref\ (hd\ stk))$

| *check-instr-MExit*:

$check_instr\ MExit\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(0 < length\ stk \wedge is_Ref\ (hd\ stk))$

definition *check-xcpt* :: 'addr jvm-prog \Rightarrow 'heap \Rightarrow nat \Rightarrow pc \Rightarrow ex-table \Rightarrow 'addr \Rightarrow bool

where

$check_xcpt\ P\ h\ n\ pc\ xt\ a \longleftrightarrow$
 $(\exists C. typeof_addr\ h\ a = \lfloor Class_type\ C \rfloor \wedge$
 $(case\ match_ex_table\ P\ C\ pc\ xt\ of\ None \Rightarrow True \mid Some\ (pc',\ d') \Rightarrow d' \leq n))$

definition *check* :: 'addr jvm-prog \Rightarrow ('addr, 'heap) jvm-state \Rightarrow bool

where

$check\ P\ \sigma \equiv let\ (xcpt,\ h,\ frs) = \sigma\ in$
 $(case\ frs\ of\ [] \Rightarrow True \mid (stk,loc,C,M,pc)\#frs' \Rightarrow$
 $P \vdash C\ has\ M \wedge$
 $(let\ (C',Ts,T,method) = method\ P\ C\ M; (mxs,mxlo,ins,xt) = the\ method; i = ins!pc\ in$
 $method \neq None \wedge pc < size\ ins \wedge size\ stk \leq mxs \wedge$
 $(case\ xcpt\ of\ None \Rightarrow check_instr\ i\ P\ h\ stk\ loc\ C\ M\ pc\ frs'$
 $\mid Some\ a \Rightarrow check_xcpt\ P\ h\ (length\ stk)\ pc\ xt\ a)))$

definition *exec-d* ::

'addr jvm-prog \Rightarrow 'thread-id \Rightarrow ('addr, 'heap) jvm-state \Rightarrow ('addr, 'thread-id, 'heap) jvm-ta-state set
 type-error

where

$exec_d\ P\ t\ \sigma \equiv if\ check\ P\ \sigma\ then\ Normal\ (exec\ P\ t\ \sigma)\ else\ TypeError$

inductive

exec-1-d ::

'addr jvm-prog \Rightarrow 'thread-id \Rightarrow ('addr, 'heap) jvm-state type-error
 \Rightarrow ('addr, 'thread-id, 'heap) jvm-thread-action \Rightarrow ('addr, 'heap) jvm-state type-error \Rightarrow bool
 $(\langle -, - \vdash - \rangle \dashv\dashv jvmd \rightarrow -) [61,0,61,0,61] 60)$

for $P :: 'addr\ jvm_prog$ **and** $t :: 'thread_id$

where

exec-1-d-ErrorI: $exec_d\ P\ t\ \sigma = TypeError \implies P, t \vdash Normal\ \sigma \dashv\dashv jvmd \rightarrow TypeError$

| *exec-1-d-NormalI*: $\llbracket exec_d\ P\ t\ \sigma = Normal\ \Sigma; (tas, \sigma') \in \Sigma \rrbracket \implies P, t \vdash Normal\ \sigma \dashv\dashv jvmd \rightarrow$

Normal σ'

lemma *jvmd-NormalD*:

$P, t \vdash \text{Normal } \sigma \text{ -ta-jvmd} \rightarrow \text{Normal } \sigma' \implies \text{check } P \sigma \wedge (ta, \sigma') \in \text{exec } P t \sigma \wedge (\exists xcp \ h \ f \ \text{frs}. \sigma = (xcp, h, f \# \text{frs}))$

<proof>

lemma *jvmd-NormalE*:

assumes $P, t \vdash \text{Normal } \sigma \text{ -ta-jvmd} \rightarrow \text{Normal } \sigma'$

obtains $xcp \ h \ f \ \text{frs}$ **where** $\text{check } P \sigma \ (ta, \sigma') \in \text{exec } P t \sigma \ \sigma = (xcp, h, f \# \text{frs})$

<proof>

lemma *exec-d-eq-TypeError*: $\text{exec-d } P t \sigma = \text{TypeError} \iff \neg \text{check } P \sigma$

<proof>

lemma *exec-d-eq-Normal*: $\text{exec-d } P t \sigma = \text{Normal } (\text{exec } P t \sigma) \iff \text{check } P \sigma$

<proof>

end

declare *split-paired-All* [*simp del*]

declare *split-paired-Ex* [*simp del*]

lemma *if-neq* [*dest!*]:

$(\text{if } P \text{ then } A \text{ else } B) \neq B \implies P$

<proof>

context *JVM-heap-base* **begin**

lemma *exec-d-no-errorI* [*intro*]:

$\text{check } P \sigma \implies \text{exec-d } P t \sigma \neq \text{TypeError}$

<proof>

theorem *no-type-error-commutes*:

$\text{exec-d } P t \sigma \neq \text{TypeError} \implies \text{exec-d } P t \sigma = \text{Normal } (\text{exec } P t \sigma)$

<proof>

lemma *defensive-imp-aggressive-1*:

$P, t \vdash (\text{Normal } \sigma) \text{ -tas-jvmd} \rightarrow (\text{Normal } \sigma') \implies P, t \vdash \sigma \text{ -tas-jvm} \rightarrow \sigma'$

<proof>

end

context *JVM-heap* **begin**

lemma *check-exec-hext*:

assumes $\text{exec}: (ta, xcp', h', \text{frs}') \in \text{exec } P t (xcp, h, \text{frs})$

and *check*: $\text{check } P (xcp, h, \text{frs})$

shows $h \sqsubseteq h'$

<proof>

lemma *exec-1-d-hext*:

$\llbracket P, t \vdash \text{Normal } (xcp, h, \text{frs}) \text{ -ta-jvmd} \rightarrow \text{Normal } (xcp', h', \text{frs}') \rrbracket \implies h \sqsubseteq h'$

<proof>

end

end

5.8 Instantiating the framework semantics with the JVM

theory *JVMThreaded*

imports

JVMDefensive

../Common/ConformThreaded

../Framework/FWLiftingSem

../Framework/FWProgressAux

begin

primrec *JVM-final* :: 'addr jvm-thread-state \Rightarrow bool

where

JVM-final (*xcp*, *frs*) = (*frs* = [])

The aggressive JVM

context *JVM-heap-base* **begin**

abbreviation *mexec* ::

'addr jvm-prog \Rightarrow 'thread-id \Rightarrow ('addr jvm-thread-state \times 'heap)

\Rightarrow ('addr, 'thread-id, 'heap) jvm-thread-action \Rightarrow ('addr jvm-thread-state \times 'heap) \Rightarrow bool

where

mexec *P* *t* \equiv ($\lambda((xcp, frstls), h)$ *ta* ((*xcp'*, *frstls'*), *h'*). *P*, *t* \vdash (*xcp*, *h*, *frstls*) $-ta-jvm\rightarrow$ (*xcp'*, *h'*, *frstls'*))

lemma *NewThread-memory-exec-instr*:

$\llbracket (ta, s) \in exec-instr\ I\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs; NewThread\ t'\ x\ m \in set\ \{ta\}_t \rrbracket \Longrightarrow m = fst\ (snd\ s)$
 $\langle proof \rangle$

lemma *NewThread-memory-exec*:

$\llbracket P, t \vdash \sigma -ta-jvm\rightarrow \sigma'; NewThread\ t'\ x\ m \in set\ \{ta\}_t \rrbracket \Longrightarrow m = (fst\ (snd\ \sigma'))$
 $\langle proof \rangle$

lemma *exec-instr-Wakeup-no-Lock-no-Join-no-Interrupt*:

$\llbracket (ta, s) \in exec-instr\ I\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs; Notified \in set\ \{ta\}_w \vee WokenUp \in set\ \{ta\}_w \rrbracket$
 $\Longrightarrow collect-locks\ \{ta\}_l = \{\} \wedge collect-cond-actions\ \{ta\}_c = \{\} \wedge collect-interrupts\ \{ta\}_i = \{\}$
 $\langle proof \rangle$

lemma *mexec-instr-Wakeup-no-Join*:

$\llbracket P, t \vdash \sigma -ta-jvm\rightarrow \sigma'; Notified \in set\ \{ta\}_w \vee WokenUp \in set\ \{ta\}_w \rrbracket$
 $\Longrightarrow collect-locks\ \{ta\}_l = \{\} \wedge collect-cond-actions\ \{ta\}_c = \{\} \wedge collect-interrupts\ \{ta\}_i = \{\}$
 $\langle proof \rangle$

lemma *mexec-final*:

$\llbracket mexec\ P\ t\ (x, m)\ ta\ (x', m'); JVM-final\ x \rrbracket \Longrightarrow False$
 $\langle proof \rangle$

lemma *exec-mthr*: *multithreaded JVM-final* (*mexec* *P*)

$\langle proof \rangle$

end

sublocale *JVM-heap-base* < *exec-mthr*:

multithreaded

JVM-final

mexec P

convert-RA

for *P*

<proof>

context *JVM-heap-base* **begin**

abbreviation *mexecT* ::

'addr jvm-prog

\Rightarrow (*'addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr*) *state*

\Rightarrow *'thread-id* \times (*'addr, 'thread-id, 'heap*) *jvm-thread-action*

\Rightarrow (*'addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr*) *state* \Rightarrow *bool*

where

mexecT P \equiv *exec-mthr.redT P*

abbreviation *mexecT-syntax1* ::

'addr jvm-prog \Rightarrow (*'addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr*) *state*

\Rightarrow *'thread-id* \Rightarrow (*'addr, 'thread-id, 'heap*) *jvm-thread-action*

\Rightarrow (*'addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr*) *state* \Rightarrow *bool*

($\langle \vdash - \dashv \rightarrow_{jvm} - \rangle [50, 0, 0, 50] 80$)

where

mexecT-syntax1 P s t ta s' \equiv *mexecT P s (t, ta) s'*

abbreviation *mExecT-syntax1* ::

'addr jvm-prog \Rightarrow (*'addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr*) *state*

\Rightarrow (*'thread-id* \times (*'addr, 'thread-id, 'heap*) *jvm-thread-action*) *list*

\Rightarrow (*'addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr*) *state* \Rightarrow *bool*

($\langle \vdash - \dashv \rightarrow_{jvm^*} - \rangle [50, 0, 0, 50] 80$)

where

P \vdash s - \dashv \rightarrow_{jvm^} s' \equiv exec-mthr.RedT P s ttas s'*

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abbreviation *mexecd* ::

'addr jvm-prog \Rightarrow *'thread-id* \Rightarrow *'addr jvm-thread-state* \times *'heap*

\Rightarrow (*'addr, 'thread-id, 'heap*) *jvm-thread-action* \Rightarrow *'addr jvm-thread-state* \times *'heap* \Rightarrow *bool*

where

mexecd P t \equiv ($\lambda((xcp, frstls), h)$ *ta* ($((xcp', frstls'), h')$). *P, t \vdash Normal (xcp, h, frstls) -ta-jvmd \rightarrow Normal (xcp', h', frstls')*)

lemma *execd-mthr*: *multithreaded JVM-final (mexecd P)*

<proof>

end

sublocale *JVM-heap-base* < *execd-mthr*:

multithreaded

JVM-final

mexecd P

convert-RA
for P
<proof>

context JVM-heap-base begin

abbreviation mexecdT ::

'addr jvm-prog \Rightarrow ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state
 \Rightarrow 'thread-id \times ('addr, 'thread-id, 'heap) jvm-thread-action
 \Rightarrow ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state \Rightarrow bool

where

mexecdT P \equiv execd-mthr.redT P

abbreviation mexecdT-syntax1 ::

'addr jvm-prog \Rightarrow ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state
 \Rightarrow 'thread-id \Rightarrow ('addr, 'thread-id, 'heap) jvm-thread-action
 \Rightarrow ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state \Rightarrow bool
($\langle \cdot \vdash - \dashv \rightarrow \rightarrow_{jvmd} \rightarrow [50, 0, 0, 0, 50] 80$)

where

mexecdT-syntax1 P s t ta s' \equiv mexecdT P s (t, ta) s'

abbreviation mExecdT-syntax1 ::

'addr jvm-prog \Rightarrow ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state
 \Rightarrow ('thread-id \times ('addr, 'thread-id, 'heap) jvm-thread-action) list
 \Rightarrow ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state \Rightarrow bool
($\langle \cdot \vdash - \dashv \rightarrow \rightarrow_{jvmd^*} \rightarrow [50, 0, 0, 50] 80$)

where

P \vdash s $\dashv \rightarrow$ ttas \rightarrow_{jvmd^*} s' \equiv execd-mthr.RedT P s ttas s'

lemma mexecd-Suspend-Invoke:

\llbracket mexecd P t (x, m) ta (x', m'); Suspend w \in set $\{\{ta\}_w\}$ \rrbracket
 $\implies \exists$ stk loc C M pc frs' n a T Ts Tr D. x' = (None, (stk, loc, C, M, pc) # frs') \wedge instrs-of P
C M ! pc = Invoke wait n \wedge stk ! n = Addr a \wedge typeof-addr m a = $\lfloor T \rfloor$ \wedge P \vdash class-type-of T sees
wait: Ts \rightarrow Tr = Native in D \wedge D.wait(Ts) :: Tr
<proof>

end

context JVM-heap begin

lemma exec-instr-New-Thread-exists-thread-object:

\llbracket (ta, xcp', h', frs') \in exec-instr ins P t h stk loc C M pc frs;
check-instr ins P h stk loc C M pc frs;
NewThread t' x h'' \in set $\{\{ta\}_t\}$ \rrbracket
 $\implies \exists$ C. typeof-addr h' (thread-id2addr t') = \lfloor Class-type C \rfloor \wedge P \vdash C \preceq^* Thread
<proof>

lemma exec-New-Thread-exists-thread-object:

\llbracket P, t \vdash Normal (xcp, h, frs) $\dashv \rightarrow$ ta $\dashv \rightarrow_{jvmd}$ Normal (xcp', h', frs'); NewThread t' x h'' \in set $\{\{ta\}_t\}$ \rrbracket
 $\implies \exists$ C. typeof-addr h' (thread-id2addr t') = \lfloor Class-type C \rfloor \wedge P \vdash C \preceq^* Thread
<proof>

lemma exec-instr-preserve-tconf:

$$\begin{aligned} & \llbracket (ta, xcp', h', frs') \in \text{exec-instr ins } P \ t \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs}; \\ & \quad \text{check-instr ins } P \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs}; \\ & \quad P, h \vdash t' \sqrt{t} \rrbracket \\ & \implies P, h' \vdash t' \sqrt{t} \\ \langle \text{proof} \rangle \end{aligned}$$

lemma *exec-preserve-tconf*:

$$\llbracket P, t \vdash \text{Normal} (xcp, h, frs) \text{ --ta--jvmd--} \rightarrow \text{Normal} (xcp', h', frs'); P, h \vdash t' \sqrt{t} \rrbracket \implies P, h' \vdash t' \sqrt{t}$$
 $\langle \text{proof} \rangle$

lemma *lifting-wf-thread-conf*: *lifting-wf JVM-final (mexecd P) ($\lambda t \ x \ m. P, m \vdash t \sqrt{t}$)*

$\langle \text{proof} \rangle$

end

sublocale *JVM-heap* < *execd-tconf*: *lifting-wf JVM-final mexecd P convert-RA $\lambda t \ x \ m. P, m \vdash t \sqrt{t}$*

$\langle \text{proof} \rangle$

context *JVM-heap* **begin**

lemma *execd-hext*:

$$P \vdash s \text{ --t>ta--} \rightarrow \text{jvmd} \ s' \implies \text{shr } s \leq \text{shr } s'$$
 $\langle \text{proof} \rangle$

lemma *Execd-hext*:

assumes $P \vdash s \text{ --\triangleright tta--} \rightarrow \text{jvmd}^* \ s'$
shows $\text{shr } s \leq \text{shr } s'$
 $\langle \text{proof} \rangle$

end

end

theory *JVM-Main*

imports

JVMState

JVMThreaded

begin

end

Chapter 6

Bytecode verifier

6.1 The JVM Type System as Semilattice

```
theory JVM-SemiType
imports
  ../Common/SemiType
begin

type-synonym tyl = ty err list
type-synonym tys = ty list
type-synonym tyi = tys × tyl
type-synonym tyi' = tyi option
type-synonym tym = tyi' list
type-synonym tyP = mname ⇒ cname ⇒ tym

definition stk-esl :: 'c prog ⇒ nat ⇒ tys esl
where
  stk-esl P mxs ≡ upto-esl mxs (SemiType.esl P)

definition loc-sl :: 'c prog ⇒ nat ⇒ tyl sl
where
  loc-sl P mxl ≡ Listn.sl mxl (Err.sl (SemiType.esl P))

definition sl :: 'c prog ⇒ nat ⇒ nat ⇒ tyi' err sl
where
  sl P mxs mxl ≡
    Err.sl(Opt.esl(Product.esl (stk-esl P mxs) (Err.esl(loc-sl P mxl))))

definition states :: 'c prog ⇒ nat ⇒ nat ⇒ tyi' err set
where
  states P mxs mxl ≡ fst(sl P mxs mxl)

definition le :: 'c prog ⇒ nat ⇒ nat ⇒ tyi' err ord
where
  le P mxs mxl ≡ fst(snd(sl P mxs mxl))

definition sup :: 'c prog ⇒ nat ⇒ nat ⇒ tyi' err binop
where
  sup P mxs mxl ≡ snd(snd(sl P mxs mxl))
```

definition $sup\text{-}ty\text{-}opt :: [c\ prog, ty\ err, ty\ err] \Rightarrow bool$
 $(\langle \cdot \vdash - \leq_T \rightarrow [71, 71, 71] \ 70 \rangle)$

where

$sup\text{-}ty\text{-}opt\ P \equiv Err.le\ (widen\ P)$

definition $sup\text{-}state :: [c\ prog, ty_i, ty_i] \Rightarrow bool$
 $(\langle \cdot \vdash - \leq_i \rightarrow [71, 71, 71] \ 70 \rangle)$

where

$sup\text{-}state\ P \equiv Product.le\ (Listn.le\ (widen\ P))\ (Listn.le\ (sup\text{-}ty\text{-}opt\ P))$

definition $sup\text{-}state\text{-}opt :: [c\ prog, ty_i', ty_i'] \Rightarrow bool$
 $(\langle \cdot \vdash - \leq'' \rightarrow [71, 71, 71] \ 70 \rangle)$

where

$sup\text{-}state\text{-}opt\ P \equiv Opt.le\ (sup\text{-}state\ P)$

abbreviation $sup\text{-}loc :: [c\ prog, ty_l, ty_l] \Rightarrow bool\ (\langle \cdot \vdash - [\leq_T] \rightarrow [71, 71, 71] \ 70 \rangle)$

where $P \vdash LT\ [\leq_T]\ LT' \equiv list\text{-}all2\ (sup\text{-}ty\text{-}opt\ P)\ LT\ LT'$

notation (*ASCII*)

$sup\text{-}ty\text{-}opt\ (\langle \cdot \vdash - \leq = T \rightarrow [71, 71, 71] \ 70 \rangle)$ **and**

$sup\text{-}state\ (\langle \cdot \vdash - \leq = i \rightarrow [71, 71, 71] \ 70 \rangle)$ **and**

$sup\text{-}state\text{-}opt\ (\langle \cdot \vdash - \leq = ' \rightarrow [71, 71, 71] \ 70 \rangle)$ **and**

$sup\text{-}loc\ (\langle \cdot \vdash - [\leq = T] \rightarrow [71, 71, 71] \ 70 \rangle)$

6.1.1 Unfolding

lemma *JVM-states-unfold*:

$states\ P\ mxs\ mxl \equiv err(opt((Union\ \{list\ n\ (types\ P)\ |n.\ n\ \leq\ mxs\}) \times list\ mxl\ (err(types\ P))))$

$\langle proof \rangle$

lemma *JVM-le-unfold*:

$le\ P\ m\ n \equiv$

$Err.le(Opt.le(Product.le(Listn.le(widen\ P))(Listn.le(Err.le(widen\ P))))))$

$\langle proof \rangle$

lemma *sl-def2*:

$JVM\text{-}SemiType.sl\ P\ mxs\ mxl \equiv$

$(states\ P\ mxs\ mxl, JVM\text{-}SemiType.le\ P\ mxs\ mxl, JVM\text{-}SemiType.sup\ P\ mxs\ mxl)$

$\langle proof \rangle$

lemma *JVM-le-conv*:

$le\ P\ m\ n\ (OK\ t1)\ (OK\ t2) = P \vdash t1 \leq' t2$

$\langle proof \rangle$

lemma *JVM-le-Err-conv*:

$le\ P\ m\ n = Err.le\ (sup\text{-}state\text{-}opt\ P)$

$\langle proof \rangle$

lemma *err-le-unfold* [*iff*]:

$Err.le\ r\ (OK\ a)\ (OK\ b) = r\ a\ b$

$\langle proof \rangle$

6.1.2 Semilattice

lemma *order-sup-state-opt* [*intro*, *simp*]:
 $wf\text{-prog } wf\text{-mb } P \implies order (sup\text{-state-opt } P)$
 ⟨*proof*⟩

lemma *semilat-JVM* [*intro?*]:
 $wf\text{-prog } wf\text{-mb } P \implies semilat (JVM\text{-SemiType.sl } P \text{ mxs mxl})$
 ⟨*proof*⟩

lemma *acc-JVM* [*intro*]:
 $wf\text{-prog } wf\text{-mb } P \implies acc (JVM\text{-SemiType.states } P \text{ mxs mxl}) (JVM\text{-SemiType.le } P \text{ mxs mxl})$
 ⟨*proof*⟩

6.1.3 Widening with \top

lemma *widen-refl*[*iff*]: $widen P t t$ ⟨*proof*⟩

lemma *sup-ty-opt-refl* [*iff*]: $P \vdash T \leq_{\top} T$
 ⟨*proof*⟩

lemma *Err-any-conv* [*iff*]: $P \vdash Err \leq_{\top} T = (T = Err)$
 ⟨*proof*⟩

lemma *any-Err* [*iff*]: $P \vdash T \leq_{\top} Err$
 ⟨*proof*⟩

lemma *OK-OK-conv* [*iff*]:
 $P \vdash OK T \leq_{\top} OK T' = P \vdash T \leq T'$
 ⟨*proof*⟩

lemma *any-OK-conv* [*iff*]:
 $P \vdash X \leq_{\top} OK T' = (\exists T. X = OK T \wedge P \vdash T \leq T')$
 ⟨*proof*⟩

lemma *OK-any-conv*:
 $P \vdash OK T \leq_{\top} X = (X = Err \vee (\exists T'. X = OK T' \wedge P \vdash T \leq T'))$
 ⟨*proof*⟩

lemma *sup-ty-opt-trans* [*intro?*, *trans*]:
 $\llbracket P \vdash a \leq_{\top} b; P \vdash b \leq_{\top} c \rrbracket \implies P \vdash a \leq_{\top} c$
 ⟨*proof*⟩

6.1.4 Stack and Registers

lemma *stk-convert*:
 $P \vdash ST [\leq] ST' = Listn.le (widen P) ST ST'$
 ⟨*proof*⟩

lemma *sup-loc-refl* [*iff*]: $P \vdash LT [\leq_{\top}] LT$
 ⟨*proof*⟩

lemmas *sup-loc-Cons1* [*iff*] = *list-all2-Cons1* [*of sup-ty-opt P*] **for** P

lemma *sup-loc-def*:

$P \vdash LT \ [\leq_{\top}] \ LT' \equiv Listn.le \ (sup\text{-}ty\text{-}opt \ P) \ LT \ LT'$
 $\langle proof \rangle$

lemma *sup-loc-widens-conv* [iff]:
 $P \vdash map \ OK \ Ts \ [\leq_{\top}] \ map \ OK \ Ts' = P \vdash Ts \ [\leq] \ Ts'$
 $\langle proof \rangle$

lemma *sup-loc-trans* [intro?, trans]:
 $\llbracket P \vdash a \ [\leq_{\top}] \ b; P \vdash b \ [\leq_{\top}] \ c \rrbracket \implies P \vdash a \ [\leq_{\top}] \ c$
 $\langle proof \rangle$

6.1.5 State Type

lemma *sup-state-conv* [iff]:
 $P \vdash (ST, LT) \leq_i (ST', LT') = (P \vdash ST \ [\leq] \ ST' \wedge P \vdash LT \ [\leq_{\top}] \ LT')$
 $\langle proof \rangle$

lemma *sup-state-conv2*:
 $P \vdash s1 \leq_i s2 = (P \vdash fst \ s1 \ [\leq] \ fst \ s2 \wedge P \vdash snd \ s1 \ [\leq_{\top}] \ snd \ s2)$
 $\langle proof \rangle$

lemma *sup-state-refl* [iff]: $P \vdash s \leq_i s$
 $\langle proof \rangle$

lemma *sup-state-trans* [intro?, trans]:
 $\llbracket P \vdash a \leq_i b; P \vdash b \leq_i c \rrbracket \implies P \vdash a \leq_i c$
 $\langle proof \rangle$

lemma *sup-state-opt-None-any* [iff]:
 $P \vdash None \leq' s$
 $\langle proof \rangle$

lemma *sup-state-opt-any-None* [iff]:
 $P \vdash s \leq' None = (s = None)$
 $\langle proof \rangle$

lemma *sup-state-opt-Some-Some* [iff]:
 $P \vdash Some \ a \leq' Some \ b = P \vdash a \leq_i b$
 $\langle proof \rangle$

lemma *sup-state-opt-any-Some*:
 $P \vdash (Some \ s) \leq' X = (\exists s'. X = Some \ s' \wedge P \vdash s \leq_i s')$
 $\langle proof \rangle$

lemma *sup-state-opt-refl* [iff]: $P \vdash s \leq' s$
 $\langle proof \rangle$

lemma *sup-state-opt-trans* [intro?, trans]:
 $\llbracket P \vdash a \leq' b; P \vdash b \leq' c \rrbracket \implies P \vdash a \leq' c$
 $\langle proof \rangle$

end

6.2 Effect of Instructions on the State Type

```

theory Effect
imports
  JVM-SemiType
  ../JVM/JVMExceptions
begin

locale jvm-method = prog +
  fixes mxs :: nat
  fixes mxl0 :: nat
  fixes Ts :: ty list
  fixes Tτ :: ty
  fixes is :: 'addr instr list'
  fixes xt :: ex-table

  fixes mxl :: nat
  defines mxl-def: mxl ≡ 1 + size Ts + mxl0

```

Program counter of successor instructions:

```

primrec succs :: 'addr instr ⇒ tyi ⇒ pc ⇒ pc list'

```

where

```

  succs (Load idx) τ pc    = [pc+1]
| succs (Store idx) τ pc  = [pc+1]
| succs (Push v) τ pc    = [pc+1]
| succs (Getfield F C) τ pc = [pc+1]
| succs (Putfield F C) τ pc = [pc+1]
| succs (CAS F C) τ pc   = [pc+1]
| succs (New C) τ pc     = [pc+1]
| succs (NewArray T) τ pc = [pc+1]
| succs ALoad τ pc      = (if (fst τ)!1 = NT then [] else [pc+1])
| succs AStore τ pc    = (if (fst τ)!2 = NT then [] else [pc+1])
| succs ALength τ pc   = (if (fst τ)!0 = NT then [] else [pc+1])
| succs (Checkcast C) τ pc = [pc+1]
| succs (Instanceof T) τ pc = [pc+1]
| succs Pop τ pc       = [pc+1]
| succs Dup τ pc      = [pc+1]
| succs Swap τ pc    = [pc+1]
| succs (BinOpInstr b) τ pc = [pc+1]
| succs-IfFalse:
  succs (IfFalse b) τ pc = [pc+1, nat (int pc + b)]
| succs-Goto:
  succs (Goto b) τ pc   = [nat (int pc + b)]
| succs-Return:
  succs Return τ pc    = []
| succs-Invoke:
  succs (Invoke M n) τ pc = (if (fst τ)!n = NT then [] else [pc+1])
| succs-Throw:
  succs ThrowExc τ pc  = []
| succs MEnter τ pc   = (if (fst τ)!0 = NT then [] else [pc+1])
| succs MExit τ pc   = (if (fst τ)!0 = NT then [] else [pc+1])

```

Effect of instruction on the state type:

```

fun effi :: 'addr instr × 'm prog × tyi ⇒ tyi'

```

where

- eff_i -Load:
 $eff_i (Load\ n,\ P,\ (ST,\ LT)) = (ok\text{-}val\ (LT\ !\ n)\ \# ST,\ LT)$
- | eff_i -Store:
 $eff_i (Store\ n,\ P,\ (T\ \#ST,\ LT)) = (ST,\ LT[n:=\ OK\ T])$
- | eff_i -Push:
 $eff_i (Push\ v,\ P,\ (ST,\ LT)) = (the\ (typeof\ v)\ \# ST,\ LT)$
- | eff_i -Getfield:
 $eff_i (Getfield\ F\ C,\ P,\ (T\ \#ST,\ LT)) = (fst\ (snd\ (field\ P\ C\ F))\ \# ST,\ LT)$
- | eff_i -Putfield:
 $eff_i (Putfield\ F\ C,\ P,\ (T_1\ \#T_2\ \#ST,\ LT)) = (ST,\ LT)$
- | eff_i -CAS:
 $eff_i (CAS\ F\ C,\ P,\ (T_1\ \#T_2\ \#T_3\ \#ST,\ LT)) = (Boolean\ \# ST,\ LT)$
- | eff_i -New:
 $eff_i (New\ C,\ P,\ (ST,\ LT)) = (Class\ C\ \# ST,\ LT)$
- | eff_i -NewArray:
 $eff_i (NewArray\ Ty,\ P,\ (T\ \#ST,\ LT)) = (Ty[]\ \# ST,\ LT)$
- | eff_i -ALoad:
 $eff_i (ALoad,\ P,\ (T_1\ \#T_2\ \#ST,\ LT)) = (the\ \text{-}Array\ T_2\ \# ST,\ LT)$
- | eff_i -AStore:
 $eff_i (AStore,\ P,\ (T_1\ \#T_2\ \#T_3\ \#ST,\ LT)) = (ST,\ LT)$
- | eff_i -ALength:
 $eff_i (ALength,\ P,\ (T_1\ \#ST,\ LT)) = (Integer\ \#ST,\ LT)$
- | eff_i -Checkcast:
 $eff_i (Checkcast\ Ty,\ P,\ (T\ \#ST,\ LT)) = (Ty\ \# ST,\ LT)$
- | eff_i -Instanceof:
 $eff_i (Instanceof\ Ty,\ P,\ (T\ \#ST,\ LT)) = (Boolean\ \# ST,\ LT)$
- | eff_i -Pop:
 $eff_i (Pop,\ P,\ (T\ \#ST,\ LT)) = (ST,\ LT)$
- | eff_i -Dup:
 $eff_i (Dup,\ P,\ (T\ \#ST,\ LT)) = (T\ \#T\ \#ST,\ LT)$
- | eff_i -Swap:
 $eff_i (Swap,\ P,\ (T_1\ \#T_2\ \#ST,\ LT)) = (T_2\ \#T_1\ \#ST,\ LT)$
- | eff_i -BinOpInstr:
 $eff_i (BinOpInstr\ bop,\ P,\ (T_2\ \#T_1\ \#ST,\ LT)) = ((THE\ T.\ P\ \vdash\ T_1\ \ll bop \gg\ T_2 : T)\ \#ST,\ LT)$
- | eff_i -IfFalse:
 $eff_i (IfFalse\ b,\ P,\ (T_1\ \#ST,\ LT)) = (ST,\ LT)$

| *eff_i-Invoke*:
 $eff_i (Invoke\ M\ n,\ P,\ (ST,LT)) =$
 $(let\ U = fst\ (snd\ (snd\ (method\ P\ (the\ (class-type-of'\ (ST\ !\ n))))\ M)))$
 $in\ (U\ \#\ drop\ (n+1)\ ST,\ LT))$

| *eff_i-Goto*:
 $eff_i (Goto\ n,\ P,\ s) = s$

| *eff_i-MEnter*:
 $eff_i (MEnter,\ P,\ (T1\ \#\ ST,LT)) = (ST,LT)$

| *eff_i-MExit*:
 $eff_i (MExit,\ P,\ (T1\ \#\ ST,LT)) = (ST,LT)$

fun *is-relevant-class* :: 'addr instr ⇒ 'm prog ⇒ cname ⇒ bool

where

rel-Getfield:
 $is-relevant-class\ (Getfield\ F\ D) = (\lambda P\ C.\ P \vdash\ NullPointer \preceq^* C)$

| *rel-Putfield*:
 $is-relevant-class\ (Putfield\ F\ D) = (\lambda P\ C.\ P \vdash\ NullPointer \preceq^* C)$

| *rel-CAS*:
 $is-relevant-class\ (CAS\ F\ D) = (\lambda P\ C.\ P \vdash\ NullPointer \preceq^* C)$

| *rel-Checcast*:
 $is-relevant-class\ (Checkcast\ T) = (\lambda P\ C.\ P \vdash\ ClassCast \preceq^* C)$

| *rel-New*:
 $is-relevant-class\ (New\ D) = (\lambda P\ C.\ P \vdash\ OutOfMemory \preceq^* C)$

| *rel-Throw*:
 $is-relevant-class\ ThrowExc = (\lambda P\ C.\ True)$

| *rel-Invoke*:
 $is-relevant-class\ (Invoke\ M\ n) = (\lambda P\ C.\ True)$

| *rel-NewArray*:
 $is-relevant-class\ (NewArray\ T) = (\lambda P\ C.\ (P \vdash\ OutOfMemory \preceq^* C) \vee (P \vdash\ NegativeArraySize \preceq^* C))$

| *rel-ALoad*:
 $is-relevant-class\ ALoad = (\lambda P\ C.\ P \vdash\ ArrayIndexOutOfBounds \preceq^* C \vee P \vdash\ NullPointer \preceq^* C)$

| *rel-ASStore*:
 $is-relevant-class\ AStore = (\lambda P\ C.\ P \vdash\ ArrayIndexOutOfBounds \preceq^* C \vee P \vdash\ ArrayStore \preceq^* C \vee P \vdash\ NullPointer \preceq^* C)$

| *rel-ALength*:
 $is-relevant-class\ ALength = (\lambda P\ C.\ P \vdash\ NullPointer \preceq^* C)$

| *rel-MEnter*:
 $is-relevant-class\ MEnter = (\lambda P\ C.\ P \vdash\ IllegalMonitorState \preceq^* C \vee P \vdash\ NullPointer \preceq^* C)$

| *rel-MExit*:
 $is-relevant-class\ MExit = (\lambda P\ C.\ P \vdash\ IllegalMonitorState \preceq^* C \vee P \vdash\ NullPointer \preceq^* C)$

| *rel-BinOp*:
 $is-relevant-class\ (BinOpInstr\ bop) = binop-relevant-class\ bop$

| *rel-default*:
 $is-relevant-class\ i = (\lambda P\ C.\ False)$

definition *is-relevant-entry* :: 'm prog ⇒ 'addr instr ⇒ pc ⇒ ex-entry ⇒ bool

where

is-relevant-entry $P\ i\ pc\ e \equiv$
 $let\ (f,t,C,h,d) = e$
 $in\ (case\ C\ of\ None \Rightarrow True \mid [C'] \Rightarrow is-relevant-class\ i\ P\ C') \wedge pc \in \{f..<t\}$

definition *relevant-entries* $:: 'm\ prog \Rightarrow 'addr\ instr \Rightarrow pc \Rightarrow ex-table \Rightarrow ex-table$

where

relevant-entries $P\ i\ pc \equiv filter\ (is-relevant-entry\ P\ i\ pc)$

definition *xcpt-eff* $:: 'addr\ instr \Rightarrow 'm\ prog \Rightarrow pc \Rightarrow ty_i \Rightarrow ex-table \Rightarrow (pc \times ty_i')$ list

where

xcpt-eff $i\ P\ pc\ \tau\ et \equiv let\ (ST,LT) = \tau\ in$
 $map\ (\lambda(f,t,C,h,d). (h,\ Some\ ((case\ C\ of\ None \Rightarrow Class\ Throwable \mid Some\ C' \Rightarrow Class\ C')\#drop$
 $(size\ ST - d)\ ST,\ LT)))\ (relevant-entries\ P\ i\ pc\ et)$

definition *norm-eff* $:: 'addr\ instr \Rightarrow 'm\ prog \Rightarrow nat \Rightarrow ty_i \Rightarrow (pc \times ty_i')$ list

where *norm-eff* $i\ P\ pc\ \tau \equiv map\ (\lambda pc'. (pc',Some\ (eff_i\ (i,P,\tau))))\ (succs\ i\ \tau\ pc)$

definition *eff* $:: 'addr\ instr \Rightarrow 'm\ prog \Rightarrow pc \Rightarrow ex-table \Rightarrow ty_i' \Rightarrow (pc \times ty_i')$ list

where

eff $i\ P\ pc\ et\ t \equiv$
 $case\ t\ of$
 $None \Rightarrow []$
 $\mid Some\ \tau \Rightarrow (norm-eff\ i\ P\ pc\ \tau) @ (xcpt-eff\ i\ P\ pc\ \tau\ et)$

lemma *eff-None*:

eff $i\ P\ pc\ xt\ None = []$
 $\langle proof \rangle$

lemma *eff-Some*:

eff $i\ P\ pc\ xt\ (Some\ \tau) = norm-eff\ i\ P\ pc\ \tau @ xcpt-eff\ i\ P\ pc\ \tau\ xt$
 $\langle proof \rangle$

Conditions under which *eff* is applicable:

fun *app_i* $:: 'addr\ instr \times 'm\ prog \times pc \times nat \times ty \times ty_i \Rightarrow bool$

where

app_i-Load:

app_i $(Load\ n,\ P,\ pc,\ mxs,\ T_r,\ (ST,LT)) =$
 $(n < length\ LT \wedge LT ! n \neq Err \wedge length\ ST < mxs)$

\mid *app_i-Store*:

app_i $(Store\ n,\ P,\ pc,\ mxs,\ T_r,\ (T\#ST,\ LT)) =$
 $(n < length\ LT)$

\mid *app_i-Push*:

app_i $(Push\ v,\ P,\ pc,\ mxs,\ T_r,\ (ST,LT)) =$
 $(length\ ST < mxs \wedge typeof\ v \neq None)$

\mid *app_i-Getfield*:

app_i $(Getfield\ F\ C,\ P,\ pc,\ mxs,\ T_r,\ (T\#ST,\ LT)) =$
 $(\exists T_f\ fm.\ P \vdash C\ sees\ F:T_f\ (fm)\ in\ C \wedge P \vdash T \leq Class\ C)$

\mid *app_i-Putfield*:

app_i $(Putfield\ F\ C,\ P,\ pc,\ mxs,\ T_r,\ (T_1\#T_2\#ST,\ LT)) =$
 $(\exists T_f\ fm.\ P \vdash C\ sees\ F:T_f\ (fm)\ in\ C \wedge P \vdash T_2 \leq (Class\ C) \wedge P \vdash T_1 \leq T_f)$

\mid *app_i-CAS*:

app_i $(CAS\ F\ C,\ P,\ pc,\ mxs,\ T_r,\ (T_3\#T_2\#T_1\#ST,\ LT)) =$
 $(\exists T_f\ fm.\ P \vdash C\ sees\ F:T_f\ (fm)\ in\ C \wedge volatile\ fm \wedge P \vdash T_1 \leq Class\ C \wedge P \vdash T_2 \leq T_f \wedge P \vdash$

$T_3 \leq T_f$
| *app_i-New*:
 $app_i (New\ C, P, pc, mxs, T_r, (ST,LT)) =$
 $(is\text{-}class\ P\ C \wedge length\ ST < mxs)$
| *app_i-NewArray*:
 $app_i (NewArray\ Ty, P, pc, mxs, T_r, (Integer\#ST,LT)) =$
 $is\text{-}type\ P\ (Ty[])$
| *app_i-ALoad*:
 $app_i (ALoad, P, pc, mxs, T_r, (T1\#T2\#ST,LT)) =$
 $(T1 = Integer \wedge (T2 \neq NT \longrightarrow (\exists Ty. T2 = Ty[])))$
| *app_i-AStore*:
 $app_i (AStore, P, pc, mxs, T_r, (T1\#T2\#T3\#ST,LT)) =$
 $(T2 = Integer \wedge (T3 \neq NT \longrightarrow (\exists Ty. T3 = Ty[])))$
| *app_i-ALength*:
 $app_i (ALength, P, pc, mxs, T_r, (T1\#ST,LT)) =$
 $(T1 = NT \vee (\exists Ty. T1 = Ty[]))$
| *app_i-Checkcast*:
 $app_i (Checkcast\ Ty, P, pc, mxs, T_r, (T\#ST,LT)) =$
 $(is\text{-}type\ P\ Ty)$
| *app_i-Instanceof*:
 $app_i (Instanceof\ Ty, P, pc, mxs, T_r, (T\#ST,LT)) =$
 $(is\text{-}type\ P\ Ty \wedge is\text{-}refT\ T)$
| *app_i-Pop*:
 $app_i (Pop, P, pc, mxs, T_r, (T\#ST,LT)) =$
 $True$
| *app_i-Dup*:
 $app_i (Dup, P, pc, mxs, T_r, (T\#ST,LT)) =$
 $(Suc\ (length\ ST) < mxs)$
| *app_i-Swap*:
 $app_i (Swap, P, pc, mxs, T_r, (T1\#T2\#ST,LT)) = True$
| *app_i-BinOpInstr*:
 $app_i (BinOpInstr\ bop, P, pc, mxs, T_r, (T2\#T1\#ST,LT)) = (\exists T. P \vdash T1 \ll bop \gg T2 : T)$
| *app_i-IfFalse*:
 $app_i (IfFalse\ b, P, pc, mxs, T_r, (Boolean\#ST,LT)) =$
 $(0 \leq int\ pc + b)$
| *app_i-Goto*:
 $app_i (Goto\ b, P, pc, mxs, T_r, s) = (0 \leq int\ pc + b)$
| *app_i-Return*:
 $app_i (Return, P, pc, mxs, T_r, (T\#ST,LT)) = (P \vdash T \leq T_r)$
| *app_i-Throw*:
 $app_i (ThrowExc, P, pc, mxs, T_r, (T\#ST,LT)) =$
 $(T = NT \vee (\exists C. T = Class\ C \wedge P \vdash C \preceq^* Throwable))$
| *app_i-Invoke*:
 $app_i (Invoke\ M\ n, P, pc, mxs, T_r, (ST,LT)) =$
 $(n < length\ ST \wedge$
 $(ST!n \neq NT \longrightarrow$
 $(\exists C\ D\ Ts\ T\ m. class\text{-}type\text{-}of'\ (ST\ !\ n) = [C] \wedge P \vdash C\ sees\ M:Ts \rightarrow T = m\ in\ D \wedge P \vdash rev$
 $(take\ n\ ST) [\leq] Ts)))$
| *app_i-MEnter*:
 $app_i (MEnter, P, pc, mxs, T_r, (T\#ST,LT)) = (is\text{-}refT\ T)$
| *app_i-MExit*:
 $app_i (MExit, P, pc, mxs, T_r, (T\#ST,LT)) = (is\text{-}refT\ T)$
| *app_i-default*:
 $app_i (i, P, pc, mxs, T_r, s) = False$

definition $xcpt\text{-}app :: 'addr\ instr \Rightarrow 'm\ prog \Rightarrow pc \Rightarrow nat \Rightarrow ex\text{-}table \Rightarrow ty_i \Rightarrow bool$

where

$xcpt\text{-}app\ i\ P\ pc\ mxs\ xt\ \tau \equiv \forall (f,t,C,h,d) \in set\ (relevant\text{-}entries\ P\ i\ pc\ xt). (case\ C\ of\ None \Rightarrow True \mid Some\ C' \Rightarrow is\text{-}class\ P\ C') \wedge d \leq size\ (fst\ \tau) \wedge d < mxs$

definition $app :: 'addr\ instr \Rightarrow 'm\ prog \Rightarrow nat \Rightarrow ty \Rightarrow nat \Rightarrow nat \Rightarrow ex\text{-}table \Rightarrow ty_i' \Rightarrow bool$

where

$app\ i\ P\ mxs\ T_r\ pc\ mpc\ xt \equiv case\ t\ of\ None \Rightarrow True \mid Some\ \tau \Rightarrow$
 $app_i\ (i,P,pc,mxs,T_r,\tau) \wedge xcpt\text{-}app\ i\ P\ pc\ mxs\ xt\ \tau \wedge$
 $(\forall (pc',\tau') \in set\ (eff\ i\ P\ pc\ xt\ t). pc' < mpc)$

lemma $app\text{-}Some$:

$app\ i\ P\ mxs\ T_r\ pc\ mpc\ xt\ (Some\ \tau) =$
 $(app_i\ (i,P,pc,mxs,T_r,\tau) \wedge xcpt\text{-}app\ i\ P\ pc\ mxs\ xt\ \tau \wedge$
 $(\forall (pc',s') \in set\ (eff\ i\ P\ pc\ xt\ (Some\ \tau)). pc' < mpc))$
 $\langle proof \rangle$

locale $eff = jvm\text{-}method +$

fixes eff_i **and** app_i **and** eff **and** app

fixes $norm\text{-}eff$ **and** $xcpt\text{-}app$ **and** $xcpt\text{-}eff$

fixes mpc

defines $mpc \equiv size\ is$

defines $eff_i\ i\ \tau \equiv Effect.eff_i\ (i,P,\tau)$

notes $eff_i\text{-}simps\ [simp] = Effect.eff_i.simps\ [where\ P = P,\ folded\ eff_i\text{-}def]$

defines $app_i\ i\ pc\ \tau \equiv Effect.app_i\ (i,\ P,\ pc,\ mxs,\ T_r,\ \tau)$

notes $app_i\text{-}simps\ [simp] = Effect.app_i.simps\ [where\ P=P\ and\ mxs=mxs\ and\ T_r=T_r,\ folded\ app_i\text{-}def]$

defines $xcpt\text{-}eff\ i\ pc\ \tau \equiv Effect.xcpt\text{-}eff\ i\ P\ pc\ \tau\ xt$

notes $xcpt\text{-}eff = Effect.xcpt\text{-}eff\text{-}def\ [of\ -\ P\ -\ xt,\ folded\ xcpt\text{-}eff\text{-}def]$

defines $norm\text{-}eff\ i\ pc\ \tau \equiv Effect.norm\text{-}eff\ i\ P\ pc\ \tau$

notes $norm\text{-}eff = Effect.norm\text{-}eff\text{-}def\ [of\ -\ P,\ folded\ norm\text{-}eff\text{-}def\ eff_i\text{-}def]$

defines $eff\ i\ pc \equiv Effect.eff\ i\ P\ pc\ xt$

notes $eff = Effect.eff\text{-}def\ [of\ -\ P\ -\ xt,\ folded\ eff\text{-}def\ norm\text{-}eff\text{-}def\ xcpt\text{-}eff\text{-}def]$

defines $xcpt\text{-}app\ i\ pc\ \tau \equiv Effect.xcpt\text{-}app\ i\ P\ pc\ mxs\ xt\ \tau$

notes $xcpt\text{-}app = Effect.xcpt\text{-}app\text{-}def\ [of\ -\ P\ -\ mxs\ xt,\ folded\ xcpt\text{-}app\text{-}def]$

defines $app\ i\ pc \equiv Effect.app\ i\ P\ mxs\ T_r\ pc\ mpc\ xt$

notes $app = Effect.app\text{-}def\ [of\ -\ P\ mxs\ T_r\ -\ mpc\ xt,\ folded\ app\text{-}def\ xcpt\text{-}app\text{-}def\ app_i\text{-}def\ eff\text{-}def]$

lemma $length\text{-}cases2$:

assumes $\bigwedge LT. P\ (\ [],LT)$

assumes $\bigwedge l\ ST\ LT. P\ (l\#\ ST,LT)$

shows $P\ s$

<proof>

lemma *length-cases3*:

assumes $\bigwedge LT. P (\[],LT)$

assumes $\bigwedge l LT. P ([l],LT)$

assumes $\bigwedge l l' ST LT. P (l\#l'\#ST,LT)$

shows $P s$

<proof>

lemma *length-cases4*:

assumes $\bigwedge LT. P (\[],LT)$

assumes $\bigwedge l LT. P ([l],LT)$

assumes $\bigwedge l l' LT. P ([l,l'],LT)$

assumes $\bigwedge l l' l'' ST LT. P (l\#l'\#l''\#ST,LT)$

shows $P s$

<proof>

lemma *length-cases5*:

assumes $\bigwedge LT. P (\[],LT)$

assumes $\bigwedge l LT. P ([l],LT)$

assumes $\bigwedge l l' LT. P ([l,l'],LT)$

assumes $\bigwedge l l' l'' LT. P ([l,l',l''],LT)$

assumes $\bigwedge l l' l'' l''' ST LT. P (l\#l'\#l''\#l'''\#ST,LT)$

shows $P s$

<proof>

simp rules for *app*

lemma *appNone[simp]*: $app\ i\ P\ mxs\ T_r\ pc\ mpc\ et\ None = True$

<proof>

lemma *appLoad[simp]*:

$app_i\ (Load\ idx,\ P,\ T_r,\ mxs,\ pc,\ s) = (\exists\ ST\ LT. s = (ST,LT) \wedge idx < length\ LT \wedge LT!idx \neq Err \wedge length\ ST < mxs)$

<proof>

lemma *appStore[simp]*:

$app_i\ (Store\ idx,P,pc,mxs,T_r,s) = (\exists\ ts\ ST\ LT. s = (ts\#ST,LT) \wedge idx < length\ LT)$

<proof>

lemma *appPush[simp]*:

$app_i\ (Push\ v,P,pc,mxs,T_r,s) = (\exists\ ST\ LT. s = (ST,LT) \wedge length\ ST < mxs \wedge typeof\ v \neq None)$

<proof>

lemma *appGetField[simp]*:

$app_i\ (Getfield\ F\ C,P,pc,mxs,T_r,s) = (\exists\ oT\ vT\ ST\ LT\ fm. s = (oT\#ST,LT) \wedge P \vdash C\ sees\ F:vT\ (fm)\ in\ C \wedge P \vdash oT \leq (Class\ C))$

<proof>

lemma *appPutField[simp]*:

$app_i\ (Putfield\ F\ C,P,pc,mxs,T_r,s) =$

$(\exists vT vT' oT ST LT fm. s = (vT\#oT\#ST, LT) \wedge$
 $P \vdash C \text{ sees } F:vT' (fm) \text{ in } C \wedge P \vdash oT \leq (Class C) \wedge P \vdash vT \leq vT')$
 ⟨proof⟩

lemma *appCAS[simp]*:

$app_i (CAS F C, P, pc, mxs, T_r, s) =$
 $(\exists T1 T2 T3 T' ST LT fm. s = (T3 \# T2 \# T1 \# ST, LT) \wedge$
 $P \vdash C \text{ sees } F:T' (fm) \text{ in } C \wedge \text{volatile } fm \wedge P \vdash T1 \leq Class C \wedge P \vdash T2 \leq T' \wedge P \vdash T3 \leq T')$
 ⟨proof⟩

lemma *appNew[simp]*:

$app_i (New C,P,pc,mxs,T_r,s) =$
 $(\exists ST LT. s=(ST,LT) \wedge \text{is-class } P C \wedge \text{length } ST < mxs)$
 ⟨proof⟩

lemma *appNewArray[simp]*:

$app_i (NewArray Ty,P,pc,mxs,T_r,s) =$
 $(\exists ST LT. s=(Integer\#ST,LT) \wedge \text{is-type } P (Ty[]))$
 ⟨proof⟩

lemma *appALoad[simp]*:

$app_i (ALoad,P,pc,mxs,T_r,s) =$
 $(\exists T ST LT. s=(Integer\#T\#ST,LT) \wedge (T \neq NT \longrightarrow (\exists T'. T = T'[])))$
 ⟨proof⟩

lemma *appAStore[simp]*:

$app_i (AStore,P,pc,mxs,T_r,s) =$
 $(\exists T U ST LT. s=(T\#Integer\#U\#ST,LT) \wedge (U \neq NT \longrightarrow (\exists T'. U = T'[])))$
 ⟨proof⟩

lemma *appALength[simp]*:

$app_i (ALength,P,pc,mxs,T_r,s) =$
 $(\exists T ST LT. s=(T\#ST,LT) \wedge (T \neq NT \longrightarrow (\exists T'. T = T'[])))$
 ⟨proof⟩

lemma *appCheckcast[simp]*:

$app_i (Checkcast Ty,P,pc,mxs,T_r,s) =$
 $(\exists T ST LT. s = (T\#ST,LT) \wedge \text{is-type } P Ty)$
 ⟨proof⟩

lemma *appInstanceof[simp]*:

$app_i (Instanceof Ty,P,pc,mxs,T_r,s) =$
 $(\exists T ST LT. s = (T\#ST,LT) \wedge \text{is-type } P Ty \wedge \text{is-ref } T T)$
 ⟨proof⟩

lemma *app_iPop[simp]*:

$app_i (Pop,P,pc,mxs,T_r,s) = (\exists ts ST LT. s = (ts\#ST,LT))$
 ⟨proof⟩

lemma *appDup[simp]*:

$app_i (Dup,P,pc,mxs,T_r,s) =$
 $(\exists T ST LT. s = (T\#ST,LT) \wedge \text{Suc } (\text{length } ST) < mxs)$
 ⟨proof⟩

lemma *app_iSwap[simp]*:

$$app_i (Swap, P, pc, mxs, T_r, s) = (\exists T1 T2 ST LT. s = (T1 \# T2 \# ST, LT))$$

<proof>

lemma *appBinOp[simp]*:

$$app_i (BinOpInstr bop, P, pc, mxs, T_r, s) = (\exists T1 T2 ST LT T. s = (T2 \# T1 \# ST, LT) \wedge P \vdash T1 \ll bop \gg T2 : T)$$

<proof>

lemma *appIfFalse [simp]*:

$$app_i (IfFalse b, P, pc, mxs, T_r, s) = (\exists ST LT. s = (Boolean \# ST, LT) \wedge 0 \leq int pc + b)$$

<proof>

lemma *appReturn[simp]*:

$$app_i (Return, P, pc, mxs, T_r, s) = (\exists T ST LT. s = (T \# ST, LT) \wedge P \vdash T \leq T_r)$$

<proof>

lemma *appThrow[simp]*:

$$app_i (ThrowExc, P, pc, mxs, T_r, s) = (\exists T ST LT. s = (T \# ST, LT) \wedge (T = NT \vee (\exists C. T = Class C \wedge P \vdash C \preceq^* Throwable)))$$

<proof>

lemma *appMEnter[simp]*:

$$app_i (MEnter, P, pc, mxs, T_r, s) = (\exists T ST LT. s = (T \# ST, LT) \wedge is-refT T)$$

<proof>

lemma *appMExit[simp]*:

$$app_i (MExit, P, pc, mxs, T_r, s) = (\exists T ST LT. s = (T \# ST, LT) \wedge is-refT T)$$

<proof>

lemma *effNone*:

$$(pc', s') \in set (eff i P pc et None) \implies s' = None$$

<proof>

lemma *relevant-entries-append [simp]*:

$$relevant-entries P i pc (xt @ xt') = relevant-entries P i pc xt @ relevant-entries P i pc xt'$$

<proof>

lemma *xcpt-app-append [iff]*:

$$xcpt-app i P pc mxs (xt @ xt') \tau = (xcpt-app i P pc mxs xt \tau \wedge xcpt-app i P pc mxs xt' \tau)$$

<proof>

lemma *xcpt-eff-append [simp]*:

$$xcpt-eff i P pc \tau (xt @ xt') = xcpt-eff i P pc \tau xt @ xcpt-eff i P pc \tau xt'$$

<proof>

lemma *app-append [simp]*:

$$app i P pc T mxs mpc (xt @ xt') \tau = (app i P pc T mxs mpc xt \tau \wedge app i P pc T mxs mpc xt' \tau)$$

<proof>

6.2.1 Code generator setup

declare *list-all2-Nil* [code]
declare *list-all2-Cons* [code]

lemma *eff_i-BinOpInstr-code*:

$\text{eff}_i (\text{BinOpInstr } \text{bop}, P, (T_2 \# T_1 \# ST, LT)) = (\text{Predicate.the } (WTrt\text{-binop-}i\text{-}i\text{-}i\text{-}i\text{-}o \text{ } P \text{ } T_1 \text{ bop } T_2) \# ST, LT)$
 ⟨proof⟩

lemmas *eff_i-code*[code] =

eff_i-Load *eff_i-Store* *eff_i-Push* *eff_i-Getfield* *eff_i-Putfield* *eff_i-New* *eff_i-NewArray* *eff_i-ALoad*
eff_i-AStore *eff_i-ALength* *eff_i-Checkcast* *eff_i-InstanceOf* *eff_i-Pop* *eff_i-Dup* *eff_i-Swap* *eff_i-BinOpInstr-code*
eff_i-IfFalse *eff_i-Invoke* *eff_i-Goto* *eff_i-MEnter* *eff_i-MExit*

lemma *app_i-Getfield-code*:

$\text{app}_i (\text{Getfield } F \ C, P, pc, mxs, T_r, (T \# ST, LT)) \longleftrightarrow$
 $\text{Predicate.holds } (\text{Predicate.bind } (\text{sees-field-}i\text{-}i\text{-}i\text{-}o\text{-}i \text{ } P \ C \ F \ C) (\lambda T. \text{Predicate.single } ())) \wedge P \vdash T \leq$
Class C
 ⟨proof⟩

lemma *app_i-Putfield-code*:

$\text{app}_i (\text{Putfield } F \ C, P, pc, mxs, T_r, (T_1 \# T_2 \# ST, LT)) \longleftrightarrow$
 $P \vdash T_2 \leq (\text{Class } C) \wedge$
 $\text{Predicate.holds } (\text{Predicate.bind } (\text{sees-field-}i\text{-}i\text{-}i\text{-}o\text{-}i \text{ } P \ C \ F \ C) (\lambda(T, fm). \text{if } P \vdash T_1 \leq T \text{ then } \text{Predicate.single } () \text{ else bot}))$
 ⟨proof⟩

lemma *app_i-CAS-code*:

$\text{app}_i (\text{CAS } F \ C, P, pc, mxs, T_r, (T_3 \# T_2 \# T_1 \# ST, LT)) \longleftrightarrow$
 $P \vdash T_1 \leq \text{Class } C \wedge$
 $\text{Predicate.holds } (\text{Predicate.bind } (\text{sees-field-}i\text{-}i\text{-}i\text{-}o\text{-}i \text{ } P \ C \ F \ C) (\lambda(T, fm). \text{if } P \vdash T_2 \leq T \wedge P \vdash T_3 \leq T \wedge \text{volatile } fm \text{ then } \text{Predicate.single } () \text{ else bot}))$
 ⟨proof⟩

lemma *app_i-ALoad-code*:

$\text{app}_i (\text{ALoad}, P, pc, mxs, T_r, (T_1 \# T_2 \# ST, LT)) =$
 $(T_1 = \text{Integer} \wedge (\text{case } T_2 \text{ of } Ty[] \Rightarrow \text{True} \mid NT \Rightarrow \text{True} \mid - \Rightarrow \text{False}))$
 ⟨proof⟩

lemma *app_i-AStore-code*:

$\text{app}_i (\text{AStore}, P, pc, mxs, T_r, (T_1 \# T_2 \# T_3 \# ST, LT)) =$
 $(T_2 = \text{Integer} \wedge (\text{case } T_3 \text{ of } Ty[] \Rightarrow \text{True} \mid NT \Rightarrow \text{True} \mid - \Rightarrow \text{False}))$
 ⟨proof⟩

lemma *app_i-ALength-code*:

$\text{app}_i (\text{ALength}, P, pc, mxs, T_r, (T_1 \# ST, LT)) =$
 $(\text{case } T_1 \text{ of } Ty[] \Rightarrow \text{True} \mid NT \Rightarrow \text{True} \mid - \Rightarrow \text{False})$
 ⟨proof⟩

lemma *app_i-BinOpInstr-code*:

$\text{app}_i (\text{BinOpInstr } \text{bop}, P, pc, mxs, T_r, (T_2 \# T_1 \# ST, LT)) =$
 $\text{Predicate.holds } (\text{Predicate.bind } (WTrt\text{-binop-}i\text{-}i\text{-}i\text{-}i\text{-}o \text{ } P \ T_1 \text{ bop } T_2) (\lambda T. \text{Predicate.single } ()))$
 ⟨proof⟩

lemma *app_i-Invoke-code*:

```

appi (Invoke M n, P, pc, mxs, Tr, (ST,LT)) =
(n < length ST ∧
(ST!n ≠ NT →
(case class-type-of' (ST ! n) of Some C ⇒
  Predicate.holds (Predicate.bind (Method-i-i-i-o-o-o P C M)
    (λ(Ts, -). if P ⊢ rev (take n ST) [≤] Ts then Predicate.single () else
bot))
| - ⇒ False)))
⟨proof⟩

```

lemma *app_i-Throw-code*:

```

appi (ThrowExc, P, pc, mxs, Tr, (T#ST,LT)) =
(case T of NT ⇒ True | Class C ⇒ P ⊢ C ≤* Throwable | - ⇒ False)
⟨proof⟩

```

lemmas *app_i-code* [code] =

```

appi-Load appi-Store appi-Push
appi-Getfield-code appi-Putfield-code appi-CAS-code
appi-New appi-NewArray
appi-ALoad-code appi-AStore-code appi-ALength-code
appi-Checkcast appi-InstanceOf
appi-Pop appi-Dup appi-Swap appi-BinOpInstr-code appi-IfFalse appi-Goto
appi-Return appi-Throw-code appi-Invoke-code appi-MEnter appi-MExit
appi-default

```

end

6.3 The Bytecode Verifier

theory *BVSpec*

imports

Effect

begin

This theory contains a specification of the BV. The specification describes correct typings of method bodies; it corresponds to type *checking*.

definition *check-types* :: 'm prog ⇒ nat ⇒ nat ⇒ ty_i' err list ⇒ bool

where

check-types P mxs mxl τs ≡ set τs ⊆ states P mxs mxl

— An instruction is welltyped if it is applicable and its effect

— is compatible with the type at all successor instructions:

definition *wt-instr* :: ['m prog,ty,nat,pc,ex-table,'addr instr,pc,ty_m] ⇒ bool

(⟨-, -, -, - ⊢ -, - :: -⟩ [60,0,0,0,0,0,0,61] 60)

where

P, T, mxs, mpc, xt ⊢ i, pc :: τs ≡

app i P mxs T pc mpc xt (τs!pc) ∧

(∀ (pc', τ') ∈ set (eff i P pc xt (τs!pc)). P ⊢ τ' ≤' τs!pc')

— The type at pc=0 conforms to the method calling convention:

definition *wt-start* :: ['m prog, cname, ty list, nat, ty_m] ⇒ bool

where

$$\begin{aligned} & wt\text{-start } P \ C \ Ts \ mxl_0 \ \tau s \equiv \\ & P \vdash \text{Some } ([], OK \ (\text{Class } C) \# \text{map } OK \ Ts @ \text{replicate } mxl_0 \ Err) \leq' \ \tau s!0 \end{aligned}$$

- A method is welltyped if the body is not empty,
- if the method type covers all instructions and mentions
- declared classes only, if the method calling convention is respected, and
- if all instructions are welltyped.

definition $wt\text{-method} :: ['m \ prog, cname, ty \ list, ty, nat, nat, 'addr \ instr \ list, ex\text{-table}, ty_m] \Rightarrow bool$

where

$$\begin{aligned} & wt\text{-method } P \ C \ Ts \ T_r \ mxs \ mxl_0 \ is \ xt \ \tau s \equiv \\ & 0 < size \ is \wedge size \ \tau s = size \ is \wedge \\ & check\text{-types } P \ mxs \ (1 + size \ Ts + mxl_0) \ (\text{map } OK \ \tau s) \wedge \\ & wt\text{-start } P \ C \ Ts \ mxl_0 \ \tau s \wedge \\ & (\forall pc < size \ is. P, T_r, mxs, size \ is, xt \vdash is!pc, pc :: \tau s) \end{aligned}$$

- A program is welltyped if it is wellformed and all methods are welltyped

definition $wf\text{-jvm}\text{-prog}\text{-phi} :: ty_P \Rightarrow 'addr \ jvm\text{-prog} \Rightarrow bool \ (\langle wf'\text{-jvm}'\text{-prog}\text{-}\rangle)$

where

$$\begin{aligned} & wf\text{-jvm}\text{-prog}_{\Phi} \equiv \\ & wf\text{-prog } (\lambda P \ C \ (M, Ts, T_r, (mxs, mxl_0, is, xt)). \\ & \quad wt\text{-method } P \ C \ Ts \ T_r \ mxs \ mxl_0 \ is \ xt \ (\Phi \ C \ M)) \end{aligned}$$

definition $wf\text{-jvm}\text{-prog} :: 'addr \ jvm\text{-prog} \Rightarrow bool$

where

$$wf\text{-jvm}\text{-prog } P \equiv \exists \Phi. wf\text{-jvm}\text{-prog}_{\Phi} \ P$$

lemma $wt\text{-jvm}\text{-prog}D$:

$$wf\text{-jvm}\text{-prog}_{\Phi} \ P \Longrightarrow \exists wt. wf\text{-prog } wt \ P \langle proof \rangle$$

lemma $wt\text{-jvm}\text{-prog}\text{-impl}\text{-wt}\text{-instr}$:

$$\begin{aligned} & \llbracket wf\text{-jvm}\text{-prog}_{\Phi} \ P; \\ & \quad P \vdash C \ sees \ M : Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C; pc < size \ ins \rrbracket \\ & \Longrightarrow P, T, mxs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M \langle proof \rangle \end{aligned}$$

lemma $wt\text{-jvm}\text{-prog}\text{-impl}\text{-wt}\text{-start}$:

$$\begin{aligned} & \llbracket wf\text{-jvm}\text{-prog}_{\Phi} \ P; \\ & \quad P \vdash C \ sees \ M : Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C \rrbracket \Longrightarrow \\ & 0 < size \ ins \wedge wt\text{-start } P \ C \ Ts \ mxl_0 \ (\Phi \ C \ M) \langle proof \rangle \end{aligned}$$

end

6.4 BV Type Safety Invariant

theory $BVConform$

imports

$BVSpec$

$../JVM/JVMExec$

begin

context $JVM\text{-heap}\text{-base}$ **begin**

definition $confT :: 'c \ prog \Rightarrow 'heap \Rightarrow 'addr \ val \Rightarrow ty \ err \Rightarrow bool$

$$(\langle -, - \vdash - : \leq_T \rightarrow [51, 51, 51, 51] \ 50)$$

where

$$P, h \vdash v : \leq_T \ E \equiv case \ E \ of \ Err \Rightarrow True \mid OK \ T \Rightarrow P, h \vdash v : \leq \ T$$

notation (*ASCII*)

$\text{confT } (\langle -, - \mid - - : \leq T \rightarrow [51, 51, 51, 51] 50)$

abbreviation $\text{confTs} :: 'c \text{ prog} \Rightarrow 'heap \Rightarrow 'addr \text{ val list} \Rightarrow ty_l \Rightarrow bool$

$(\langle -, - \vdash - : \leq \top \rangle \rightarrow [51, 51, 51, 51] 50)$

where

$P, h \vdash vs : \leq \top \text{ Ts} \equiv \text{list-all2 } (\text{confT } P h) vs \text{ Ts}$

notation (*ASCII*)

$\text{confTs } (\langle -, - \mid - - : \leq T \rangle \rightarrow [51, 51, 51, 51] 50)$

definition $\text{conf-f} :: 'addr \text{ jvm-prog} \Rightarrow 'heap \Rightarrow ty_i \Rightarrow 'addr \text{ bytecode} \Rightarrow 'addr \text{ frame} \Rightarrow bool$

where

$\text{conf-f } P h \equiv \lambda(ST, LT) \text{ is } (stk, loc, C, M, pc). P, h \vdash stk : \leq ST \wedge P, h \vdash loc : \leq \top LT \wedge pc < \text{size is}$

primrec $\text{conf-fs} :: ['addr \text{ jvm-prog}, 'heap, ty_P, mname, nat, ty, 'addr \text{ frame list}] \Rightarrow bool$

where

$\text{conf-fs } P h \Phi M_0 n_0 T_0 [] = True$

| $\text{conf-fs } P h \Phi M_0 n_0 T_0 (f \# frs) =$

$(\text{let } (stk, loc, C, M, pc) = f \text{ in}$

$(\exists ST LT Ts T mxs mxl_0 \text{ is } xt.$

$\Phi C M ! pc = \text{Some } (ST, LT) \wedge$

$(P \vdash C \text{ sees } M : Ts \rightarrow T = [(mxs, mxl_0, is, xt)] \text{ in } C) \wedge$

$(\exists Ts' T' D m D'.$

$\text{is!pc} = (\text{Invoke } M_0 n_0) \wedge \text{class-type-of}' (ST!n_0) = [D] \wedge P \vdash D \text{ sees } M_0 : Ts' \rightarrow T' = m \text{ in } D'$

$\wedge P \vdash T_0 \leq T') \wedge$

$\text{conf-f } P h (ST, LT) \text{ is } f \wedge \text{conf-fs } P h \Phi M (\text{size } Ts) T \text{ frs}))$

primrec $\text{conf-xcp} :: 'addr \text{ jvm-prog} \Rightarrow 'heap \Rightarrow 'addr \text{ option} \Rightarrow 'addr \text{ instr} \Rightarrow bool$ **where**

$\text{conf-xcp } P h \text{ None } i = True$

| $\text{conf-xcp } P h [a] \quad i = (\exists D. \text{typeof-addr } h a = [\text{Class-type } D] \wedge P \vdash D \leq^* \text{Throwable} \wedge$

$(\forall D'. P \vdash D \leq^* D' \longrightarrow \text{is-relevant-class } i P D'))$

end

context *JVM-heap-conf-base* **begin**

definition $\text{correct-state} :: [ty_P, 'thread-id, ('addr, 'heap) \text{ jvm-state}] \Rightarrow bool$

where

$\text{correct-state } \Phi t \equiv \lambda(xp, h, frs).$

$P, h \vdash t \sqrt{t} \wedge h\text{conf } h \wedge \text{preallocated } h \wedge$

$(\text{case } frs \text{ of}$

$[] \Rightarrow True$

| $(f \# fs) \Rightarrow$

$(\text{let } (stk, loc, C, M, pc) = f$

$\text{in } \exists Ts T mxs mxl_0 \text{ is } xt \tau.$

$(P \vdash C \text{ sees } M : Ts \rightarrow T = [(mxs, mxl_0, is, xt)] \text{ in } C) \wedge$

$\Phi C M ! pc = \text{Some } \tau \wedge$

$\text{conf-f } P h \tau \text{ is } f \wedge \text{conf-fs } P h \Phi M (\text{size } Ts) T \text{ fs} \wedge$

$\text{conf-xcp } P h xp (\text{is } ! pc))$

notation

correct-state $\langle \vdash \dashv \! \! \! \surd \rangle$ [61,0,0] 61)

notation (*ASCII*)

correct-state $\langle \vdash \! \! \! \dashv \! \! \! \surd \rangle$ [61,0,0] 61)

end

context *JVM-heap-base* **begin**

lemma *conf-f-def2*:

conf-f $P\ h\ (ST,LT)$ is $(stk,loc,C,M,pc) \equiv$
 $P, h \vdash stk\ [:\leq] ST \wedge P, h \vdash loc\ [:\leq_{\top}] LT \wedge pc < size$ is
 $\langle proof \rangle$

6.4.1 Values and \top

lemma *confT-Err* [*iff*]: $P, h \vdash x\ [:\leq_{\top}] Err$

$\langle proof \rangle$

lemma *confT-OK* [*iff*]: $P, h \vdash x\ [:\leq_{\top}] OK\ T = (P, h \vdash x\ [:\leq] T)$

$\langle proof \rangle$

lemma *confT-cases*:

$P, h \vdash x\ [:\leq_{\top}] X = (X = Err \vee (\exists T. X = OK\ T \wedge P, h \vdash x\ [:\leq] T))$
 $\langle proof \rangle$

lemma *confT-widen* [*intro?*, *trans*]:

$\llbracket P, h \vdash x\ [:\leq_{\top}] T; P \vdash T \leq_{\top} T' \rrbracket \implies P, h \vdash x\ [:\leq_{\top}] T'$
 $\langle proof \rangle$

end

context *JVM-heap* **begin**

lemma *confT-heap* [*intro?*, *trans*]:

$\llbracket P, h \vdash x\ [:\leq_{\top}] T; h \sqsubseteq h' \rrbracket \implies P, h' \vdash x\ [:\leq_{\top}] T$
 $\langle proof \rangle$

end

6.4.2 Stack and Registers

context *JVM-heap-base* **begin**

lemma *confTs-Cons1* [*iff*]:

$P, h \vdash x \# xs\ [:\leq_{\top}] Ts = (\exists z\ zs. Ts = z \# zs \wedge P, h \vdash x\ [:\leq_{\top}] z \wedge list-all2\ (confT\ P\ h)\ xs\ zs)$
 $\langle proof \rangle$

lemma *confTs-confT-sup*:

$\llbracket P, h \vdash loc\ [:\leq_{\top}] LT; n < size\ LT; LT!n = OK\ T; P \vdash T \leq T' \rrbracket$
 $\implies P, h \vdash (loc!n)\ [:\leq] T'$
 $\langle proof \rangle$

lemma *confTs-widen* [*intro?*, *trans*]:

$P, h \vdash \text{loc } [:\leq_{\top}] LT \implies P \vdash LT [:\leq_{\top}] LT' \implies P, h \vdash \text{loc } [:\leq_{\top}] LT'$
 ⟨proof⟩

lemma *confTs-map* [iff]:
 $(P, h \vdash \text{vs } [:\leq_{\top}] \text{map OK } Ts) = (P, h \vdash \text{vs } [:\leq] Ts)$
 ⟨proof⟩

lemma (*in -*) *reg-widen-Err*:
 $(P \vdash \text{replicate } n \text{ Err } [:\leq_{\top}] LT) = (LT = \text{replicate } n \text{ Err})$
 ⟨proof⟩

declare *reg-widen-Err* [iff]

lemma *confTs-Err* [iff]:
 $P, h \vdash \text{replicate } n v [:\leq_{\top}] \text{replicate } n \text{ Err}$
 ⟨proof⟩

end

context *JVM-heap* **begin**

lemma *confTs-heat* [intro?]:
 $P, h \vdash \text{loc } [:\leq_{\top}] LT \implies h \sqsubseteq h' \implies P, h' \vdash \text{loc } [:\leq_{\top}] LT$
 ⟨proof⟩

6.4.3 correct-frames

declare *fun-upd-apply*[simp del]

lemma *conf-f-heat*:
 $\llbracket \text{conf-f } P h \Phi M f; h \sqsubseteq h' \rrbracket \implies \text{conf-f } P h' \Phi M f$
 ⟨proof⟩

lemma *conf-fs-heat*:
 $\llbracket \text{conf-fs } P h \Phi M n T_r \text{ frs}; h \sqsubseteq h' \rrbracket \implies \text{conf-fs } P h' \Phi M n T_r \text{ frs}$
 ⟨proof⟩

declare *fun-upd-apply*[simp]

lemma *conf-xcp-heat*:
 $\llbracket \text{conf-xcp } P h \text{ xcp } i; h \sqsubseteq h' \rrbracket \implies \text{conf-xcp } P h' \text{ xcp } i$
 ⟨proof⟩

end

context *JVM-heap-conf-base* **begin**

lemmas *defs1 = correct-state-def conf-f-def wt-instr-def eff-def norm-eff-def app-def xcpt-app-def*

lemma *correct-state-impl-Some-method*:
 $\Phi \vdash t: (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs}) \surd$
 $\implies \exists m \text{ Ts } T. P \vdash C \text{ sees } M: \text{Ts} \rightarrow T = \lfloor m \rfloor \text{ in } C$
 ⟨proof⟩

end

context *JVM-heap-conf-base'* begin

lemma *correct-state-heap-mono*:

$\llbracket \Phi \vdash t: (xcp, h, frs) \checkmark; h \leq h'; hconf\ h' \rrbracket \implies \Phi \vdash t: (xcp, h', frs) \checkmark$
 $\langle proof \rangle$

end

end

6.5 BV Type Safety Proof

theory *BVSpecTypeSafe*

imports

BVConform

../Common/ExternalCallWF

begin

declare *listE-length* [*simp del*]

This theory contains proof that the specification of the bytecode verifier only admits type safe programs.

6.5.1 Preliminaries

Simp and intro setup for the type safety proof:

context *JVM-heap-conf-base* begin

lemmas *widen-rules* [*intro*] = *conf-widen confT-widen confs-widens confTs-widen*

end

6.5.2 Exception Handling

For the *Invoke* instruction the BV has checked all handlers that guard the current *pc*.

lemma *Invoke-handlers*:

match-ex-table *P C pc xt* = *Some (pc', d')* \implies
 $\exists (f, t, D, h, d) \in \text{set } (\text{relevant-entries } P (\text{Invoke } n M) pc xt).$
 $(\text{case } D \text{ of } \text{None} \Rightarrow \text{True} \mid \text{Some } D' \Rightarrow P \vdash C \preceq^* D') \wedge pc \in \{f..<t\} \wedge pc' = h \wedge d' = d$
 $\langle proof \rangle$

lemma *match-is-relevant*:

assumes *rv*: $\bigwedge D'. P \vdash D \preceq^* D' \implies \text{is-relevant-class } (ins ! i) P D'$

assumes *match*: *match-ex-table* *P D pc xt* = *Some (pc', d')*

shows $\exists (f, t, D', h, d) \in \text{set } (\text{relevant-entries } P (ins ! i) pc xt).$ $(\text{case } D' \text{ of } \text{None} \Rightarrow \text{True} \mid \text{Some } D'' \Rightarrow P \vdash D \preceq^* D'') \wedge pc \in \{f..<t\} \wedge pc' = h \wedge d' = d$

$\langle proof \rangle$

context *JVM-heap-conf-base* begin

lemma *exception-step-conform*:
fixes $\sigma' :: ('addr, 'heap) \text{jvm-state}$
assumes $wtp: wf\text{-jvm-prog}_{\Phi} P$
assumes $correct: \Phi \vdash t:([xcp], h, fr \# frs) \checkmark$
shows $\Phi \vdash t:\text{exception-step } P \ xcp \ h \ fr \ frs \checkmark$
 $\langle proof \rangle$

end

6.5.3 Single Instructions

In this subsection we prove for each single (welltyped) instruction that the state after execution of the instruction still conforms. Since we have already handled raised exceptions above, we can now assume that no exception has been raised in this step.

context *JVM-conf-read* **begin**

declare *defs1* [*simp*]

lemma *Invoke-correct*:
fixes $\sigma' :: ('addr, 'heap) \text{jvm-state}$
assumes $wtprog: wf\text{-jvm-prog}_{\Phi} P$
assumes $meth\text{-}C: P \vdash C \text{ sees } M:Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C$
assumes $ins: ins ! pc = \text{Invoke } M' \ n$
assumes $wti: P, T, mxs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M$
assumes $approx: \Phi \vdash t:(None, h, (stk, loc, C, M, pc) \# frs) \checkmark$
assumes $exec: (tas, \sigma) \in exec\text{-instr } (ins!pc) \ P \ t \ h \ stk \ loc \ C \ M \ pc \ frs$
shows $\Phi \vdash t:\sigma \checkmark$
 $\langle proof \rangle$

declare *list-all2-Cons2* [*iff*]

lemma *Return-correct*:
assumes $wtprog: wf\text{-jvm-prog}_{\Phi} P$
assumes $meth: P \vdash C \text{ sees } M:Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C$
assumes $ins: ins ! pc = \text{Return}$
assumes $wt: P, T, mxs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M$
assumes $correct: \Phi \vdash t:(None, h, (stk, loc, C, M, pc) \# frs) \checkmark$
assumes $s': (tas, \sigma') \in exec \ P \ t \ (None, h, (stk, loc, C, M, pc) \# frs)$
shows $\Phi \vdash t:\sigma' \checkmark$
 $\langle proof \rangle$

declare *sup-state-opt-any-Some* [*iff*]

declare *not-Err-eq* [*iff*]

lemma *Load-correct*:
 $\llbracket wf\text{-prog } wt \ P;$
 $P \vdash C \text{ sees } M:Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C;$
 $ins!pc = \text{Load } idx;$
 $P, T, mxs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M;$
 $\Phi \vdash t:(None, h, (stk, loc, C, M, pc) \# frs) \checkmark ;$
 $(tas, \sigma') \in exec \ P \ t \ (None, h, (stk, loc, C, M, pc) \# frs) \rrbracket$
 $\implies \Phi \vdash t:\sigma' \checkmark$

<proof>

declare $[[\text{simproc del: list-to-set-comprehension}]]$

lemma *Store-correct:*

$[[$ *wf-prog wt P;*
 $P \vdash C \text{ sees } M:Ts \rightarrow T = [(m\text{xs}, m\text{x}l_0, \text{ins}, \text{xt})]$ *in C;*
 $\text{ins!pc} = \text{Store idx};$
 $P, T, m\text{xs}, \text{size ins}, \text{xt} \vdash \text{ins!pc}, \text{pc} :: \Phi \ C \ M;$
 $\Phi \vdash t: (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs}) \checkmark;$
 $(\text{tas}, \sigma') \in \text{exec } P \ t \ (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs}) \]$
 $\implies \Phi \vdash t: \sigma' \checkmark$
<proof>

lemma *Push-correct:*

$[[$ *wf-prog wt P;*
 $P \vdash C \text{ sees } M:Ts \rightarrow T = [(m\text{xs}, m\text{x}l_0, \text{ins}, \text{xt})]$ *in C;*
 $\text{ins!pc} = \text{Push } v;$
 $P, T, m\text{xs}, \text{size ins}, \text{xt} \vdash \text{ins!pc}, \text{pc} :: \Phi \ C \ M;$
 $\Phi \vdash t: (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs}) \checkmark;$
 $(\text{tas}, \sigma') \in \text{exec } P \ t \ (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs}) \]$
 $\implies \Phi \vdash t: \sigma' \checkmark$
<proof>

declare $[[\text{simproc add: list-to-set-comprehension}]]$

lemma *Checkcast-correct:*

$[[$ *wf-jvm-prog Φ P;*
 $P \vdash C \text{ sees } M:Ts \rightarrow T = [(m\text{xs}, m\text{x}l_0, \text{ins}, \text{xt})]$ *in C;*
 $\text{ins!pc} = \text{Checkcast } D;$
 $P, T, m\text{xs}, \text{size ins}, \text{xt} \vdash \text{ins!pc}, \text{pc} :: \Phi \ C \ M;$
 $\Phi \vdash t: (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs}) \checkmark;$
 $(\text{tas}, \sigma) \in \text{exec-instr } (\text{ins!pc}) \ P \ t \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs} \]$
 $\implies \Phi \vdash t: \sigma \checkmark$
<proof>

lemma *Instanceof-correct:*

$[[$ *wf-jvm-prog Φ P;*
 $P \vdash C \text{ sees } M:Ts \rightarrow T = [(m\text{xs}, m\text{x}l_0, \text{ins}, \text{xt})]$ *in C;*
 $\text{ins!pc} = \text{Instanceof } \text{Ty};$
 $P, T, m\text{xs}, \text{size ins}, \text{xt} \vdash \text{ins!pc}, \text{pc} :: \Phi \ C \ M;$
 $\Phi \vdash t: (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs}) \checkmark;$
 $(\text{tas}, \sigma) \in \text{exec-instr } (\text{ins!pc}) \ P \ t \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs} \]$
 $\implies \Phi \vdash t: \sigma \checkmark$
<proof>

declare *split-paired-All* $[\text{simp del}]$

end

lemma *widens-Cons* $[\text{iff}]$:

$P \vdash (T \# Ts) [\leq] Us = (\exists z \ zs. Us = z \# zs \wedge P \vdash T \leq z \wedge P \vdash Ts [\leq] zs)$
<proof>

context *heap-conf-base* **begin**

end

context *JVM-conf-read* **begin**

lemma *Getfield-correct*:

assumes *wf*: *wf-prog wt P*

assumes *mC*: $P \vdash C \text{ sees } M:Ts \rightarrow T = [(m\!xs, m\!xl_0, ins, xt)] \text{ in } C$

assumes *i*: $ins!pc = \text{Getfield } F D$

assumes *wt*: $P, T, m\!xs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M$

assumes *cf*: $\Phi \vdash t:(None, h, (stk, loc, C, M, pc)\#frs)\checkmark$

assumes *xc*: $(tas, \sigma') \in exec\text{-instr } (ins!pc) \ P \ t \ h \ stk \ loc \ C \ M \ pc \ frs$

shows $\Phi \vdash t:\sigma'\checkmark$

<proof>

lemma *Putfield-correct*:

assumes *wf*: *wf-prog wt P*

assumes *mC*: $P \vdash C \text{ sees } M:Ts \rightarrow T = [(m\!xs, m\!xl_0, ins, xt)] \text{ in } C$

assumes *i*: $ins!pc = \text{Putfield } F D$

assumes *wt*: $P, T, m\!xs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M$

assumes *cf*: $\Phi \vdash t:(None, h, (stk, loc, C, M, pc)\#frs)\checkmark$

assumes *xc*: $(tas, \sigma') \in exec\text{-instr } (ins!pc) \ P \ t \ h \ stk \ loc \ C \ M \ pc \ frs$

shows $\Phi \vdash t:\sigma'\checkmark$

<proof>

lemma *CAS-correct*:

assumes *wf*: *wf-prog wt P*

assumes *mC*: $P \vdash C \text{ sees } M:Ts \rightarrow T = [(m\!xs, m\!xl_0, ins, xt)] \text{ in } C$

assumes *i*: $ins!pc = \text{CAS } F D$

assumes *wt*: $P, T, m\!xs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M$

assumes *cf*: $\Phi \vdash t:(None, h, (stk, loc, C, M, pc)\#frs)\checkmark$

assumes *xc*: $(tas, \sigma') \in exec\text{-instr } (ins!pc) \ P \ t \ h \ stk \ loc \ C \ M \ pc \ frs$

shows $\Phi \vdash t:\sigma'\checkmark$

<proof>

lemma *New-correct*:

assumes *wf*: *wf-prog wt P*

assumes *meth*: $P \vdash C \text{ sees } M:Ts \rightarrow T = [(m\!xs, m\!xl_0, ins, xt)] \text{ in } C$

assumes *ins*: $ins!pc = \text{New } X$

assumes *wt*: $P, T, m\!xs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M$

assumes *conf*: $\Phi \vdash t:(None, h, (stk, loc, C, M, pc)\#frs)\checkmark$

assumes *no-x*: $(tas, \sigma) \in exec\text{-instr } (ins!pc) \ P \ t \ h \ stk \ loc \ C \ M \ pc \ frs$

shows $\Phi \vdash t:\sigma \checkmark$

<proof>

lemma *Goto-correct*:

$\llbracket wf\text{-prog } wt \ P;$

$P \vdash C \text{ sees } M:Ts \rightarrow T = [(m\!xs, m\!xl_0, ins, xt)] \text{ in } C;$

$ins \ ! \ pc = \text{Goto } branch;$

$P, T, m\!xs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M;$

$\Phi \vdash t:(None, h, (stk, loc, C, M, pc)\#frs)\checkmark;$

$(tas, \sigma') \in \text{exec } P \ t \ (None, h, (stk, loc, C, M, pc) \# frs) \]$
 $\implies \Phi \vdash t : \sigma' \checkmark$
 <proof>

declare [[*simproc del: list-to-set-comprehension*]]

lemma *IfFalse-correct:*

[[*wf-prog wt P;*
 $P \vdash C \text{ sees } M : Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C;$
 $ins \ ! \ pc = \text{IfFalse branch};$
 $P, T, mxs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M;$
 $\Phi \vdash t : (None, h, (stk, loc, C, M, pc) \# frs) \checkmark;$
 $(tas, \sigma') \in \text{exec } P \ t \ (None, h, (stk, loc, C, M, pc) \# frs) \]$
 $\implies \Phi \vdash t : \sigma' \checkmark$
 <proof>

declare [[*simproc add: list-to-set-comprehension*]]

lemma *BinOp-correct:*

[[*wf-prog wt P;*
 $P \vdash C \text{ sees } M : Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C;$
 $ins \ ! \ pc = \text{BinOpInstr bop};$
 $P, T, mxs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M;$
 $\Phi \vdash t : (None, h, (stk, loc, C, M, pc) \# frs) \checkmark;$
 $(tas, \sigma') \in \text{exec } P \ t \ (None, h, (stk, loc, C, M, pc) \# frs) \]$
 $\implies \Phi \vdash t : \sigma' \checkmark$
 <proof>

lemma *Pop-correct:*

[[*wf-prog wt P;*
 $P \vdash C \text{ sees } M : Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C;$
 $ins \ ! \ pc = \text{Pop};$
 $P, T, mxs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M;$
 $\Phi \vdash t : (None, h, (stk, loc, C, M, pc) \# frs) \checkmark;$
 $(tas, \sigma') \in \text{exec } P \ t \ (None, h, (stk, loc, C, M, pc) \# frs) \]$
 $\implies \Phi \vdash t : \sigma' \checkmark$
 <proof>

lemma *Dup-correct:*

[[*wf-prog wt P;*
 $P \vdash C \text{ sees } M : Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C;$
 $ins \ ! \ pc = \text{Dup};$
 $P, T, mxs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M;$
 $\Phi \vdash t : (None, h, (stk, loc, C, M, pc) \# frs) \checkmark;$
 $(tas, \sigma') \in \text{exec } P \ t \ (None, h, (stk, loc, C, M, pc) \# frs) \]$
 $\implies \Phi \vdash t : \sigma' \checkmark$
 <proof>

lemma *Swap-correct:*

[[*wf-prog wt P;*
 $P \vdash C \text{ sees } M : Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C;$
 $ins \ ! \ pc = \text{Swap};$
 $P, T, mxs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M;$
 $\Phi \vdash t : (None, h, (stk, loc, C, M, pc) \# frs) \checkmark;$

$(tas, \sigma') \in exec\ P\ t\ (None, h, (stk, loc, C, M, pc) \# frs) \]$
 $\implies \Phi \vdash t : \sigma' \checkmark$
 $\langle proof \rangle$

declare $[[simproc\ del:\ list\ to\ set\ comprehension]]$

lemma *Throw-correct:*

$[[\ wf\ prog\ wt\ P;$
 $P \vdash C\ sees\ M : Ts \rightarrow T = [(mxs, mxl_0, ins, xt)]\ in\ C;$
 $ins\ !\ pc = ThrowExc;$
 $P, T, mxs, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M;$
 $\Phi \vdash t : (None, h, (stk, loc, C, M, pc) \# frs) \checkmark;$
 $(tas, \sigma') \in exec\ instr\ (ins!pc)\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs \]$
 $\implies \Phi \vdash t : \sigma' \checkmark$
 $\langle proof \rangle$

declare $[[simproc\ add:\ list\ to\ set\ comprehension]]$

lemma *NewArray-correct:*

assumes $wf:$ $wf\ prog\ wt\ P$
assumes $meth:$ $P \vdash C\ sees\ M : Ts \rightarrow T = [(mxs, mxl_0, ins, xt)]\ in\ C$
assumes $ins:$ $ins!pc = NewArray\ X$
assumes $wt:$ $P, T, mxs, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$
assumes $conf:$ $\Phi \vdash t : (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$
assumes $no-x:$ $(tas, \sigma) \in exec\ instr\ (ins!pc)\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$
shows $\Phi \vdash t : \sigma \checkmark$
 $\langle proof \rangle$

lemma *ALoad-correct:*

assumes $wf:$ $wf\ prog\ wt\ P$
assumes $meth:$ $P \vdash C\ sees\ M : Ts \rightarrow T = [(mxs, mxl_0, ins, xt)]\ in\ C$
assumes $ins:$ $ins!pc = ALoad$
assumes $wt:$ $P, T, mxs, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$
assumes $conf:$ $\Phi \vdash t : (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$
assumes $no-x:$ $(tas, \sigma) \in exec\ instr\ (ins!pc)\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$
shows $\Phi \vdash t : \sigma \checkmark$
 $\langle proof \rangle$

lemma *AStore-correct:*

assumes $wf:$ $wf\ prog\ wt\ P$
assumes $meth:$ $P \vdash C\ sees\ M : Ts \rightarrow T = [(mxs, mxl_0, ins, xt)]\ in\ C$
assumes $ins:$ $ins!pc = AStore$
assumes $wt:$ $P, T, mxs, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$
assumes $conf:$ $\Phi \vdash t : (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$
assumes $no-x:$ $(tas, \sigma) \in exec\ instr\ (ins!pc)\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$
shows $\Phi \vdash t : \sigma \checkmark$
 $\langle proof \rangle$

lemma *ALength-correct:*

assumes $wf:$ $wf\ prog\ wt\ P$
assumes $meth:$ $P \vdash C\ sees\ M : Ts \rightarrow T = [(mxs, mxl_0, ins, xt)]\ in\ C$
assumes $ins:$ $ins!pc = ALength$
assumes $wt:$ $P, T, mxs, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$

assumes *conf*: $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$
assumes *no-x*: $(tas, \sigma) \in exec\text{-instr} (ins!pc) P t h stk loc C M pc frs$
shows $\Phi \vdash t: \sigma \checkmark$
 ⟨*proof*⟩

lemma *MEnter-correct*:

assumes *wf*: *wf-prog wt P*
assumes *meth*: $P \vdash C \text{ sees } M: Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C$
assumes *ins*: $ins!pc = MEnter$
assumes *wt*: $P, T, mxs, size\ ins, xt \vdash ins!pc, pc :: \Phi C M$
assumes *conf*: $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$
assumes *no-x*: $(tas, \sigma) \in exec\text{-instr} (ins!pc) P t h stk loc C M pc frs$
shows $\Phi \vdash t: \sigma \checkmark$
 ⟨*proof*⟩

lemma *MExit-correct*:

assumes *wf*: *wf-prog wt P*
assumes *meth*: $P \vdash C \text{ sees } M: Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C$
assumes *ins*: $ins!pc = MExit$
assumes *wt*: $P, T, mxs, size\ ins, xt \vdash ins!pc, pc :: \Phi C M$
assumes *conf*: $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$
assumes *no-x*: $(tas, \sigma) \in exec\text{-instr} (ins!pc) P t h stk loc C M pc frs$
shows $\Phi \vdash t: \sigma \checkmark$
 ⟨*proof*⟩

The next theorem collects the results of the sections above, i.e. exception handling and the execution step for each instruction. It states type safety for single step execution: in welltyped programs, a conforming state is transformed into another conforming state when one instruction is executed.

theorem *instr-correct*:

$\llbracket wf\text{-jvm-prog}_{\Phi} P;$
 $P \vdash C \text{ sees } M: Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C;$
 $(tas, \sigma') \in exec\ P t (None, h, (stk, loc, C, M, pc) \# frs);$
 $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark \rrbracket$
 $\implies \Phi \vdash t: \sigma' \checkmark$
 ⟨*proof*⟩

declare *defs1* [*simp del*]

end

6.5.4 Main

lemma (in *JVM-conf-read*) *BV-correct-1* [*rule-format*]:

$\bigwedge \sigma. \llbracket wf\text{-jvm-prog}_{\Phi} P; \Phi \vdash t: \sigma \checkmark \rrbracket \implies P, t \vdash \sigma \text{-tas-jvm} \rightarrow \sigma' \longrightarrow \Phi \vdash t: \sigma' \checkmark$
 ⟨*proof*⟩

theorem (in *JVM-progress*) *progress*:

assumes *wt*: *wf-jvm-prog_Φ P*
and *cs*: $\Phi \vdash t: (xcp, h, f \# frs) \checkmark$
shows $\exists ta\ \sigma'. P, t \vdash (xcp, h, f \# frs) \text{-ta-jvm} \rightarrow \sigma'$
 ⟨*proof*⟩

lemma (in *JVM-heap-conf*) *BV-correct-initial*:
shows $\llbracket wf\text{-}jvm\text{-}prog_{\Phi} P; start\text{-}heap\text{-}ok; P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor m \rfloor \text{ in } D; P, start\text{-}heap \vdash vs \lfloor \leq \rfloor Ts \rrbracket$
 $\implies \Phi \vdash start\text{-}tid: JVM\text{-}start\text{-}state' P C M vs \checkmark$
 $\langle proof \rangle$

end

6.6 Welltyped Programs produce no Type Errors

theory *BVNoTypeError*

imports

../JVM/JVMDefensive

BVSpecTypeSafe

begin

lemma *wt-jvm-prog-states*:

$\llbracket wf\text{-}jvm\text{-}prog_{\Phi} P; P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor (m\text{-}xs, m\text{-}xl, ins, et) \rfloor \text{ in } C;$

$\Phi C M ! pc = \tau; pc < size\ ins \rrbracket$

$\implies OK \tau \in states\ P\ m\text{-}xs\ (1 + size\ Ts + m\text{-}xl) \langle proof \rangle$

context *JVM-heap-conf-base'* **begin**

declare *is-IntI* [*simp*, *intro*]

declare *is-BoolI* [*simp*, *intro*]

declare *is-Refl* [*simp*]

The main theorem: welltyped programs do not produce type errors if they are started in a conformant state.

theorem *no-type-error*:

assumes *welltyped*: $wf\text{-}jvm\text{-}prog_{\Phi} P$ **and** *conforms*: $\Phi \vdash t: \sigma \checkmark$

shows $exec\text{-}d\ P\ t\ \sigma \neq TypeError \langle proof \rangle \langle proof \rangle \langle proof \rangle \langle proof \rangle$

6.7 Progress result for both of the multithreaded JVMs

theory *BVProgressThreaded*

imports

../Framework/FWProgress

../Framework/FWLTS

BVNoTypeError

../JVM/JVMThreaded

begin

lemma (in *JVM-heap-conf-base'*) *mexec-eq-mexecd*:

$\llbracket wf\text{-}jvm\text{-}prog_{\Phi} P; \Phi \vdash t: (xcp, h, frs) \checkmark \rrbracket \implies mexec\ P\ t\ ((xcp, frs), h) = mexecd\ P\ t\ ((xcp, frs), h)$
 $\langle proof \rangle$

context *JVM-heap-conf-base* **begin**

abbreviation

correct-state-ts :: $ty_P \Rightarrow ('addr, 'thread\text{-}id, 'addr\ jvm\text{-}thread\text{-}state)\ thread\text{-}info \Rightarrow 'heap \Rightarrow bool$

where

correct-state-ts $\Phi \equiv ts\text{-ok} (\lambda t (xcp, frstls) h. \Phi \vdash t: (xcp, h, frstls) \checkmark)$

lemma *correct-state-ts-thread-conf*:

correct-state-ts Φ (*thr* *s*) (*shr* *s*) \implies *thread-conf* *P* (*thr* *s*) (*shr* *s*)
 \langle *proof* \rangle

lemma *invoke-new-thread*:

assumes *wf-jvm-prog* Φ *P*
and $P \vdash C$ *sees* $M: Ts \rightarrow T = [(mxs, mxl0, ins, xt)]$ *in* *C*
and $ins \neq pc = \text{Invoke } \text{Type.start } 0$
and $P, T, mxs, size \ ins, xt \vdash ins \neq pc, pc :: \Phi \ C \ M$
and $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$
and *typeof-addr* h (*thread-id2addr* *a*) = $[Class\text{-type } D]$
and $P \vdash D \preceq^* \text{Thread}$
and $P \vdash D$ *sees* $run: [] \rightarrow Void = [(mxs', mxl0', ins', xt')]$ *in* D'
shows $\Phi \vdash a: (None, h, ([], \text{Addr } (\text{thread-id2addr } a) \# \text{replicate } mxl0' \ \text{undefined-value}, D', run, 0)) \checkmark$
 \langle *proof* \rangle

lemma *exec-new-threadE*:

assumes *wf-jvm-prog* Φ *P*
and $P, t \vdash \text{Normal } \sigma \text{ --ta--jvmd} \rightarrow \text{Normal } \sigma'$
and $\Phi \vdash t: \sigma \checkmark$
and $\{ta\}_t \neq []$
obtains $h \ frs \ a \ stk \ loc \ C \ M \ pc \ Ts \ T \ mxs \ mxl0 \ ins \ xt \ M' \ n \ Ta \ ta' \ va \ Us \ Us' \ U \ m' \ D'$
where $\sigma = (None, h, (stk, loc, C, M, pc) \# frs)$
and $(ta, \sigma') \in \text{exec } P \ t \ (None, h, (stk, loc, C, M, pc) \# frs)$
and $P \vdash C$ *sees* $M: Ts \rightarrow T = [(mxs, mxl0, ins, xt)]$ *in* *C*
and $stk \neq n = \text{Addr } a$
and $ins \neq pc = \text{Invoke } M' \ n$
and $n < \text{length } stk$
and *typeof-addr* $h \ a = [Ta]$
and *is-native* $P \ Ta \ M'$
and $ta = \text{extTA2JVM } P \ ta'$
and $\sigma' = \text{extRet2JVM } n \ m' \ stk \ loc \ C \ M \ pc \ frs \ va$
and $(ta', va, m') \in \text{red-external-aggr } P \ t \ a \ M' \ (\text{rev } (\text{take } n \ stk)) \ h$
and $\text{map } \text{typeof}_h \ (\text{rev } (\text{take } n \ stk)) = \text{map } \text{Some } Us$
and $P \vdash \text{class-type-of } Ta$ *sees* $M': Us' \rightarrow U = \text{Native}$ *in* D'
and $D' \cdot M'(Us') :: U$
and $P \vdash Us \ [\leq] \ Us'$
 \langle *proof* \rangle

end

context *JVM-conf-read* **begin**

lemma *correct-state-new-thread*:

assumes *wf*: *wf-jvm-prog* Φ *P*
and *red*: $P, t \vdash \text{Normal } \sigma \text{ --ta--jvmd} \rightarrow \text{Normal } \sigma'$
and *cs*: $\Phi \vdash t: \sigma \checkmark$
and *nt*: *NewThread* $t'' (xcp, frs) \ h'' \in \text{set } \{ta\}_t$
shows $\Phi \vdash t'': (xcp, h'', frs) \checkmark$
 \langle *proof* \rangle

lemma *correct-state-heap-change*:

assumes *wf*: $wf\text{-jvm-prog}_{\Phi} P$

and *red*: $P, t \vdash \text{Normal} (xcp, h, frs) \text{--}ta\text{--}jvmd \rightarrow \text{Normal} (xcp', h', frs')$

and *cs*: $\Phi \vdash t: (xcp, h, frs) \checkmark$

and *cs''*: $\Phi \vdash t'': (xcp'', h, frs'') \checkmark$

shows $\Phi \vdash t'': (xcp'', h', frs'') \checkmark$

$\langle proof \rangle$

lemma *lifting-wf-correct-state-d*:

$wf\text{-jvm-prog}_{\Phi} P \implies \text{lifting-wf JVM-final} (mexecd P) (\lambda t (xcp, frs) h. \Phi \vdash t: (xcp, h, frs) \checkmark)$

$\langle proof \rangle$

lemma *lifting-wf-correct-state*:

assumes *wf*: $wf\text{-jvm-prog}_{\Phi} P$

shows $\text{lifting-wf JVM-final} (mexec P) (\lambda t (xcp, frs) h. \Phi \vdash t: (xcp, h, frs) \checkmark)$

$\langle proof \rangle$

lemmas *preserves-correct-state* = $FWLiftingSem.lifting-wf.RedT\text{-preserves}[OF \text{lifting-wf-correct-state}]$

lemmas *preserves-correct-state-d* = $FWLiftingSem.lifting-wf.RedT\text{-preserves}[OF \text{lifting-wf-correct-state-d}]$

end

context *JVM-heap-conf-base* **begin**

definition *correct-jvm-state* :: $ty_P \Rightarrow ('addr, 'thread-id, 'addr \text{ jvm-thread-state}, 'heap, 'addr) \text{ state set}$

where

correct-jvm-state Φ

= $\{s. \text{correct-state-ts } \Phi (thr s) (shr s) \wedge \text{lock-thread-ok} (locks s) (thr s)\}$

end

context *JVM-heap-conf* **begin**

lemma *correct-jvm-state-initial*:

assumes *wf*: $wf\text{-jvm-prog}_{\Phi} P$

and *wf-start*: $wf\text{-start-state } P C M vs$

shows $\text{JVM-start-state } P C M vs \in \text{correct-jvm-state } \Phi$

$\langle proof \rangle$

end

context *JVM-conf-read* **begin**

lemma *invariant3p-correct-jvm-state-mexecdT*:

assumes *wf*: $wf\text{-jvm-prog}_{\Phi} P$

shows $\text{invariant3p} (mexecdT P) (\text{correct-jvm-state } \Phi)$

$\langle proof \rangle$

lemma *invariant3p-correct-jvm-state-mexecT*:

assumes *wf*: $wf\text{-jvm-prog}_{\Phi} P$

shows $\text{invariant3p} (mexecT P) (\text{correct-jvm-state } \Phi)$

$\langle proof \rangle$

lemma *correct-jvm-state-preserved*:

assumes $wf: wf\text{-jvm-prog}_{\Phi} P$
and $correct: s \in correct\text{-jvm-state } \Phi$
and $red: P \vdash s \multimap_{ttas} \rightarrow_{jvm^*} s'$
shows $s' \in correct\text{-jvm-state } \Phi$
 $\langle proof \rangle$

theorem *jvm-typesafe*:

assumes $wf: wf\text{-jvm-prog}_{\Phi} P$
and $start: wf\text{-start-state } P C M vs$
and $exec: P \vdash JVM\text{-start-state } P C M vs \multimap_{ttas} \rightarrow_{jvm^*} s'$
shows $s' \in correct\text{-jvm-state } \Phi$
 $\langle proof \rangle$

end

declare (in *JVM-typesafe*) *split-paired-Ex* [*simp del*]

context *JVM-heap-conf-base'* **begin**

lemma *execd-NewThread-Thread-Object*:

assumes $wf: wf\text{-jvm-prog}_{\Phi} P$
and $conf: \Phi \vdash t': \sigma \checkmark$
and $red: P, t' \vdash Normal \sigma \text{-} ta\text{-}jvmd \rightarrow Normal \sigma'$
and $nt: NewThread t x m \in set \{ta\}_t$
shows $\exists C. typeof\text{-addr } (fst (snd \sigma')) (thread\text{-id2addr } t) = \lfloor Class\text{-type } C \rfloor \wedge P \vdash Class C \leq Class$
 $Thread$
 $\langle proof \rangle$

lemma *mexecdT-NewThread-Thread-Object*:

$\llbracket wf\text{-jvm-prog}_{\Phi} P; correct\text{-state-ts } \Phi (thr s) (shr s); P \vdash s \text{-} t' \triangleright ta \rightarrow_{jvmd} s'; NewThread t x m \in set \{ta\}_t \rrbracket$
 $\implies \exists C. typeof\text{-addr } (shr s') (thread\text{-id2addr } t) = \lfloor Class\text{-type } C \rfloor \wedge P \vdash C \preceq^* Thread$
 $\langle proof \rangle$

end

context *JVM-heap* **begin**

lemma *exec-ta-satisfiable*:

assumes $P, t \vdash s \text{-} ta\text{-}jvm \rightarrow s'$
shows $\exists s. exec\text{-mthr.actions-ok } s t ta$
 $\langle proof \rangle$

end

context *JVM-typesafe* **begin**

lemma *execd-wf-progress*:

assumes $wf: wf\text{-jvm-prog}_{\Phi} P$
shows $progress JVM\text{-final } (mexecd P) (execd\text{-mthr.wset-Suspend-ok } P (correct\text{-jvm-state } \Phi))$
 $(is\ progress \text{-} - \text{-} ?wf\text{-state})$
 $\langle proof \rangle$

end

context *JVM-conf-read* begin

lemma *mExecT-eq-mExecdT*:

assumes *wf*: *wf-jvm-prog* Φ *P*

and *cs*: *correct-state-ts* Φ (*thr s*) (*shr s*)

shows $P \vdash s \dashv\rightarrow_{jvm} s' = P \vdash s \dashv\rightarrow_{jvmd} s'$

\langle proof \rangle

lemma *mExecT-eq-mExecdT*:

assumes *wf*: *wf-jvm-prog* Φ *P*

and *ct*: *correct-state-ts* Φ (*thr s*) (*shr s*)

shows $P \vdash s \dashv\rightarrow_{jvm}^* s' = P \vdash s \dashv\rightarrow_{jvmd}^* s'$

\langle proof \rangle

lemma *mExecT-preserves-thread-conf*:

\llbracket *wf-jvm-prog* Φ *P*; *correct-state-ts* Φ (*thr s*) (*shr s*);

$P \vdash s \dashv\rightarrow_{jvm} s'$; *thread-conf* *P* (*thr s*) (*shr s*) \rrbracket

\implies *thread-conf* *P* (*thr s'*) (*shr s'*)

\langle proof \rangle

lemma *mExecT-preserves-thread-conf*:

\llbracket *wf-jvm-prog* Φ *P*; *correct-state-ts* Φ (*thr s*) (*shr s*);

$P \vdash s \dashv\rightarrow_{jvm}^* s'$; *thread-conf* *P* (*thr s*) (*shr s*) \rrbracket

\implies *thread-conf* *P* (*thr s'*) (*shr s'*)

\langle proof \rangle

lemma *wset-Suspend-ok-mExecd-mExec*:

assumes *wf*: *wf-jvm-prog* Φ *P*

shows *exec-mthr.wset-Suspend-ok* *P* (*correct-jvm-state* Φ) = *execd-mthr.wset-Suspend-ok* *P* (*correct-jvm-state* Φ)

\langle proof \rangle

end

context *JVM-typesafe* begin

lemma *exec-wf-progress*:

assumes *wf*: *wf-jvm-prog* Φ *P*

shows *progress JVM-final* (*mExec* *P*) (*exec-mthr.wset-Suspend-ok* *P* (*correct-jvm-state* Φ))

(*is progress* - - ?*wf-state*)

\langle proof \rangle

theorem *mExecd-TypeSafety*:

fixes *ln* :: 'addr \Rightarrow f nat

assumes *wf*: *wf-jvm-prog* Φ *P*

and *s*: $s \in$ *execd-mthr.wset-Suspend-ok* *P* (*correct-jvm-state* Φ)

and *Exec*: $P \vdash s \dashv\rightarrow_{jvmd}^* s'$

and $\neg (\exists t \ ta \ s''. P \vdash s' \dashv\rightarrow_{jvmd} s'')$

and *ts't*: *thr s' t* = $\llbracket ((xcp, frs), ln) \rrbracket$

shows *frs* \neq $\llbracket \rrbracket \vee ln \neq$ *no-wait-locks* $\implies t \in$ *execd-mthr.deadlocked* *P s'*

and $\Phi \vdash t$: (*xcp*, *shr s'*, *frs*) \surd

\langle proof \rangle

theorem *mexec-TypeSafety*:

fixes $ln :: 'addr \Rightarrow f \text{ nat}$

assumes $wf: wf\text{-jvm-prog}_{\Phi} P$

and $s: s \in exec\text{-mthr.wset-Suspend-ok } P \text{ (correct-jvm-state } \Phi)$

and $Exec: P \vdash s \multimap ttas \rightarrow_{jvm^*} s'$

and $\neg (\exists t \ ta \ s''. P \vdash s' \multimap ta \rightarrow_{jvm} s'')$

and $ts't: thr \ s' \ t = \llbracket ((xcp, frs), ln) \rrbracket$

shows $frs \neq [] \vee ln \neq no\text{-wait-locks} \implies t \in multithreaded\text{-base.deadlocked JVM-final (mexec } P) \ s'$

and $\Phi \vdash t: (xcp, shr \ s', frs) \checkmark$

<proof>

lemma *start-mexec-mexecd-commute*:

assumes $wf: wf\text{-jvm-prog}_{\Phi} P$

and $start: wf\text{-start-state } P \ C \ M \ vs$

shows $P \vdash JVM\text{-start-state } P \ C \ M \ vs \multimap ttas \rightarrow_{jvmd^*} s \longleftrightarrow P \vdash JVM\text{-start-state } P \ C \ M \ vs \multimap ttas \rightarrow_{jvm^*} s$

<proof>

theorem *mRtrancl-eq-mRtrancl*:

assumes $wf: wf\text{-jvm-prog}_{\Phi} P$

and $ct: correct\text{-state-ts } \Phi \ (thr \ s) \ (shr \ s)$

shows $exec\text{-mthr.mthr.Rtrancl3p } P \ s \ ttas \longleftrightarrow execd\text{-mthr.mthr.Rtrancl3p } P \ s \ ttas \text{ (is ?lhs } \longleftrightarrow \text{ ?rhs)}$

<proof>

lemma *start-mRtrancl-mRtrancl-commute*:

assumes $wf: wf\text{-jvm-prog}_{\Phi} P$

and $start: wf\text{-start-state } P \ C \ M \ vs$

shows $exec\text{-mthr.mthr.Rtrancl3p } P \ (JVM\text{-start-state } P \ C \ M \ vs) \ ttas \longleftrightarrow execd\text{-mthr.mthr.Rtrancl3p } P \ (JVM\text{-start-state } P \ C \ M \ vs) \ ttas$

<proof>

end

6.7.1 Determinism

context *JVM-heap-conf begin*

lemma *exec-instr-deterministic*:

assumes $wf: wf\text{-prog } wf\text{-md } P$

and $det: deterministic\text{-heap-ops}$

and $exec1: (ta', \sigma') \in exec\text{-instr } i \ P \ t \ (shr \ s) \ stk \ loc \ C \ M \ pc \ frs$

and $exec2: (ta'', \sigma'') \in exec\text{-instr } i \ P \ t \ (shr \ s) \ stk \ loc \ C \ M \ pc \ frs$

and $check: check\text{-instr } i \ P \ (shr \ s) \ stk \ loc \ C \ M \ pc \ frs$

and $aok1: final\text{-thread.actions-ok final } s \ t \ ta'$

and $aok2: final\text{-thread.actions-ok final } s \ t \ ta''$

and $tconf: P, shr \ s \vdash t \checkmark$

shows $ta' = ta'' \wedge \sigma' = \sigma''$

<proof>

lemma *exec-1-deterministic*:

assumes $wf: wf\text{-jvm-prog}_{\Phi} P$

and $det: deterministic\text{-heap-ops}$

and $exec1: P, t \vdash (xcp, shr \ s, frs) \multimap ta' \text{-jvm} \rightarrow \sigma'$

```

and exec2:  $P, t \vdash (xcp, shr\ s, frs) -ta''-jvm \rightarrow \sigma''$ 
and aok1: final-thread.actions-ok final s t ta'
and aok2: final-thread.actions-ok final s t ta''
and conf:  $\Phi \vdash t:(xcp, shr\ s, frs) \checkmark$ 
shows  $ta' = ta'' \wedge \sigma' = \sigma''$ 
<proof>

```

end

context *JVM-conf-read* **begin**

lemma *invariant3p-correct-state-ts*:

```

assumes wf-jvm-prog $\Phi$  P
shows invariant3p (mexecT P) {s. correct-state-ts  $\Phi$  (thr s) (shr s)}
<proof>

```

lemma *mexec-deterministic*:

```

assumes wf: wf-jvm-prog $\Phi$  P
and det: deterministic-heap-ops
shows exec-mthr.deterministic P {s. correct-state-ts  $\Phi$  (thr s) (shr s)}
<proof>

```

end

end

6.8 Preservation of deadlock for the JVMs

theory *JVMDeadlocked*

imports

BVProgressThreaded

begin

context *JVM-progress* **begin**

lemma *must-sync-preserved-d*:

```

assumes wf: wf-jvm-prog $\Phi$  P
and ml: execd-mthr.must-sync P t (xcp, frs) h
and hext: hext h h'
and hconf': hconf h'
and cs:  $\Phi \vdash t:(xcp, h, frs) \checkmark$ 
shows execd-mthr.must-sync P t (xcp, frs) h'
<proof>

```

lemma *can-sync-devreserp-d*:

```

assumes wf: wf-jvm-prog $\Phi$  P
and cl': execd-mthr.can-sync P t (xcp, frs) h' L
and cs:  $\Phi \vdash t:(xcp, h, frs) \checkmark$ 
and hext: hext h h'
and hconf': hconf h'
shows  $\exists L' \subseteq L. \text{execd-mthr.can-sync } P\ t\ (xcp, frs)\ h\ L'$ 
<proof>

```

end

context *JVM-typesafe* **begin**

lemma *execd-preserve-deadlocked*:

assumes *wf*: *wf-jvm-prog* $_{\Phi}$ *P*

shows *preserve-deadlocked JVM-final* (*mexecd P*) *convert-RA* (*correct-jvm-state* Φ)
 \langle *proof* \rangle

end

and now everything again for the aggressive VM

context *JVM-heap-conf-base'* **begin**

lemma *must-lock-d-eq-must-lock*:

\llbracket *wf-jvm-prog* $_{\Phi}$ *P*; $\Phi \vdash t$: (*xcp*, *h*, *frs*) \surd \rrbracket

\implies *execd-mthr.must-sync* *P t* (*xcp*, *frs*) *h* = *exec-mthr.must-sync* *P t* (*xcp*, *frs*) *h*
 \langle *proof* \rangle

lemma *can-lock-d-eq-can-lock*:

\llbracket *wf-jvm-prog* $_{\Phi}$ *P*; $\Phi \vdash t$: (*xcp*, *h*, *frs*) \surd \rrbracket

\implies *execd-mthr.can-sync* *P t* (*xcp*, *frs*) *h L* = *exec-mthr.can-sync* *P t* (*xcp*, *frs*) *h L*
 \langle *proof* \rangle

end

context *JVM-typesafe* **begin**

lemma *exec-preserve-deadlocked*:

assumes *wf*: *wf-jvm-prog* $_{\Phi}$ *P*

shows *preserve-deadlocked JVM-final* (*mexec P*) *convert-RA* (*correct-jvm-state* Φ)
 \langle *proof* \rangle

end

end

6.9 Monotonicity of eff and app

theory *EffectMono*

imports

Effect

begin

declare *not-Err-eq* [*iff*]

declare *widens-trans*[*trans*]

lemma *app_i-mono*:

assumes *wf*: *wf-prog* *p P*

assumes *less*: $P \vdash \tau \leq_i \tau'$

shows *app_i* (*i*, *P*, *mxs*, *mpc*, *rT*, τ) \implies *app_i* (*i*, *P*, *mxs*, *mpc*, *rT*, τ)
 \langle *proof* \rangle

lemma *succs-mono*:

assumes *wf*: *wf-prog* *p* *P* **and** *app_i*: *app_i* (*i*,*P*,*m_{xs}*,*m_{pc}*,*rT*, τ')
shows $P \vdash \tau \leq_i \tau' \implies \text{set } (\text{succs } i \ \tau \ \text{pc}) \subseteq \text{set } (\text{succs } i \ \tau' \ \text{pc})$

<proof>

lemma *app-mono*:

assumes *wf*: *wf-prog* *p* *P*
assumes *less'*: $P \vdash \tau \leq' \tau'$
shows $\text{app } i \ P \ m \ rT \ \text{pc} \ mpc \ xt \ \tau' \implies \text{app } i \ P \ m \ rT \ \text{pc} \ mpc \ xt \ \tau$

<proof>

lemma *eff_i-mono*:

assumes *wf*: *wf-prog* *p* *P*
assumes *less*: $P \vdash \tau \leq_i \tau'$
assumes *app_i*: $\text{app } i \ P \ m \ rT \ \text{pc} \ mpc \ xt \ (\text{Some } \tau')$
assumes *succs*: $\text{succs } i \ \tau \ \text{pc} \neq [] \ \text{succs } i \ \tau' \ \text{pc} \neq []$
shows $P \vdash \text{eff}_i (i, P, \tau) \leq_i \text{eff}_i (i, P, \tau')$

<proof>

end

6.10 The Typing Framework for the JVM

theory *TF-JVM*

imports

../DFA/Typing-Framework-err

EffectMono

BVSpec

../Common/ExternalCallWF

begin

definition *exec* :: '*addr jvm-prog* \Rightarrow *nat* \Rightarrow *ty* \Rightarrow *ex-table* \Rightarrow '*addr instr list* \Rightarrow *ty_i*' *err step-type*

where

exec *G* *m_{xs}* *rT* *et* *bs* \equiv

err-step (*size* *bs*) ($\lambda pc. \text{app } (bs!pc) \ G \ m_{xs} \ rT \ pc \ (\text{size } bs) \ et$) ($\lambda pc. \text{eff } (bs!pc) \ G \ pc \ et$)

locale *JVM-sl* =

fixes *P* :: '*addr jvm-prog* **and** *m_{xs}* **and** *m_{xl₀}*

fixes *T_s* :: *ty list* **and** *is* :: '*addr instr list* **and** *xt* **and** *T_r*

fixes *m_{xl}* **and** *A* **and** *r* **and** *f* **and** *app* **and** *eff* **and** *step*

defines [*simp*]: *m_{xl}* $\equiv 1 + \text{size } T_s + m_{xl_0}$

defines [*simp*]: *A* $\equiv \text{states } P \ m_{xs} \ m_{xl}$

defines [*simp*]: *r* $\equiv \text{JVM-SemiType.le } P \ m_{xs} \ m_{xl}$

defines [*simp*]: *f* $\equiv \text{JVM-SemiType.sup } P \ m_{xs} \ m_{xl}$

defines [*simp*]: *app* $\equiv \lambda pc. \text{Effect.app } (is!pc) \ P \ m_{xs} \ T_r \ pc \ (\text{size } is) \ xt$

defines [*simp*]: *eff* $\equiv \lambda pc. \text{Effect.eff } (is!pc) \ P \ pc \ xt$

defines [*simp*]: *step* $\equiv \text{err-step } (\text{size } is) \ \text{app } \text{eff}$

locale *start-context* = *JVM-sl* +

fixes *p* **and** *C*

assumes wf : $wf\text{-prog } p \ P$
assumes C : $is\text{-class } P \ C$
assumes Ts : $set \ Ts \subseteq \ types \ P$

fixes $first :: ty_i'$ **and** $start$
defines $[simp]$:
 $first \equiv Some \ ([], OK \ (Class \ C) \ \# \ map \ OK \ Ts \ @ \ replicate \ mxl_0 \ Err)$
defines $[simp]$:
 $start \equiv OK \ first \ \# \ replicate \ (size \ is - 1) \ (OK \ None)$

6.10.1 Connecting JVM and Framework

lemma (in $JVM\text{-sl}$) $step\text{-def-exec}$: $step \equiv exec \ P \ mxs \ T_r \ xt \ is$
 $\langle proof \rangle$

lemma $special\text{-ex-swap-lemma}$ $[iff]$:
 $(\exists X. (\exists n. X = A \ n \wedge P \ n) \wedge Q \ X) = (\exists n. Q \ (A \ n) \wedge P \ n)$
 $\langle proof \rangle$

lemma $ex\text{-in-list}$ $[iff]$:
 $(\exists n. ST \in list \ n \ A \wedge n \leq mxs) = (set \ ST \subseteq A \wedge size \ ST \leq mxs)$
 $\langle proof \rangle$

lemma $singleton\text{-list}$:
 $(\exists n. [Class \ C] \in list \ n \ (types \ P) \wedge n \leq mxs) = (is\text{-class } P \ C \wedge 0 < mxs)$
 $\langle proof \rangle$

lemma $set\text{-drop-subset}$:
 $set \ xs \subseteq A \implies set \ (drop \ n \ xs) \subseteq A$
 $\langle proof \rangle$

lemma $Suc\text{-minus-minus-le}$:
 $n < mxs \implies Suc \ (n - (n - b)) \leq mxs$
 $\langle proof \rangle$

lemma $in\text{-listE}$:
 $\llbracket xs \in list \ n \ A; \llbracket size \ xs = n; set \ xs \subseteq A \rrbracket \implies P \rrbracket \implies P$
 $\langle proof \rangle$

declare $is\text{-relevant-entry-def}$ $[simp]$
declare $set\text{-drop-subset}$ $[simp]$

lemma (in $start\text{-context}$) $[simp, intro!]$: $is\text{-class } P \ Throwable$
 $\langle proof \rangle$

declare $option.splits$ $[split \ del]$
declare $option.case\text{-cong}$ $[cong]$
declare $is\text{-type-array}$ $[simp \ del]$

theorem (in $start\text{-context}$) $exec\text{-pres-type}$:
 $pres\text{-type } step \ (size \ is) \ A \langle proof \rangle$
declare $option.case\text{-cong-weak}$ $[cong]$
declare $option.splits$ $[split]$
declare $is\text{-type-array}$ $[simp]$

declare *is-relevant-entry-def* [*simp del*]
declare *set-drop-subset* [*simp del*]

lemma *lesubstep-type-simple*:

$xs \sqsubseteq_{\text{Product.le } (=) \ r} ys \implies \text{set } xs \ \{\sqsubseteq_r\} \ \text{set } ys \langle \text{proof} \rangle$

declare *is-relevant-entry-def* [*simp del*]

lemma *conjI2*: $\llbracket A; A \implies B \rrbracket \implies A \wedge B \langle \text{proof} \rangle$

lemma (in *JVM-sl*) *eff-mono*:

$\llbracket \text{wf-prog } p \ P; \text{pc} < \text{length } is; s \sqsubseteq_{\text{sup-state-opt } P} t; \text{app } pc \ t \rrbracket$
 $\implies \text{set } (\text{eff } pc \ s) \ \{\sqsubseteq_{\text{sup-state-opt } P}\} \ \text{set } (\text{eff } pc \ t) \langle \text{proof} \rangle$

lemma (in *JVM-sl*) *bounded-step*: *bounded step* (*size is*) $\langle \text{proof} \rangle$

theorem (in *JVM-sl*) *step-mono*:

$\text{wf-prog } wf\text{-mb } P \implies \text{mono } r \ \text{step } (\text{size } is) \ A \langle \text{proof} \rangle$

lemma (in *start-context*) *first-in-A* [*iff*]: *OK first* $\in A$
 $\langle \text{proof} \rangle$

lemma (in *JVM-sl*) *wt-method-def2*:

$\text{wt-method } P \ C' \ Ts \ T_r \ mxs \ mxl_0 \ is \ xt \ \tau s =$

$(is \neq [] \wedge$

$\text{size } \tau s = \text{size } is \wedge$

$OK \ ' \ \text{set } \tau s \subseteq \text{states } P \ mxs \ mxl \wedge$

$\text{wt-start } P \ C' \ Ts \ mxl_0 \ \tau s \wedge$

$\text{wt-app-eff } (\text{sup-state-opt } P) \ \text{app } \text{eff } \ \tau s) \langle \text{proof} \rangle$

end

6.11 LBV for the JVM

theory *LBVJVM*

imports

../DFA/Abstract-BV

TF-JVM

begin

type-synonym *prog-cert* = *cname* \Rightarrow *mname* \Rightarrow *ty_i'* *err list*

definition *check-cert* :: *'addr jvm-prog* \Rightarrow *nat* \Rightarrow *nat* \Rightarrow *nat* \Rightarrow *ty_i'* *err list* \Rightarrow *bool*

where

$\text{check-cert } P \ mxs \ mxl \ n \ \text{cert} \equiv \text{check-types } P \ mxs \ mxl \ \text{cert} \wedge \text{size } \text{cert} = n+1 \wedge$
 $(\forall i < n. \ \text{cert}!i \neq \text{Err}) \wedge \text{cert}!n = \text{OK } \text{None}$

definition *lbvjvm* :: *'addr jvm-prog* \Rightarrow *nat* \Rightarrow *nat* \Rightarrow *ty* \Rightarrow *ex-table* \Rightarrow

ty_i' *err list* \Rightarrow *'addr instr list* \Rightarrow *ty_i'* *err* \Rightarrow *ty_i'* *err*

where

$\text{lbvjvm } P \ mxs \ maxr \ T_r \ \text{et } \text{cert } bs \equiv$

$\text{wtl-inst-list } bs \ \text{cert } (\text{JVM-SemiType.sup } P \ mxs \ maxr) \ (\text{JVM-SemiType.le } P \ mxs \ maxr) \ \text{Err } (\text{OK } \text{None}) \ (\text{exec } P \ mxs \ T_r \ \text{et } bs) \ 0$

definition $wt\text{-}lbv :: 'addr\ jvm\text{-}prog \Rightarrow cname \Rightarrow ty\ list \Rightarrow ty \Rightarrow nat \Rightarrow nat \Rightarrow$
 $ex\text{-}table \Rightarrow ty_i' err\ list \Rightarrow 'addr\ instr\ list \Rightarrow bool$

where

$wt\text{-}lbv\ P\ C\ Ts\ T_r\ mxs\ mxl_0\ et\ cert\ ins \equiv$
 $check\text{-}cert\ P\ mxs\ (1+size\ Ts+mxl_0)\ (size\ ins)\ cert \wedge$
 $0 < size\ ins \wedge$
 $(let\ start = Some\ ([],(OK\ (Class\ C))\#\((map\ OK\ Ts))\@\((replicate\ mxl_0\ Err)));$
 $result = lbvjvm\ P\ mxs\ (1+size\ Ts+mxl_0)\ T_r\ et\ cert\ ins\ (OK\ start)$
 $in\ result \neq Err)$

definition $wt\text{-}jvm\text{-}prog\text{-}lbv :: 'addr\ jvm\text{-}prog \Rightarrow prog\text{-}cert \Rightarrow bool$

where

$wt\text{-}jvm\text{-}prog\text{-}lbv\ P\ cert \equiv$
 $wf\text{-}prog\ (\lambda P\ C\ (mn, Ts, T_r, (mxs, mxl_0, b, et)). wt\text{-}lbv\ P\ C\ Ts\ T_r\ mxs\ mxl_0\ et\ (cert\ C\ mn)\ b)\ P$

definition $mk\text{-}cert :: 'addr\ jvm\text{-}prog \Rightarrow nat \Rightarrow ty \Rightarrow ex\text{-}table \Rightarrow 'addr\ instr\ list$
 $\Rightarrow ty_m \Rightarrow ty_i' err\ list$

where

$mk\text{-}cert\ P\ mxs\ T_r\ et\ bs\ phi \equiv make\text{-}cert\ (exec\ P\ mxs\ T_r\ et\ bs)\ (map\ OK\ phi)\ (OK\ None)$

definition $prg\text{-}cert :: 'addr\ jvm\text{-}prog \Rightarrow ty_P \Rightarrow prog\text{-}cert$

where

$prg\text{-}cert\ P\ phi\ C\ mn \equiv let\ (C, Ts, T_r, meth) = method\ P\ C\ mn; (mxs, mxl_0, ins, et) = the\ meth$
 $in\ mk\text{-}cert\ P\ mxs\ T_r\ et\ ins\ (phi\ C\ mn)$

lemma $check\text{-}certD$ [intro?]:

$check\text{-}cert\ P\ mxs\ mxl\ n\ cert \implies cert\text{-}ok\ cert\ n\ Err\ (OK\ None)\ (states\ P\ mxs\ mxl)$
 $\langle proof \rangle$

lemma (in *start-context*) $wt\text{-}lbv\text{-}wt\text{-}step$:

assumes lbv : $wt\text{-}lbv\ P\ C\ Ts\ T_r\ mxs\ mxl_0\ xt\ cert\ is$
shows $\exists \tau s \in list\ (size\ is)\ A.$ $wt\text{-}step\ r\ Err\ step\ \tau s \wedge OK\ first \sqsubseteq_r \tau s!0 \langle proof \rangle$

lemma (in *start-context*) $wt\text{-}lbv\text{-}wt\text{-}method$:

assumes lbv : $wt\text{-}lbv\ P\ C\ Ts\ T_r\ mxs\ mxl_0\ xt\ cert\ is$
shows $\exists \tau s.$ $wt\text{-}method\ P\ C\ Ts\ T_r\ mxs\ mxl_0\ is\ at\ \tau s \langle proof \rangle$

lemma (in *start-context*) $wt\text{-}method\text{-}wt\text{-}lbv$:

assumes wt : $wt\text{-}method\ P\ C\ Ts\ T_r\ mxs\ mxl_0\ is\ at\ \tau s$
defines [simp]: $cert \equiv mk\text{-}cert\ P\ mxs\ T_r\ xt\ is\ \tau s$

shows $wt\text{-}lbv\ P\ C\ Ts\ T_r\ mxs\ mxl_0\ xt\ cert\ is \langle proof \rangle$

theorem $jvm\text{-}lbv\text{-}correct$:

$wt\text{-}jvm\text{-}prog\text{-}lbv\ P\ Cert \implies wf\text{-}jvm\text{-}prog\ P \langle proof \rangle$

theorem $jvm\text{-}lbv\text{-}complete$:

assumes wt : $wf\text{-}jvm\text{-}prog_{\Phi}\ P$
shows $wt\text{-}jvm\text{-}prog\text{-}lbv\ P\ (prg\text{-}cert\ P\ \Phi) \langle proof \rangle$

end

6.12 Kildall for the JVM

theory *BVExec*

imports

../DFA/Abstract-BV

TF-JVM

begin

definition *kiljvm* :: 'addr jvm-prog \Rightarrow nat \Rightarrow nat \Rightarrow ty \Rightarrow
 'addr instr list \Rightarrow ex-table \Rightarrow ty_i' err list \Rightarrow ty_i' err list

where

kiljvm *P mxs mxl T_r is xt* \equiv

kildall (*JVM-SemiType.le* *P mxs mxl*) (*JVM-SemiType.sup* *P mxs mxl*)
 (*exec* *P mxs T_r xt is*)

definition *wt-kildall* :: 'addr jvm-prog \Rightarrow cname \Rightarrow ty list \Rightarrow ty \Rightarrow nat \Rightarrow nat \Rightarrow
 'addr instr list \Rightarrow ex-table \Rightarrow bool

where

wt-kildall *P C' Ts T_r mxs mxl₀ is xt* \equiv

$0 < \text{size } is \wedge$

(*let* *first* = *Some* ([],[*OK* (*Class* *C'*)]@(*map* *OK* *Ts*)@(replicate *mxl₀* *Err*));

start = *OK first* # (replicate (*size is* - 1) (*OK None*));

result = *kiljvm* *P mxs (1+size Ts+mxl₀) T_r is xt start*

 in $\forall n < \text{size } is. \text{result!}n \neq \text{Err}$)

definition *wf-jvm-prog_k* :: 'addr jvm-prog \Rightarrow bool

where

wf-jvm-prog_k *P* \equiv

wf-prog ($\lambda P C' (M, Ts, T_r, (mxs, mxl_0, is, xt)). \text{wt-kildall } P C' Ts T_r mxs mxl_0 is xt$) *P*

theorem (**in** *start-context*) *is-bcv-kiljvm*:

is-bcv *r Err step (size is) A (kiljvm P mxs mxl T_r is xt)* <proof>

lemma *subset-replicate* [*intro?*]: *set (replicate n x) \subseteq {x}*

<proof>

lemma *in-set-replicate*:

assumes *x \in set (replicate n y)*

shows *x = y* <proof>

lemma (**in** *start-context*) *start-in-A* [*intro?*]:

$0 < \text{size } is \implies \text{start} \in \text{list } (size is) A$

<proof>

theorem (**in** *start-context*) *wt-kil-correct*:

assumes *wtk: wt-kildall P C Ts T_r mxs mxl₀ is xt*

shows $\exists \tau s. \text{wt-method } P C Ts T_r mxs mxl_0 is xt \tau s$ <proof>

theorem (**in** *start-context*) *wt-kil-complete*:

assumes *wtm: wt-method P C Ts T_r mxs mxl₀ is xt τs*

shows *wt-kildall P C Ts T_r mxs mxl₀ is xt* <proof>

theorem *jvm-kildall-correct*:

wf-jvm-prog_k *P = wf-jvm-prog P* <proof>

end

6.13 Code generation for the byte code verifier

```

theory BCVExec
imports
  BVNoTypeError
  BVExec
begin

lemmas [code-unfold] = exec-lub-def

lemmas [code] = JVM-le-unfold[THEN meta-eq-to-obj-eq]

lemma err-code [code]:
  Err.err A = Collect (case-err True ( $\lambda x. x \in A$ ))
  <proof>

lemma list-code [code]:
  list n A = {xs. size xs = n  $\wedge$  list-all ( $\lambda x. x \in A$ ) xs}
  <proof>

lemma opt-code [code]:
  opt A = Collect (case-option True ( $\lambda x. x \in A$ ))
  <proof>

lemma Times-code [code-unfold]:
  Sigma A (%-. B) = {(a, b). a  $\in$  A  $\wedge$  b  $\in$  B}
  <proof>

lemma upto-esl-code [code]:
  upto-esl m (A, r, f) = (Union (( $\lambda n. list\ n\ A$ ) ‘ {..m}), Listn.le r, Listn.sup f)
  <proof>

lemmas [code] = lesub-def plussub-def

lemma JVM-sup-unfold [code]:
  JVM-SemiType.sup S m n =
  lift2 (Opt.sup (Product.sup (Listn.sup (SemiType.sup S)) ( $\lambda x\ y. OK (map2 (lift2 (SemiType.sup S))$ 
  x y))))
  <proof>

declare sup-fun-def [code]

lemma [code]: states P mxs mxl = fst(sl P mxs mxl)
  <proof>

lemma check-types-code [code]:
  check-types P mxs mxl  $\tau s$  = (list-all ( $\lambda x. x \in (states\ P\ mxs\ mxl)$ )  $\tau s$ )
  <proof>

lemma wf-jvm-prog-code [code-unfold]:

```

$wf\text{-}jvm\text{-}prog = wf\text{-}jvm\text{-}prog_k$
<proof>

definition $wf\text{-}jvm\text{-}prog' = wf\text{-}jvm\text{-}prog$

<ML>

end

theory *BV-Main*

imports

JVMDeadlocked

LBVJVM

BCVExec

begin

end

Chapter 7

Compilation

7.1 Method calls in expressions

theory *CallExpr* **imports**

../J/Expr

begin

fun *inline-call* :: ('a,'b,'addr) *exp* \Rightarrow ('a,'b,'addr) *exp* \Rightarrow ('a,'b,'addr) *exp*

and *inline-calls* :: ('a,'b,'addr) *exp* \Rightarrow ('a,'b,'addr) *exp list* \Rightarrow ('a,'b,'addr) *exp list*

where

inline-call *f* (*new C*) = *new C*
| *inline-call* *f* (*newA T* [*e*]) = *newA T* [*inline-call f e*]
| *inline-call* *f* (*Cast C e*) = *Cast C* (*inline-call f e*)
| *inline-call* *f* (*e instanceof T*) = (*inline-call f e*) *instanceof T*
| *inline-call* *f* (*Val v*) = *Val v*
| *inline-call* *f* (*Var V*) = *Var V*
| *inline-call* *f* (*V:=e*) = *V := inline-call f e*
| *inline-call* *f* (*e «bop» e'*) = (*if is-val e then (e «bop» inline-call f e') else (inline-call f e «bop» e')*)
| *inline-call* *f* (*a* [*i*]) = (*if is-val a then a* [*inline-call f i*] *else (inline-call f a)* [*i*])
| *inline-call* *f* (*AAss a i e*) =
 (*if is-val a then if is-val i then AAss a i (inline-call f e) else AAss a (inline-call f i) e*
 else AAss (inline-call f a) i e)
| *inline-call* *f* (*a.length*) = *inline-call f a.length*
| *inline-call* *f* (*e.F{D}*) = *inline-call f e.F{D}*
| *inline-call* *f* (*FAss e F D e'*) = (*if is-val e then FAss e F D (inline-call f e') else FAss (inline-call f e) F D e'*)
| *inline-call* *f* (*CompareAndSwap e D F e' e''*) =
 (*if is-val e then if is-val e' then CompareAndSwap e D F e' (inline-call f e'')*
 else CompareAndSwap e D F (inline-call f e') e''
 else CompareAndSwap (inline-call f e) D F e' e'')
| *inline-call* *f* (*e.M(es)*) =
 (*if is-val e then if is-vals es \wedge is-addr e then f else e.M(inline-calls f es) else inline-call f e.M(es)*)
| *inline-call* *f* (*{V:T=vo; e}*) = *{V:T=vo; inline-call f e}*
| *inline-call* *f* (*sync_V (o') e*) = *sync_V (inline-call f o') e*
| *inline-call* *f* (*insync_V (a) e*) = *insync_V (a) (inline-call f e)*
| *inline-call* *f* (*e;;e'*) = *inline-call f e;;e'*
| *inline-call* *f* (*if (b) e else e'*) = (*if (inline-call f b) e else e'*)
| *inline-call* *f* (*while (b) e*) = *while (b) e*
| *inline-call* *f* (*throw e*) = *throw (inline-call f e)*
| *inline-call* *f* (*try e1 catch(C V) e2*) = *try inline-call f e1 catch(C V) e2*

| *inline-calls* f [] = []
 | *inline-calls* f ($e \# es$) = (if *is-val* e then $e \# \text{inline-calls } f \text{ } es$ else *inline-call* f $e \# es$)

fun *collapse* :: 'addr expr × 'addr expr list ⇒ 'addr expr **where**
 collapse (e , []) = e
 | *collapse* (e , ($e' \# es$)) = *collapse* (*inline-call* e e' , es)

definition *is-call* :: ('a, 'b, 'addr) exp ⇒ bool
where *is-call* e = (*call* $e \neq \text{None}$)

definition *is-calls* :: ('a, 'b, 'addr) exp list ⇒ bool
where *is-calls* es = (*calls* $es \neq \text{None}$)

lemma *inline-calls-map-Val-append* [*simp*]:
inline-calls f (*map* *Val* vs @ es) = *map* *Val* vs @ *inline-calls* f es
 ⟨*proof*⟩

lemma *inline-call-eq-Val-aux*:
inline-call e $E = \text{Val } v \implies \text{call } E = \lfloor aMvs \rfloor \implies e = \text{Val } v$
 ⟨*proof*⟩

lemmas *inline-call-eq-Val* [*dest*] = *inline-call-eq-Val-aux inline-call-eq-Val-aux* [*OF sym, THEN sym*]

lemma *inline-calls-eq-empty* [*simp*]: *inline-calls* e $es = [] \longleftrightarrow es = []$
 ⟨*proof*⟩

lemma *inline-calls-map-Val* [*simp*]: *inline-calls* e (*map* *Val* vs) = *map* *Val* vs
 ⟨*proof*⟩

lemma *fixes* E :: ('a, 'b, 'addr) exp **and** Es :: ('a, 'b, 'addr) exp list
shows *inline-call-eq-Throw* [*dest*]: *inline-call* e $E = \text{Throw } a \implies \text{call } E = \lfloor aMvs \rfloor \implies e = \text{Throw } a \vee e = \text{addr } a$
 ⟨*proof*⟩

lemma *Throw-eq-inline-call-eq* [*dest*]:
inline-call e $E = \text{Throw } a \implies \text{call } E = \lfloor aMvs \rfloor \implies \text{Throw } a = e \vee \text{addr } a = e$
 ⟨*proof*⟩

lemma *is-vals-inline-calls* [*dest*]:
 [*is-vals* (*inline-calls* e es); *calls* $es = \lfloor aMvs \rfloor$] ⇒ *is-val* e
 ⟨*proof*⟩

lemma [*dest*]: [*inline-calls* e $es = \text{map } \text{Val } vs$; *calls* $es = \lfloor aMvs \rfloor$] ⇒ *is-val* e
 [*map* *Val* $vs = \text{inline-calls } e \text{ } es$; *calls* $es = \lfloor aMvs \rfloor$] ⇒ *is-val* e
 ⟨*proof*⟩

lemma *inline-calls-eq-Val-Throw* [*dest*]:
 [*inline-calls* e $es = \text{map } \text{Val } vs$ @ $\text{Throw } a \# es'$; *calls* $es = \lfloor aMvs \rfloor$] ⇒ $e = \text{Throw } a \vee \text{is-val } e$
 ⟨*proof*⟩

lemma *Val-Throw-eq-inline-calls* [*dest*]:

$\llbracket \text{map Val vs } @ \text{ Throw } a \# es' = \text{inline-calls } e \text{ es}; \text{calls } es = \llbracket aMvs \rrbracket \rrbracket \implies \text{Throw } a = e \vee \text{is-val } e$
 <proof>

declare *option.split* [*split del*] *if-split-asm* [*split*] *if-split* [*split del*]

lemma *call-inline-call* [*simp*]:

$\text{call } e = \llbracket aMvs \rrbracket \implies \text{call } (\text{inline-call } \{v:T=vo; e'\} e) = \text{call } e'$
 $\text{calls } es = \llbracket aMvs \rrbracket \implies \text{calls } (\text{inline-calls } \{v:T=vo;e'\} es) = \text{call } e'$
 <proof>

declare *option.split* [*split*] *if-split* [*split*] *if-split-asm* [*split del*]

lemma *fv-inline-call*: $\text{fv } (\text{inline-call } e' e) \subseteq \text{fv } e \cup \text{fv } e'$

and *fvs-inline-calls*: $\text{fvs } (\text{inline-calls } e' es) \subseteq \text{fvs } es \cup \text{fv } e'$

<proof>

lemma *contains-insync-inline-call-conv*:

$\text{contains-insync } (\text{inline-call } e e') \longleftrightarrow \text{contains-insync } e \wedge \text{call } e' \neq \text{None} \vee \text{contains-insync } e'$

and *contains-insyncs-inline-calls-conv*:

$\text{contains-insyncs } (\text{inline-calls } e es') \longleftrightarrow \text{contains-insync } e \wedge \text{calls } es' \neq \text{None} \vee \text{contains-insyncs } es'$
 <proof>

lemma *contains-insync-inline-call* [*simp*]:

$\text{call } e' = \llbracket aMvs \rrbracket \implies \text{contains-insync } (\text{inline-call } e e') \longleftrightarrow \text{contains-insync } e \vee \text{contains-insync } e'$

and *contains-insyncs-inline-calls* [*simp*]:

$\text{calls } es' = \llbracket aMvs \rrbracket \implies \text{contains-insyncs } (\text{inline-calls } e es') \longleftrightarrow \text{contains-insync } e \vee \text{contains-insyncs } es'$
 <proof>

lemma *collapse-append* [*simp*]:

$\text{collapse } (e, es @ es') = \text{collapse } (\text{collapse } (e, es), es')$
 <proof>

lemma *collapse-conv-foldl*:

$\text{collapse } (e, es) = \text{foldl } \text{inline-call } e \text{ es}$
 <proof>

lemma *fv-collapse*: $\forall e \in \text{set } es. \text{is-call } e \implies \text{fv } (\text{collapse } (e, es)) \subseteq \text{fvs } (e \# es)$

<proof>

lemma *final-inline-callD*: $\llbracket \text{final } (\text{inline-call } E e); \text{is-call } e \rrbracket \implies \text{final } E$

<proof>

lemma *collapse-finalD*: $\llbracket \text{final } (\text{collapse } (e, es)); \forall e \in \text{set } es. \text{is-call } e \rrbracket \implies \text{final } e$

<proof>

context *heap-base* **begin**

definition *synthesized-call* :: $'m \text{ prog} \Rightarrow 'heap \Rightarrow ('addr \times \text{mname} \times 'addr \text{ val list}) \Rightarrow \text{bool}$

where

$\text{synthesized-call } P h =$
 $(\lambda(a, M, vs). \exists T Ts Tr D. \text{typeof-addr } h a = \llbracket T \rrbracket \wedge P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native}$
in } D)

lemma *synthesized-call-conv*:

synthesized-call $P\ h\ (a, M, vs) =$
 $(\exists T\ Ts\ Tr\ D. \text{typeof-addr } h\ a = \lfloor T \rfloor \wedge P \vdash \text{class-type-of } T\ \text{sees } M:Ts \rightarrow Tr = \text{Native in } D)$
 $\langle \text{proof} \rangle$

end

end

7.2 The JinjaThreads source language with explicit call stacks

theory *J0 imports*

../J/WWellForm
../J/WellType
../J/Threaded
../Framework/FWBisimulation
CallExpr

begin

declare *widen-refT* [elim]

abbreviation *final-expr0* :: *'addr expr* \times *'addr expr list* \Rightarrow *bool* **where**
final-expr0 $\equiv \lambda(e, es). \text{final } e \wedge es = []$

type-synonym

('addr, 'thread-id, 'heap) J0-thread-action =
('addr, 'thread-id, 'addr expr \times *'addr expr list, 'heap) Jinja-thread-action*

type-synonym

('addr, 'thread-id, 'heap) J0-state = *('addr, 'thread-id, 'addr expr* \times *'addr expr list, 'heap, 'addr) state*

$\langle ML \rangle$

typ *('addr, 'thread-id, 'heap) J0-thread-action*

$\langle ML \rangle$

typ *('addr, 'thread-id, 'heap) J0-state*

definition *extNTA2J0* :: *'addr J-prog* \Rightarrow (*cname* \times *mname* \times *'addr*) \Rightarrow (*'addr expr* \times *'addr expr list*)
where

extNTA2J0 P = $(\lambda(C, M, a). \text{let } (D, -, -, \text{meth}) = \text{method } P\ C\ M; (-, \text{body}) = \text{the meth}$
 $\text{in } (\{ \text{this:Class } D = \lfloor \text{Addr } a \rfloor; \text{body} \}, []))$

lemma *extNTA2J0-iff* [simp]:

extNTA2J0 P (*C, M, a*) =
 $(\{ \text{this:Class } (\text{fst } (\text{method } P\ C\ M)) = \lfloor \text{Addr } a \rfloor; \text{snd } (\text{the } (\text{snd } (\text{snd } (\text{snd } (\text{method } P\ C\ M)))) \}, [])$
 $\langle \text{proof} \rangle$

abbreviation *extTA2J0* ::

'addr J-prog \Rightarrow (*'addr, 'thread-id, 'heap) external-thread-action* \Rightarrow (*'addr, 'thread-id, 'heap) J0-thread-action*
where *extTA2J0 P* $\equiv \text{convert-extTA } (extNTA2J0\ P)$

lemma *obs-a-extTA2J-eq-obs-a-extTA2J0* [simp]: $\{\{extTA2J P ta\}_o = \{\{extTA2J0 P ta\}_o$
 ⟨proof⟩

lemma *extTA2J0-ε*: $extTA2J0 P \varepsilon = \varepsilon$
 ⟨proof⟩

context *J-heap-base* **begin**

definition *no-call* :: $'m \text{ prog} \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr) \text{ exp} \Rightarrow \text{bool}$
where *no-call* $P h e = (\forall aMvs. \text{call } e = [aMvs] \longrightarrow \text{synthesized-call } P h aMvs)$

definition *no-calls* :: $'m \text{ prog} \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr) \text{ exp list} \Rightarrow \text{bool}$
where *no-calls* $P h es = (\forall aMvs. \text{calls } es = [aMvs] \longrightarrow \text{synthesized-call } P h aMvs)$

inductive *red0* ::

$'addr \text{ J-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'addr \text{ expr} \Rightarrow 'addr \text{ expr list} \Rightarrow 'heap$
 $\Rightarrow ('addr, 'thread\text{-id}, 'heap) \text{ J0-thread-action} \Rightarrow 'addr \text{ expr} \Rightarrow 'addr \text{ expr list} \Rightarrow 'heap \Rightarrow \text{bool}$
 $(\langle -, - \rangle \vdash_0 ((1 \langle -' / -, - \rangle) \dashrightarrow (1 \langle -' / -, - \rangle))) \triangleright [51, 0, 0, 0, 0, 0, 0, 0] \ 81$

for $P :: 'addr \text{ J-prog}$ **and** $t :: 'thread\text{-id}$

where

red0Red:

$\llbracket extTA2J0 P, P, t \vdash \langle e, (h, \text{Map.empty}) \rangle -ta \rightarrow \langle e', (h', xs') \rangle;$
 $\forall aMvs. \text{call } e = [aMvs] \longrightarrow \text{synthesized-call } P h aMvs \rrbracket$
 $\implies P, t \vdash_0 \langle e/es, h \rangle -ta \rightarrow \langle e'/es, h' \rangle$

| *red0Call*:

$\llbracket \text{call } e = [(a, M, vs)]; \text{typeof-addr } h a = [U];$
 $P \vdash \text{class-type-of } U \text{ sees } M:Ts \rightarrow T = [(pns, \text{body})] \text{ in } D;$
 $\text{size } vs = \text{size } pns; \text{size } Ts = \text{size } pns \rrbracket$
 $\implies P, t \vdash_0 \langle e/es, h \rangle -\varepsilon \rightarrow \langle \text{blocks } (this \# pns) (\text{Class } D \# Ts) (\text{Addr } a \# vs) \text{ body}/e\#es, h \rangle$

| *red0Return*:

$\text{final } e' \implies P, t \vdash_0 \langle e'/e\#es, h \rangle -\varepsilon \rightarrow \langle \text{inline-call } e' e/es, h \rangle$

abbreviation *J0-start-state* :: $'addr \text{ J-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow 'addr \text{ val list} \Rightarrow ('addr, 'thread\text{-id}, 'heap) \text{ J0-state}$

where

$J0\text{-start-state} \equiv$
 $\text{start-state } (\lambda C M Ts T (pns, \text{body}) \text{ vs}. (\text{blocks } (this \# pns) (\text{Class } C \# Ts) (\text{Null} \# vs) \text{ body}, []))$

abbreviation *mred0* ::

$'addr \text{ J-prog} \Rightarrow ('addr, 'thread\text{-id}, 'addr \text{ expr} \times 'addr \text{ expr list}, 'heap, 'addr, ('addr, 'thread\text{-id}) \text{ obs-event})$
semantics

where $mred0 P \equiv (\lambda t ((e, es), h) ta ((e', es'), h'). \text{red0 } P t e es h ta e' es' h')$

end

declare *domIff*[*iff*, *simp del*]

context *J-heap-base* **begin**

lemma **assumes** *wf*: $wf\text{-J-prog } P$

shows *red-fv-subset*: $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{fv } e' \subseteq \text{fv } e$

and *reds-fvs-subset*: $\text{extTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \text{fvs } es' \subseteq \text{fvs } es$
 ⟨proof⟩

end

declare *domIff*[*iff del*]

context *J-heap-base* **begin**

lemma *assumes wwf*: *wwf-J-prog* *P*

shows *red-fv-ok*: $\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{fv } e \subseteq \text{dom } (\text{lcl } s) \rrbracket \implies \text{fv } e' \subseteq \text{dom } (\text{lcl } s')$
and *reds-fvs-ok*: $\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{fvs } es \subseteq \text{dom } (\text{lcl } s) \rrbracket \implies \text{fvs } es' \subseteq \text{dom } (\text{lcl } s')$
 ⟨proof⟩

lemma *is-call-red-state-unchanged*:

$\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{call } e = \llbracket aMvs \rrbracket; \neg \text{synthesized-call } P (\text{hp } s) aMvs \rrbracket \implies s' = s \wedge ta = \varepsilon$

and *is-calls-reds-state-unchanged*:

$\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{calls } es = \llbracket aMvs \rrbracket; \neg \text{synthesized-call } P (\text{hp } s) aMvs \rrbracket \implies s' = s \wedge ta = \varepsilon$
 ⟨proof⟩

lemma *called-methodD*:

$\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{call } e = \llbracket (a, M, vs) \rrbracket; \neg \text{synthesized-call } P (\text{hp } s) (a, M, vs) \rrbracket$
 $\implies \exists hT D Us U pns \text{ body. } \text{hp } s' = \text{hp } s \wedge \text{typeof-addr } (\text{hp } s) a = \llbracket hT \rrbracket \wedge$
 $P \vdash \text{class-type-of } hT \text{ sees } M: Us \rightarrow U = \llbracket (pns, \text{body}) \rrbracket \text{ in } D \wedge$
 $\text{length } vs = \text{length } pns \wedge \text{length } Us = \text{length } pns$

and *called-methodsD*:

$\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{calls } es = \llbracket (a, M, vs) \rrbracket; \neg \text{synthesized-call } P (\text{hp } s) (a, M, vs) \rrbracket$
 $\implies \exists hT D Us U pns \text{ body. } \text{hp } s' = \text{hp } s \wedge \text{typeof-addr } (\text{hp } s) a = \llbracket hT \rrbracket \wedge$
 $P \vdash \text{class-type-of } hT \text{ sees } M: Us \rightarrow U = \llbracket (pns, \text{body}) \rrbracket \text{ in } D \wedge$
 $\text{length } vs = \text{length } pns \wedge \text{length } Us = \text{length } pns$

⟨proof⟩

7.2.1 Silent moves

primrec $\tau\text{move0} :: 'm \text{ prog} \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr) \text{ exp} \Rightarrow \text{bool}$

and $\tau\text{moves0} :: 'm \text{ prog} \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr) \text{ exp list} \Rightarrow \text{bool}$

where

$\tau\text{move0 } P h (\text{new } C) \longleftrightarrow \text{False}$
 $\tau\text{move0 } P h (\text{newA } T [e]) \longleftrightarrow \tau\text{move0 } P h e \vee (\exists a. e = \text{Throw } a)$
 $\tau\text{move0 } P h (\text{Cast } U e) \longleftrightarrow \tau\text{move0 } P h e \vee (\exists a. e = \text{Throw } a) \vee (\exists v. e = \text{Val } v)$
 $\tau\text{move0 } P h (e \text{ instanceof } T) \longleftrightarrow \tau\text{move0 } P h e \vee (\exists a. e = \text{Throw } a) \vee (\exists v. e = \text{Val } v)$
 $\tau\text{move0 } P h (e \text{ «bop» } e') \longleftrightarrow \tau\text{move0 } P h e \vee (\exists a. e = \text{Throw } a) \vee (\exists v. e = \text{Val } v \wedge$
 $(\tau\text{move0 } P h e' \vee (\exists a. e' = \text{Throw } a) \vee (\exists v. e' = \text{Val } v)))$
 $\tau\text{move0 } P h (\text{Val } v) \longleftrightarrow \text{False}$
 $\tau\text{move0 } P h (\text{Var } V) \longleftrightarrow \text{True}$
 $\tau\text{move0 } P h (V := e) \longleftrightarrow \tau\text{move0 } P h e \vee (\exists a. e = \text{Throw } a) \vee (\exists v. e = \text{Val } v)$
 $\tau\text{move0 } P h (a[i]) \longleftrightarrow \tau\text{move0 } P h a \vee (\exists ad. a = \text{Throw } ad) \vee (\exists v. a = \text{Val } v \wedge (\tau\text{move0 } P h i$
 $\vee (\exists a. i = \text{Throw } a)))$

$\tau\text{move0 } P h (AAss a i e) \longleftrightarrow \tau\text{move0 } P h a \vee (\exists ad. a = Throw ad) \vee (\exists v. a = Val v \wedge$
 $(\tau\text{move0 } P h i \vee (\exists a. i = Throw a) \vee (\exists v. i = Val v \wedge (\tau\text{move0 } P h e \vee (\exists a. e = Throw a))))))$
 $\tau\text{move0 } P h (a\text{-length}) \longleftrightarrow \tau\text{move0 } P h a \vee (\exists ad. a = Throw ad)$
 $\tau\text{move0 } P h (e \cdot F\{D\}) \longleftrightarrow \tau\text{move0 } P h e \vee (\exists a. e = Throw a)$
 $\tau\text{move0 } P h (FAss e F D e') \longleftrightarrow \tau\text{move0 } P h e \vee (\exists a. e = Throw a) \vee (\exists v. e = Val v \wedge (\tau\text{move0 } P h e' \vee (\exists a. e' = Throw a)))$
 $\tau\text{move0 } P h (e \cdot compareAndSwap(D \cdot F, e', e'')) \longleftrightarrow \tau\text{move0 } P h e \vee (\exists a. e = Throw a) \vee (\exists v. e = Val v \wedge$
 $(\tau\text{move0 } P h e' \vee (\exists a. e' = Throw a) \vee (\exists v. e' = Val v \wedge (\tau\text{move0 } P h e'' \vee (\exists a. e'' = Throw a))))))$
 $\tau\text{move0 } P h (e \cdot M(es)) \longleftrightarrow \tau\text{move0 } P h e \vee (\exists a. e = Throw a) \vee (\exists v. e = Val v \wedge$
 $((\tau\text{moves0 } P h es \vee (\exists vs a es'. es = map Val vs @ Throw a \# es')) \vee$
 $(\exists vs. es = map Val vs \wedge (v = Null \vee (\forall T C Ts Tr D. \text{typeof}_h v = \lfloor T \rfloor \longrightarrow \text{class-type-of}' T =$
 $\lfloor C \rfloor \longrightarrow P \vdash C \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D \longrightarrow \tau\text{external-defs } D M))))))$
 $\tau\text{move0 } P h (\{V:T=vo; e\}) \longleftrightarrow \tau\text{move0 } P h e \vee ((\exists a. e = Throw a) \vee (\exists v. e = Val v))$
 $\tau\text{move0 } P h (sync_{V'}(e) e') \longleftrightarrow \tau\text{move0 } P h e \vee (\exists a. e = Throw a)$
 $\tau\text{move0 } P h (insync_{V'}(ad) e) \longleftrightarrow \tau\text{move0 } P h e$
 $\tau\text{move0 } P h (e; e') \longleftrightarrow \tau\text{move0 } P h e \vee (\exists a. e = Throw a) \vee (\exists v. e = Val v)$
 $\tau\text{move0 } P h (\text{if } (e) e' \text{ else } e'') \longleftrightarrow \tau\text{move0 } P h e \vee (\exists a. e = Throw a) \vee (\exists v. e = Val v)$
 $\tau\text{move0 } P h (\text{while } (e) e') = True$
 — *Throw a* is no τmove0 because there is no reduction for it. If it were, most defining equations would be simpler. However, $insync_{V'}(ad) Throw ad$ must not be a τmove0 , but would be if *Throw a* was.

$\tau\text{move0 } P h (throw e) \longleftrightarrow \tau\text{move0 } P h e \vee (\exists a. e = Throw a) \vee e = null$
 $\tau\text{move0 } P h (try e catch(C V) e') \longleftrightarrow \tau\text{move0 } P h e \vee (\exists a. e = Throw a) \vee (\exists v. e = Val v)$

$\tau\text{moves0 } P h [] \longleftrightarrow False$
 $\tau\text{moves0 } P h (e \# es) \longleftrightarrow \tau\text{move0 } P h e \vee (\exists v. e = Val v \wedge \tau\text{moves0 } P h es)$

abbreviation $\tau\text{MOVE} :: 'm \text{ prog} \Rightarrow (('addr \text{ expr} \times 'addr \text{ locals}) \times 'heap, ('addr, 'thread-id, 'heap) J\text{-thread-action}) \text{ trsys}$

where $\tau\text{MOVE} \equiv \lambda P ((e, x), h) ta s'. \tau\text{move0 } P h e \wedge ta = \varepsilon$

primrec $\tau\text{Move0} :: 'm \text{ prog} \Rightarrow 'heap \Rightarrow ('addr \text{ expr} \times 'addr \text{ expr list}) \Rightarrow bool$

where

$\tau\text{Move0 } P h (e, exs) = (\tau\text{move0 } P h e \vee final e)$

abbreviation $\tau\text{MOVE0} :: 'm \text{ prog} \Rightarrow (('addr \text{ expr} \times 'addr \text{ expr list}) \times 'heap, ('addr, 'thread-id, 'heap) J0\text{-thread-action}) \text{ trsys}$

where $\tau\text{MOVE0} \equiv \lambda P (es, h) ta s. \tau\text{Move0 } P h es \wedge ta = \varepsilon$

definition $\tau\text{red0} ::$

$(('addr, 'thread-id, 'heap) \text{ external-thread-action} \Rightarrow ('addr, 'thread-id, 'x, 'heap) \text{ Jinja-thread-action})$
 $\Rightarrow 'addr \text{ J-prog} \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr \text{ expr} \times 'addr \text{ locals}) \Rightarrow ('addr \text{ expr} \times 'addr \text{ locals})$
 $\Rightarrow bool$

where

$\tau\text{red0 extTA } P t h exs e'xs' =$
 $(extTA, P, t \vdash \langle fst exs, (h, snd exs) \rangle -\varepsilon \rightarrow \langle fst e'xs', (h, snd e'xs') \rangle \wedge \tau\text{move0 } P h (fst exs) \wedge \text{no-call } P h (fst exs))$

definition $\tau\text{reds0} ::$

$(('addr, 'thread-id, 'heap) \text{ external-thread-action} \Rightarrow ('addr, 'thread-id, 'x, 'heap) \text{ Jinja-thread-action})$
 $\Rightarrow 'addr \text{ J-prog} \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr \text{ expr list} \times 'addr \text{ locals}) \Rightarrow ('addr \text{ expr list} \times 'addr \text{ locals})$
 $\Rightarrow bool$

where

$$\begin{aligned} \tau reds0 \text{ extTA } P \ t \ h \ esxs \ es'xs' = \\ (\text{extTA}, P, t \vdash \langle \text{fst } esxs, (h, \text{snd } esxs) \rangle [-\varepsilon \rightarrow] \langle \text{fst } es'xs', (h, \text{snd } es'xs') \rangle) \wedge \tau moves0 \ P \ h \ (\text{fst } esxs) \wedge \\ \text{no-calls } P \ h \ (\text{fst } esxs) \end{aligned}$$

abbreviation $\tau red0t ::$

$$\begin{aligned} ((\text{'addr}, \text{'thread-id}, \text{'heap}) \text{ external-thread-action} \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'x}, \text{'heap}) \text{ Jinja-thread-action}) \\ \Rightarrow \text{'addr J-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr} \times \text{'addr locals}) \Rightarrow (\text{'addr expr} \times \text{'addr locals}) \\ \Rightarrow \text{bool} \end{aligned}$$

where $\tau red0t \text{ extTA } P \ t \ h \equiv (\tau red0 \text{ extTA } P \ t \ h) \hat{+}+$

abbreviation $\tau reds0t ::$

$$\begin{aligned} ((\text{'addr}, \text{'thread-id}, \text{'heap}) \text{ external-thread-action} \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'x}, \text{'heap}) \text{ Jinja-thread-action}) \\ \Rightarrow \text{'addr J-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr list} \times \text{'addr locals}) \Rightarrow (\text{'addr expr list} \times \text{'addr locals}) \\ \Rightarrow \text{bool} \end{aligned}$$

where $\tau reds0t \text{ extTA } P \ t \ h \equiv (\tau reds0 \text{ extTA } P \ t \ h) \hat{+}+$

abbreviation $\tau red0r ::$

$$\begin{aligned} ((\text{'addr}, \text{'thread-id}, \text{'heap}) \text{ external-thread-action} \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'x}, \text{'heap}) \text{ Jinja-thread-action}) \\ \Rightarrow \text{'addr J-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr} \times \text{'addr locals}) \Rightarrow (\text{'addr expr} \times \text{'addr locals}) \\ \Rightarrow \text{bool} \end{aligned}$$

where $\tau red0r \text{ extTA } P \ t \ h \equiv (\tau red0 \text{ extTA } P \ t \ h) \hat{*}*$

abbreviation $\tau reds0r ::$

$$\begin{aligned} ((\text{'addr}, \text{'thread-id}, \text{'heap}) \text{ external-thread-action} \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'x}, \text{'heap}) \text{ Jinja-thread-action}) \\ \Rightarrow \text{'addr J-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr list} \times \text{'addr locals}) \Rightarrow (\text{'addr expr list} \times \text{'addr locals}) \\ \Rightarrow \text{bool} \end{aligned}$$

where $\tau reds0r \text{ extTA } P \ t \ h \equiv (\tau reds0 \text{ extTA } P \ t \ h) \hat{*}*$

definition $\tau Red0 ::$

$$\begin{aligned} \text{'addr J-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \\ \Rightarrow \text{bool} \end{aligned}$$

where $\tau Red0 \ P \ t \ h \ ees \ e'es' = (P, t \vdash 0 \langle \text{fst } ees/\text{snd } ees, h \rangle -\varepsilon \rightarrow \langle \text{fst } e'es'/\text{snd } e'es', h \rangle) \wedge \tau Move0 \ P \ h \ ees)$

abbreviation $\tau Red0r ::$

$$\begin{aligned} \text{'addr J-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \\ \Rightarrow \text{bool} \end{aligned}$$

where $\tau Red0r \ P \ t \ h \equiv (\tau Red0 \ P \ t \ h) \hat{*}*$

abbreviation $\tau Red0t ::$

$$\begin{aligned} \text{'addr J-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \\ \Rightarrow \text{bool} \end{aligned}$$

where $\tau Red0t \ P \ t \ h \equiv (\tau Red0 \ P \ t \ h) \hat{+}+$

lemma $\tau move0\text{-}\tau moves0\text{-intros}$:

fixes $e \ e1 \ e2 \ e' :: (\text{'a}, \text{'b}, \text{'addr}) \text{ exp}$ **and** $es :: (\text{'a}, \text{'b}, \text{'addr}) \text{ exp list}$
shows $\tau move0NewArray$: $\tau move0 \ P \ h \ e \Longrightarrow \tau move0 \ P \ h \ (\text{newA } T[e])$
and $\tau move0Cast$: $\tau move0 \ P \ h \ e \Longrightarrow \tau move0 \ P \ h \ (\text{Cast } U \ e)$
and $\tau move0CastRed$: $\tau move0 \ P \ h \ (\text{Cast } U \ (\text{Val } v))$
and $\tau move0InstanceOf$: $\tau move0 \ P \ h \ e \Longrightarrow \tau move0 \ P \ h \ (e \text{ instanceof } T)$
and $\tau move0InstanceOfRed$: $\tau move0 \ P \ h \ ((\text{Val } v) \text{ instanceof } T)$
and $\tau move0BinOp1$: $\tau move0 \ P \ h \ e \Longrightarrow \tau move0 \ P \ h \ (e \llbracket \text{bop} \rrbracket e')$
and $\tau move0BinOp2$: $\tau move0 \ P \ h \ e \Longrightarrow \tau move0 \ P \ h \ (\text{Val } v \llbracket \text{bop} \rrbracket e)$

and $\tau\text{move0BinOp}$: $\tau\text{move0 } P h (Val v \ll bop \gg Val v')$
and $\tau\text{move0Var}$: $\tau\text{move0 } P h (Var V)$
and $\tau\text{move0LAss}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (V := e)$
and $\tau\text{move0LAssRed}$: $\tau\text{move0 } P h (V := Val v)$
and $\tau\text{move0AAcc1}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (e[e'])$
and $\tau\text{move0AAcc2}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (Val v[e])$
and $\tau\text{move0AAss1}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (e[e1] := e2)$
and $\tau\text{move0AAss2}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (Val v[e] := e')$
and $\tau\text{move0AAss3}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (Val v[Val v'] := e)$
and $\tau\text{move0ALength}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (e.length)$
and $\tau\text{move0FAcc}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (e.F\{D\})$
and $\tau\text{move0FAss1}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (FAss e F D e')$
and $\tau\text{move0FAss2}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (Val v.F\{D\} := e)$
and $\tau\text{move0CAS1}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (e.compareAndSwap(D.F, e', e'))$
and $\tau\text{move0CAS2}$: $\tau\text{move0 } P h e' \implies \tau\text{move0 } P h (Val v.compareAndSwap(D.F, e', e''))$
and $\tau\text{move0CAS3}$: $\tau\text{move0 } P h e'' \implies \tau\text{move0 } P h (Val v.compareAndSwap(D.F, Val v', e''))$
and $\tau\text{move0CallObj}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (e.M(es))$
and $\tau\text{move0CallParams}$: $\tau\text{moves0 } P h es \implies \tau\text{move0 } P h (Val v.M(es))$
and $\tau\text{move0Call}$: $(\bigwedge T C Ts Tr D. \llbracket \text{typeof}_h v = \lfloor T \rfloor; \text{class-type-of}' T = \lfloor C \rfloor; P \vdash C \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \rrbracket \implies \tau\text{external-defs } D M) \implies \tau\text{move0 } P h (Val v.M(\text{map } Val vs))$
and $\tau\text{move0Block}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h \{V:T=vo; e\}$
and $\tau\text{move0BlockRed}$: $\tau\text{move0 } P h \{V:T=vo; Val v\}$
and $\tau\text{move0Sync}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (\text{sync}_{V'}(e) e')$
and $\tau\text{move0InSync}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (\text{insync}_{V'}(a) e)$
and $\tau\text{move0Seq}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (e;;e')$
and $\tau\text{move0SeqRed}$: $\tau\text{move0 } P h (Val v;; e')$
and $\tau\text{move0Cond}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (\text{if } (e) e1 \text{ else } e2)$
and $\tau\text{move0CondRed}$: $\tau\text{move0 } P h (\text{if } (Val v) e1 \text{ else } e2)$
and $\tau\text{move0WhileRed}$: $\tau\text{move0 } P h (\text{while } (e) e')$
and $\tau\text{move0Throw}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (\text{throw } e)$
and $\tau\text{move0ThrowNull}$: $\tau\text{move0 } P h (\text{throw null})$
and $\tau\text{move0Try}$: $\tau\text{move0 } P h e \implies \tau\text{move0 } P h (\text{try } e \text{ catch } (C V) e')$
and $\tau\text{move0TryRed}$: $\tau\text{move0 } P h (\text{try } Val v \text{ catch } (C V) e)$
and $\tau\text{move0TryThrow}$: $\tau\text{move0 } P h (\text{try } Throw a \text{ catch } (C V) e)$
and $\tau\text{move0NewArrayThrow}$: $\tau\text{move0 } P h (\text{newA } T \lfloor Throw a \rfloor)$
and $\tau\text{move0CastThrow}$: $\tau\text{move0 } P h (\text{Cast } T (Throw a))$
and $\tau\text{move0CInstanceOfThrow}$: $\tau\text{move0 } P h ((Throw a) \text{ instanceof } T)$
and $\tau\text{move0BinOpThrow1}$: $\tau\text{move0 } P h (Throw a \ll bop \gg e')$
and $\tau\text{move0BinOpThrow2}$: $\tau\text{move0 } P h (Val v \ll bop \gg Throw a)$
and $\tau\text{move0LAssThrow}$: $\tau\text{move0 } P h (V := (Throw a))$
and $\tau\text{move0AAccThrow1}$: $\tau\text{move0 } P h (Throw a[e])$
and $\tau\text{move0AAccThrow2}$: $\tau\text{move0 } P h (Val v \lfloor Throw a \rfloor)$
and $\tau\text{move0AAssThrow1}$: $\tau\text{move0 } P h (AAss (Throw a) e e')$
and $\tau\text{move0AAssThrow2}$: $\tau\text{move0 } P h (AAss (Val v) (Throw a) e')$
and $\tau\text{move0AAssThrow3}$: $\tau\text{move0 } P h (AAss (Val v) (Val v') (Throw a))$
and $\tau\text{move0ALengthThrow}$: $\tau\text{move0 } P h (Throw a.length)$
and $\tau\text{move0FAccThrow}$: $\tau\text{move0 } P h (Throw a.F\{D\})$
and $\tau\text{move0FAssThrow1}$: $\tau\text{move0 } P h (Throw a.F\{D\} := e)$
and $\tau\text{move0FAssThrow2}$: $\tau\text{move0 } P h (FAss (Val v) F D (Throw a))$
and $\tau\text{move0CallThrowObj}$: $\tau\text{move0 } P h (Throw a.M(es))$
and $\tau\text{move0CallThrowParams}$: $\tau\text{move0 } P h (Val v.M(\text{map } Val vs @ Throw a \# es))$
and $\tau\text{move0BlockThrow}$: $\tau\text{move0 } P h \{V:T=vo; Throw a\}$
and $\tau\text{move0SyncThrow}$: $\tau\text{move0 } P h (\text{sync}_{V'}(Throw a) e)$
and $\tau\text{move0SeqThrow}$: $\tau\text{move0 } P h (Throw a;;e)$

and $\tau\text{move0CondThrow}$: $\tau\text{move0 } P \ h \ (if \ (Throw \ a) \ e1 \ else \ e2)$
and $\tau\text{move0ThrowThrow}$: $\tau\text{move0 } P \ h \ (throw \ (Throw \ a))$

and $\tau\text{moves0Hd}$: $\tau\text{move0 } P \ h \ e \implies \tau\text{moves0 } P \ h \ (e \ \# \ es)$
and $\tau\text{moves0Tl}$: $\tau\text{moves0 } P \ h \ es \implies \tau\text{moves0 } P \ h \ (Val \ v \ \# \ es)$
 $\langle\text{proof}\rangle$

lemma $\tau\text{moves0-map-Val}$ [iff]:
 $\neg \tau\text{moves0 } P \ h \ (map \ Val \ vs)$
 $\langle\text{proof}\rangle$

lemma $\tau\text{moves0-map-Val-append}$ [simp]:
 $\tau\text{moves0 } P \ h \ (map \ Val \ vs \ @ \ es) = \tau\text{moves0 } P \ h \ es$
 $\langle\text{proof}\rangle$

lemma $\text{no-reds-map-Val-Throw}$ [simp]:
 $extTA, P, t \vdash \langle map \ Val \ vs \ @ \ Throw \ a \ \# \ es, s \rangle [-ta \rightarrow] \langle es', s' \rangle = False$
 $\langle\text{proof}\rangle$

lemma **assumes** [simp]: $extTA \ \varepsilon = \varepsilon$
shows $\text{red-}\tau\text{-taD}$: $\llbracket extTA, P, t \vdash \langle e, s \rangle [-ta \rightarrow] \langle e', s' \rangle; \tau\text{move0 } P \ (hp \ s) \ e \rrbracket \implies ta = \varepsilon$
and $\text{reds-}\tau\text{-taD}$: $\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \tau\text{moves0 } P \ (hp \ s) \ es \rrbracket \implies ta = \varepsilon$
 $\langle\text{proof}\rangle$

lemma $\tau\text{move0-heap-unchanged}$: $\llbracket extTA, P, t \vdash \langle e, s \rangle [-ta \rightarrow] \langle e', s' \rangle; \tau\text{move0 } P \ (hp \ s) \ e \rrbracket \implies hp \ s' = hp \ s$
and $\tau\text{moves0-heap-unchanged}$: $\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \tau\text{moves0 } P \ (hp \ s) \ es \rrbracket \implies hp \ s' = hp \ s$
 $\langle\text{proof}\rangle$

lemma $\tau\text{Move0-iff}$:
 $\tau\text{Move0 } P \ h \ ees \longleftrightarrow (let \ (e, -) = ees \ in \ \tau\text{move0 } P \ h \ e \ \vee \ final \ e)$
 $\langle\text{proof}\rangle$

lemma no-call-simps [simp]:
 $no\text{-call } P \ h \ (new \ C) = True$
 $no\text{-call } P \ h \ (newA \ T[e]) = no\text{-call } P \ h \ e$
 $no\text{-call } P \ h \ (Cast \ T \ e) = no\text{-call } P \ h \ e$
 $no\text{-call } P \ h \ (e \ instanceof \ T) = no\text{-call } P \ h \ e$
 $no\text{-call } P \ h \ (Val \ v) = True$
 $no\text{-call } P \ h \ (Var \ V) = True$
 $no\text{-call } P \ h \ (V := e) = no\text{-call } P \ h \ e$
 $no\text{-call } P \ h \ (e \ \ll\text{bop}\ e') = (if \ is\text{-val} \ e \ then \ no\text{-call } P \ h \ e' \ else \ no\text{-call } P \ h \ e)$
 $no\text{-call } P \ h \ (a[i]) = (if \ is\text{-val} \ a \ then \ no\text{-call } P \ h \ i \ else \ no\text{-call } P \ h \ a)$
 $no\text{-call } P \ h \ (AAss \ a \ i \ e) = (if \ is\text{-val} \ a \ then \ (if \ is\text{-val} \ i \ then \ no\text{-call } P \ h \ e \ else \ no\text{-call } P \ h \ i) \ else \ no\text{-call } P \ h \ a)$
 $no\text{-call } P \ h \ (a \cdot length) = no\text{-call } P \ h \ a$
 $no\text{-call } P \ h \ (e \cdot F\{D\}) = no\text{-call } P \ h \ e$
 $no\text{-call } P \ h \ (FAss \ e \ F \ D \ e') = (if \ is\text{-val} \ e \ then \ no\text{-call } P \ h \ e' \ else \ no\text{-call } P \ h \ e)$
 $no\text{-call } P \ h \ (e \cdot compareAndSwap(D \cdot F, e', e'')) = (if \ is\text{-val} \ e \ then \ (if \ is\text{-val} \ e' \ then \ no\text{-call } P \ h \ e'' \ else \ no\text{-call } P \ h \ e') \ else \ no\text{-call } P \ h \ e)$
 $no\text{-call } P \ h \ (e \cdot M(es)) = (if \ is\text{-val} \ e \ then \ (if \ is\text{-vals} \ es \ \wedge \ is\text{-addr} \ e \ then \ synthesized\text{-call } P \ h \ (THE \ a. \ e = addr \ a, \ M, \ THE \ vs. \ es = map \ Val \ vs) \ else \ no\text{-calls } P \ h \ es) \ else \ no\text{-call } P \ h \ e)$
 $no\text{-call } P \ h \ (\{V:T=vo; e\}) = no\text{-call } P \ h \ e$

$no\text{-}call\ P\ h\ (sync_{V'}\ (e)\ e') = no\text{-}call\ P\ h\ e$
 $no\text{-}call\ P\ h\ (insync_{V'}\ (ad)\ e) = no\text{-}call\ P\ h\ e$
 $no\text{-}call\ P\ h\ (e;;e') = no\text{-}call\ P\ h\ e$
 $no\text{-}call\ P\ h\ (if\ (e)\ e1\ else\ e2) = no\text{-}call\ P\ h\ e$
 $no\text{-}call\ P\ h\ (while(e)\ e') = True$
 $no\text{-}call\ P\ h\ (throw\ e) = no\text{-}call\ P\ h\ e$
 $no\text{-}call\ P\ h\ (try\ e\ catch(C\ V)\ e') = no\text{-}call\ P\ h\ e$
 ⟨proof⟩

lemma *no-calls-simps* [simp]:

$no\text{-}calls\ P\ h\ [] = True$
 $no\text{-}calls\ P\ h\ (e\ \#\ es) = (if\ is\text{-}val\ e\ then\ no\text{-}calls\ P\ h\ es\ else\ no\text{-}call\ P\ h\ e)$
 ⟨proof⟩

lemma *no-calls-map-Val* [simp]:

$no\text{-}calls\ P\ h\ (map\ Val\ vs)$
 ⟨proof⟩

lemma *assumes nfin*: $\neg\ final\ e'$

shows *inline-call- τ move0-inv*: $call\ e = [aMvs] \implies \tau\ move0\ P\ h\ (inline\text{-}call\ e'\ e) = \tau\ move0\ P\ h\ e'$
and *inline-calls- τ moves0-inv*: $calls\ es = [aMvs] \implies \tau\ moves0\ P\ h\ (inline\text{-}calls\ e'\ es) = \tau\ move0\ P\ h\ e'$
 ⟨proof⟩

lemma *τ red0-iff* [iff]:

$\tau\ red0\ extTA\ P\ t\ h\ (e,\ xs)\ (e',\ xs') = (extTA,P,t\ \vdash\ \langle e,\ (h,\ xs) \rangle\ \text{-}\varepsilon\ \rightarrow\ \langle e',\ (h,\ xs') \rangle) \wedge \tau\ move0\ P\ h\ e\ \wedge\ no\text{-}call\ P\ h\ e$
 ⟨proof⟩

lemma *τ reds0-iff* [iff]:

$\tau\ reds0\ extTA\ P\ t\ h\ (es,\ xs)\ (es',\ xs') =$
 $(extTA,P,t\ \vdash\ \langle es,\ (h,\ xs) \rangle\ \text{-}\varepsilon\ \rightarrow\ \langle es',\ (h,\ xs') \rangle) \wedge \tau\ moves0\ P\ h\ es\ \wedge\ no\text{-}calls\ P\ h\ es$
 ⟨proof⟩

lemma *τ red0t-1step*:

$\llbracket extTA,P,t\ \vdash\ \langle e,\ (h,\ xs) \rangle\ \text{-}\varepsilon\ \rightarrow\ \langle e',\ (h,\ xs') \rangle; \tau\ move0\ P\ h\ e; no\text{-}call\ P\ h\ e \rrbracket$
 $\implies \tau\ red0t\ extTA\ P\ t\ h\ (e,\ xs)\ (e',\ xs')$
 ⟨proof⟩

lemma *τ red0t-2step*:

$\llbracket extTA,P,t\ \vdash\ \langle e,\ (h,\ xs) \rangle\ \text{-}\varepsilon\ \rightarrow\ \langle e',\ (h,\ xs') \rangle; \tau\ move0\ P\ h\ e; no\text{-}call\ P\ h\ e;$
 $extTA,P,t\ \vdash\ \langle e',\ (h,\ xs') \rangle\ \text{-}\varepsilon\ \rightarrow\ \langle e'',\ (h,\ xs'') \rangle; \tau\ move0\ P\ h\ e'; no\text{-}call\ P\ h\ e' \rrbracket$
 $\implies \tau\ red0t\ extTA\ P\ t\ h\ (e,\ xs)\ (e'',\ xs'')$
 ⟨proof⟩

lemma *τ red1t-3step*:

$\llbracket extTA,P,t\ \vdash\ \langle e,\ (h,\ xs) \rangle\ \text{-}\varepsilon\ \rightarrow\ \langle e',\ (h,\ xs') \rangle; \tau\ move0\ P\ h\ e; no\text{-}call\ P\ h\ e;$
 $extTA,P,t\ \vdash\ \langle e',\ (h,\ xs') \rangle\ \text{-}\varepsilon\ \rightarrow\ \langle e'',\ (h,\ xs'') \rangle; \tau\ move0\ P\ h\ e'; no\text{-}call\ P\ h\ e';$
 $extTA,P,t\ \vdash\ \langle e'',\ (h,\ xs'') \rangle\ \text{-}\varepsilon\ \rightarrow\ \langle e''',\ (h,\ xs''') \rangle; \tau\ move0\ P\ h\ e''; no\text{-}call\ P\ h\ e'' \rrbracket$
 $\implies \tau\ red0t\ extTA\ P\ t\ h\ (e,\ xs)\ (e''',\ xs''')$
 ⟨proof⟩

lemma *τ reds0t-1step*:

$\llbracket extTA,P,t\ \vdash\ \langle es,\ (h,\ xs) \rangle\ \text{-}\varepsilon\ \rightarrow\ \langle es',\ (h,\ xs') \rangle; \tau\ moves0\ P\ h\ es; no\text{-}calls\ P\ h\ es \rrbracket$

$\implies \tau\text{reds0t extTA P t h } (e \# es, xs) (e' \# es, xs')$
 ⟨proof⟩

lemma $\tau\text{reds0t-cons-}\tau\text{reds0t}$:

$\tau\text{reds0t extTA P t h } (es, xs) (es', xs')$
 $\implies \tau\text{reds0t extTA P t h } (\text{Val } v \# es, xs) (\text{Val } v \# es', xs')$
 ⟨proof⟩

lemma $\tau\text{red0r-inj-}\tau\text{reds0r}$:

$\tau\text{red0r extTA P t h } (e, xs) (e', xs')$
 $\implies \tau\text{reds0r extTA P t h } (e \# es, xs) (e' \# es, xs')$
 ⟨proof⟩

lemma $\tau\text{reds0r-cons-}\tau\text{reds0r}$:

$\tau\text{reds0r extTA P t h } (es, xs) (es', xs')$
 $\implies \tau\text{reds0r extTA P t h } (\text{Val } v \# es, xs) (\text{Val } v \# es', xs')$
 ⟨proof⟩

lemma $\text{NewArray-}\tau\text{red0t-xt}$:

$\tau\text{red0t extTA P t h } (e, xs) (e', xs')$
 $\implies \tau\text{red0t extTA P t h } (\text{newA } T[e], xs) (\text{newA } T[e'], xs')$
 ⟨proof⟩

lemma $\text{Cast-}\tau\text{red0t-xt}$:

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (\text{Cast } T e, xs) (\text{Cast } T e', xs')$
 ⟨proof⟩

lemma $\text{InstanceOf-}\tau\text{red0t-xt}$:

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (e \text{ instanceof } T, xs) (e' \text{ instanceof } T, xs')$
 ⟨proof⟩

lemma $\text{BinOp-}\tau\text{red0t-xt1}$:

$\tau\text{red0t extTA P t h } (e1, xs) (e1', xs') \implies \tau\text{red0t extTA P t h } (e1 \llbracket \text{bop} \rrbracket e2, xs) (e1' \llbracket \text{bop} \rrbracket e2, xs')$
 ⟨proof⟩

lemma $\text{BinOp-}\tau\text{red0t-xt2}$:

$\tau\text{red0t extTA P t h } (e2, xs) (e2', xs') \implies \tau\text{red0t extTA P t h } (\text{Val } v \llbracket \text{bop} \rrbracket e2, xs) (\text{Val } v \llbracket \text{bop} \rrbracket e2', xs')$
 ⟨proof⟩

lemma $\text{LAss-}\tau\text{red0t}$:

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (V := e, xs) (V := e', xs')$
 ⟨proof⟩

lemma $\text{AAcc-}\tau\text{red0t-xt1}$:

$\tau\text{red0t extTA P t h } (a, xs) (a', xs') \implies \tau\text{red0t extTA P t h } (a[i], xs) (a'[i], xs')$
 ⟨proof⟩

lemma $\text{AAcc-}\tau\text{red0t-xt2}$:

$\tau\text{red0t extTA P t h } (i, xs) (i', xs') \implies \tau\text{red0t extTA P t h } (\text{Val } a[i], xs) (\text{Val } a[i'], xs')$
 ⟨proof⟩

lemma $\text{AAss-}\tau\text{red0t-xt1}$:

$\tau\text{red0t extTA P t h } (a, xs) (a', xs') \implies \tau\text{red0t extTA P t h } (a[i] := e, xs) (a'[i] := e, xs')$

$\langle \text{proof} \rangle$

lemma *AAss- τ red0t-xt2:*

$\tau\text{red0t extTA P t h } (i, xs) (i', xs') \implies \tau\text{red0t extTA P t h } (Val\ a[i] := e, xs) (Val\ a[i'] := e, xs')$
 $\langle \text{proof} \rangle$

lemma *AAss- τ red0t-xt3:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (Val\ a[Val\ i] := e, xs) (Val\ a[Val\ i] := e', xs')$
 $\langle \text{proof} \rangle$

lemma *ALength- τ red0t-xt:*

$\tau\text{red0t extTA P t h } (a, xs) (a', xs') \implies \tau\text{red0t extTA P t h } (a.\text{length}, xs) (a'.\text{length}, xs')$
 $\langle \text{proof} \rangle$

lemma *FAcc- τ red0t-xt:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (e.F\{D\}, xs) (e'.F\{D\}, xs')$
 $\langle \text{proof} \rangle$

lemma *FAss- τ red0t-xt1:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (e.F\{D\} := e2, xs) (e'.F\{D\} := e2, xs')$
 $\langle \text{proof} \rangle$

lemma *FAss- τ red0t-xt2:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (Val\ v.F\{D\} := e, xs) (Val\ v.F\{D\} := e', xs')$
 $\langle \text{proof} \rangle$

lemma *CAS- τ red0t-xt1:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (e.\text{compareAndSwap}(D.F, e2, e3), xs) (e'.\text{compareAndSwap}(D.F, e2, e3), xs')$
 $\langle \text{proof} \rangle$

lemma *CAS- τ red0t-xt2:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (Val\ v.\text{compareAndSwap}(D.F, e, e3), xs) (Val\ v.\text{compareAndSwap}(D.F, e', e3), xs')$
 $\langle \text{proof} \rangle$

lemma *CAS- τ red0t-xt3:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (Val\ v.\text{compareAndSwap}(D.F, Val\ v', e), xs) (Val\ v.\text{compareAndSwap}(D.F, Val\ v', e'), xs')$
 $\langle \text{proof} \rangle$

lemma *Call- τ red0t-obj:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (e.M(ps), xs) (e'.M(ps), xs')$
 $\langle \text{proof} \rangle$

lemma *Call- τ red0t-param:*

$\tau\text{red0t extTA P t h } (es, xs) (es', xs') \implies \tau\text{red0t extTA P t h } (Val\ v.M(es), xs) (Val\ v.M(es'), xs')$
 $\langle \text{proof} \rangle$

lemma *Block- τ red0t-xt:*

$\tau\text{red0t extTA P t h } (e, xs(V := vo)) (e', xs') \implies \tau\text{red0t extTA P t h } (\{V:T=vo; e\}, xs) (\{V:T=xs' V; e'\}, xs'(V := xs\ V))$

$\langle \text{proof} \rangle$

lemma *Sync- τ red0t-xt:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (\text{sync}_V (e) e2, xs) (\text{sync}_V (e') e2, xs')$
 $\langle \text{proof} \rangle$

lemma *InSync- τ red0t-xt:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (\text{insync}_V (a) e, xs) (\text{insync}_V (a) e', xs')$
 $\langle \text{proof} \rangle$

lemma *Seq- τ red0t-xt:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (e;;e2, xs) (e';;e2, xs')$
 $\langle \text{proof} \rangle$

lemma *Cond- τ red0t-xt:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (\text{if } (e) e1 \text{ else } e2, xs) (\text{if } (e') e1 \text{ else } e2, xs')$
 $\langle \text{proof} \rangle$

lemma *Throw- τ red0t-xt:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (\text{throw } e, xs) (\text{throw } e', xs')$
 $\langle \text{proof} \rangle$

lemma *Try- τ red0t-xt:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (\text{try } e \text{ catch } (C V) e2, xs) (\text{try } e' \text{ catch } (C V) e2, xs')$
 $\langle \text{proof} \rangle$

lemma *NewArray- τ red0r-xt:*

$\tau\text{red0r extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA P t h } (\text{newA } T[e], xs) (\text{newA } T[e'], xs')$
 $\langle \text{proof} \rangle$

lemma *Cast- τ red0r-xt:*

$\tau\text{red0r extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA P t h } (\text{Cast } T e, xs) (\text{Cast } T e', xs')$
 $\langle \text{proof} \rangle$

lemma *InstanceOf- τ red0r-xt:*

$\tau\text{red0r extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA P t h } (e \text{ instanceof } T, xs) (e' \text{ instanceof } T, xs')$
 $\langle \text{proof} \rangle$

lemma *BinOp- τ red0r-xt1:*

$\tau\text{red0r extTA P t h } (e1, xs) (e1', xs') \implies \tau\text{red0r extTA P t h } (e1 \ll \text{bop} \gg e2, xs) (e1' \ll \text{bop} \gg e2, xs')$
 $\langle \text{proof} \rangle$

lemma *BinOp- τ red0r-xt2:*

$\tau\text{red0r extTA P t h } (e2, xs) (e2', xs') \implies \tau\text{red0r extTA P t h } (\text{Val } v \ll \text{bop} \gg e2, xs) (\text{Val } v \ll \text{bop} \gg e2', xs')$
 $\langle \text{proof} \rangle$

lemma *LAss- τ red0r:*

$\tau\text{red0r extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA P t h } (V := e, xs) (V := e', xs')$
 $\langle \text{proof} \rangle$

lemma *AAcc- τ red0r-xt1:*

$\tau red0r extTA P t h (a, xs) (a', xs') \implies \tau red0r extTA P t h (a[i], xs) (a'[i], xs')$
 ⟨proof⟩

lemma *AAcc- $\tau red0r$ -xt2:*

$\tau red0r extTA P t h (i, xs) (i', xs') \implies \tau red0r extTA P t h (Val a[i], xs) (Val a[i'], xs')$
 ⟨proof⟩

lemma *AAss- $\tau red0r$ -xt1:*

$\tau red0r extTA P t h (a, xs) (a', xs') \implies \tau red0r extTA P t h (a[i] := e, xs) (a'[i] := e, xs')$
 ⟨proof⟩

lemma *AAss- $\tau red0r$ -xt2:*

$\tau red0r extTA P t h (i, xs) (i', xs') \implies \tau red0r extTA P t h (Val a[i] := e, xs) (Val a[i'] := e, xs')$
 ⟨proof⟩

lemma *AAss- $\tau red0r$ -xt3:*

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (Val a[Val i] := e, xs) (Val a[Val i'] := e', xs')$
 ⟨proof⟩

lemma *ALength- $\tau red0r$ -xt:*

$\tau red0r extTA P t h (a, xs) (a', xs') \implies \tau red0r extTA P t h (a.length, xs) (a'.length, xs')$
 ⟨proof⟩

lemma *FAcc- $\tau red0r$ -xt:*

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (e.F\{D\}, xs) (e'.F\{D\}, xs')$
 ⟨proof⟩

lemma *FAss- $\tau red0r$ -xt1:*

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (e.F\{D\} := e2, xs) (e'.F\{D\} := e2, xs')$
 ⟨proof⟩

lemma *FAss- $\tau red0r$ -xt2:*

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (Val v.F\{D\} := e, xs) (Val v.F\{D\} := e', xs')$
 ⟨proof⟩

lemma *CAS- $\tau red0r$ -xt1:*

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (e.compareAndSwap(D.F, e2, e3), xs) (e'.compareAndSwap(D.F, e2, e3), xs')$
 ⟨proof⟩

lemma *CAS- $\tau red0r$ -xt2:*

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (Val v.compareAndSwap(D.F, e, e3), xs) (Val v.compareAndSwap(D.F, e', e3), xs')$
 ⟨proof⟩

lemma *CAS- $\tau red0r$ -xt3:*

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (Val v.compareAndSwap(D.F, Val v', e), xs) (Val v.compareAndSwap(D.F, Val v', e'), xs')$
 ⟨proof⟩

lemma *Call- $\tau red0r$ -obj:*

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (e.M(ps), xs) (e'.M(ps), xs')$

$\langle \text{proof} \rangle$

lemma *Call- τ red0r-param:*

$\tau \text{red0r extTA P t h } (es, xs) (es', xs') \implies \tau \text{red0r extTA P t h } (Val\ v \cdot M(es), xs) (Val\ v \cdot M(es'), xs')$
 $\langle \text{proof} \rangle$

lemma *Block- τ red0r-xt:*

$\tau \text{red0r extTA P t h } (e, xs(V := vo)) (e', xs') \implies \tau \text{red0r extTA P t h } (\{V:T=vo; e\}, xs) (\{V:T=xs' V; e'\}, xs'(V := xs\ V))$
 $\langle \text{proof} \rangle$

lemma *Sync- τ red0r-xt:*

$\tau \text{red0r extTA P t h } (e, xs) (e', xs') \implies \tau \text{red0r extTA P t h } (\text{sync}_V(e)\ e2, xs) (\text{sync}_V(e')\ e2, xs')$
 $\langle \text{proof} \rangle$

lemma *InSync- τ red0r-xt:*

$\tau \text{red0r extTA P t h } (e, xs) (e', xs') \implies \tau \text{red0r extTA P t h } (\text{insync}_V(a)\ e, xs) (\text{insync}_V(a)\ e', xs')$
 $\langle \text{proof} \rangle$

lemma *Seq- τ red0r-xt:*

$\tau \text{red0r extTA P t h } (e, xs) (e', xs') \implies \tau \text{red0r extTA P t h } (e;;e2, xs) (e';;e2, xs')$
 $\langle \text{proof} \rangle$

lemma *Cond- τ red0r-xt:*

$\tau \text{red0r extTA P t h } (e, xs) (e', xs') \implies \tau \text{red0r extTA P t h } (\text{if } (e)\ e1\ \text{else } e2, xs) (\text{if } (e')\ e1\ \text{else } e2, xs')$
 $\langle \text{proof} \rangle$

lemma *Throw- τ red0r-xt:*

$\tau \text{red0r extTA P t h } (e, xs) (e', xs') \implies \tau \text{red0r extTA P t h } (\text{throw } e, xs) (\text{throw } e', xs')$
 $\langle \text{proof} \rangle$

lemma *Try- τ red0r-xt:*

$\tau \text{red0r extTA P t h } (e, xs) (e', xs') \implies \tau \text{red0r extTA P t h } (\text{try } e\ \text{catch}(C\ V)\ e2, xs) (\text{try } e'\ \text{catch}(C\ V)\ e2, xs')$
 $\langle \text{proof} \rangle$

lemma τ Red0-conv [iff]:

$\tau \text{Red0 P t h } (e, es) (e', es') = (P, t \vdash 0 \langle e/es, h \rangle \xrightarrow{-\varepsilon} \langle e'/es', h \rangle \wedge \tau \text{Move0 P t h } (e, es))$
 $\langle \text{proof} \rangle$

lemma τ red0r-lcl-incr:

$\tau \text{red0r extTA P t h } (e, xs) (e', xs') \implies \text{dom } xs \subseteq \text{dom } xs'$
 $\langle \text{proof} \rangle$

lemma τ red0t-lcl-incr:

$\tau \text{red0t extTA P t h } (e, xs) (e', xs') \implies \text{dom } xs \subseteq \text{dom } xs'$
 $\langle \text{proof} \rangle$

lemma τ red0r-dom-lcl:

assumes *wwf: wwf-J-prog P*
shows $\tau \text{red0r extTA P t h } (e, xs) (e', xs') \implies \text{dom } xs' \subseteq \text{dom } xs \cup \text{fv } e$
 $\langle \text{proof} \rangle$

lemma $\tau red0t\text{-}dom\text{-}lcl$:

assumes $wwf: wwf\text{-}J\text{-}prog\ P$

shows $\tau red0t\ extTA\ P\ t\ h\ (e, xs)\ (e', xs') \implies dom\ xs' \subseteq dom\ xs \cup fv\ e$

$\langle proof \rangle$

lemma $\tau red0r\text{-}fv\text{-}subset$:

assumes $wwf: wwf\text{-}J\text{-}prog\ P$

shows $\tau red0r\ extTA\ P\ t\ h\ (e, xs)\ (e', xs') \implies fv\ e' \subseteq fv\ e$

$\langle proof \rangle$

lemma $\tau red0t\text{-}fv\text{-}subset$:

assumes $wwf: wwf\text{-}J\text{-}prog\ P$

shows $\tau red0t\ extTA\ P\ t\ h\ (e, xs)\ (e', xs') \implies fv\ e' \subseteq fv\ e$

$\langle proof \rangle$

lemma **fixes** $e :: ('a, 'b, 'addr)\ exp$ **and** $es :: ('a, 'b, 'addr)\ exp\ list$

shows $\tau move0\text{-}callD: call\ e = [(a, M, vs)] \implies \tau move0\ P\ h\ e \longleftrightarrow (synthesized\text{-}call\ P\ h\ (a, M, vs) \longrightarrow \tau external'\ P\ h\ a\ M)$

and $\tau moves0\text{-}callsD: calls\ es = [(a, M, vs)] \implies \tau moves0\ P\ h\ es \longleftrightarrow (synthesized\text{-}call\ P\ h\ (a, M, vs) \longrightarrow \tau external'\ P\ h\ a\ M)$

$\langle proof \rangle$

lemma **fixes** $e :: ('a, 'b, 'addr)\ exp$ **and** $es :: ('a, 'b, 'addr)\ exp\ list$

shows $\tau move0\text{-}not\text{-}call: [\tau move0\ P\ h\ e; call\ e = [(a, M, vs)]; synthesized\text{-}call\ P\ h\ (a, M, vs)] \implies \tau external'\ P\ h\ a\ M$

and $\tau moves0\text{-}not\text{-}calls: [\tau moves0\ P\ h\ es; calls\ es = [(a, M, vs)]; synthesized\text{-}call\ P\ h\ (a, M, vs)] \implies \tau external'\ P\ h\ a\ M$

$\langle proof \rangle$

lemma $\tau red0\text{-}into\text{-}\tau Red0$:

assumes $red: \tau red0\ (extTA2J0\ P)\ P\ t\ h\ (e, Map.empty)\ (e', xs')$

shows $\tau Red0\ P\ t\ h\ (e, es)\ (e', es)$

$\langle proof \rangle$

lemma $\tau red0r\text{-}into\text{-}\tau Red0r$:

assumes $wwf: wwf\text{-}J\text{-}prog\ P$

shows

$[\tau red0r\ (extTA2J0\ P)\ P\ t\ h\ (e, Map.empty)\ (e'', Map.empty); fv\ e = \{\}]$

$\implies \tau Red0r\ P\ t\ h\ (e, es)\ (e'', es)$

$\langle proof \rangle$

lemma $\tau red0t\text{-}into\text{-}\tau Red0t$:

assumes $wwf: wwf\text{-}J\text{-}prog\ P$

shows

$[\tau red0t\ (extTA2J0\ P)\ P\ t\ h\ (e, Map.empty)\ (e'', Map.empty); fv\ e = \{\}]$

$\implies \tau Red0t\ P\ t\ h\ (e, es)\ (e'', es)$

$\langle proof \rangle$

lemma $\tau red0r\text{-}Val$:

$\tau red0r\ extTA\ P\ t\ h\ (Val\ v, xs)\ s' \longleftrightarrow s' = (Val\ v, xs)$

$\langle proof \rangle$

lemma $\tau red0t\text{-}Val$:

$\tau red0t \text{ extTA } P t h (Val v, xs) s' \longleftrightarrow False$
 ⟨proof⟩

lemma $\tau reds0r\text{-map-Val}$:

$\tau reds0r \text{ extTA } P t h (\text{map Val } vs, xs) s' \longleftrightarrow s' = (\text{map Val } vs, xs)$
 ⟨proof⟩

lemma $\tau reds0t\text{-map-Val}$:

$\tau reds0t \text{ extTA } P t h (\text{map Val } vs, xs) s' \longleftrightarrow False$
 ⟨proof⟩

lemma *Red-Suspend-is-call*:

$\llbracket P, t \vdash 0 \langle e/exs, h \rangle -ta \rightarrow \langle e'/exs', h' \rangle; \text{Suspend } w \in \text{set } \{\{ta\}_w\} \rrbracket \implies \text{is-call } e'$
 ⟨proof⟩

lemma *red0-mthr*: *multithreaded final-expr0* (*mred0* *P*)

⟨proof⟩

lemma *red0- τ mthr-wf*: τ *multithreaded-wf final-expr0* (*mred0* *P*) (τ *MOVE0* *P*)

⟨proof⟩

lemma *red- τ mthr-wf*: τ *multithreaded-wf final-expr* (*mred* *P*) (τ *MOVE* *P*)

⟨proof⟩

end

sublocale *J-heap-base* < *red-mthr*:

τ *multithreaded-wf*
final-expr
mred *P*
convert-RA
 τ *MOVE* *P*

for *P*

⟨proof⟩

sublocale *J-heap-base* < *red0-mthr*:

τ *multithreaded-wf*
final-expr0
mred0 *P*
convert-RA
 τ *MOVE0* *P*

for *P*

⟨proof⟩

context *J-heap-base* **begin**

lemma τ *Red0r-into-red0- τ mthr-silent-moves*:

$\tau Red0r P t h (e, es) (e'', es'') \implies \text{red0-mthr.silent-moves } P t ((e, es), h) ((e'', es''), h)$
 ⟨proof⟩

lemma τ *Red0t-into-red0- τ mthr-silent-movet*:

$\tau Red0t P t h (e, es) (e'', es'') \implies \text{red0-mthr.silent-movet } P t ((e, es), h) ((e'', es''), h)$
 ⟨proof⟩

end

end

7.3 Bisimulation proof for between source code small step semantics with and without callstacks for single threads

theory *J0Bisim* **imports**

J0
../J/JWellForm
../Common/ExternalCallWF

begin

inductive *wf-state* :: *'addr expr* × *'addr expr list* ⇒ *bool*

where

$\llbracket \text{fvs } (e \# es) = \{\}; \forall e \in \text{set } es. \text{is-call } e \rrbracket$
 ⇒ *wf-state* (*e*, *es*)

inductive *bisim-red-red0* :: (*'addr expr* × *'addr locals*) × *'heap* ⇒ (*'addr expr* × *'addr expr list*) × *'heap* ⇒ *bool*

where

wf-state ees ⇒ *bisim-red-red0* ((*collapse ees*, *Map.empty*), *h*) (*ees*, *h*)

abbreviation *ta-bisim0* :: (*'addr*, *'thread-id*, *'heap*) *J-thread-action* ⇒ (*'addr*, *'thread-id*, *'heap*) *J0-thread-action* ⇒ *bool*

where *ta-bisim0* ≡ *ta-bisim* ($\lambda t. \text{bisim-red-red0}$)

lemma *wf-state-iff* [*simp*, *code*]:

$\text{wf-state } (e, es) \longleftrightarrow \text{fvs } (e \# es) = \{\} \wedge (\forall e \in \text{set } es. \text{is-call } e)$
 ⟨*proof*⟩

lemma *bisim-red-red0I* [*intro*]:

$\llbracket e' = \text{collapse } ees; xs = \text{Map.empty}; h' = h; \text{wf-state } ees \rrbracket \Longrightarrow \text{bisim-red-red0 } ((e', xs), h') (ees, h)$
 ⟨*proof*⟩

lemma *bisim-red-red0-final0D*:

$\llbracket \text{bisim-red-red0 } (x1, m1) (x2, m2); \text{final-expr0 } x2 \rrbracket \Longrightarrow \text{final-expr } x1$
 ⟨*proof*⟩

context *J-heap-base* **begin**

lemma *red0-preserves-wf-state*:

assumes *wf*: *wf-J-prog P*

and *red*: $P, t \vdash_0 \langle e / es, h \rangle -ta \rightarrow \langle e' / es', h' \rangle$

and *wf-state*: *wf-state* (*e*, *es*)

shows *wf-state* (*e'*, *es'*)

⟨*proof*⟩

lemma *new-thread-bisim0-extNTA2J-extNTA2J0*:

assumes *wf*: *wf-J-prog P*

and *red*: $P, t \vdash \langle a' \cdot M'(vs), h \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle$

and *nt*: *NewThread t' CMa m* ∈ *set* $\{\text{ta}\}_t$

shows *bisim-red-red0* (*extNTA2J P CMa, m*) (*extNTA2J0 P CMa, m*)
 ⟨*proof*⟩

lemma *ta-bisim0-extNTA2J-extNTA2J0*:
 $\llbracket \text{wuf-J-prog } P; P, t \vdash \langle a' \cdot M'(vs), h \rangle -ta \rightarrow \text{ext} \langle va, h' \rangle \rrbracket$
 $\implies \text{ta-bisim0} (\text{extTA2J } P \text{ ta}) (\text{extTA2J0 } P \text{ ta})$
 ⟨*proof*⟩

lemma assumes *wf: wuf-J-prog P*
shows *red-red0-tabisim0*:
 $P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \exists ta'. \text{extTA2J0 } P, P, t \vdash \langle e, s \rangle -ta' \rightarrow \langle e', s' \rangle \wedge \text{ta-bisim0 } ta \text{ ta}'$
and *reds-reds0-tabisim0*:
 $P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \exists ta'. \text{extTA2J0 } P, P, t \vdash \langle es, s \rangle [-ta' \rightarrow] \langle es', s' \rangle \wedge \text{ta-bisim0 } ta \text{ ta}'$
 ⟨*proof*⟩

lemma assumes *wf: wuf-J-prog P*
shows *red0-red-tabisim0*:
 $\text{extTA2J0 } P, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \exists ta'. P, t \vdash \langle e, s \rangle -ta' \rightarrow \langle e', s' \rangle \wedge \text{ta-bisim0 } ta' \text{ ta}$
and *reds0-reds-tabisim0*:
 $\text{extTA2J0 } P, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \exists ta'. P, t \vdash \langle es, s \rangle [-ta' \rightarrow] \langle es', s' \rangle \wedge \text{ta-bisim0 } ta' \text{ ta}$
 ⟨*proof*⟩

lemma *red-inline-call-red*:
assumes *red: P, t ⊢ ⟨e, (h, Map.empty)⟩ -ta → ⟨e', (h', Map.empty)⟩*
shows *call E = [aMvs] ⇒ P, t ⊢ ⟨inline-call e E, (h, x)⟩ -ta → ⟨inline-call e' E, (h', x)⟩*
 (**is** - ⇒ ?*concl E x*)
and
calls Es = [aMvs] ⇒ P, t ⊢ ⟨inline-calls e Es, (h, x)⟩ [-ta →] ⟨inline-calls e' Es, (h', x)⟩
 (**is** - ⇒ ?*concls Es x*)
 ⟨*proof*⟩

lemma
assumes *P ⊢ class-type-of T sees M:Us → U = [(pns, body)] in D length vs = length pns length Us = length pns*
shows *is-call-red-inline-call*:
 $\llbracket \text{call } e = \llbracket (a, M, vs) \rrbracket; \text{typeof-addr } (hp \ s) \ a = \llbracket T \rrbracket \rrbracket$
 $\implies P, t \vdash \langle e, s \rangle -\varepsilon \rightarrow \langle \text{inline-call } (\text{blocks } (\text{this} \# \text{pns}) (\text{Class } D \# Us) (\text{Addr } a \# vs) \text{ body}) \ e, s \rangle$
 (**is** - ⇒ - ⇒ ?*red e s*)
and *is-calls-reds-inline-calls*:
 $\llbracket \text{calls } es = \llbracket (a, M, vs) \rrbracket; \text{typeof-addr } (hp \ s) \ a = \llbracket T \rrbracket \rrbracket$
 $\implies P, t \vdash \langle es, s \rangle [-\varepsilon \rightarrow] \langle \text{inline-calls } (\text{blocks } (\text{this} \# \text{pns}) (\text{Class } D \# Us) (\text{Addr } a \# vs) \text{ body}) \ es, s \rangle$
 (**is** - ⇒ - ⇒ ?*reds es s*)
 ⟨*proof*⟩

lemma *red-inline-call-red'*:
assumes *fv: fv ee = {}*
and *eefin: ¬ final ee*
shows $\llbracket \text{call } E = \llbracket aMvs \rrbracket; P, t \vdash \langle \text{inline-call } ee \ E, (h, x) \rangle -ta \rightarrow \langle E', (h', x') \rangle \rrbracket$
 $\implies \exists ee'. E' = \text{inline-call } ee' \ E \wedge P, t \vdash \langle ee, (h, \text{Map.empty}) \rangle -ta \rightarrow \langle ee', (h', \text{Map.empty}) \rangle \wedge$
 $x = x'$
 (**is** $\llbracket -; - \rrbracket \implies ?\text{concl } E \ E' \ x \ x'$)
and $\llbracket \text{calls } Es = \llbracket aMvs \rrbracket; P, t \vdash \langle \text{inline-calls } ee \ Es, (h, x) \rangle [-ta \rightarrow] \langle Es', (h', x') \rangle \rrbracket$
 $\implies \exists ee'. Es' = \text{inline-calls } ee' \ Es \wedge P, t \vdash \langle ee, (h, \text{Map.empty}) \rangle -ta \rightarrow \langle ee', (h', \text{Map.empty}) \rangle$

$\wedge x = x'$
 (is $\llbracket -; - \rrbracket \Longrightarrow ?concls\ Es\ Es'\ x\ x'$)
 $\langle proof \rangle$

lemma assumes sees: $P \vdash \text{class-type-of } T \text{ sees } M:Us \rightarrow U = \llbracket (pns, \text{body}) \rrbracket$ in D
shows is-call-red-inline-callD:
 $\llbracket P, t \vdash \langle e, s \rangle \text{ --ta--} \langle e', s' \rangle; \text{call } e = \llbracket (a, M, vs) \rrbracket; \text{typeof-addr } (hp\ s)\ a = \llbracket T \rrbracket \rrbracket$
 $\Longrightarrow e' = \text{inline-call } (\text{blocks } (\text{this } \# \text{pns}) (\text{Class } D \# Us) (\text{Addr } a \# vs) \text{body})\ e$
and is-calls-reds-inline-callsD:
 $\llbracket P, t \vdash \langle es, s \rangle \text{ [-ta--]} \langle es', s' \rangle; \text{calls } es = \llbracket (a, M, vs) \rrbracket; \text{typeof-addr } (hp\ s)\ a = \llbracket T \rrbracket \rrbracket$
 $\Longrightarrow es' = \text{inline-calls } (\text{blocks } (\text{this } \# \text{pns}) (\text{Class } D \# Us) (\text{Addr } a \# vs) \text{body})\ es$
 $\langle proof \rangle$

lemma (in -) wf-state-ConsD: $\text{wf-state } (e, e' \# es) \Longrightarrow \text{wf-state } (e', es)$
 $\langle proof \rangle$

lemma red-fold-exs:
 $\llbracket P, t \vdash \langle e, (h, \text{Map.empty}) \rangle \text{ --ta--} \langle e', (h', \text{Map.empty}) \rangle; \text{wf-state } (e, es) \rrbracket$
 $\Longrightarrow P, t \vdash \langle \text{collapse } (e, es), (h, \text{Map.empty}) \rangle \text{ --ta--} \langle \text{collapse } (e', es), (h', \text{Map.empty}) \rangle$
 (is $\llbracket -; - \rrbracket \Longrightarrow ?concl\ e\ e'$)
 $\langle proof \rangle$

lemma red-fold-exs':
 $\llbracket P, t \vdash \langle \text{collapse } (e, es), (h, \text{Map.empty}) \rangle \text{ --ta--} \langle e', (h', x') \rangle; \text{wf-state } (e, es); \neg \text{final } e \rrbracket$
 $\Longrightarrow \exists E'. e' = \text{collapse } (E', es) \wedge P, t \vdash \langle e, (h, \text{Map.empty}) \rangle \text{ --ta--} \langle E', (h', \text{Map.empty}) \rangle$
 (is $\llbracket -; -; - \rrbracket \Longrightarrow ?concl\ e\ es$)
 $\langle proof \rangle$

lemma τRed0r -inline-call-not-final:
 $\exists e' es'. \tau\text{Red0r } P\ t\ h\ (e, es)\ (e', es') \wedge (\text{final } e' \longrightarrow es' = []) \wedge \text{collapse } (e, es) = \text{collapse } (e', es')$
 $\langle proof \rangle$

lemma τRed0 -preserves-wf-state:
 $\llbracket \text{wf-J-prog } P; \tau\text{Red0 } P\ t\ h\ (e, es)\ (e', es'); \text{wf-state } (e, es) \rrbracket \Longrightarrow \text{wf-state } (e', es')$
 $\langle proof \rangle$

lemma τRed0r -preserves-wf-state:
assumes wf: $\text{wf-J-prog } P$
shows $\llbracket \tau\text{Red0r } P\ t\ h\ (e, es)\ (e', es'); \text{wf-state } (e, es) \rrbracket \Longrightarrow \text{wf-state } (e', es')$
 $\langle proof \rangle$

lemma τRed0t -preserves-wf-state:
assumes wf: $\text{wf-J-prog } P$
shows $\llbracket \tau\text{Red0t } P\ t\ h\ (e, es)\ (e', es'); \text{wf-state } (e, es) \rrbracket \Longrightarrow \text{wf-state } (e', es')$
 $\langle proof \rangle$

lemma collapse- τmove0 -inv:
 $\llbracket \forall e \in \text{set } es. \text{is-call } e; \neg \text{final } e \rrbracket \Longrightarrow \tau\text{move0 } P\ h\ (\text{collapse } (e, es)) = \tau\text{move0 } P\ h\ e$
 $\langle proof \rangle$

lemma τRed0r -into-silent-moves:
 $\tau\text{Red0r } P\ t\ h\ (e, es)\ (e', es') \Longrightarrow \text{red0-mthr.silent-moves } P\ t\ ((e, es), h)\ ((e', es'), h)$
 $\langle proof \rangle$

lemma τ Red0t-into-silent-movet:

τ Red0t P t h (e, es) (e', es') \implies red0-mthr.silent-movet P t ((e, es), h) ((e', es'), h)

\langle proof \rangle

lemma red-simulates-red0:

assumes wwf: wwf-J-prog P
and sim: bisim-red-red0 s1 s2 mred0 P t s2 ta2 s2' \neg τ MOVE0 P s2 ta2 s2'
shows \exists s1' ta1. mred P t s1 ta1 s1' \wedge \neg τ MOVE P s1 ta1 s1' \wedge bisim-red-red0 s1' s2' \wedge ta-bisim0 ta1 ta2

\langle proof \rangle

lemma delay-bisimulation-measure-red-red0:

assumes wf: wwf-J-prog P
shows delay-bisimulation-measure (mred P t) (mred0 P t) bisim-red-red0 ta-bisim0 (τ MOVE P) (τ MOVE0 P) (λe e'. False) ($\lambda((e, es), h)$ ((e, es'), h). length es < length es')

\langle proof \rangle

lemma delay-bisimulation-diverge-red-red0:

assumes wwf-J-prog P
shows delay-bisimulation-diverge (mred P t) (mred0 P t) bisim-red-red0 ta-bisim0 (τ MOVE P) (τ MOVE0 P)

\langle proof \rangle

lemma bisim-red-red0-finalD:

assumes bisim: bisim-red-red0 (x1, m1) (x2, m2)
and final-expr x1
shows \exists x2'. red0-mthr.silent-moves P t (x2, m2) (x2', m2) \wedge bisim-red-red0 (x1, m1) (x2', m2)

\wedge final-expr0 x2'

\langle proof \rangle

lemma red0-simulates-red-not-final:

assumes wwf: wwf-J-prog P
assumes bisim: bisim-red-red0 ((e, xs), h) ((e0, es0), h0)
and red: P, t \vdash $\langle e, (h, xs) \rangle \rightarrow \langle e', (h', xs') \rangle$
and fin: \neg final e0
and n τ : \neg τ move0 P h e
shows \exists e0' ta0. P, t \vdash 0 $\langle e0/es0, h \rangle \rightarrow \langle e0'/es0, h' \rangle \wedge$ bisim-red-red0 ((e', xs'), h') ((e0', es0), h') \wedge ta-bisim0 ta ta0

\langle proof \rangle

lemma red-red0-FWbisim:

assumes wf: wwf-J-prog P
shows FWdelay-bisimulation-diverge final-expr (mred P) final-expr0 (mred0 P) (P) (λt . bisim-red-red0) (λe xs (e0, es0). \neg final e0) (τ MOVE P) (τ MOVE0 P)

\langle proof \rangle

end

sublocale J-heap-base < red-red0:

FWdelay-bisimulation-base
 final-expr
 mred P
 final-expr0

```

  mred0 P
  convert-RA
  λt. bisim-red-red0
  λexs (e0, es0). ¬ final e0
  τMOVE P τMOVE0 P
  for P
  ⟨proof⟩

```

context *J-heap-base* **begin**

lemma *bisim-J-J0-start*:

assumes *wf*: *wf-J-prog P*

and *wf-start*: *wf-start-state P C M vs*

shows *red-red0.mbisim (J-start-state P C M vs) (J0-start-state P C M vs)*

⟨proof⟩

end

end

7.4 The intermediate language J1

theory *J1State* **imports**

../J/State

CallExpr

begin

type-synonym

'addr expr1 = (*nat*, *nat*, *'addr*) *exp*

type-synonym

'addr J1-prog = *'addr expr1 prog*

type-synonym

'addr locals1 = *'addr val list*

translations

(*type*) *'addr expr1* <= (*type*) (*nat*, *nat*, *'addr*) *exp*

(*type*) *'addr J1-prog* <= (*type*) *'addr expr1 prog*

type-synonym

'addr J1state = (*'addr expr1* × *'addr locals1*) *list*

type-synonym

(*'addr*, *'thread-id*, *'heap*) *J1-thread-action* =

(*'addr*, *'thread-id*, (*'addr expr1* × *'addr locals1*) × (*'addr expr1* × *'addr locals1*) *list*, *'heap*) *Jinja-thread-action*

type-synonym

(*'addr*, *'thread-id*, *'heap*) *J1-state* =

(*'addr*, *'thread-id*, (*'addr expr1* × *'addr locals1*) × (*'addr expr1* × *'addr locals1*) *list*, *'heap*, *'addr*) *state*

⟨ML⟩

typ ('addr,'thread-id,'heap) *J1-thread-action*

⟨ML⟩

typ ('addr, 'thread-id, 'heap) *J1-state*

fun *blocks1* :: *nat* ⇒ *ty list* ⇒ (*nat*,*'b*,*'addr*) *exp* ⇒ (*nat*,*'b*,*'addr*) *exp*

where

blocks1 *n* [] *e* = *e*
 | *blocks1* *n* (*T#Ts*) *e* = {*n*:*T=None*; *blocks1* (*Suc n*) *Ts* *e*}

primrec *max-vars*:: ('a,'b,'addr) *exp* ⇒ *nat*

and *max-varss*:: ('a,'b,'addr) *exp list* ⇒ *nat*

where

max-vars (*new C*) = 0
 | *max-vars* (*newA T[e]*) = *max-vars* *e*
 | *max-vars* (*Cast C e*) = *max-vars* *e*
 | *max-vars* (*e instanceof T*) = *max-vars* *e*
 | *max-vars* (*Val v*) = 0
 | *max-vars* (*e «bop» e'*) = *max* (*max-vars* *e*) (*max-vars* *e'*)
 | *max-vars* (*Var V*) = 0
 | *max-vars* (*V:=e*) = *max-vars* *e*
 | *max-vars* (*a[i]*) = *max* (*max-vars* *a*) (*max-vars* *i*)
 | *max-vars* (*AAss a i e*) = *max* (*max* (*max-vars* *a*) (*max-vars* *i*)) (*max-vars* *e*)
 | *max-vars* (*a.length*) = *max-vars* *a*
 | *max-vars* (*e.F{D}*) = *max-vars* *e*
 | *max-vars* (*FAss e₁ F D e₂*) = *max* (*max-vars* *e₁*) (*max-vars* *e₂*)
 | *max-vars* (*e.compareAndSwap(D.F, e', e'')*) = *max* (*max* (*max-vars* *e*) (*max-vars* *e'*)) (*max-vars* *e''*)
 | *max-vars* (*e.M(es)*) = *max* (*max-vars* *e*) (*max-varss* *es*)
 | *max-vars* ({*V:T=vo*; *e*}) = *max-vars* *e* + 1

— *sync* and *insync* will need an extra local variable when compiling to bytecode to store the object that is being synchronized on until its release

| *max-vars* (*sync_V (e') e*) = *max* (*max-vars* *e'*) (*max-vars* *e* + 1)
 | *max-vars* (*insync_V (a) e*) = *max-vars* *e* + 1
 | *max-vars* (*e₁;;e₂*) = *max* (*max-vars* *e₁*) (*max-vars* *e₂*)
 | *max-vars* (*if (e) e₁ else e₂*) =
 max (*max-vars* *e*) (*max* (*max-vars* *e₁*) (*max-vars* *e₂*))
 | *max-vars* (*while (b) e*) = *max* (*max-vars* *b*) (*max-vars* *e*)
 | *max-vars* (*throw e*) = *max-vars* *e*
 | *max-vars* (*try e₁ catch(C V) e₂*) = *max* (*max-vars* *e₁*) (*max-vars* *e₂* + 1)

| *max-varss* [] = 0

| *max-varss* (*e#es*) = *max* (*max-vars* *e*) (*max-varss* *es*)

— Indices in blocks increase by 1

primrec *B* :: 'addr *expr1* ⇒ *nat* ⇒ *bool*

and *Bs* :: 'addr *expr1 list* ⇒ *nat* ⇒ *bool*

where

B (*new C*) *i* = *True*
 | *B* (*newA T[e]*) *i* = *B* *e* *i*
 | *B* (*Cast C e*) *i* = *B* *e* *i*
 | *B* (*e instanceof T*) *i* = *B* *e* *i*
 | *B* (*Val v*) *i* = *True*

$$\begin{aligned}
& \mathcal{B} (e1 \ll bop \gg e2) i = (\mathcal{B} e1 i \wedge \mathcal{B} e2 i) \\
& \mathcal{B} (\text{Var } j) i = \text{True} \\
& \mathcal{B} (j := e) i = \mathcal{B} e i \\
& \mathcal{B} (a[j]) i = (\mathcal{B} a i \wedge \mathcal{B} j i) \\
& \mathcal{B} (a[j] := e) i = (\mathcal{B} a i \wedge \mathcal{B} j i \wedge \mathcal{B} e i) \\
& \mathcal{B} (a \cdot \text{length}) i = \mathcal{B} a i \\
& \mathcal{B} (e \cdot F\{D\}) i = \mathcal{B} e i \\
& \mathcal{B} (e1 \cdot F\{D\} := e2) i = (\mathcal{B} e1 i \wedge \mathcal{B} e2 i) \\
& \mathcal{B} (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) i = (\mathcal{B} e i \wedge \mathcal{B} e' i \wedge \mathcal{B} e'' i) \\
& \mathcal{B} (e \cdot M(es)) i = (\mathcal{B} e i \wedge \mathcal{B} s es i) \\
& \mathcal{B} (\{j: T=vo; e\}) i = (i = j \wedge \mathcal{B} e (i+1)) \\
& \mathcal{B} (\text{sync}_V(o') e) i = (i = V \wedge \mathcal{B} o' i \wedge \mathcal{B} e (i+1)) \\
& \mathcal{B} (\text{insync}_V(a) e) i = (i = V \wedge \mathcal{B} e (i+1)) \\
& \mathcal{B} (e1 ;; e2) i = (\mathcal{B} e1 i \wedge \mathcal{B} e2 i) \\
& \mathcal{B} (\text{if } (e) e1 \text{ else } e2) i = (\mathcal{B} e i \wedge \mathcal{B} e1 i \wedge \mathcal{B} e2 i) \\
& \mathcal{B} (\text{throw } e) i = \mathcal{B} e i \\
& \mathcal{B} (\text{while } (e) c) i = (\mathcal{B} e i \wedge \mathcal{B} c i) \\
& \mathcal{B} (\text{try } e1 \text{ catch}(C j) e2) i = (\mathcal{B} e1 i \wedge i=j \wedge \mathcal{B} e2 (i+1)) \\
& \mathcal{B} s [] i = \text{True} \\
& \mathcal{B} s (e \# es) i = (\mathcal{B} e i \wedge \mathcal{B} s es i)
\end{aligned}$$

Variables for monitor addresses do not occur freely in synchronization blocks

primrec $\text{syncvars} :: ('a, 'a, 'addr) \text{exp} \Rightarrow \text{bool}$
and $\text{syncvarss} :: ('a, 'a, 'addr) \text{exp list} \Rightarrow \text{bool}$
where

$$\begin{aligned}
& \text{syncvars } (\text{new } C) = \text{True} \\
& \text{syncvars } (\text{newA } T[e]) = \text{syncvars } e \\
& \text{syncvars } (\text{Cast } T e) = \text{syncvars } e \\
& \text{syncvars } (e \text{ instanceof } T) = \text{syncvars } e \\
& \text{syncvars } (\text{Val } v) = \text{True} \\
& \text{syncvars } (e1 \ll bop \gg e2) = (\text{syncvars } e1 \wedge \text{syncvars } e2) \\
& \text{syncvars } (\text{Var } V) = \text{True} \\
& \text{syncvars } (V := e) = \text{syncvars } e \\
& \text{syncvars } (a[i]) = (\text{syncvars } a \wedge \text{syncvars } i) \\
& \text{syncvars } (a[i] := e) = (\text{syncvars } a \wedge \text{syncvars } i \wedge \text{syncvars } e) \\
& \text{syncvars } (a \cdot \text{length}) = \text{syncvars } a \\
& \text{syncvars } (e \cdot F\{D\}) = \text{syncvars } e \\
& \text{syncvars } (e \cdot F\{D\} := e2) = (\text{syncvars } e \wedge \text{syncvars } e2) \\
& \text{syncvars } (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) = (\text{syncvars } e \wedge \text{syncvars } e' \wedge \text{syncvars } e'') \\
& \text{syncvars } (e \cdot M(es)) = (\text{syncvars } e \wedge \text{syncvarss } es) \\
& \text{syncvars } \{V: T=vo; e\} = \text{syncvars } e \\
& \text{syncvars } (\text{sync}_V(e1) e2) = (\text{syncvars } e1 \wedge \text{syncvars } e2 \wedge V \notin \text{fv } e2) \\
& \text{syncvars } (\text{insync}_V(a) e) = (\text{syncvars } e \wedge V \notin \text{fv } e) \\
& \text{syncvars } (e1 ;; e2) = (\text{syncvars } e1 \wedge \text{syncvars } e2) \\
& \text{syncvars } (\text{if } (b) e1 \text{ else } e2) = (\text{syncvars } b \wedge \text{syncvars } e1 \wedge \text{syncvars } e2) \\
& \text{syncvars } (\text{while } (b) c) = (\text{syncvars } b \wedge \text{syncvars } c) \\
& \text{syncvars } (\text{throw } e) = \text{syncvars } e \\
& \text{syncvars } (\text{try } e1 \text{ catch}(C V) e2) = (\text{syncvars } e1 \wedge \text{syncvars } e2) \\
& \text{syncvarss } [] = \text{True} \\
& \text{syncvarss } (e \# es) = (\text{syncvars } e \wedge \text{syncvarss } es)
\end{aligned}$$

definition $\text{bsok} :: 'addr \text{expr1} \Rightarrow \text{nat} \Rightarrow \text{bool}$

where $bsok\ e\ n \equiv \mathcal{B}\ e\ n \wedge expr\text{-locks}\ e = (\lambda ad.\ 0)$

definition $bsoks :: 'addr\ expr1\ list \Rightarrow nat \Rightarrow bool$

where $bsoks\ es\ n \equiv \mathcal{B}s\ es\ n \wedge expr\text{-lockss}\ es = (\lambda ad.\ 0)$

primrec $call1 :: ('a, 'b, 'addr)\ exp \Rightarrow ('addr \times mname \times 'addr\ val\ list)\ option$

and $calls1 :: ('a, 'b, 'addr)\ exp\ list \Rightarrow ('addr \times mname \times 'addr\ val\ list)\ option$

where

$call1\ (new\ C) = None$
 $| call1\ (newA\ T\ [e]) = call1\ e$
 $| call1\ (Cast\ C\ e) = call1\ e$
 $| call1\ (e\ instanceof\ T) = call1\ e$
 $| call1\ (Val\ v) = None$
 $| call1\ (Var\ V) = None$
 $| call1\ (V := e) = call1\ e$
 $| call1\ (e\ \ll bop \gg\ e') = (if\ is\text{-val}\ e\ then\ call1\ e'\ else\ call1\ e)$
 $| call1\ (a\ [i]) = (if\ is\text{-val}\ a\ then\ call1\ i\ else\ call1\ a)$
 $| call1\ (AAss\ a\ i\ e) = (if\ is\text{-val}\ a\ then\ (if\ is\text{-val}\ i\ then\ call1\ e\ else\ call1\ i)\ else\ call1\ a)$
 $| call1\ (a \cdot length) = call1\ a$
 $| call1\ (e \cdot F\ \{D\}) = call1\ e$
 $| call1\ (FAss\ e\ F\ D\ e') = (if\ is\text{-val}\ e\ then\ call1\ e'\ else\ call1\ e)$
 $| call1\ (CompareAndSwap\ e\ D\ F\ e'\ e'') = (if\ is\text{-val}\ e\ then\ (if\ is\text{-val}\ e'\ then\ call1\ e''\ else\ call1\ e')\ else\ call1\ e)$
 $| call1\ (e \cdot M(es)) = (if\ is\text{-val}\ e\ then$
 $\quad (if\ is\text{-vals}\ es \wedge is\text{-addr}\ e\ then\ [(THE\ a.\ e = addr\ a,\ M,\ THE\ vs.\ es = map\ Val\ vs)])$
 $else\ calls1\ es)$
 $\quad else\ call1\ e)$
 $| call1\ (\{V:T=vo;\ e\}) = (case\ vo\ of\ None \Rightarrow call1\ e\ | Some\ v \Rightarrow None)$
 $| call1\ (sync_V\ (o^\wedge)\ e) = call1\ o'$
 $| call1\ (insync_V\ (a)\ e) = call1\ e$
 $| call1\ (e;\ e') = call1\ e$
 $| call1\ (if\ (e)\ e1\ else\ e2) = call1\ e$
 $| call1\ (while(b)\ e) = None$
 $| call1\ (throw\ e) = call1\ e$
 $| call1\ (try\ e1\ catch(C\ V)\ e2) = call1\ e1$

 $| calls1\ [] = None$
 $| calls1\ (e\ #\ es) = (if\ is\text{-val}\ e\ then\ calls1\ es\ else\ call1\ e)$

lemma $expr\text{-locks}\text{-blocks1}\ [simp]:$

$expr\text{-locks}\ (blocks1\ n\ Ts\ e) = expr\text{-locks}\ e$

$\langle proof \rangle$

lemma $max\text{-varss}\text{-append}\ [simp]:$

$max\text{-varss}\ (es\ @\ es') = max\ (max\text{-varss}\ es)\ (max\text{-varss}\ es')$

$\langle proof \rangle$

lemma $max\text{-varss}\text{-map}\text{-Val}\ [simp]:\ max\text{-varss}\ (map\ Val\ vs) = 0$

$\langle proof \rangle$

lemma $blocks1\text{-max}\text{-vars}:$

$max\text{-vars}\ (blocks1\ n\ Ts\ e) = max\text{-vars}\ e + length\ Ts$

$\langle proof \rangle$

lemma *blocks-max-vars*:

$\llbracket \text{length } vs = \text{length } pns; \text{length } Ts = \text{length } pns \rrbracket$
 $\implies \text{max-vars } (\text{blocks } pns \ Ts \ vs \ e) = \text{max-vars } e + \text{length } pns$
 ⟨proof⟩

lemma *Bs-append [simp]*: $\mathcal{B} s (es \ @ \ es') \ n \longleftrightarrow \mathcal{B} s \ es \ n \ \wedge \ \mathcal{B} s \ es' \ n$
 ⟨proof⟩

lemma *Bs-map-Val [simp]*: $\mathcal{B} s (\text{map } Val \ vs) \ n$
 ⟨proof⟩

lemma *B-blocks1 [intro]*: $\mathcal{B} \ \text{body} \ (n + \text{length } Ts) \implies \mathcal{B} \ (\text{blocks1 } n \ Ts \ \text{body}) \ n$
 ⟨proof⟩

lemma *B-extRet2J [simp]*: $\mathcal{B} \ e \ n \implies \mathcal{B} \ (\text{extRet2J } e \ va) \ n$
 ⟨proof⟩

lemma *B-inline-call*: $\llbracket \mathcal{B} \ e \ n; \bigwedge n. \mathcal{B} \ e' \ n \rrbracket \implies \mathcal{B} \ (\text{inline-call } e' \ e) \ n$
and *Bs-inline-calls*: $\llbracket \mathcal{B} s \ es \ n; \bigwedge n. \mathcal{B} \ e' \ n \rrbracket \implies \mathcal{B} s \ (\text{inline-calls } e' \ es) \ n$
 ⟨proof⟩

lemma *syncvarss-append [simp]*: $\text{syncvarss } (es \ @ \ es') \longleftrightarrow \text{syncvarss } es \ \wedge \ \text{syncvarss } es'$
 ⟨proof⟩

lemma *syncvarss-map-Val [simp]*: $\text{syncvarss } (\text{map } Val \ vs)$
 ⟨proof⟩

lemma *bsok-simps [simp]*:

$bsok \ (\text{new } C) \ n = True$
 $bsok \ (\text{newA } T [e]) \ n = bsok \ e \ n$
 $bsok \ (\text{Cast } T \ e) \ n = bsok \ e \ n$
 $bsok \ (e \ \text{instanceof } T) \ n = bsok \ e \ n$
 $bsok \ (e1 \ \llbracket \text{bop} \rrbracket \ e2) \ n = (bsok \ e1 \ n \ \wedge \ bsok \ e2 \ n)$
 $bsok \ (\text{Var } V) \ n = True$
 $bsok \ (\text{Val } v) \ n = True$
 $bsok \ (V := e) \ n = bsok \ e \ n$
 $bsok \ (a[i]) \ n = (bsok \ a \ n \ \wedge \ bsok \ i \ n)$
 $bsok \ (a[i] := e) \ n = (bsok \ a \ n \ \wedge \ bsok \ i \ n \ \wedge \ bsok \ e \ n)$
 $bsok \ (a \cdot \text{length}) \ n = bsok \ a \ n$
 $bsok \ (e \cdot F\{D\}) \ n = bsok \ e \ n$
 $bsok \ (e \cdot F\{D\} := e') \ n = (bsok \ e \ n \ \wedge \ bsok \ e' \ n)$
 $bsok \ (e \cdot \text{compareAndSwap}(D \cdot F, \ e', \ e'')) \ n = (bsok \ e \ n \ \wedge \ bsok \ e' \ n \ \wedge \ bsok \ e'' \ n)$
 $bsok \ (e \cdot M(ps)) \ n = (bsok \ e \ n \ \wedge \ bsoks \ ps \ n)$
 $bsok \ \{V:T=vo; \ e\} \ n = (bsok \ e \ (\text{Suc } n) \ \wedge \ V = n)$
 $bsok \ (\text{sync}_V \ (e) \ e') \ n = (bsok \ e \ n \ \wedge \ bsok \ e' \ (\text{Suc } n) \ \wedge \ V = n)$
 $bsok \ (\text{insync}_V \ (ad) \ e) \ n = False$
 $bsok \ (e;; \ e') \ n = (bsok \ e \ n \ \wedge \ bsok \ e' \ n)$
 $bsok \ (\text{if } (e) \ e1 \ \text{else } e2) \ n = (bsok \ e \ n \ \wedge \ bsok \ e1 \ n \ \wedge \ bsok \ e2 \ n)$
 $bsok \ (\text{while } (b) \ c) \ n = (bsok \ b \ n \ \wedge \ bsok \ c \ n)$
 $bsok \ (\text{throw } e) \ n = bsok \ e \ n$
 $bsok \ (\text{try } e \ \text{catch}(C \ V) \ e') \ n = (bsok \ e \ n \ \wedge \ bsok \ e' \ (\text{Suc } n) \ \wedge \ V = n)$
and *bsoks-simps [simp]*:
 $bsoks \ [] \ n = True$

$bsoks (e \# es) n = (bsok e n \wedge bsoks es n)$
 <proof>

lemma *call1-callE*:

assumes *call1* (*obj*·*M*(*pns*)) = [(*a*, *M'*, *vs*)]
obtains (*CallObj*) *call1 obj* = [(*a*, *M'*, *vs*)]
 | (*CallParams*) *v* **where** *obj* = *Val v calls1 pns* = [(*a*, *M'*, *vs*)]
 | (*Call*) *obj* = *addr a pns = map Val vs M = M'*
 <proof>

lemma *calls1-map-Val-append [simp]*:

calls1 (map Val vs @ es) = calls1 es
 <proof>

lemma *calls1-map-Val [simp]*:

calls1 (map Val vs) = None
 <proof>

lemma *fixes e :: ('a, 'b, 'addr) exp and es :: ('a, 'b, 'addr) exp list*

shows *call1-imp-call*: *call1 e = [aMvs] \implies call e = [aMvs]*
and *calls1-imp-calls*: *calls1 es = [aMvs] \implies calls es = [aMvs]*
 <proof>

lemma *max-vars-inline-call*: *max-vars (inline-call e' e) \leq max-vars e + max-vars e'*
and *max-varss-inline-calls*: *max-varss (inline-calls e' es) \leq max-varss es + max-vars e'*
 <proof>

lemmas *inline-call-max-vars1 = max-vars-inline-call*

lemmas *inline-calls-max-varss1 = max-varss-inline-calls*

end

7.5 Abstract heap locales for J1 programs

theory *J1Heap imports*

J1State

../Common/Conform

begin

locale *J1-heap-base = heap-base +*

constrains *addr2thread-id :: ('addr :: addr) \Rightarrow 'thread-id*
and *thread-id2addr :: 'thread-id \Rightarrow 'addr*
and *sc-spurious-wakeups :: bool*
and *empty-heap :: 'heap*
and *allocate :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) set*
and *typeof-addr :: 'heap \Rightarrow 'addr \rightarrow htype*
and *heap-read :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow bool*
and *heap-write :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow 'heap \Rightarrow bool*

locale *J1-heap = heap +*

constrains *addr2thread-id :: ('addr :: addr) \Rightarrow 'thread-id*
and *thread-id2addr :: 'thread-id \Rightarrow 'addr*
and *sc-spurious-wakeups :: bool*

```

and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and P :: 'addr J1-prog

```

```

sublocale J1-heap < J1-heap-base ⟨proof⟩

```

```

locale J1-heap-conf-base = heap-conf-base +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and sc-spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr J1-prog

```

```

sublocale J1-heap-conf-base < J1-heap-base ⟨proof⟩

```

```

locale J1-heap-conf =
  J1-heap-conf-base +
  heap-conf +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and sc-spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr J1-prog

```

```

sublocale J1-heap-conf < J1-heap ⟨proof⟩

```

```

locale J1-conf-read =
  J1-heap-conf +
  heap-conf-read +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and sc-spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr J1-prog

```

```

end

```

7.6 Semantics of the intermediate language

theory *J1 imports*

J1State

J1Heap

../Framework/FWBisimulation

begin

abbreviation *final-expr1* :: ('addr expr1 × 'addr locals1) × ('addr expr1 × 'addr locals1) list ⇒ bool
where

final-expr1 ≡ λ(*ex, exs*). *final* (fst *ex*) ∧ *exs* = []

definition *extNTA2J1* ::

'addr *J1-prog* ⇒ (*cname* × *mname* × 'addr) ⇒ (('addr expr1 × 'addr locals1) × ('addr expr1 × 'addr locals1) list)

where

extNTA2J1 P = (λ(*C, M, a*). let (*D, -, -, meth*) = *method P C M*; *body* = *the meth*
in (({0:Class *D=*None; *body*}, Addr *a* # replicate (*max-vars body*)
undefined-value), []))

lemma *extNTA2J1-iff [simp]*:

extNTA2J1 P (*C, M, a*) = (({0:Class (fst (*method P C M*))=None; *the* (snd (snd (snd (*method P C M*))))}, Addr *a* # replicate (*max-vars* (*the* (snd (snd (snd (*method P C M*)))))) undefined-value), [])

⟨*proof*⟩

abbreviation *extTA2J1* ::

'addr *J1-prog* ⇒ ('addr, 'thread-id, 'heap) *external-thread-action* ⇒ ('addr, 'thread-id, 'heap) *J1-thread-action*

where *extTA2J1 P* ≡ *convert-extTA* (*extNTA2J1 P*)

abbreviation (*input*) *extRet2J1* :: 'addr expr1 ⇒ 'addr extCallRet ⇒ 'addr expr1

where *extRet2J1* ≡ *extRet2J*

lemma *max-vars-extRet2J1 [simp]*:

max-vars e = 0 ⇒ *max-vars* (*extRet2J1 e va*) = 0

⟨*proof*⟩

context *J1-heap-base begin*

abbreviation *J1-start-state* :: 'addr *J1-prog* ⇒ *cname* ⇒ *mname* ⇒ 'addr *val list* ⇒ ('addr, 'thread-id, 'heap) *J1-state*

where

J1-start-state ≡
start-state (λ*C M Ts T body vs*. ((*blocks1 0* (Class *C* # *Ts*) *body*, Null # *vs* @ replicate (*max-vars body*) undefined-value), []))

inductive *red1* ::

bool ⇒ 'addr *J1-prog* ⇒ 'thread-id ⇒ 'addr expr1 ⇒ 'heap × 'addr locals1
⇒ ('addr, 'thread-id, 'heap) *external-thread-action* ⇒ 'addr expr1 ⇒ 'heap × 'addr locals1 ⇒ *bool*
(⟨-, - ⊢ 1 ((1 ⟨-, -⟩) - -> / (1 ⟨-, -⟩))⟩ [51, 51, 0, 0, 0, 0, 0] 81)

and *reds1* ::

bool ⇒ 'addr *J1-prog* ⇒ 'thread-id ⇒ 'addr expr1 list ⇒ 'heap × 'addr locals1
⇒ ('addr, 'thread-id, 'heap) *external-thread-action* ⇒ 'addr expr1 list ⇒ 'heap × 'addr locals1 ⇒ *bool*

$(\langle -, - \rangle \vdash 1 ((1 \langle -, - \rangle) [-\rightarrow] / (1 \langle -, - \rangle))) \triangleright [51, 51, 0, 0, 0, 0, 0, 0] \ 81)$
for $uf :: \text{bool}$ **and** $P :: \text{'addr J1-prog}$ **and** $t :: \text{'thread-id}$
where
Red1New:
 $(h', a) \in \text{allocate } h \ (\text{Class-type } C)$
 $\implies uf, P, t \vdash 1 \langle \text{new } C, (h, l) \rangle -\{\!\!\{ \text{NewHeapElem } a \ (\text{Class-type } C) \}\!\!\} \rightarrow \langle \text{addr } a, (h', l) \rangle$
| *Red1NewFail:*
 $\text{allocate } h \ (\text{Class-type } C) = \{\}$
 $\implies uf, P, t \vdash 1 \langle \text{new } C, (h, l) \rangle -\varepsilon \rightarrow \langle \text{THROW OutOfMemory}, (h, l) \rangle$
| *New1ArrayRed:*
 $uf, P, t \vdash 1 \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\implies uf, P, t \vdash 1 \langle \text{newA } T[e], s \rangle -\text{ta} \rightarrow \langle \text{newA } T[e'], s' \rangle$
| *Red1NewArray:*
 $\llbracket 0 \leq s \ i; (h', a) \in \text{allocate } h \ (\text{Array-type } T \ (\text{nat} \ (\text{sint } i))) \rrbracket$
 $\implies uf, P, t \vdash 1 \langle \text{newA } T[\text{Val} \ (\text{Intg } i)], (h, l) \rangle -\{\!\!\{ \text{NewHeapElem } a \ (\text{Array-type } T \ (\text{nat} \ (\text{sint } i))) \}\!\!\} \rightarrow \langle \text{addr } a, (h', l) \rangle$
| *Red1NewArrayNegative:*
 $i < s \ 0 \implies uf, P, t \vdash 1 \langle \text{newA } T[\text{Val} \ (\text{Intg } i)], s \rangle -\varepsilon \rightarrow \langle \text{THROW NegativeArraySize}, s \rangle$
| *Red1NewArrayFail:*
 $\llbracket 0 \leq s \ i; \text{allocate } h \ (\text{Array-type } T \ (\text{nat} \ (\text{sint } i))) = \{\} \rrbracket$
 $\implies uf, P, t \vdash 1 \langle \text{newA } T[\text{Val} \ (\text{Intg } i)], (h, l) \rangle -\varepsilon \rightarrow \langle \text{THROW OutOfMemory}, (h, l) \rangle$
| *Cast1Red:*
 $uf, P, t \vdash 1 \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\implies uf, P, t \vdash 1 \langle \text{Cast } C \ e, s \rangle -\text{ta} \rightarrow \langle \text{Cast } C \ e', s' \rangle$
| *Red1Cast:*
 $\llbracket \text{typeof}_{hp} \ s \ v = [U]; P \vdash U \leq T \rrbracket$
 $\implies uf, P, t \vdash 1 \langle \text{Cast } T \ (\text{Val } v), s \rangle -\varepsilon \rightarrow \langle \text{Val } v, s \rangle$
| *Red1CastFail:*
 $\llbracket \text{typeof}_{hp} \ s \ v = [U]; \neg P \vdash U \leq T \rrbracket$
 $\implies uf, P, t \vdash 1 \langle \text{Cast } T \ (\text{Val } v), s \rangle -\varepsilon \rightarrow \langle \text{THROW ClassCast}, s \rangle$
| *InstanceOf1Red:*
 $uf, P, t \vdash 1 \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle e \ \text{instanceof } T, s \rangle -\text{ta} \rightarrow \langle e' \ \text{instanceof } T, s' \rangle$
| *Red1InstanceOf:*
 $\llbracket \text{typeof}_{hp} \ s \ v = [U]; b \longleftrightarrow v \neq \text{Null} \wedge P \vdash U \leq T \rrbracket$
 $\implies uf, P, t \vdash 1 \langle (\text{Val } v) \ \text{instanceof } T, s \rangle -\varepsilon \rightarrow \langle \text{Val} \ (\text{Bool } b), s \rangle$
| *Bin1OpRed1:*
 $uf, P, t \vdash 1 \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle e \ \langle\langle \text{bop} \rangle\rangle \ e2, s \rangle -\text{ta} \rightarrow \langle e' \ \langle\langle \text{bop} \rangle\rangle \ e2, s' \rangle$
| *Bin1OpRed2:*
 $uf, P, t \vdash 1 \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle (\text{Val } v) \ \langle\langle \text{bop} \rangle\rangle \ e, s \rangle -\text{ta} \rightarrow \langle (\text{Val } v) \ \langle\langle \text{bop} \rangle\rangle \ e', s' \rangle$
| *Red1BinOp:*
 $\text{binop } \text{bop} \ v1 \ v2 = \text{Some} \ (\text{Inl } v) \implies$

$$uf, P, t \vdash 1 \langle (Val v1) \ll bop \gg (Val v2), s \rangle -\varepsilon \rightarrow \langle Val v, s \rangle$$

| *Red1BinOpFail*:

$$\begin{aligned} binop \ bop \ v1 \ v2 = Some \ (Inr \ a) &\Longrightarrow \\ uf, P, t \vdash 1 \langle (Val v1) \ll bop \gg (Val v2), s \rangle -\varepsilon &\rightarrow \langle Throw \ a, s \rangle \end{aligned}$$

| *Red1Var*:

$$\begin{aligned} \ll (lcl \ s)!V = v; V < size \ (lcl \ s) \gg & \\ \Longrightarrow uf, P, t \vdash 1 \langle Var \ V, s \rangle -\varepsilon &\rightarrow \langle Val \ v, s \rangle \end{aligned}$$

| *LAss1Red*:

$$\begin{aligned} uf, P, t \vdash 1 \langle e, s \rangle -ta &\rightarrow \langle e', s' \rangle \\ \Longrightarrow uf, P, t \vdash 1 \langle V := e, s \rangle -ta &\rightarrow \langle V := e', s' \rangle \end{aligned}$$

| *Red1LAss*:

$$\begin{aligned} V < size \ l & \\ \Longrightarrow uf, P, t \vdash 1 \langle V := (Val \ v), (h, l) \rangle -\varepsilon &\rightarrow \langle unit, (h, l[V := v]) \rangle \end{aligned}$$

| *AAcc1Red1*:

$$uf, P, t \vdash 1 \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \Longrightarrow uf, P, t \vdash 1 \langle a[i], s \rangle -ta \rightarrow \langle a'[i], s' \rangle$$

| *AAcc1Red2*:

$$uf, P, t \vdash 1 \langle i, s \rangle -ta \rightarrow \langle i', s' \rangle \Longrightarrow uf, P, t \vdash 1 \langle (Val \ a)[i], s \rangle -ta \rightarrow \langle (Val \ a)[i'], s' \rangle$$

| *Red1AAccNull*:

$$uf, P, t \vdash 1 \langle null[Val \ i], s \rangle -\varepsilon \rightarrow \langle THROW \ NullPointer, s \rangle$$

| *Red1AAccBounds*:

$$\begin{aligned} \ll typeof\text{-addr} \ (hp \ s) \ a = \llbracket Array\text{-type} \ T \ n \rrbracket; i < s \ 0 \vee sint \ i \geq int \ n \gg & \\ \Longrightarrow uf, P, t \vdash 1 \langle (addr \ a)[Val \ (Intg \ i)], s \rangle -\varepsilon &\rightarrow \langle THROW \ ArrayIndexOutOfBounds, s \rangle \end{aligned}$$

| *Red1AAcc*:

$$\begin{aligned} \ll typeof\text{-addr} \ h \ a = \llbracket Array\text{-type} \ T \ n \rrbracket; 0 \leq s \ i; sint \ i < int \ n; & \\ \text{heap-read} \ h \ a \ (ACell \ (nat \ (sint \ i))) \ v \gg & \\ \Longrightarrow uf, P, t \vdash 1 \langle (addr \ a)[Val \ (Intg \ i)], (h, xs) \rangle -\{\!| \text{ReadMem} \ a \ (ACell \ (nat \ (sint \ i))) \ v \}\!\} &\rightarrow \langle Val \ v, \\ (h, xs) \rangle & \end{aligned}$$

| *AAss1Red1*:

$$uf, P, t \vdash 1 \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \Longrightarrow uf, P, t \vdash 1 \langle a[i] := e, s \rangle -ta \rightarrow \langle a'[i] := e, s' \rangle$$

| *AAss1Red2*:

$$uf, P, t \vdash 1 \langle i, s \rangle -ta \rightarrow \langle i', s' \rangle \Longrightarrow uf, P, t \vdash 1 \langle (Val \ a)[i] := e, s \rangle -ta \rightarrow \langle (Val \ a)[i'] := e, s' \rangle$$

| *AAss1Red3*:

$$uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \Longrightarrow uf, P, t \vdash 1 \langle AAss \ (Val \ a) \ (Val \ i) \ e, s \rangle -ta \rightarrow \langle (Val \ a)[Val \ i] := e', s' \rangle$$

| *Red1AAssNull*:

$$uf, P, t \vdash 1 \langle AAss \ null \ (Val \ i) \ (Val \ e), s \rangle -\varepsilon \rightarrow \langle THROW \ NullPointer, s \rangle$$

| *Red1AAssBounds*:

$$\begin{aligned} \ll typeof\text{-addr} \ (hp \ s) \ a = \llbracket Array\text{-type} \ T \ n \rrbracket; i < s \ 0 \vee sint \ i \geq int \ n \gg & \\ \Longrightarrow uf, P, t \vdash 1 \langle AAss \ (addr \ a) \ (Val \ (Intg \ i)) \ (Val \ e), s \rangle -\varepsilon &\rightarrow \langle THROW \ ArrayIndexOutOfBounds, s \rangle \end{aligned}$$

- | *Red1AAssStore*:

$$\llbracket \text{typeof-addr } (hp\ s)\ a = \lfloor \text{Array-type } T\ n \rfloor; 0 \leq s\ i; \text{sint } i < \text{int } n; \text{typeof}_{hp\ s}\ w = \lfloor U \rfloor; \neg (P \vdash U \leq T) \rrbracket$$

$$\implies uf, P, t \vdash 1 \langle \text{AAss } (addr\ a)\ (Val\ (Intg\ i))\ (Val\ w),\ s \rangle -\varepsilon \rightarrow \langle \text{THROW ArrayStore},\ s \rangle$$
- | *Red1AAss*:

$$\llbracket \text{typeof-addr } h\ a = \lfloor \text{Array-type } T\ n \rfloor; 0 \leq s\ i; \text{sint } i < \text{int } n; \text{typeof}_h\ w = \text{Some } U; P \vdash U \leq T; \text{heap-write } h\ a\ (ACell\ (nat\ (\text{sint } i)))\ w\ h' \rrbracket$$

$$\implies uf, P, t \vdash 1 \langle \text{AAss } (addr\ a)\ (Val\ (Intg\ i))\ (Val\ w),\ (h,\ l) \rangle -\{\!\! \{ \text{WriteMem } a\ (ACell\ (nat\ (\text{sint } i)))\ w \}\!\!\} \rightarrow \langle \text{unit},\ (h',\ l) \rangle$$
- | *ALength1Red*:

$$uf, P, t \vdash 1 \langle a,\ s \rangle -ta \rightarrow \langle a',\ s^\wedge \rangle \implies uf, P, t \vdash 1 \langle a \cdot \text{length},\ s \rangle -ta \rightarrow \langle a' \cdot \text{length},\ s^\wedge \rangle$$
- | *Red1ALength*:

$$\text{typeof-addr } h\ a = \lfloor \text{Array-type } T\ n \rfloor$$

$$\implies uf, P, t \vdash 1 \langle addr\ a \cdot \text{length},\ (h,\ xs) \rangle -\varepsilon \rightarrow \langle Val\ (Intg\ (\text{word-of-nat } n)),\ (h,\ xs) \rangle$$
- | *Red1ALengthNull*:

$$uf, P, t \vdash 1 \langle \text{null} \cdot \text{length},\ s \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer},\ s \rangle$$
- | *FAcc1Red*:

$$uf, P, t \vdash 1 \langle e,\ s \rangle -ta \rightarrow \langle e',\ s^\wedge \rangle \implies uf, P, t \vdash 1 \langle e \cdot F\{D\},\ s \rangle -ta \rightarrow \langle e' \cdot F\{D\},\ s^\wedge \rangle$$
- | *Red1FAcc*:

$$\text{heap-read } h\ a\ (CField\ D\ F)\ v$$

$$\implies uf, P, t \vdash 1 \langle (addr\ a) \cdot F\{D\},\ (h,\ xs) \rangle -\{\!\! \{ \text{ReadMem } a\ (CField\ D\ F)\ v \}\!\!\} \rightarrow \langle Val\ v,\ (h,\ xs) \rangle$$
- | *Red1FAccNull*:

$$uf, P, t \vdash 1 \langle \text{null} \cdot F\{D\},\ s \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer},\ s \rangle$$
- | *FAss1Red1*:

$$uf, P, t \vdash 1 \langle e,\ s \rangle -ta \rightarrow \langle e',\ s^\wedge \rangle \implies uf, P, t \vdash 1 \langle e \cdot F\{D\} := e2,\ s \rangle -ta \rightarrow \langle e' \cdot F\{D\} := e2,\ s^\wedge \rangle$$
- | *FAss1Red2*:

$$uf, P, t \vdash 1 \langle e,\ s \rangle -ta \rightarrow \langle e',\ s^\wedge \rangle \implies uf, P, t \vdash 1 \langle FAss\ (Val\ v)\ F\ D\ e,\ s \rangle -ta \rightarrow \langle Val\ v \cdot F\{D\} := e',\ s^\wedge \rangle$$
- | *Red1FAss*:

$$\text{heap-write } h\ a\ (CField\ D\ F)\ v\ h' \implies$$

$$uf, P, t \vdash 1 \langle FAss\ (addr\ a)\ F\ D\ (Val\ v),\ (h,\ l) \rangle -\{\!\! \{ \text{WriteMem } a\ (CField\ D\ F)\ v \}\!\!\} \rightarrow \langle \text{unit},\ (h',\ l) \rangle$$
- | *Red1FAssNull*:

$$uf, P, t \vdash 1 \langle FAss\ \text{null}\ F\ D\ (Val\ v),\ s \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer},\ s \rangle$$
- | *CAS1Red1*:

$$uf, P, t \vdash 1 \langle e,\ s \rangle -ta \rightarrow \langle e',\ s^\wedge \rangle \implies$$

$$uf, P, t \vdash 1 \langle e \cdot \text{compareAndSwap}(D \cdot F,\ e2,\ e3),\ s \rangle -ta \rightarrow \langle e' \cdot \text{compareAndSwap}(D \cdot F,\ e2,\ e3),\ s^\wedge \rangle$$
- | *CAS1Red2*:

$$uf, P, t \vdash 1 \langle e,\ s \rangle -ta \rightarrow \langle e',\ s^\wedge \rangle \implies$$

$$uf, P, t \vdash 1 \langle Val\ v \cdot \text{compareAndSwap}(D \cdot F,\ e,\ e3),\ s \rangle -ta \rightarrow \langle Val\ v \cdot \text{compareAndSwap}(D \cdot F,\ e',\ e3),\ s^\wedge \rangle$$
- | *CAS1Red3*:

$$uf, P, t \vdash 1 \langle e,\ s \rangle -ta \rightarrow \langle e',\ s^\wedge \rangle \implies$$

$uf, P, t \vdash 1 \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e), s \rangle -ta \rightarrow \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e'), s' \rangle$

| *CAS1Null*:

$uf, P, t \vdash 1 \langle \text{null} \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v'), s \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, s \rangle$

| *Red1CASSucceed*:

$\llbracket \text{heap-read } h \ a \ (CField \ D \ F) \ v; \text{heap-write } h \ a \ (CField \ D \ F) \ v' \ h' \rrbracket \Longrightarrow$
 $uf, P, t \vdash 1 \langle \text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v'), (h, l) \rangle$
 $-\llbracket \text{ReadMem } a \ (CField \ D \ F) \ v, \text{WriteMem } a \ (CField \ D \ F) \ v' \rrbracket \rightarrow$
 $\langle \text{true}, (h', l) \rangle$

| *Red1CASFail*:

$\llbracket \text{heap-read } h \ a \ (CField \ D \ F) \ v''; \ v \neq v'' \rrbracket \Longrightarrow$
 $uf, P, t \vdash 1 \langle \text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v'), (h, l) \rangle$
 $-\llbracket \text{ReadMem } a \ (CField \ D \ F) \ v'' \rrbracket \rightarrow$
 $\langle \text{false}, (h, l) \rangle$

| *Call1Obj*:

$uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \Longrightarrow uf, P, t \vdash 1 \langle e \cdot M(es), s \rangle -ta \rightarrow \langle e' \cdot M(es), s' \rangle$

| *Call1Params*:

$uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \Longrightarrow$
 $uf, P, t \vdash 1 \langle (\text{Val } v) \cdot M(es), s \rangle -ta \rightarrow \langle (\text{Val } v) \cdot M(es'), s' \rangle$

| *Red1CallExternal*:

$\llbracket \text{typeof-addr } (hp \ s) \ a = \lfloor T \rfloor; \ P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D; \ P, t \vdash \langle a \cdot M(vs), hp \ s \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle;$
 $e' = \text{extRet2J1 } ((\text{addr } a) \cdot M(\text{map } \text{Val } vs)) \ va; \ s' = (h', \text{lcl } s) \rrbracket$
 $\Longrightarrow uf, P, t \vdash 1 \langle (\text{addr } a) \cdot M(\text{map } \text{Val } vs), s \rangle -ta \rightarrow \langle e', s' \rangle$

| *Red1CallNull*:

$uf, P, t \vdash 1 \langle \text{null} \cdot M(\text{map } \text{Val } vs), s \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, s \rangle$

| *Block1Some*:

$V < \text{length } x \Longrightarrow uf, P, t \vdash 1 \langle \{V:T=\lfloor v \rfloor\}; e \rangle, (h, x) \rangle -\varepsilon \rightarrow \langle \{V:T=None\}; e \rangle, (h, x[V := v]) \rangle$

| *Block1Red*:

$uf, P, t \vdash 1 \langle e, (h, x) \rangle -ta \rightarrow \langle e', (h', x') \rangle$
 $\Longrightarrow uf, P, t \vdash 1 \langle \{V:T=None\}; e \rangle, (h, x) \rangle -ta \rightarrow \langle \{V:T=None\}; e' \rangle, (h', x') \rangle$

| *Red1Block*:

$uf, P, t \vdash 1 \langle \{V:T=None; \text{Val } u\}, s \rangle -\varepsilon \rightarrow \langle \text{Val } u, s \rangle$

| *Synchronized1Red1*:

$uf, P, t \vdash 1 \langle o', s \rangle -ta \rightarrow \langle o'', s' \rangle \Longrightarrow uf, P, t \vdash 1 \langle \text{sync}_V(o') \ e, s \rangle -ta \rightarrow \langle \text{sync}_V(o'') \ e, s' \rangle$

| *Synchronized1Null*:

$V < \text{length } xs \Longrightarrow uf, P, t \vdash 1 \langle \text{sync}_V(\text{null}) \ e, (h, xs) \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, (h, xs[V := \text{Null}]) \rangle$

| *Lock1Synchronized*:

$V < \text{length } xs \Longrightarrow uf, P, t \vdash 1 \langle \text{sync}_V(\text{addr } a) \ e, (h, xs) \rangle -\llbracket \text{Lock} \rightarrow a, \text{SyncLock } a \rrbracket \rightarrow \langle \text{insync}_V(a) \ e, (h, xs[V := \text{Addr } a]) \rangle$

- | *Synchronized1Red2*:
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle insync_V(a) e, s \rangle -ta \rightarrow \langle insync_V(a) e', s' \rangle$
- | *Unlock1Synchronized*:
 $\llbracket xs ! V = Addr a'; V < length xs \rrbracket \implies uf, P, t \vdash 1 \langle insync_V(a) (Val v), (h, xs) \rangle -\{\!\{Unlock \rightarrow a', SyncUnlock a'\}\!\} \rightarrow \langle Val v, (h, xs) \rangle$
- | *Unlock1SynchronizedNull*:
 $\llbracket xs ! V = Null; V < length xs \rrbracket \implies uf, P, t \vdash 1 \langle insync_V(a) (Val v), (h, xs) \rangle -\varepsilon \rightarrow \langle THROW NullPointer, (h, xs) \rangle$
- | *Unlock1SynchronizedFail*:
 $\llbracket uf; xs ! V = Addr a'; V < length xs \rrbracket \implies uf, P, t \vdash 1 \langle insync_V(a) (Val v), (h, xs) \rangle -\{\!\{UnlockFail \rightarrow a'\}\!\} \rightarrow \langle THROW IllegalMonitorState, (h, xs) \rangle$
- | *Seq1Red*:
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle e;;e2, s \rangle -ta \rightarrow \langle e';e2, s' \rangle$
- | *Red1Seq*:
 $uf, P, t \vdash 1 \langle Seq (Val v) e, s \rangle -\varepsilon \rightarrow \langle e, s \rangle$
- | *Cond1Red*:
 $uf, P, t \vdash 1 \langle b, s \rangle -ta \rightarrow \langle b', s' \rangle \implies uf, P, t \vdash 1 \langle if (b) e1 else e2, s \rangle -ta \rightarrow \langle if (b') e1 else e2, s' \rangle$
- | *Red1CondT*:
 $uf, P, t \vdash 1 \langle if (true) e1 else e2, s \rangle -\varepsilon \rightarrow \langle e1, s \rangle$
- | *Red1CondF*:
 $uf, P, t \vdash 1 \langle if (false) e1 else e2, s \rangle -\varepsilon \rightarrow \langle e2, s \rangle$
- | *Red1While*:
 $uf, P, t \vdash 1 \langle while(b) c, s \rangle -\varepsilon \rightarrow \langle if (b) (c;;while(b) c) else unit, s \rangle$
- | *Throw1Red*:
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle throw e, s \rangle -ta \rightarrow \langle throw e', s' \rangle$
- | *Red1ThrowNull*:
 $uf, P, t \vdash 1 \langle throw null, s \rangle -\varepsilon \rightarrow \langle THROW NullPointer, s \rangle$
- | *Try1Red*:
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle try e catch(C V) e2, s \rangle -ta \rightarrow \langle try e' catch(C V) e2, s' \rangle$
- | *Red1Try*:
 $uf, P, t \vdash 1 \langle try (Val v) catch(C V) e2, s \rangle -\varepsilon \rightarrow \langle Val v, s \rangle$
- | *Red1TryCatch*:
 $\llbracket typeof-addr h a = \lfloor Class\text{-}type D \rfloor; P \vdash D \preceq^* C; V < length x \rrbracket \implies uf, P, t \vdash 1 \langle try (Throw a) catch(C V) e2, (h, x) \rangle -\varepsilon \rightarrow \langle \{V:Class C=None; e2\}, (h, x[V := Addr a]) \rangle$
- | *Red1TryFail*:

$\llbracket \text{typeof-addr } (hp \ s) \ a = \lfloor \text{Class-type } D \rfloor; \neg \ P \vdash D \preceq^* C \rrbracket$
 $\implies uf, P, t \vdash 1 \langle \text{try } (Throw \ a) \ \text{catch}(C \ V) \ e2, s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *List1Red1:*

$uf, P, t \vdash 1 \langle e, s \rangle \xrightarrow{-ta} \langle e', s' \rangle \implies$
 $uf, P, t \vdash 1 \langle e \# es, s \rangle \xrightarrow{[-ta]} \langle e' \# es, s' \rangle$

| *List1Red2:*

$uf, P, t \vdash 1 \langle es, s \rangle \xrightarrow{[-ta]} \langle es', s' \rangle \implies$
 $uf, P, t \vdash 1 \langle Val \ v \ \# \ es, s \rangle \xrightarrow{[-ta]} \langle Val \ v \ \# \ es', s' \rangle$

| *New1ArrayThrow:* $uf, P, t \vdash 1 \langle \text{newA } T \lfloor Throw \ a \rfloor, s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *Cast1Throw:* $uf, P, t \vdash 1 \langle \text{Cast } C \ (Throw \ a), s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *InstanceOf1Throw:* $uf, P, t \vdash 1 \langle (Throw \ a) \ \text{instanceof } T, s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *Bin1OpThrow1:* $uf, P, t \vdash 1 \langle (Throw \ a) \ \llbracket \text{bop} \rrbracket \ e2, s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *Bin1OpThrow2:* $uf, P, t \vdash 1 \langle (Val \ v1) \ \llbracket \text{bop} \rrbracket \ (Throw \ a), s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *LAss1Throw:* $uf, P, t \vdash 1 \langle V := (Throw \ a), s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *AAcc1Throw1:* $uf, P, t \vdash 1 \langle (Throw \ a) \lfloor i \rfloor, s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *AAcc1Throw2:* $uf, P, t \vdash 1 \langle (Val \ v) \lfloor Throw \ a \rfloor, s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *AAss1Throw1:* $uf, P, t \vdash 1 \langle (Throw \ a) \lfloor i \rfloor := e, s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *AAss1Throw2:* $uf, P, t \vdash 1 \langle (Val \ v) \lfloor Throw \ a \rfloor := e, s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *AAss1Throw3:* $uf, P, t \vdash 1 \langle \text{AAss } (Val \ v) \ (Val \ i) \ (Throw \ a), s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *ALength1Throw:* $uf, P, t \vdash 1 \langle (Throw \ a) \cdot \text{length}, s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *FAcc1Throw:* $uf, P, t \vdash 1 \langle (Throw \ a) \cdot F \{D\}, s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *FAss1Throw1:* $uf, P, t \vdash 1 \langle (Throw \ a) \cdot F \{D\} := e2, s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *FAss1Throw2:* $uf, P, t \vdash 1 \langle \text{FAss } (Val \ v) \ F \ D \ (Throw \ a), s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *CAS1Throw:* $uf, P, t \vdash 1 \langle \text{Throw } a \cdot \text{compareAndSwap}(D \cdot F, e2, e3), s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *CAS1Throw2:* $uf, P, t \vdash 1 \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } a, e3), s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *CAS1Throw3:* $uf, P, t \vdash 1 \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', \text{Throw } a), s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *Call1ThrowObj:* $uf, P, t \vdash 1 \langle (Throw \ a) \cdot M(es), s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *Call1ThrowParams:* $\llbracket es = \text{map } Val \ vs \ @ \ Throw \ a \ \# \ es' \rrbracket \implies uf, P, t \vdash 1 \langle (Val \ v) \cdot M(es), s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *Block1Throw:* $uf, P, t \vdash 1 \langle \{V: T = \text{None}; \text{Throw } a\}, s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *Synchronized1Throw1:* $uf, P, t \vdash 1 \langle \text{sync}_V \ (Throw \ a) \ e, s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *Synchronized1Throw2:*

$\llbracket xs \ ! \ V = \text{Addr } a'; \ V < \text{length } xs \rrbracket$

$\implies uf, P, t \vdash 1 \langle \text{insync}_V \ (a) \ \text{Throw } ad, (h, xs) \rangle \xrightarrow{-\{\text{Unlock} \rightarrow a', \text{SyncUnlock } a'\}} \langle \text{Throw } ad, (h, xs) \rangle$

| *Synchronized1Throw2Fail:*

$\llbracket uf; xs \ ! \ V = \text{Addr } a'; \ V < \text{length } xs \rrbracket$

$\implies uf, P, t \vdash 1 \langle \text{insync}_V \ (a) \ \text{Throw } ad, (h, xs) \rangle \xrightarrow{-\{\text{UnlockFail} \rightarrow a'\}} \langle \text{THROW } \text{IllegalMonitorState}, (h, xs) \rangle$

| *Synchronized1Throw2Null:*

$\llbracket xs \ ! \ V = \text{Null}; \ V < \text{length } xs \rrbracket$

$\implies uf, P, t \vdash 1 \langle \text{insync}_V \ (a) \ \text{Throw } ad, (h, xs) \rangle \xrightarrow{-\varepsilon} \langle \text{THROW } \text{NullPointer}, (h, xs) \rangle$

| *Seq1Throw:* $uf, P, t \vdash 1 \langle (Throw \ a); e2, s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *Cond1Throw:* $uf, P, t \vdash 1 \langle \text{if } (Throw \ a) \ e1 \ \text{else } e2, s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

| *Throw1Throw:* $uf, P, t \vdash 1 \langle \text{throw}(Throw \ a), s \rangle \xrightarrow{-\varepsilon} \langle Throw \ a, s \rangle$

inductive-cases red1-cases:

$uf, P, t \vdash 1 \langle \text{new } C, s \rangle \xrightarrow{-ta} \langle e', s' \rangle$

$uf, P, t \vdash 1 \langle \text{new } T \lfloor e \rfloor, s \rangle \xrightarrow{-ta} \langle e', s' \rangle$

$uf, P, t \vdash 1 \langle e \ \llbracket \text{bop} \rrbracket \ e', s \rangle \xrightarrow{-ta} \langle e'', s' \rangle$

$uf, P, t \vdash 1 \langle \text{Var } V, s \rangle \xrightarrow{-ta} \langle e', s' \rangle$

$uf, P, t \vdash 1 \langle V := e, s \rangle \xrightarrow{-ta} \langle e', s' \rangle$

$uf, P, t \vdash 1 \langle a[i], s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle a[i] := e, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle a.length, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle e.F\{D\}, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle e.F\{D\} := e2, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle e.compareAndSwap(D.F, e', e''), s \rangle -ta \rightarrow \langle e''', s' \rangle$
 $uf, P, t \vdash 1 \langle e.M(es), s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle \{V:T=vo; e\}, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle sync_V(o') e, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle insync_V(a) e, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle e;;e', s \rangle -ta \rightarrow \langle e'', s' \rangle$
 $uf, P, t \vdash 1 \langle throw e, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle try e catch(C V) e'', s \rangle -ta \rightarrow \langle e', s' \rangle$

inductive *Red1* ::

$bool \Rightarrow 'addr\ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \Rightarrow ('addr\ expr1 \times 'addr\ locals1)$
 $list \Rightarrow 'heap$
 $\Rightarrow ('addr, 'thread\text{-id}, 'heap)\ J1\text{-thread}\text{-action}$
 $\Rightarrow ('addr\ expr1 \times 'addr\ locals1) \Rightarrow ('addr\ expr1 \times 'addr\ locals1)\ list \Rightarrow 'heap \Rightarrow bool$
 $(\langle -, - \rangle \vdash 1 ((1\langle -'/-,- \rangle) \dashrightarrow / (1\langle -'/-,- \rangle))) \triangleright [51, 51, 0, 0, 0, 0, 0, 0, 0, 0] 81$

for $uf :: bool$ **and** $P :: 'addr\ J1\text{-prog}$ **and** $t :: 'thread\text{-id}$

where

red1Red:

$uf, P, t \vdash 1 \langle e, (h, x) \rangle -ta \rightarrow \langle e', (h', x') \rangle$
 $\implies uf, P, t \vdash 1 \langle (e, x)/exs, h \rangle -extTA2J1\ P\ ta \rightarrow \langle (e', x')/exs, h' \rangle$

| *red1Call*:

$\llbracket call1\ e = \llbracket (a, M, vs) \rrbracket; \text{typeof}\text{-addr}\ h\ a = \llbracket U \rrbracket;$
 $P \vdash \text{class}\text{-type}\text{-of}\ U\ \text{sees}\ M:T_s \rightarrow T = \llbracket body \rrbracket\ \text{in}\ D;$
 $\text{size}\ vs = \text{size}\ T_s \rrbracket$
 $\implies uf, P, t \vdash 1 \langle (e, x)/exs, h \rangle -\varepsilon \rightarrow \langle (blocks1\ 0\ (Class\ D\ \# T_s)\ body, Addr\ a\ \# vs\ @\ replicate\ (max\text{-vars}\ body)\ \text{undefined}\text{-value})/(e, x)\ \# exs, h \rangle$

| *red1Return*:

$final\ e' \implies uf, P, t \vdash 1 \langle (e', x')/(e, x)\ \# exs, h \rangle -\varepsilon \rightarrow \langle (inline\text{-call}\ e'\ e, x)/exs, h \rangle$

abbreviation *mred1g* :: $bool \Rightarrow 'addr\ J1\text{-prog} \Rightarrow ('addr, 'thread\text{-id}, ('addr\ expr1 \times 'addr\ locals1) \times ('addr\ expr1 \times 'addr\ locals1)\ list, 'heap, 'addr, ('addr, 'thread\text{-id})\ \text{obs}\text{-event})\ \text{semantics}$

where $mred1g\ uf\ P \equiv \lambda t ((ex, exs), h)\ ta\ ((ex', exs'), h').\ uf, P, t \vdash 1 \langle ex/exs, h \rangle -ta \rightarrow \langle ex'/exs', h' \rangle$

abbreviation *mred1'* ::

$'addr\ J1\text{-prog} \Rightarrow ('addr, 'thread\text{-id}, ('addr\ expr1 \times 'addr\ locals1) \times ('addr\ expr1 \times 'addr\ locals1)$
 $list, 'heap, 'addr, ('addr, 'thread\text{-id})\ \text{obs}\text{-event})\ \text{semantics}$

where $mred1' \equiv mred1g\ False$

abbreviation *mred1* ::

$'addr\ J1\text{-prog} \Rightarrow ('addr, 'thread\text{-id}, ('addr\ expr1 \times 'addr\ locals1) \times ('addr\ expr1 \times 'addr\ locals1)$
 $list, 'heap, 'addr, ('addr, 'thread\text{-id})\ \text{obs}\text{-event})\ \text{semantics}$

where $mred1 \equiv mred1g\ True$

lemma *red1-preserves-len*: $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{length}\ (lcl\ s') = \text{length}\ (lcl\ s)$

and *reds1-preserves-len*: $uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \text{length}\ (lcl\ s') = \text{length}\ (lcl\ s)$

<proof>

lemma *reds1-preserves-elen*: $uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \text{length } es' = \text{length } es$
 ⟨proof⟩

lemma *red1-Val-iff* [iff]:
 $\neg uf, P, t \vdash 1 \langle \text{Val } v, s \rangle -ta \rightarrow \langle e', s' \rangle$
 ⟨proof⟩

lemma *red1-Throw-iff* [iff]:
 $\neg uf, P, t \vdash 1 \langle \text{Throw } a, xs \rangle -ta \rightarrow \langle e', s' \rangle$
 ⟨proof⟩

lemma *reds1-Nil-iff* [iff]:
 $\neg uf, P, t \vdash 1 \langle [], s \rangle [-ta \rightarrow] \langle es', s' \rangle$
 ⟨proof⟩

lemma *reds1-Val-iff* [iff]:
 $\neg uf, P, t \vdash 1 \langle \text{map Val } vs, s \rangle [-ta \rightarrow] \langle es', s' \rangle$
 ⟨proof⟩

lemma *reds1-map-Val-Throw-iff* [iff]:
 $\neg uf, P, t \vdash 1 \langle \text{map Val } vs @ \text{Throw } a \# es, s \rangle [-ta \rightarrow] \langle es', s' \rangle$
 ⟨proof⟩

lemma *red1-max-vars-decr*: $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{max-vars } e' \leq \text{max-vars } e$
and *reds1-max-varss-decr*: $uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \text{max-varss } es' \leq \text{max-varss } es$
 ⟨proof⟩

lemma *red1-new-thread-heap*: $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewThread } t' \text{ ex } h \in \text{set } \{ta\}_t \rrbracket \implies h = \text{hp } s'$
and *reds1-new-thread-heap*: $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{NewThread } t' \text{ ex } h \in \text{set } \{ta\}_t \rrbracket \implies h = \text{hp } s'$
 ⟨proof⟩

lemma *red1-new-threadD*:
 $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewThread } t' \text{ x } H \in \text{set } \{ta\}_t \rrbracket$
 $\implies \exists a M vs va T Ts Tr D. P, t \vdash \langle a \cdot M(vs), \text{hp } s \rangle -ta \rightarrow \text{ext } \langle va, \text{hp } s' \rangle \wedge \text{typeof-addr } (\text{hp } s) a = \lfloor T \rfloor \wedge P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D$
and *reds1-new-threadD*:
 $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{NewThread } t' \text{ x } H \in \text{set } \{ta\}_t \rrbracket$
 $\implies \exists a M vs va T Ts Tr D. P, t \vdash \langle a \cdot M(vs), \text{hp } s \rangle -ta \rightarrow \text{ext } \langle va, \text{hp } s' \rangle \wedge \text{typeof-addr } (\text{hp } s) a = \lfloor T \rfloor \wedge P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D$
 ⟨proof⟩

lemma *red1-call-synthesized*: $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{call1 } e = \lfloor aMvs \rfloor \rrbracket \implies \text{synthesized-call } P (\text{hp } s) aMvs$
and *reds1-calls-synthesized*: $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{calls1 } es = \lfloor aMvs \rfloor \rrbracket \implies \text{synthesized-call } P (\text{hp } s) aMvs$
 ⟨proof⟩

lemma *red1-preserves-B*: $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \mathcal{B} e n \rrbracket \implies \mathcal{B} e' n$
and *reds1-preserves-Bs*: $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \mathcal{B} s es n \rrbracket \implies \mathcal{B} s es' n$
 ⟨proof⟩

end

context *J1-heap* begin

lemma *red1-heat-incr*: $uf, P, t \vdash 1 \langle e, s \rangle \text{-ta} \rightarrow \langle e', s' \rangle \implies \text{heat} (hp\ s) (hp\ s')$
and *reds1-heat-incr*: $uf, P, t \vdash 1 \langle es, s \rangle \text{-ta} \rightarrow \langle es', s' \rangle \implies \text{heat} (hp\ s) (hp\ s')$
 <proof>

lemma *Red1-heat-incr*: $uf, P, t \vdash 1 \langle ex/exs, h \rangle \text{-ta} \rightarrow \langle ex'/exs', h' \rangle \implies h \leq h'$
 <proof>

end

7.6.1 Silent moves

context *J1-heap-base* begin

primrec $\tau\text{move1} :: 'm\ \text{prog} \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr)\ \text{exp} \Rightarrow \text{bool}$
and $\tau\text{moves1} :: 'm\ \text{prog} \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr)\ \text{exp}\ \text{list} \Rightarrow \text{bool}$
where

$\tau\text{move1}\ P\ h\ (\text{new}\ C) \longleftrightarrow \text{False}$
 $|\ \tau\text{move1}\ P\ h\ (\text{newA}\ T[e]) \longleftrightarrow \tau\text{move1}\ P\ h\ e \vee (\exists a. e = \text{Throw}\ a)$
 $|\ \tau\text{move1}\ P\ h\ (\text{Cast}\ U\ e) \longleftrightarrow \tau\text{move1}\ P\ h\ e \vee \text{final}\ e$
 $|\ \tau\text{move1}\ P\ h\ (e\ \text{instanceof}\ T) \longleftrightarrow \tau\text{move1}\ P\ h\ e \vee \text{final}\ e$
 $|\ \tau\text{move1}\ P\ h\ (e\ \ll\text{bop}\ \gg\ e') \longleftrightarrow \tau\text{move1}\ P\ h\ e \vee (\exists a. e = \text{Throw}\ a) \vee (\exists v. e = \text{Val}\ v \wedge (\tau\text{move1}\ P\ h\ e' \vee \text{final}\ e'))$
 $|\ \tau\text{move1}\ P\ h\ (\text{Val}\ v) \longleftrightarrow \text{False}$
 $|\ \tau\text{move1}\ P\ h\ (\text{Var}\ V) \longleftrightarrow \text{True}$
 $|\ \tau\text{move1}\ P\ h\ (V := e) \longleftrightarrow \tau\text{move1}\ P\ h\ e \vee \text{final}\ e$
 $|\ \tau\text{move1}\ P\ h\ (a[i]) \longleftrightarrow \tau\text{move1}\ P\ h\ a \vee (\exists ad. a = \text{Throw}\ ad) \vee (\exists v. a = \text{Val}\ v \wedge (\tau\text{move1}\ P\ h\ i \vee (\exists a. i = \text{Throw}\ a)))$
 $|\ \tau\text{move1}\ P\ h\ (A\text{Ass}\ a\ i\ e) \longleftrightarrow \tau\text{move1}\ P\ h\ a \vee (\exists ad. a = \text{Throw}\ ad) \vee (\exists v. a = \text{Val}\ v \wedge (\tau\text{move1}\ P\ h\ i \vee (\exists a. i = \text{Throw}\ a) \vee (\exists v. i = \text{Val}\ v \wedge (\tau\text{move1}\ P\ h\ e \vee (\exists a. e = \text{Throw}\ a))))))$
 $|\ \tau\text{move1}\ P\ h\ (a.\text{length}) \longleftrightarrow \tau\text{move1}\ P\ h\ a \vee (\exists ad. a = \text{Throw}\ ad)$
 $|\ \tau\text{move1}\ P\ h\ (e.F\{D\}) \longleftrightarrow \tau\text{move1}\ P\ h\ e \vee (\exists a. e = \text{Throw}\ a)$
 $|\ \tau\text{move1}\ P\ h\ (F\text{Ass}\ e\ F\ D\ e') \longleftrightarrow \tau\text{move1}\ P\ h\ e \vee (\exists a. e = \text{Throw}\ a) \vee (\exists v. e = \text{Val}\ v \wedge (\tau\text{move1}\ P\ h\ e' \vee (\exists a. e' = \text{Throw}\ a)))$
 $|\ \tau\text{move1}\ P\ h\ (e.\text{compareAndSwap}(D.F, e', e'')) \longleftrightarrow \tau\text{move1}\ P\ h\ e \vee (\exists a. e = \text{Throw}\ a) \vee (\exists v. e = \text{Val}\ v \wedge (\tau\text{move1}\ P\ h\ e' \vee (\exists a. e' = \text{Throw}\ a) \vee (\exists v. e' = \text{Val}\ v \wedge (\tau\text{move1}\ P\ h\ e'' \vee (\exists a. e'' = \text{Throw}\ a))))))$
 $|\ \tau\text{move1}\ P\ h\ (e.M(es)) \longleftrightarrow \tau\text{move1}\ P\ h\ e \vee (\exists a. e = \text{Throw}\ a) \vee (\exists v. e = \text{Val}\ v \wedge (\tau\text{moves1}\ P\ h\ es \vee (\exists vs\ a\ es'. es = \text{map}\ \text{Val}\ vs\ @\ \text{Throw}\ a\ \# es') \vee (\exists vs. es = \text{map}\ \text{Val}\ vs \wedge (v = \text{Null} \vee (\forall T\ C\ Ts\ Tr\ D. \text{typeof}_h\ v = \lfloor T \rfloor \longrightarrow \text{class-type-of}'\ T = \lfloor C \rfloor \longrightarrow P \vdash C\ \text{sees}\ M:Ts \rightarrow Tr = \text{Native}\ \text{in}\ D \longrightarrow \tau\text{external-defs}\ D\ M))))))$
 $|\ \tau\text{move1}\ P\ h\ (\{V:T=vo; e\}) \longleftrightarrow vo \neq \text{None} \vee \tau\text{move1}\ P\ h\ e \vee \text{final}\ e$
 $|\ \tau\text{move1}\ P\ h\ (\text{sync}_{V'}(e)\ e') \longleftrightarrow \tau\text{move1}\ P\ h\ e \vee (\exists a. e = \text{Throw}\ a)$
 $|\ \tau\text{move1}\ P\ h\ (\text{insync}_{V'}(ad)\ e) \longleftrightarrow \tau\text{move1}\ P\ h\ e$
 $|\ \tau\text{move1}\ P\ h\ (e;;e') \longleftrightarrow \tau\text{move1}\ P\ h\ e \vee \text{final}\ e$
 $|\ \tau\text{move1}\ P\ h\ (\text{if}\ (e)\ e'\ \text{else}\ e'') \longleftrightarrow \tau\text{move1}\ P\ h\ e \vee \text{final}\ e$
 $|\ \tau\text{move1}\ P\ h\ (\text{while}\ (e)\ e') = \text{True}$
 $|\ \tau\text{move1}\ P\ h\ (\text{throw}\ e) \longleftrightarrow \tau\text{move1}\ P\ h\ e \vee (\exists a. e = \text{Throw}\ a) \vee e = \text{null}$
 $|\ \tau\text{move1}\ P\ h\ (\text{try}\ e\ \text{catch}(C\ V)\ e') \longleftrightarrow \tau\text{move1}\ P\ h\ e \vee \text{final}\ e$

$\tau moves1 P h [] \longleftrightarrow False$
 $\tau moves1 P h (e \# es) \longleftrightarrow \tau move1 P h e \vee (\exists v. e = Val v \wedge \tau moves1 P h es)$

fun $\tau Move1 :: 'm prog \Rightarrow 'heap \Rightarrow (('a, 'b, 'addr) exp \times 'c) \times (('a, 'b, 'addr) exp \times 'd) list \Rightarrow bool$
where
 $\tau Move1 P h ((e, x), exs) = (\tau move1 P h e \vee final e)$

definition $\tau red1g :: bool \Rightarrow 'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 \times 'addr locals1) \Rightarrow ('addr expr1 \times 'addr locals1) \Rightarrow bool$
where $\tau red1g uf P t h exs e'xs' = (uf, P, t \vdash 1 \langle fst exs, (h, snd exs) \rangle -\varepsilon \rightarrow \langle fst e'xs', (h, snd e'xs') \rangle) \wedge \tau move1 P h (fst exs)$

definition $\tau reds1g :: bool \Rightarrow 'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 list \times 'addr locals1) \Rightarrow ('addr expr1 list \times 'addr locals1) \Rightarrow bool$
where
 $\tau reds1g uf P t h exs es'xs' = (uf, P, t \vdash 1 \langle fst exs, (h, snd exs) \rangle [-\varepsilon \rightarrow] \langle fst es'xs', (h, snd es'xs') \rangle) \wedge \tau moves1 P h (fst exs)$

abbreviation $\tau red1gt :: bool \Rightarrow 'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 \times 'addr locals1) \Rightarrow ('addr expr1 \times 'addr locals1) \Rightarrow bool$
where $\tau red1gt uf P t h \equiv (\tau red1g uf P t h)^{++}$

abbreviation $\tau reds1gt :: bool \Rightarrow 'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 list \times 'addr locals1) \Rightarrow ('addr expr1 list \times 'addr locals1) \Rightarrow bool$
where $\tau reds1gt uf P t h \equiv (\tau reds1g uf P t h)^{++}$

abbreviation $\tau red1gr :: bool \Rightarrow 'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 \times 'addr locals1) \Rightarrow ('addr expr1 \times 'addr locals1) \Rightarrow bool$
where $\tau red1gr uf P t h \equiv (\tau red1g uf P t h)^{**}$

abbreviation $\tau reds1gr :: bool \Rightarrow 'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 list \times 'addr locals1) \Rightarrow ('addr expr1 list \times 'addr locals1) \Rightarrow bool$
where $\tau reds1gr uf P t h \equiv (\tau reds1g uf P t h)^{**}$

definition $\tau Red1g :: bool \Rightarrow 'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 \times 'addr locals1) \times (('addr expr1 \times 'addr locals1) list) \Rightarrow ('addr expr1 \times 'addr locals1) \times (('addr expr1 \times 'addr locals1) list) \Rightarrow bool$
where $\tau Red1g uf P t h exxs ex'xs' = (uf, P, t \vdash 1 \langle fst exxs/snd exxs, h \rangle -\varepsilon \rightarrow \langle fst ex'xs'/snd ex'xs', h \rangle) \wedge \tau Move1 P h exxs$

abbreviation $\tau Red1gt :: bool \Rightarrow 'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 \times 'addr locals1) \times (('addr expr1 \times 'addr locals1) list) \Rightarrow ('addr expr1 \times 'addr locals1) \times (('addr expr1 \times 'addr locals1) list) \Rightarrow bool$
where $\tau Red1gt uf P t h \equiv (\tau Red1g uf P t h)^{++}$

abbreviation $\tau Red1gr :: bool \Rightarrow 'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 \times 'addr locals1) \times (('addr expr1 \times 'addr locals1) list) \Rightarrow ('addr expr1 \times 'addr locals1) \times (('addr expr1 \times 'addr locals1) list) \Rightarrow bool$

locals1) list)
 $\Rightarrow ('addr\ expr1 \times 'addr\ locals1) \times (('addr\ expr1 \times 'addr\ locals1)\ list) \Rightarrow bool$
where $\tau Red1gr\ uf\ P\ t\ h \equiv (\tau Red1g\ uf\ P\ t\ h)^{**}$

abbreviation $\tau red1 ::$
 $'addr\ J1\ prog \Rightarrow 'thread\ id \Rightarrow 'heap \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \Rightarrow ('addr\ expr1 \times 'addr\ locals1)$
 $\Rightarrow bool$
where $\tau red1 \equiv \tau red1g\ True$

abbreviation $\tau reds1 ::$
 $'addr\ J1\ prog \Rightarrow 'thread\ id \Rightarrow 'heap \Rightarrow ('addr\ expr1\ list \times 'addr\ locals1) \Rightarrow ('addr\ expr1\ list \times 'addr\ locals1)$
 $\Rightarrow bool$
where $\tau reds1 \equiv \tau reds1g\ True$

abbreviation $\tau red1t ::$
 $'addr\ J1\ prog \Rightarrow 'thread\ id \Rightarrow 'heap \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \Rightarrow ('addr\ expr1 \times 'addr\ locals1)$
 $\Rightarrow bool$
where $\tau red1t \equiv \tau red1gt\ True$

abbreviation $\tau reds1t ::$
 $'addr\ J1\ prog \Rightarrow 'thread\ id \Rightarrow 'heap \Rightarrow ('addr\ expr1\ list \times 'addr\ locals1) \Rightarrow ('addr\ expr1\ list \times 'addr\ locals1)$
 $\Rightarrow bool$
where $\tau reds1t \equiv \tau reds1gt\ True$

abbreviation $\tau red1r ::$
 $'addr\ J1\ prog \Rightarrow 'thread\ id \Rightarrow 'heap \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \Rightarrow ('addr\ expr1 \times 'addr\ locals1)$
 $\Rightarrow bool$
where $\tau red1r \equiv \tau red1gr\ True$

abbreviation $\tau reds1r ::$
 $'addr\ J1\ prog \Rightarrow 'thread\ id \Rightarrow 'heap \Rightarrow ('addr\ expr1\ list \times 'addr\ locals1) \Rightarrow ('addr\ expr1\ list \times 'addr\ locals1)$
 $\Rightarrow bool$
where $\tau reds1r \equiv \tau reds1gr\ True$

abbreviation $\tau Red1 ::$
 $'addr\ J1\ prog \Rightarrow 'thread\ id \Rightarrow 'heap \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \times (('addr\ expr1 \times 'addr\ locals1)$
 $list)$
 $\Rightarrow ('addr\ expr1 \times 'addr\ locals1) \times (('addr\ expr1 \times 'addr\ locals1)\ list) \Rightarrow bool$
where $\tau Red1 \equiv \tau Red1g\ True$

abbreviation $\tau Red1t ::$
 $'addr\ J1\ prog \Rightarrow 'thread\ id \Rightarrow 'heap \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \times (('addr\ expr1 \times 'addr\ locals1)$
 $list)$
 $\Rightarrow ('addr\ expr1 \times 'addr\ locals1) \times (('addr\ expr1 \times 'addr\ locals1)\ list) \Rightarrow bool$
where $\tau Red1t \equiv \tau Red1gt\ True$

abbreviation $\tau Red1r ::$
 $'addr\ J1\ prog \Rightarrow 'thread\ id \Rightarrow 'heap \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \times (('addr\ expr1 \times 'addr\ locals1)$
 $list)$
 $\Rightarrow ('addr\ expr1 \times 'addr\ locals1) \times (('addr\ expr1 \times 'addr\ locals1)\ list) \Rightarrow bool$
where $\tau Red1r \equiv \tau Red1gr\ True$

abbreviation $\tau red1' ::$
 $'addr\ J1\ prog \Rightarrow 'thread\ id \Rightarrow 'heap \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \Rightarrow ('addr\ expr1 \times 'addr\ locals1)$

$\Rightarrow \text{bool}$

where $\tau\text{red1}' \equiv \tau\text{red1g False}$

abbreviation $\tau\text{reds1}' ::$

$'\text{addr J1-prog} \Rightarrow '\text{thread-id} \Rightarrow '\text{heap} \Rightarrow (' \text{addr expr1 list} \times '\text{addr locals1}) \Rightarrow (' \text{addr expr1 list} \times '\text{addr locals1}) \Rightarrow \text{bool}$

where $\tau\text{reds1}' \equiv \tau\text{reds1g False}$

abbreviation $\tau\text{red1}'t ::$

$'\text{addr J1-prog} \Rightarrow '\text{thread-id} \Rightarrow '\text{heap} \Rightarrow (' \text{addr expr1} \times '\text{addr locals1}) \Rightarrow (' \text{addr expr1} \times '\text{addr locals1}) \Rightarrow \text{bool}$

where $\tau\text{red1}'t \equiv \tau\text{red1gt False}$

abbreviation $\tau\text{reds1}'t ::$

$'\text{addr J1-prog} \Rightarrow '\text{thread-id} \Rightarrow '\text{heap} \Rightarrow (' \text{addr expr1 list} \times '\text{addr locals1}) \Rightarrow (' \text{addr expr1 list} \times '\text{addr locals1}) \Rightarrow \text{bool}$

where $\tau\text{reds1}'t \equiv \tau\text{reds1gt False}$

abbreviation $\tau\text{red1}'r ::$

$'\text{addr J1-prog} \Rightarrow '\text{thread-id} \Rightarrow '\text{heap} \Rightarrow (' \text{addr expr1} \times '\text{addr locals1}) \Rightarrow (' \text{addr expr1} \times '\text{addr locals1}) \Rightarrow \text{bool}$

where $\tau\text{red1}'r \equiv \tau\text{red1gr False}$

abbreviation $\tau\text{reds1}'r ::$

$'\text{addr J1-prog} \Rightarrow '\text{thread-id} \Rightarrow '\text{heap} \Rightarrow (' \text{addr expr1 list} \times '\text{addr locals1}) \Rightarrow (' \text{addr expr1 list} \times '\text{addr locals1}) \Rightarrow \text{bool}$

where $\tau\text{reds1}'r \equiv \tau\text{reds1gr False}$

abbreviation $\tau\text{Red1}' ::$

$'\text{addr J1-prog} \Rightarrow '\text{thread-id} \Rightarrow '\text{heap} \Rightarrow (' \text{addr expr1} \times '\text{addr locals1}) \times ((' \text{addr expr1} \times '\text{addr locals1}) \text{ list})$

$\Rightarrow (' \text{addr expr1} \times '\text{addr locals1}) \times ((' \text{addr expr1} \times '\text{addr locals1}) \text{ list}) \Rightarrow \text{bool}$

where $\tau\text{Red1}' \equiv \tau\text{Red1g False}$

abbreviation $\tau\text{Red1}'t ::$

$'\text{addr J1-prog} \Rightarrow '\text{thread-id} \Rightarrow '\text{heap} \Rightarrow (' \text{addr expr1} \times '\text{addr locals1}) \times ((' \text{addr expr1} \times '\text{addr locals1}) \text{ list})$

$\Rightarrow (' \text{addr expr1} \times '\text{addr locals1}) \times ((' \text{addr expr1} \times '\text{addr locals1}) \text{ list}) \Rightarrow \text{bool}$

where $\tau\text{Red1}'t \equiv \tau\text{Red1gt False}$

abbreviation $\tau\text{Red1}'r ::$

$'\text{addr J1-prog} \Rightarrow '\text{thread-id} \Rightarrow '\text{heap} \Rightarrow (' \text{addr expr1} \times '\text{addr locals1}) \times ((' \text{addr expr1} \times '\text{addr locals1}) \text{ list})$

$\Rightarrow (' \text{addr expr1} \times '\text{addr locals1}) \times ((' \text{addr expr1} \times '\text{addr locals1}) \text{ list}) \Rightarrow \text{bool}$

where $\tau\text{Red1}'r \equiv \tau\text{Red1gr False}$

abbreviation $\tau\text{MOVE1} ::$

$'m \text{ prog} \Rightarrow (((\text{addr expr1} \times '\text{addr locals1}) \times (' \text{addr expr1} \times '\text{addr locals1}) \text{ list}) \times '\text{heap}, (' \text{addr}, '\text{thread-id}, '\text{heap}) \text{ J1-thread-action}) \text{ trsys}$

where $\tau\text{MOVE1 } P \equiv \lambda(\text{exes}, h) \text{ ta s. } \tau\text{Move1 } P \ h \ \text{exes} \wedge \text{ ta} = \varepsilon$

lemma $\tau\text{move1-}\tau\text{moves1-intros}$:

fixes $e :: ('a, 'b, 'addr) \text{ exp}$ **and** $es :: ('a, 'b, 'addr) \text{ exp list}$

shows $\tau\text{move1NewArray: } \tau\text{move1 } P \ h \ e \Longrightarrow \tau\text{move1 } P \ h \ (\text{newA } T[e])$

and $\tau\text{move1Cast}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (\text{Cast } U \ e)$
and $\tau\text{move1CastRed}$: $\tau\text{move1 } P \ h \ (\text{Cast } U \ (\text{Val } v))$
and $\tau\text{move1InstanceOf}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (e \text{ instanceof } U)$
and $\tau\text{move1InstanceOfRed}$: $\tau\text{move1 } P \ h \ ((\text{Val } v) \text{ instanceof } U)$
and $\tau\text{move1BinOp1}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (e \llbracket \text{bop} \rrbracket e')$
and $\tau\text{move1BinOp2}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (\text{Val } v \llbracket \text{bop} \rrbracket e)$
and $\tau\text{move1BinOp}$: $\tau\text{move1 } P \ h \ (\text{Val } v \llbracket \text{bop} \rrbracket \text{Val } v')$
and $\tau\text{move1Var}$: $\tau\text{move1 } P \ h \ (\text{Var } V)$
and $\tau\text{move1LAss}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (V := e)$
and $\tau\text{move1LAssRed}$: $\tau\text{move1 } P \ h \ (V := \text{Val } v)$
and $\tau\text{move1AAcc1}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (e[e'])$
and $\tau\text{move1AAcc2}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (\text{Val } v[e])$
and $\tau\text{move1AAss1}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (\text{AAss } e \ e' \ e'')$
and $\tau\text{move1AAss2}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (\text{AAss } (\text{Val } v) \ e \ e')$
and $\tau\text{move1AAss3}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (\text{AAss } (\text{Val } v) \ (\text{Val } v') \ e)$
and $\tau\text{move1ALength}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (e.\text{length})$
and $\tau\text{move1FAcc}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (e.F\{D\})$
and $\tau\text{move1FAss1}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (\text{FAss } e \ F \ D \ e')$
and $\tau\text{move1FAss2}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (\text{FAss } (\text{Val } v) \ F \ D \ e)$
and $\tau\text{move1CAS1}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (e.\text{compareAndSwap}(D.F, e', e''))$
and $\tau\text{move1CAS2}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (\text{Val } v.\text{compareAndSwap}(D.F, e, e''))$
and $\tau\text{move1CAS3}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (\text{Val } v.\text{compareAndSwap}(D.F, \text{Val } v', e))$
and $\tau\text{move1CallObj}$: $\tau\text{move1 } P \ h \ \text{obj} \implies \tau\text{move1 } P \ h \ (\text{obj}.M(\text{ps}))$
and $\tau\text{move1CallParams}$: $\tau\text{moves1 } P \ h \ \text{ps} \implies \tau\text{move1 } P \ h \ (\text{Val } v.M(\text{ps}))$
and $\tau\text{move1Call}$: $(\bigwedge T \ C \ Ts \ Tr \ D. \llbracket \text{typeof}_h \ v = \lfloor T \rfloor; \text{class-type-of}' \ T = \lfloor C \rfloor; P \vdash C \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \rrbracket \implies \tau\text{external-defs } D \ M) \implies \tau\text{move1 } P \ h \ (\text{Val } v.M(\text{map } \text{Val } vs))$
and $\tau\text{move1BlockSome}$: $\tau\text{move1 } P \ h \ \{V:T=\lfloor v \rfloor; e\}$
and $\tau\text{move1Block}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ \{V:T=None; e\}$
and $\tau\text{move1BlockRed}$: $\tau\text{move1 } P \ h \ \{V:T=None; \text{Val } v\}$
and $\tau\text{move1Sync}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (\text{sync}_{V'}(e) \ e')$
and $\tau\text{move1InSync}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (\text{insync}_{V'}(a) \ e)$
and $\tau\text{move1Seq}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (e;; e')$
and $\tau\text{move1SeqRed}$: $\tau\text{move1 } P \ h \ (\text{Val } v;; e)$
and $\tau\text{move1Cond}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (\text{if } (e) \ e1 \ \text{else } \ e2)$
and $\tau\text{move1CondRed}$: $\tau\text{move1 } P \ h \ (\text{if } (\text{Val } v) \ e1 \ \text{else } \ e2)$
and $\tau\text{move1WhileRed}$: $\tau\text{move1 } P \ h \ (\text{while } (c) \ e)$
and $\tau\text{move1Throw}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (\text{throw } e)$
and $\tau\text{move1ThrowNull}$: $\tau\text{move1 } P \ h \ (\text{throw } \text{null})$
and $\tau\text{move1Try}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{move1 } P \ h \ (\text{try } e \ \text{catch}(C \ V) \ e'')$
and $\tau\text{move1TryRed}$: $\tau\text{move1 } P \ h \ (\text{try } \text{Val } v \ \text{catch}(C \ V) \ e)$
and $\tau\text{move1TryThrow}$: $\tau\text{move1 } P \ h \ (\text{try } \text{Throw } a \ \text{catch}(C \ V) \ e)$
and $\tau\text{move1NewArrayThrow}$: $\tau\text{move1 } P \ h \ (\text{newA } T \lfloor \text{Throw } a \rfloor)$
and $\tau\text{move1CastThrow}$: $\tau\text{move1 } P \ h \ (\text{Cast } T \ (\text{Throw } a))$
and $\tau\text{move1InstanceOfThrow}$: $\tau\text{move1 } P \ h \ ((\text{Throw } a) \text{ instanceof } T)$
and $\tau\text{move1BinOpThrow1}$: $\tau\text{move1 } P \ h \ (\text{Throw } a \llbracket \text{bop} \rrbracket \ e2)$
and $\tau\text{move1BinOpThrow2}$: $\tau\text{move1 } P \ h \ (\text{Val } v \llbracket \text{bop} \rrbracket \ \text{Throw } a)$
and $\tau\text{move1LAssThrow}$: $\tau\text{move1 } P \ h \ (V := (\text{Throw } a))$
and $\tau\text{move1AAccThrow1}$: $\tau\text{move1 } P \ h \ (\text{Throw } a[e])$
and $\tau\text{move1AAccThrow2}$: $\tau\text{move1 } P \ h \ (\text{Val } v \lfloor \text{Throw } a \rfloor)$
and $\tau\text{move1AAssThrow1}$: $\tau\text{move1 } P \ h \ (\text{AAss } (\text{Throw } a) \ e \ e')$
and $\tau\text{move1AAssThrow2}$: $\tau\text{move1 } P \ h \ (\text{AAss } (\text{Val } v) \ (\text{Throw } a) \ e')$
and $\tau\text{move1AAssThrow3}$: $\tau\text{move1 } P \ h \ (\text{AAss } (\text{Val } v) \ (\text{Val } v') \ (\text{Throw } a))$
and $\tau\text{move1ALengthThrow}$: $\tau\text{move1 } P \ h \ (\text{Throw } a.\text{length})$
and $\tau\text{move1FAccThrow}$: $\tau\text{move1 } P \ h \ (\text{Throw } a.F\{D\})$

and $\tau\text{move1FAssThrow1}$: $\tau\text{move1 } P h (Throw a \cdot F\{D\} := e)$
and $\tau\text{move1FAssThrow2}$: $\tau\text{move1 } P h (FAss (Val v) F D (Throw a))$
and $\tau\text{move1CASThrow1}$: $\tau\text{move1 } P h (CompareAndSwap (Throw a) D F e e')$
and $\tau\text{move1CASThrow2}$: $\tau\text{move1 } P h (CompareAndSwap (Val v) D F (Throw a) e')$
and $\tau\text{move1CASThrow3}$: $\tau\text{move1 } P h (CompareAndSwap (Val v) D F (Val v') (Throw a))$
and $\tau\text{move1CallThrowObj}$: $\tau\text{move1 } P h (Throw a \cdot M(es))$
and $\tau\text{move1CallThrowParams}$: $\tau\text{move1 } P h (Val v \cdot M(\text{map } Val vs @ Throw a \# es))$
and $\tau\text{move1BlockThrow}$: $\tau\text{move1 } P h \{V:T=None; Throw a\}$
and $\tau\text{move1SyncThrow}$: $\tau\text{move1 } P h (sync_{V'} (Throw a) e)$
and $\tau\text{move1SeqThrow}$: $\tau\text{move1 } P h (Throw a;; e)$
and $\tau\text{move1CondThrow}$: $\tau\text{move1 } P h (if (Throw a) e1 else e2)$
and $\tau\text{move1ThrowThrow}$: $\tau\text{move1 } P h (throw (Throw a))$

and $\tau\text{moves1Hd}$: $\tau\text{move1 } P h e \implies \tau\text{moves1 } P h (e \# es)$
and $\tau\text{moves1Tl}$: $\tau\text{moves1 } P h es \implies \tau\text{moves1 } P h (Val v \# es)$

$\langle\text{proof}\rangle$

lemma $\tau\text{moves1-map-Val } [dest!]$:

$\tau\text{moves1 } P h (\text{map } Val es) \implies False$

$\langle\text{proof}\rangle$

lemma $\tau\text{moves1-map-Val-ThrowD } [simp]$: $\tau\text{moves1 } P h (\text{map } Val vs @ Throw a \# es) = False$

$\langle\text{proof}\rangle$

lemma fixes $e :: ('a, 'b, 'addr) \text{exp}$ **and** $es :: ('a, 'b, 'addr) \text{exp list}$

shows $\tau\text{move1-not-call1}$:

$\text{call1 } e = [(a, M, vs)] \implies \tau\text{move1 } P h e \longleftrightarrow (\text{synthesized-call } P h (a, M, vs) \longrightarrow \tau\text{external}' P h a M)$

and $\tau\text{moves1-not-calls1}$:

$\text{calls1 } es = [(a, M, vs)] \implies \tau\text{moves1 } P h es \longleftrightarrow (\text{synthesized-call } P h (a, M, vs) \longrightarrow \tau\text{external}' P h a M)$

$\langle\text{proof}\rangle$

lemma $\text{red1-}\tau\text{-taD}$: $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \tau\text{move1 } P (hp s) e \rrbracket \implies ta = \varepsilon$

and $\text{reds1-}\tau\text{-taD}$: $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \tau\text{moves1 } P (hp s) es \rrbracket \implies ta = \varepsilon$

$\langle\text{proof}\rangle$

lemma $\tau\text{move1-heap-unchanged}$: $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \tau\text{move1 } P (hp s) e \rrbracket \implies hp s' = hp s$

and $\tau\text{moves1-heap-unchanged}$: $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \tau\text{moves1 } P (hp s) es \rrbracket \implies hp s' = hp s$

$\langle\text{proof}\rangle$

lemma $\tau\text{Move1-iff}$:

$\tau\text{Move1 } P h \text{exxs} \longleftrightarrow (\text{let } ((e, -), -) = \text{exxs in } \tau\text{move1 } P h e \vee \text{final } e)$

$\langle\text{proof}\rangle$

lemma $\tau\text{red1-iff } [iff]$:

$\tau\text{red1g } uf P t h (e, xs) (e', xs') = (uf, P, t \vdash 1 \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle) \wedge \tau\text{move1 } P h e$

$\langle\text{proof}\rangle$

lemma $\tau\text{reds1-iff } [iff]$:

$\tau\text{reds1g } uf P t h (es, xs) (es', xs') = (uf, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle) \wedge \tau\text{moves1 } P h es$

$\langle \text{proof} \rangle$

lemma $\tau\text{reds1r-1step}$:

$$\llbracket \text{uf}, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves1 } P \text{ h } es \rrbracket \\ \implies \tau\text{reds1gr uf } P \text{ t h } (es, xs) (es', xs')$$

$\langle \text{proof} \rangle$

lemma $\tau\text{reds1r-2step}$:

$$\llbracket \text{uf}, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves1 } P \text{ h } es; \\ \text{uf}, P, t \vdash 1 \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau\text{moves1 } P \text{ h } es' \rrbracket \\ \implies \tau\text{reds1gr uf } P \text{ t h } (es, xs) (es'', xs'')$$

$\langle \text{proof} \rangle$

lemma $\tau\text{reds1r-3step}$:

$$\llbracket \text{uf}, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves1 } P \text{ h } es; \\ \text{uf}, P, t \vdash 1 \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau\text{moves1 } P \text{ h } es'; \\ \text{uf}, P, t \vdash 1 \langle es'', (h, xs'') \rangle [-\varepsilon \rightarrow] \langle es''', (h, xs''') \rangle; \tau\text{moves1 } P \text{ h } es'' \rrbracket \\ \implies \tau\text{reds1gr uf } P \text{ t h } (es, xs) (es''', xs''')$$

$\langle \text{proof} \rangle$

lemma $\tau\text{red1t-preserves-len}$: $\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \text{length } xs' = \text{length } xs$

$\langle \text{proof} \rangle$

lemma $\tau\text{red1r-preserves-len}$: $\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \text{length } xs' = \text{length } xs$

$\langle \text{proof} \rangle$

lemma $\tau\text{red1t-inj-}\tau\text{reds1t}$: $\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{reds1gt uf } P \text{ t h } (e \# es, xs) (e' \# es, xs')$

$\langle \text{proof} \rangle$

lemma $\tau\text{reds1t-cons-}\tau\text{reds1t}$: $\tau\text{reds1gt uf } P \text{ t h } (es, xs) (es', xs') \implies \tau\text{reds1gt uf } P \text{ t h } (\text{Val } v \# es, xs) (\text{Val } v \# es', xs')$

$\langle \text{proof} \rangle$

lemma $\tau\text{red1r-inj-}\tau\text{reds1r}$: $\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{reds1gr uf } P \text{ t h } (e \# es, xs) (e' \# es, xs')$

$\langle \text{proof} \rangle$

lemma $\tau\text{reds1r-cons-}\tau\text{reds1r}$: $\tau\text{reds1gr uf } P \text{ t h } (es, xs) (es', xs') \implies \tau\text{reds1gr uf } P \text{ t h } (\text{Val } v \# es, xs) (\text{Val } v \# es', xs')$

$\langle \text{proof} \rangle$

lemma $\text{NewArray-}\tau\text{red1t-xt}$:

$$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (\text{newA } T[e], xs) (\text{newA } T[e'], xs')$$

$\langle \text{proof} \rangle$

lemma $\text{Cast-}\tau\text{red1t-xt}$:

$$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (\text{Cast } T \text{ e}, xs) (\text{Cast } T \text{ e}', xs')$$

$\langle \text{proof} \rangle$

lemma $\text{InstanceOf-}\tau\text{red1t-xt}$:

$$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (e \text{ instanceof } T, xs) (e' \text{ instanceof } T, xs')$$

$\langle \text{proof} \rangle$

lemma *BinOp- τ red1t-xt1:*

$\tau\text{red1gt uf P t h } (e1, xs) (e1', xs') \implies \tau\text{red1gt uf P t h } (e1 \ll\text{bop}\gg e2, xs) (e1' \ll\text{bop}\gg e2, xs')$
 $\langle\text{proof}\rangle$

lemma *BinOp- τ red1t-xt2:*

$\tau\text{red1gt uf P t h } (e2, xs) (e2', xs') \implies \tau\text{red1gt uf P t h } (Val v \ll\text{bop}\gg e2, xs) (Val v \ll\text{bop}\gg e2', xs')$
 $\langle\text{proof}\rangle$

lemma *LAss- τ red1t:*

$\tau\text{red1gt uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf P t h } (V := e, xs) (V := e', xs')$
 $\langle\text{proof}\rangle$

lemma *AAcc- τ red1t-xt1:*

$\tau\text{red1gt uf P t h } (a, xs) (a', xs') \implies \tau\text{red1gt uf P t h } (a[i], xs) (a'[i], xs')$
 $\langle\text{proof}\rangle$

lemma *AAcc- τ red1t-xt2:*

$\tau\text{red1gt uf P t h } (i, xs) (i', xs') \implies \tau\text{red1gt uf P t h } (Val a[i], xs) (Val a[i'], xs')$
 $\langle\text{proof}\rangle$

lemma *AAss- τ red1t-xt1:*

$\tau\text{red1gt uf P t h } (a, xs) (a', xs') \implies \tau\text{red1gt uf P t h } (a[i] := e, xs) (a'[i] := e, xs')$
 $\langle\text{proof}\rangle$

lemma *AAss- τ red1t-xt2:*

$\tau\text{red1gt uf P t h } (i, xs) (i', xs') \implies \tau\text{red1gt uf P t h } (Val a[i] := e, xs) (Val a[i'] := e, xs')$
 $\langle\text{proof}\rangle$

lemma *AAss- τ red1t-xt3:*

$\tau\text{red1gt uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf P t h } (Val a[Val i] := e, xs) (Val a[Val i] := e', xs')$
 $\langle\text{proof}\rangle$

lemma *ALength- τ red1t-xt:*

$\tau\text{red1gt uf P t h } (a, xs) (a', xs') \implies \tau\text{red1gt uf P t h } (a.\text{length}, xs) (a'.\text{length}, xs')$
 $\langle\text{proof}\rangle$

lemma *FAcc- τ red1t-xt:*

$\tau\text{red1gt uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf P t h } (e.F\{D\}, xs) (e'.F\{D\}, xs')$
 $\langle\text{proof}\rangle$

lemma *FAss- τ red1t-xt1:*

$\tau\text{red1gt uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf P t h } (e.F\{D\} := e2, xs) (e'.F\{D\} := e2, xs')$
 $\langle\text{proof}\rangle$

lemma *FAss- τ red1t-xt2:*

$\tau\text{red1gt uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf P t h } (Val v.F\{D\} := e, xs) (Val v.F\{D\} := e', xs')$
 $\langle\text{proof}\rangle$

lemma *CAS- τ red1t-xt1:*

$\tau\text{red1gt uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf P t h } (e.\text{compareAndSwap}(D.F, e2, e3), xs)$
 $(e'.\text{compareAndSwap}(D.F, e2, e3), xs')$
 $\langle\text{proof}\rangle$

lemma *CAS- τ red1t-xt2:*

$\tau red1gt\ uf\ P\ th\ (e, xs)\ (e', xs') \implies \tau red1gt\ uf\ P\ th\ (Val\ v \cdot compareAndSwap(D \cdot F, e, e3), xs)\ (Val\ v \cdot compareAndSwap(D \cdot F, e', e3), xs')$
 ⟨proof⟩

lemma *CAS- $\tau red1t$ -xt3:*

$\tau red1gt\ uf\ P\ th\ (e, xs)\ (e', xs') \implies \tau red1gt\ uf\ P\ th\ (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e), xs)\ (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e'), xs')$
 ⟨proof⟩

lemma *Call- $\tau red1t$ -obj:*

$\tau red1gt\ uf\ P\ th\ (e, xs)\ (e', xs') \implies \tau red1gt\ uf\ P\ th\ (e \cdot M(ps), xs)\ (e' \cdot M(ps), xs')$
 ⟨proof⟩

lemma *Call- $\tau red1t$ -param:*

$\tau reds1gt\ uf\ P\ th\ (es, xs)\ (es', xs') \implies \tau red1gt\ uf\ P\ th\ (Val\ v \cdot M(es), xs)\ (Val\ v \cdot M(es'), xs')$
 ⟨proof⟩

lemma *Block-None- $\tau red1t$ -xt:*

$\tau red1gt\ uf\ P\ th\ (e, xs)\ (e', xs') \implies \tau red1gt\ uf\ P\ th\ (\{V:T=None; e\}, xs)\ (\{V:T=None; e'\}, xs')$
 ⟨proof⟩

lemma *Block- $\tau red1t$ -Some:*

$\llbracket \tau red1gt\ uf\ P\ th\ (e, xs[V := v])\ (e', xs');\ V < length\ xs \rrbracket$
 $\implies \tau red1gt\ uf\ P\ th\ (\{V:Ty=[v]; e\}, xs)\ (\{V:Ty=None; e'\}, xs')$
 ⟨proof⟩

lemma *Sync- $\tau red1t$ -xt:*

$\tau red1gt\ uf\ P\ th\ (e, xs)\ (e', xs') \implies \tau red1gt\ uf\ P\ th\ (sync_V(e)\ e2, xs)\ (sync_V(e')\ e2, xs')$
 ⟨proof⟩

lemma *InSync- $\tau red1t$ -xt:*

$\tau red1gt\ uf\ P\ th\ (e, xs)\ (e', xs') \implies \tau red1gt\ uf\ P\ th\ (insync_V(a)\ e, xs)\ (insync_V(a)\ e', xs')$
 ⟨proof⟩

lemma *Seq- $\tau red1t$ -xt:*

$\tau red1gt\ uf\ P\ th\ (e, xs)\ (e', xs') \implies \tau red1gt\ uf\ P\ th\ (e;;e2, xs)\ (e';;e2, xs')$
 ⟨proof⟩

lemma *Cond- $\tau red1t$ -xt:*

$\tau red1gt\ uf\ P\ th\ (e, xs)\ (e', xs') \implies \tau red1gt\ uf\ P\ th\ (if\ (e)\ e1\ else\ e2, xs)\ (if\ (e')\ e1\ else\ e2, xs')$
 ⟨proof⟩

lemma *Throw- $\tau red1t$ -xt:*

$\tau red1gt\ uf\ P\ th\ (e, xs)\ (e', xs') \implies \tau red1gt\ uf\ P\ th\ (throw\ e, xs)\ (throw\ e', xs')$
 ⟨proof⟩

lemma *Try- $\tau red1t$ -xt:*

$\tau red1gt\ uf\ P\ th\ (e, xs)\ (e', xs') \implies \tau red1gt\ uf\ P\ th\ (try\ e\ catch(C\ V)\ e2, xs)\ (try\ e'\ catch(C\ V)\ e2, xs')$
 ⟨proof⟩

lemma *NewArray- $\tau red1r$ -xt:*

$\tau red1gr\ uf\ P\ th\ (e, xs)\ (e', xs') \implies \tau red1gr\ uf\ P\ th\ (newA\ T[e], xs)\ (newA\ T[e'], xs')$

$\langle \text{proof} \rangle$

lemma *Cast- τ red1r-xt:*

$\tau\text{red1gr uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf P t h } (\text{Cast } T e, xs) (\text{Cast } T e', xs')$
 $\langle \text{proof} \rangle$

lemma *InstanceOf- τ red1r-xt:*

$\tau\text{red1gr uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf P t h } (e \text{ instanceof } T, xs) (e' \text{ instanceof } T, xs')$
 $\langle \text{proof} \rangle$

lemma *BinOp- τ red1r-xt1:*

$\tau\text{red1gr uf P t h } (e1, xs) (e1', xs') \implies \tau\text{red1gr uf P t h } (e1 \llbracket \text{bop} \rrbracket e2, xs) (e1' \llbracket \text{bop} \rrbracket e2, xs')$
 $\langle \text{proof} \rangle$

lemma *BinOp- τ red1r-xt2:*

$\tau\text{red1gr uf P t h } (e2, xs) (e2', xs') \implies \tau\text{red1gr uf P t h } (\text{Val } v \llbracket \text{bop} \rrbracket e2, xs) (\text{Val } v \llbracket \text{bop} \rrbracket e2', xs')$
 $\langle \text{proof} \rangle$

lemma *LAss- τ red1r:*

$\tau\text{red1gr uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf P t h } (V := e, xs) (V := e', xs')$
 $\langle \text{proof} \rangle$

lemma *AAcc- τ red1r-xt1:*

$\tau\text{red1gr uf P t h } (a, xs) (a', xs') \implies \tau\text{red1gr uf P t h } (a[i], xs) (a'[i], xs')$
 $\langle \text{proof} \rangle$

lemma *AAcc- τ red1r-xt2:*

$\tau\text{red1gr uf P t h } (i, xs) (i', xs') \implies \tau\text{red1gr uf P t h } (\text{Val } a[i], xs) (\text{Val } a[i'], xs')$
 $\langle \text{proof} \rangle$

lemma *AAss- τ red1r-xt1:*

$\tau\text{red1gr uf P t h } (a, xs) (a', xs') \implies \tau\text{red1gr uf P t h } (a[i] := e, xs) (a'[i] := e, xs')$
 $\langle \text{proof} \rangle$

lemma *AAss- τ red1r-xt2:*

$\tau\text{red1gr uf P t h } (i, xs) (i', xs') \implies \tau\text{red1gr uf P t h } (\text{Val } a[i] := e, xs) (\text{Val } a[i'] := e, xs')$
 $\langle \text{proof} \rangle$

lemma *AAss- τ red1r-xt3:*

$\tau\text{red1gr uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf P t h } (\text{Val } a[\text{Val } i] := e, xs) (\text{Val } a[\text{Val } i] := e', xs')$
 $\langle \text{proof} \rangle$

lemma *ALength- τ red1r-xt:*

$\tau\text{red1gr uf P t h } (a, xs) (a', xs') \implies \tau\text{red1gr uf P t h } (a.\text{length}, xs) (a'.\text{length}, xs')$
 $\langle \text{proof} \rangle$

lemma *FAcc- τ red1r-xt:*

$\tau\text{red1gr uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf P t h } (e.F\{D\}, xs) (e'.F\{D\}, xs')$
 $\langle \text{proof} \rangle$

lemma *FAss- τ red1r-xt1:*

$\tau\text{red1gr uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf P t h } (e.F\{D\} := e2, xs) (e'.F\{D\} := e2, xs')$
 $\langle \text{proof} \rangle$

lemma *FAss- τ red1r-xt2*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (Val \ v \cdot F\{D\} := e, xs) (Val \ v \cdot F\{D\} := e', xs')$
 $\langle \text{proof} \rangle$

lemma *CAS- τ red1r-xt1*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (e \cdot \text{compareAndSwap}(D \cdot F, e2, e3), xs)$
 $(e' \cdot \text{compareAndSwap}(D \cdot F, e2, e3), xs')$
 $\langle \text{proof} \rangle$

lemma *CAS- τ red1r-xt2*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (Val \ v \cdot \text{compareAndSwap}(D \cdot F, e, e3), xs) (Val$
 $v \cdot \text{compareAndSwap}(D \cdot F, e', e3), xs')$
 $\langle \text{proof} \rangle$

lemma *CAS- τ red1r-xt3*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (Val \ v \cdot \text{compareAndSwap}(D \cdot F, Val \ v', e), xs)$
 $(Val \ v \cdot \text{compareAndSwap}(D \cdot F, Val \ v', e'), xs')$
 $\langle \text{proof} \rangle$

lemma *Call- τ red1r-obj*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (e \cdot M(ps), xs) (e' \cdot M(ps), xs')$
 $\langle \text{proof} \rangle$

lemma *Call- τ red1r-param*:

$\tau\text{reds1gr uf } P \text{ t h } (es, xs) (es', xs') \implies \tau\text{red1gr uf } P \text{ t h } (Val \ v \cdot M(es), xs) (Val \ v \cdot M(es'), xs')$
 $\langle \text{proof} \rangle$

lemma *Block-None- τ red1r-xt*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (\{V:T=None; e\}, xs) (\{V:T=None; e'\}, xs')$
 $\langle \text{proof} \rangle$

lemma *Block- τ red1r-Some*:

$\llbracket \tau\text{red1gr uf } P \text{ t h } (e, xs[V := v]) (e', xs'); V < \text{length } xs \rrbracket$
 $\implies \tau\text{red1gr uf } P \text{ t h } (\{V:Ty=[v]; e\}, xs) (\{V:Ty=None; e'\}, xs')$
 $\langle \text{proof} \rangle$

lemma *Sync- τ red1r-xt*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (\text{sync}_V(e) \ e2, xs) (\text{sync}_V(e') \ e2, xs')$
 $\langle \text{proof} \rangle$

lemma *InSync- τ red1r-xt*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (\text{insync}_V(a) \ e, xs) (\text{insync}_V(a) \ e', xs')$
 $\langle \text{proof} \rangle$

lemma *Seq- τ red1r-xt*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (e;;e2, xs) (e';;e2, xs')$
 $\langle \text{proof} \rangle$

lemma *Cond- τ red1r-xt*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (\text{if } (e) \ e1 \ \text{else } e2, xs) (\text{if } (e') \ e1 \ \text{else } e2, xs')$
 $\langle \text{proof} \rangle$

lemma *Throw- τ red1r-xt*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (\text{throw } e, xs) (\text{throw } e', xs')$

<proof>

lemma *Try- τ red1r-xt:*

$\tau red1gr\ uf\ P\ t\ h\ (e, xs)\ (e', xs') \implies \tau red1gr\ uf\ P\ t\ h\ (try\ e\ catch(C\ V)\ e2, xs)\ (try\ e'\ catch(C\ V)\ e2, xs')$

<proof>

lemma *$\tau red1t$ -ThrowD [dest]:* $\tau red1gt\ uf\ P\ t\ h\ (Throw\ a, xs)\ (e'', xs'') \implies e'' = Throw\ a \wedge xs'' = xs$

<proof>

lemma *$\tau red1r$ -ThrowD [dest]:* $\tau red1gr\ uf\ P\ t\ h\ (Throw\ a, xs)\ (e'', xs'') \implies e'' = Throw\ a \wedge xs'' = xs$

<proof>

lemma *$\tau Red1$ -conv [iff]:*

$\tau Red1g\ uf\ P\ t\ h\ (ex, exs)\ (ex', exs') = (uf, P, t \vdash 1\ \langle ex/exs, h \rangle -\varepsilon \rightarrow \langle ex'/exs', h \rangle \wedge \tau Move1\ P\ h\ (ex, exs))$

<proof>

lemma *$\tau red1t$ -into- $\tau Red1t$:*

$\tau red1gt\ uf\ P\ t\ h\ (e, xs)\ (e'', xs'') \implies \tau Red1gt\ uf\ P\ t\ h\ ((e, xs), exs)\ ((e'', xs''), exs)$

<proof>

lemma *$\tau red1r$ -into- $\tau Red1r$:*

$\tau red1gr\ uf\ P\ t\ h\ (e, xs)\ (e'', xs'') \implies \tau Red1gr\ uf\ P\ t\ h\ ((e, xs), exs)\ ((e'', xs''), exs)$

<proof>

lemma *red1-max-vars:* $uf, P, t \vdash 1\ \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies max\ vars\ e' \leq max\ vars\ e$

and *reds1-max-varss:* $uf, P, t \vdash 1\ \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies max\ varss\ es' \leq max\ varss\ es$

<proof>

lemma *$\tau red1t$ -max-vars:* $\tau red1gt\ uf\ P\ t\ h\ (e, xs)\ (e', xs') \implies max\ vars\ e' \leq max\ vars\ e$

<proof>

lemma *$\tau red1r$ -max-vars:* $\tau red1gr\ uf\ P\ t\ h\ (e, xs)\ (e', xs') \implies max\ vars\ e' \leq max\ vars\ e$

<proof>

lemma *$\tau red1r$ -Val:*

$\tau red1gr\ uf\ P\ t\ h\ (Val\ v, xs)\ s' \longleftrightarrow s' = (Val\ v, xs)$

<proof>

lemma *$\tau red1t$ -Val:*

$\tau red1gt\ uf\ P\ t\ h\ (Val\ v, xs)\ s' \longleftrightarrow False$

<proof>

lemma *$\tau reds1r$ -map-Val:*

$\tau reds1gr\ uf\ P\ t\ h\ (map\ Val\ vs, xs)\ s' \longleftrightarrow s' = (map\ Val\ vs, xs)$

<proof>

lemma *$\tau reds1t$ -map-Val:*

$\tau reds1gt\ uf\ P\ t\ h\ (map\ Val\ vs, xs)\ s' \longleftrightarrow False$

<proof>

lemma *$\tau reds1r$ -map-Val-Throw:*

$\tau\text{reds1gr } uf P t h (map \text{ Val vs } @ \text{ Throw } a \# es, xs) s' \longleftrightarrow s' = (map \text{ Val vs } @ \text{ Throw } a \# es, xs)$
 (is ?lhs \longleftrightarrow ?rhs)
 <proof>

lemma $\tau\text{reds1t-map-Val-Throw}$:

$\tau\text{reds1gt } uf P t h (map \text{ Val vs } @ \text{ Throw } a \# es, xs) s' \longleftrightarrow \text{False}$
 (is ?lhs \longleftrightarrow ?rhs)
 <proof>

lemma $\tau\text{red1r-Throw}$:

$\tau\text{red1gr } uf P t h (\text{Throw } a, xs) s' \longleftrightarrow s' = (\text{Throw } a, xs)$ (is ?lhs \longleftrightarrow ?rhs)
 <proof>

lemma $\tau\text{red1t-Throw}$:

$\tau\text{red1gt } uf P t h (\text{Throw } a, xs) s' \longleftrightarrow \text{False}$ (is ?lhs \longleftrightarrow ?rhs)
 <proof>

lemma $\text{red1-False-into-red1-True}$:

$\text{False}, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{True}, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle$
and $\text{reds1-False-into-reds1-True}$:
 $\text{False}, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \text{True}, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle$
 <proof>

lemma $\text{Red1-False-into-Red1-True}$:

assumes $\text{False}, P, t \vdash 1 \langle ex/exs, shr s \rangle -ta \rightarrow \langle ex'/exs', m' \rangle$
shows $\text{True}, P, t \vdash 1 \langle ex/exs, shr s \rangle -ta \rightarrow \langle ex'/exs', m' \rangle$
 <proof>

lemma $\text{red1-Suspend-is-call}$:

$\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{Suspend } w \in \text{set } \{ta\}_w \rrbracket \implies \text{call1 } e' \neq \text{None}$
and $\text{reds-Suspend-is-calls}$:
 $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{Suspend } w \in \text{set } \{ta\}_w \rrbracket \implies \text{calls1 } es' \neq \text{None}$
 <proof>

lemma $\text{Red1-Suspend-is-call}$:

$\llbracket uf, P, t \vdash 1 \langle (e, xs)/exs, h \rangle -ta \rightarrow \langle (e', xs')/exs', h' \rangle; \text{Suspend } w \in \text{set } \{ta\}_w \rrbracket \implies \text{call1 } e' \neq \text{None}$
 <proof>

lemma Red1-mthr : *multithreaded final-expr1* ($m\text{red1g } uf P$)

<proof>

lemma $\text{red1-}\tau\text{move1-heap-unchanged}$: $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \tau\text{move1 } P (hp s) e \rrbracket \implies hp s' = hp s$

and $\text{red1-}\tau\text{moves1-heap-unchanged}$: $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \tau\text{moves1 } P (hp s) es \rrbracket \implies hp s' = hp s$
 <proof>

lemma $\text{Red1-}\tau\text{mthr-wf}$: $\tau\text{multithreaded-wf final-expr1 } (m\text{red1g } uf P) (\tau\text{MOVE1 } P)$

<proof>

end

sublocale $J1\text{-heap-base} < \text{Red1-mthr}$:

$\tau\text{multithreaded-wf}$

```

    final-expr1
    mred1g uf P
    convert-RA
     $\tau$ MOVE1 P
    for uf P
    <proof>

```

context *J1-heap-base* **begin**

lemma τ Red1'-t-into-Red1'- τ mthr-silent-movet:
 τ Red1gt uf P t h (ex2, exs2) (ex2'', exs2'')
 \implies Red1-mthr.silent-movet uf P t ((ex2, exs2), h) ((ex2'', exs2''), h)
 <proof>

lemma τ Red1t-into-Red1'- τ mthr-silent-moves:
 τ Red1gt uf P t h (ex2, exs2) (ex2'', exs2'')
 \implies Red1-mthr.silent-moves uf P t ((ex2, exs2), h) ((ex2'', exs2''), h)
 <proof>

lemma τ Red1'-r-into-Red1'- τ mthr-silent-moves:
 τ Red1gr uf P t h (ex, exs) (ex', exs') \implies Red1-mthr.silent-moves uf P t ((ex, exs), h) ((ex', exs'), h)
 <proof>

lemma τ Red1r-rtranclpD:
 τ Red1gr uf P t h s s' \implies τ trsys.silent-moves (mred1g uf P t) (τ MOVE1 P) (s, h) (s', h)
 <proof>

lemma τ Red1t-tranclpD:
 τ Red1gt uf P t h s s' \implies τ trsys.silent-movet (mred1g uf P t) (τ MOVE1 P) (s, h) (s', h)
 <proof>

lemma τ mreds1-Val-Nil: τ trsys.silent-moves (mred1g uf P t) (τ MOVE1 P) (((Val v, xs), []), h) s \longleftrightarrow s = (((Val v, xs), []), h)
 <proof>

lemma τ mreds1-Throw-Nil:
 τ trsys.silent-moves (mred1g uf P t) (τ MOVE1 P) (((Throw a, xs), []), h) s \longleftrightarrow s = (((Throw a, xs), []), h)
 <proof>

end

end

7.7 Deadlock perservation for the intermediate language

```

theory J1Deadlock imports
    J1
    ../Framework/FWDeadlock
    ../Common/ExternalCallWF
begin

```

context *J1-heap-base* **begin**

lemma *IUF-red-taD*:

$True, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $\implies \exists e' ta' s'. False, P, t \vdash 1 \langle e, s \rangle -ta' \rightarrow \langle e', s' \rangle \wedge$
 $collect_locks \{ta'\}_l \subseteq collect_locks \{ta\}_l \wedge set \{ta'\}_c \subseteq set \{ta\}_c \wedge collect_interrupts \{ta'\}_i \subseteq$
 $collect_interrupts \{ta\}_i \wedge$
 $(\exists s. Red1_mthr.actions_ok\ s\ t\ ta')$

and *IUFs-reds-taD*:

$True, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle$
 $\implies \exists es' ta' s'. False, P, t \vdash 1 \langle es, s \rangle [-ta' \rightarrow] \langle es', s' \rangle \wedge$
 $collect_locks \{ta'\}_l \subseteq collect_locks \{ta\}_l \wedge set \{ta'\}_c \subseteq set \{ta\}_c \wedge collect_interrupts \{ta'\}_i \subseteq$
 $collect_interrupts \{ta\}_i \wedge$
 $(\exists s. Red1_mthr.actions_ok\ s\ t\ ta')$
 $\langle proof \rangle$

lemma *IUF-Red1-taD*:

assumes $True, P, t \vdash 1 \langle ex/exs, h \rangle -ta \rightarrow \langle ex'/exs', h' \rangle$
shows $\exists ex' exs' h' ta'. False, P, t \vdash 1 \langle ex/exs, h \rangle -ta' \rightarrow \langle ex'/exs', h' \rangle \wedge$
 $collect_locks \{ta'\}_l \subseteq collect_locks \{ta\}_l \wedge set \{ta'\}_c \subseteq set \{ta\}_c \wedge collect_interrupts \{ta'\}_i \subseteq$
 $collect_interrupts \{ta\}_i \wedge$
 $(\exists s. Red1_mthr.actions_ok\ s\ t\ ta')$
 $\langle proof \rangle$

lemma *mred1'-mred1-must-sync-eq*:

$Red1_mthr.must_sync\ False\ P\ t\ x\ (shr\ s) = Red1_mthr.must_sync\ True\ P\ t\ x\ (shr\ s)$
 $\langle proof \rangle$

lemma *Red1-Red1'-deadlock-inv*:

$Red1_mthr.deadlock\ True\ P\ s = Red1_mthr.deadlock\ False\ P\ s$
 $\langle proof \rangle$

end

end

7.8 Program Compilation

theory *PCompiler*

imports

../Common/WellForm

../Common/BinOp

../Common/Conform

begin

definition *compM* :: $(mname \Rightarrow ty\ list \Rightarrow ty \Rightarrow 'a \Rightarrow 'b) \Rightarrow 'a\ mdecl' \Rightarrow 'b\ mdecl'$

where $compM\ f \equiv \lambda(M, Ts, T, m). (M, Ts, T, map_option\ (f\ M\ Ts\ T)\ m)$

definition *compC* :: $(cname \Rightarrow mname \Rightarrow ty\ list \Rightarrow ty \Rightarrow 'a \Rightarrow 'b) \Rightarrow 'a\ cdecl \Rightarrow 'b\ cdecl$

where $compC\ f \equiv \lambda(C, D, Fdecls, Mdecls). (C, D, Fdecls, map\ (compM\ (f\ C))\ Mdecls)$

primrec *compP* :: $(cname \Rightarrow mname \Rightarrow ty\ list \Rightarrow ty \Rightarrow 'a \Rightarrow 'b) \Rightarrow 'a\ prog \Rightarrow 'b\ prog$

where $\text{compP } f \text{ (Program } P) = \text{Program (map (compC } f) P)$

Compilation preserves the program structure. Therefore lookup functions either commute with compilation (like method lookup) or are preserved by it (like the subclass relation).

lemma *map-of-map4*:

$$\begin{aligned} & \text{map-of (map } (\lambda(x,a,b,c).(x,a,b,f \ x \ a \ b \ c)) \ ts) = \\ & (\lambda x. \text{map-option } (\lambda(a,b,c).(a,b,f \ x \ a \ b \ c)) \ (\text{map-of } \ ts \ x)) \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma *class-compP*:

$$\begin{aligned} & \text{class } P \ C = \text{Some } (D, fs, ms) \\ & \implies \text{class } (\text{compP } f \ P) \ C = \text{Some } (D, fs, \text{map } (\text{compM } (f \ C)) \ ms) \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma *class-compPD*:

$$\begin{aligned} & \text{class } (\text{compP } f \ P) \ C = \text{Some } (D, fs, cms) \\ & \implies \exists ms. \text{class } P \ C = \text{Some}(D,fs,ms) \wedge cms = \text{map } (\text{compM } (f \ C)) \ ms \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma [*simp*]: *is-class* (compP f P) C = *is-class* P C $\langle \text{proof} \rangle$

lemma [*simp*]: *class* (compP f P) C = *map-option* ($\lambda c. \text{snd}(\text{compC } f \ (C,c))$) (*class* P C) $\langle \text{proof} \rangle$

lemma *sees-methods-compP*:

$$\begin{aligned} & P \vdash C \text{ sees-methods } Mm \implies \\ & \text{compP } f \ P \vdash C \text{ sees-methods } (\lambda M. \text{map-option } (\lambda((Ts,T,m),D). ((Ts,T,\text{map-option } (f \ D \ M \ Ts \ T) \\ & m),D)) \ (Mm \ M)) \langle \text{proof} \rangle \end{aligned}$$

lemma *sees-method-compP*:

$$\begin{aligned} & P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \implies \\ & \text{compP } f \ P \vdash C \text{ sees } M: Ts \rightarrow T = \text{map-option } (f \ D \ M \ Ts \ T) \ m \text{ in } D \langle \text{proof} \rangle \end{aligned}$$

lemma [*simp*]:

$$\begin{aligned} & P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \implies \\ & \text{method } (\text{compP } f \ P) \ C \ M = (D, Ts, T, \text{map-option } (f \ D \ M \ Ts \ T) \ m) \langle \text{proof} \rangle \end{aligned}$$

lemma *sees-methods-compPD*:

$$\begin{aligned} & \llbracket cP \vdash C \text{ sees-methods } Mm'; cP = \text{compP } f \ P \rrbracket \implies \\ & \exists Mm. P \vdash C \text{ sees-methods } Mm \wedge \\ & Mm' = (\lambda M. \text{map-option } (\lambda((Ts,T,m),D). ((Ts,T,\text{map-option } (f \ D \ M \ Ts \ T) \ m),D)) \ (Mm \\ & M)) \langle \text{proof} \rangle \end{aligned}$$

lemma *sees-method-compPD*:

$$\begin{aligned} & \text{compP } f \ P \vdash C \text{ sees } M: Ts \rightarrow T = fm \text{ in } D \implies \\ & \exists m. P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \wedge \text{map-option } (f \ D \ M \ Ts \ T) \ m = fm \langle \text{proof} \rangle \end{aligned}$$

lemma *sees-method-native-compP* [*simp*]:

$$\text{compP } f \ P \vdash C \text{ sees } M: Ts \rightarrow T = \text{Native in } D \iff P \vdash C \text{ sees } M: Ts \rightarrow T = \text{Native in } D \langle \text{proof} \rangle$$

lemma [*simp*]: *subcls1* (compP f P) = *subcls1* P

$\langle \text{proof} \rangle$

lemma [*simp*]: *is-type* (compP f P) T = *is-type* P T

$\langle \text{proof} \rangle$

lemma *is-type-compP* [simp]: *is-type* (compP f P) = *is-type* P
 ⟨proof⟩

lemma *compP-widen*[simp]:
 (compP f P ⊢ T ≤ T') = (P ⊢ T ≤ T')
 ⟨proof⟩

lemma [simp]: (compP f P ⊢ Ts [≤] Ts') = (P ⊢ Ts [≤] Ts')⟨proof⟩

lemma *is-lub-compP* [simp]:
is-lub (compP f P) = *is-lub* P
 ⟨proof⟩

lemma [simp]:
 fixes f :: cname ⇒ mname ⇒ ty list ⇒ ty ⇒ 'a ⇒ 'b
 shows (compP f P ⊢ C has-fields FDTs) = (P ⊢ C has-fields FDTs)⟨proof⟩

lemma [simp]: *fields* (compP f P) C = *fields* P C⟨proof⟩

lemma [simp]: (compP f P ⊢ C sees F:T (fm) in D) = (P ⊢ C sees F:T (fm) in D)⟨proof⟩

lemma [simp]: *field* (compP f P) F D = *field* P F D⟨proof⟩

7.8.1 Invariance of wf-prog under compilation

lemma [iff]: *distinct-fst* (classes (compP f P)) = *distinct-fst* (classes P)⟨proof⟩

lemma [iff]: *distinct-fst* (map (compM f) ms) = *distinct-fst* ms⟨proof⟩

lemma [iff]: *wf-syscls* (compP f P) = *wf-syscls* P
 ⟨proof⟩

lemma [iff]: *wf-fdecl* (compP f P) = *wf-fdecl* P⟨proof⟩

lemma *set-compP*:
 (class (compP f P) C = [(D,fs,ms')]) ↔
 (∃ ms. class P C = [(D,fs,ms)] ∧ ms' = map (compM (f C)) ms)
 ⟨proof⟩

lemma *compP-has-method*: compP f P ⊢ C has M ↔ P ⊢ C has M
 ⟨proof⟩

lemma *is-native-compP* [simp]: *is-native* (compP f P) = *is-native* P
 ⟨proof⟩

lemma *τexternal-compP* [simp]:
 τexternal (compP f P) = τexternal P
 ⟨proof⟩

context *heap-base* **begin**

lemma *heap-clone-compP* [simp]:
heap-clone (compP f P) = *heap-clone* P
 ⟨proof⟩

lemma *red-external-compP* [simp]:

$compP f P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle \longleftrightarrow P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$
 ⟨proof⟩

lemma $\tau external'$ -*compP* [simp]:

$\tau external' (compP f P) = \tau external' P$
 ⟨proof⟩

end

lemma *wf-overriding-compP* [simp]: *wf-overriding* (*compP f P*) *D* (*compM* (*f C*) *m*) = *wf-overriding* *P D m*

⟨proof⟩

lemma *wf-cdecl-compPI*:

assumes *wf1-imp-wf2*:

$\bigwedge C M Ts T m. \llbracket wf\text{-mdecl } wf_1 P C (M, Ts, T, [m]); P \vdash C \text{ sees } M:Ts \rightarrow T = [m] \text{ in } C \rrbracket$
 $\implies wf\text{-mdecl } wf_2 (compP f P) C (M, Ts, T, [f C M Ts T m])$

and *wfCP1*: $\forall C \text{ rest. class } P C = [rest] \longrightarrow wf\text{-cdecl } wf_1 P (C, \text{rest})$

and *xcomp*: *class* (*compP f P*) *C* = [*rest'*]

and *wf*: *wf-prog p P*

shows *wf-cdecl wf₂ (compP f P) (C, rest')*

⟨proof⟩

lemma *wf-prog-compPI*:

assumes *lift*:

$\bigwedge C M Ts T m.$

$\llbracket P \vdash C \text{ sees } M:Ts \rightarrow T = [m] \text{ in } C; wf\text{-mdecl } wf_1 P C (M, Ts, T, [m]) \rrbracket$

$\implies wf\text{-mdecl } wf_2 (compP f P) C (M, Ts, T, [f C M Ts T m])$

and *wf*: *wf-prog wf₁ P*

shows *wf-prog wf₂ (compP f P)*

⟨proof⟩

lemma *wf-cdecl-compPD*:

assumes *wf1-imp-wf2*:

$\bigwedge C M Ts T m. \llbracket wf\text{-mdecl } wf_1 (compP f P) C (M, Ts, T, [f C M Ts T m]); compP f P \vdash C \text{ sees } M:Ts \rightarrow T = [f C M Ts T m] \text{ in } C \rrbracket$

$\implies wf\text{-mdecl } wf_2 P C (M, Ts, T, [m])$

and *wfCP1*: $\forall C \text{ rest. class } (compP f P) C = [rest] \longrightarrow wf\text{-cdecl } wf_1 (compP f P) (C, \text{rest})$

and *xcomp*: *class* *P C* = [*rest*]

and *wf*: *wf-prog wf-md (compP f P)*

shows *wf-cdecl wf₂ P (C, rest)*

⟨proof⟩

lemma *wf-prog-compPD*:

assumes *wf*: *wf-prog wf1 (compP f P)*

and *lift*:

$\bigwedge C M Ts T m.$

$\llbracket compP f P \vdash C \text{ sees } M:Ts \rightarrow T = [f C M Ts T m] \text{ in } C; wf\text{-mdecl } wf_1 (compP f P) C (M, Ts, T, [f C M Ts T m]) \rrbracket$

$\implies wf\text{-mdecl } wf_2 P C (M, Ts, T, [m])$

shows *wf-prog wf₂ P*

⟨proof⟩

lemma *WT-binop-compP* [simp]: $\text{compP } f \ P \vdash T1 \ll bop \gg T2 :: T \longleftrightarrow P \vdash T1 \ll bop \gg T2 :: T$
 ⟨proof⟩

lemma *WTrt-binop-compP* [simp]: $\text{compP } f \ P \vdash T1 \ll bop \gg T2 : T \longleftrightarrow P \vdash T1 \ll bop \gg T2 : T$
 ⟨proof⟩

lemma *binop-relevant-class-compP* [simp]: $\text{binop-relevant-class } bop \ (\text{compP } f \ P) = \text{binop-relevant-class } bop \ P$
 ⟨proof⟩

lemma *is-class-compP* [simp]:
 $\text{is-class } (\text{compP } f \ P) = \text{is-class } P$
 ⟨proof⟩

lemma *has-field-compP* [simp]:
 $\text{compP } f \ P \vdash C \text{ has } F:T \ (fm) \text{ in } D \longleftrightarrow P \vdash C \text{ has } F:T \ (fm) \text{ in } D$
 ⟨proof⟩

context *heap-base* **begin**

lemma *compP-addr-loc-type* [simp]:
 $\text{addr-loc-type } (\text{compP } f \ P) = \text{addr-loc-type } P$
 ⟨proof⟩

lemma *conf-compP* [simp]:
 $\text{compP } f \ P, h \vdash v : \leq T \longleftrightarrow P, h \vdash v : \leq T$
 ⟨proof⟩

lemma *compP-conf*: $\text{conf } (\text{compP } f \ P) = \text{conf } P$
 ⟨proof⟩

lemma *compP-confs*: $\text{compP } f \ P, h \vdash vs [:\leq] Ts \longleftrightarrow P, h \vdash vs [:\leq] Ts$
 ⟨proof⟩

lemma *tconf-compP* [simp]: $\text{compP } f \ P, h \vdash t \sqrt{t} \longleftrightarrow P, h \vdash t \sqrt{t}$
 ⟨proof⟩

lemma *wf-start-state-compP* [simp]:
 $\text{wf-start-state } (\text{compP } f \ P) = \text{wf-start-state } P$
 ⟨proof⟩

end

lemma *compP-addr-conv*:
 $\text{addr-conv } \text{addr2thread-id } \text{thread-id2addr } \text{typeof-addr } (\text{compP } f \ P) = \text{addr-conv } \text{addr2thread-id } \text{thread-id2addr } \text{typeof-addr } P$
 ⟨proof⟩

lemma *compP-heap*:
 $\text{heap } \text{addr2thead-id } \text{thread-id2addr } \text{allocate } \text{typeof-addr } \text{heap-write } (\text{compP } f \ P) =$
 $\text{heap } \text{addr2thead-id } \text{thread-id2addr } \text{allocate } \text{typeof-addr } \text{heap-write } P$
 ⟨proof⟩

lemma *compP-heap-conf*:

heap-conf addr2thead-id thread-id2addr empty-heap allocate typeof-addr heap-write hconf (compP f P) =
heap-conf addr2thead-id thread-id2addr empty-heap allocate typeof-addr heap-write hconf P
 ⟨proof⟩

lemma *compP-heap-conf-read*:

heap-conf-read addr2thead-id thread-id2addr empty-heap allocate typeof-addr heap-read heap-write hconf (compP f P) =
heap-conf-read addr2thead-id thread-id2addr empty-heap allocate typeof-addr heap-read heap-write hconf P
 ⟨proof⟩

compiler composition

lemma *compM-compM*:

compM f (compM g md) = compM (λM Ts T. f M Ts T ∘ g M Ts T) md
 ⟨proof⟩

lemma *compC-compC*:

compC f (compC g cd) = compC (λC M Ts T. f C M Ts T ∘ g C M Ts T) cd
 ⟨proof⟩

lemma *compP-compP*:

compP f (compP g P) = compP (λC M Ts T. f C M Ts T ∘ g C M Ts T) P
 ⟨proof⟩

end

7.9 Compilation Stage 2

theory *Compiler2*

imports *PCompiler J1State ../JVM/JVMInstructions*

begin

primrec *compE2* :: 'addr expr1 ⇒ 'addr instr list

and *compEs2* :: 'addr expr1 list ⇒ 'addr instr list

where

compE2 (new C) = [New C]
 | *compE2 (newA T[e]) = compE2 e @ [NewArray T]*
 | *compE2 (Cast T e) = compE2 e @ [Checkcast T]*
 | *compE2 (e instanceof T) = compE2 e @ [Instanceof T]*
 | *compE2 (Val v) = [Push v]*
 | *compE2 (e1 «bop» e2) = compE2 e1 @ compE2 e2 @ [BinOpInstr bop]*
 | *compE2 (Var i) = [Load i]*
 | *compE2 (i:=e) = compE2 e @ [Store i, Push Unit]*
 | *compE2 (a[i]) = compE2 a @ compE2 i @ [ALoad]*
 | *compE2 (a[i] := e) = compE2 a @ compE2 i @ compE2 e @ [AStore, Push Unit]*
 | *compE2 (a.length) = compE2 a @ [ALength]*
 | *compE2 (e.F{D}) = compE2 e @ [Getfield F D]*
 | *compE2 (e1.F{D} := e2) = compE2 e1 @ compE2 e2 @ [Putfield F D, Push Unit]*
 | *compE2 (e.compareAndSwap(D.F, e', e'')) = compE2 e @ compE2 e' @ compE2 e'' @ [CAS F D]*
 | *compE2 (e.M(es)) = compE2 e @ compEs2 es @ [Invoke M (size es)]*
 | *compE2 ({i:T=vo; e}) = (case vo of None ⇒ [] | [v] ⇒ [Push v, Store i]) @ compE2 e*
 | *compE2 (sync_V (o') e) = compE2 o' @ [Dup, Store V, MEnter] @*

```

      compE2 e @ [Load V, MExit, Goto 4, Load V, MExit, ThrowExc]
| compE2 (insyncV (a) e) = [Goto 1] — Define insync sensibly
| compE2 (e1;;e2) = compE2 e1 @ [Pop] @ compE2 e2
| compE2 (if (e) e1 else e2) =
  (let cnd = compE2 e;
    thn = compE2 e1;
    els = compE2 e2;
    test = IfFalse (int(size thn + 2));
    thnex = Goto (int(size els + 1))
    in cnd @ [test] @ thn @ [thnex] @ els)
| compE2 (while (e) c) =
  (let cnd = compE2 e;
    bdy = compE2 c;
    test = IfFalse (int(size bdy + 3));
    loop = Goto (-int(size bdy + size cnd + 2))
    in cnd @ [test] @ bdy @ [Pop] @ [loop] @ [Push Unit])
| compE2 (throw e) = compE2 e @ [ThrowExc]
| compE2 (try e1 catch (C i) e2) =
  (let catch = compE2 e2
    in compE2 e1 @ [Goto (int(size catch)+2), Store i] @ catch)

| compEs2 [] = []
| compEs2 (e#es) = compE2 e @ compEs2 es

```

Compilation of exception table. Is given start address of code to compute absolute addresses necessary in exception table.

```

fun compxE2 :: 'addr expr1 ⇒ pc ⇒ nat ⇒ ex-table
and compEs2 :: 'addr expr1 list ⇒ pc ⇒ nat ⇒ ex-table
where
  compxE2 (new C) pc d = []
| compxE2 (newA T[e]) pc d = compxE2 e pc d
| compxE2 (Cast T e) pc d = compxE2 e pc d
| compxE2 (e instanceof T) pc d = compxE2 e pc d
| compxE2 (Val v) pc d = []
| compxE2 (e1 «bop» e2) pc d =
  compxE2 e1 pc d @ compxE2 e2 (pc + size(compE2 e1)) (d+1)
| compxE2 (Var i) pc d = []
| compxE2 (i:=e) pc d = compxE2 e pc d
| compxE2 (a[i]) pc d = compxE2 a pc d @ compxE2 i (pc + size (compE2 a)) (d + 1)
| compxE2 (a[i] := e) pc d =
  (let pc1 = pc + size (compE2 a);
    pc2 = pc1 + size (compE2 i)
    in compxE2 a pc d @ compxE2 i pc1 (d + 1) @ compxE2 e pc2 (d + 2))
| compxE2 (a.length) pc d = compxE2 a pc d
| compxE2 (e·F{D}) pc d = compxE2 e pc d
| compxE2 (e1·F{D} := e2) pc d = compxE2 e1 pc d @ compxE2 e2 (pc + size (compE2 e1)) (d + 1)
| compxE2 (e·compareAndSwap(D·F, e', e'')) pc d =
  (let pc1 = pc + size (compE2 e);
    pc2 = pc1 + size (compE2 e')
    in compxE2 e pc d @ compxE2 e' pc1 (d + 1) @ compxE2 e'' pc2 (d + 2))
| compxE2 (e·M(es)) pc d = compxE2 e pc d @ compEs2 es (pc + size(compE2 e)) (d+1)
| compxE2 ({i:T=vo; e}) pc d = compxE2 e (case vo of None ⇒ pc | [v] ⇒ Suc (Suc pc)) d
| compxE2 (syncV (o') e) pc d =

```

```

      (let pc1 = pc + size (compE2 o') + 3;
         pc2 = pc1 + size(compE2 e)
         in compxE2 o' pc d @ compxE2 e pc1 d @ [(pc1, pc2, None, Suc (Suc (Suc pc2))), d])
| compxE2 (insyncV (a) e) pc d = []
| compxE2 (e1;;e2) pc d =
  compxE2 e1 pc d @ compxE2 e2 (pc+size(compE2 e1)+1) d
| compxE2 (if (e) e1 else e2) pc d =
  (let pc1 = pc + size(compE2 e) + 1;
     pc2 = pc1 + size(compE2 e1) + 1
     in compxE2 e pc d @ compxE2 e1 pc1 d @ compxE2 e2 pc2 d)
| compxE2 (while (b) e) pc d =
  compxE2 b pc d @ compxE2 e (pc+size(compE2 b)+1) d
| compxE2 (throw e) pc d = compxE2 e pc d
| compxE2 (try e1 catch (C i) e2) pc d =
  (let pc1 = pc + size(compE2 e1)
     in compxE2 e1 pc d @ compxE2 e2 (pc1+2) d @ [(pc,pc1,Some C,pc1+1,d)])
| compxEs2 [] pc d = []
| compxEs2 (e#es) pc d = compxE2 e pc d @ compxEs2 es (pc+size(compE2 e)) (d+1)

```

lemmas *compxE2-compxEs2-induct* =
compxE2-compxEs2.induct[
unfolded meta-all5-eq-conv meta-all4-eq-conv meta-all3-eq-conv meta-all2-eq-conv meta-all-eq-conv,
case-names
new NewArray Cast InstanceOf Val BinOp Var LAss AAcc AAss ALen FAcc FAss Call Block
Synchronized InSynchronized Seq Cond While throw TryCatch
Nil Cons]

lemma *compE2-neq-Nil* [*simp*]: *compE2 e* ≠ []
 ⟨*proof*⟩

declare *compE2-neq-Nil*[*symmetric, simp*]

lemma *compEs2-append* [*simp*]: *compEs2 (es @ es')* = *compEs2 es @ compEs2 es'*
 ⟨*proof*⟩

lemma *compEs2-eq-Nil-conv* [*simp*]: *compEs2 es* = [] ↔ *es* = []
 ⟨*proof*⟩

lemma *compEs2-map-Val*: *compEs2 (map Val vs)* = *map Push vs*
 ⟨*proof*⟩

lemma *compE2-0th-neq-Invoke* [*simp*]:
compE2 e ! 0 ≠ *Invoke M n*
 ⟨*proof*⟩

declare *compE2-0th-neq-Invoke*[*symmetric, simp*]

lemma *compxEs2-append* [*simp*]:
compxEs2 (es @ es') pc d = *compxEs2 es pc d @ compxEs2 es' (length (compEs2 es) + pc) (length*
es + d)
 ⟨*proof*⟩

lemma *compxEs2-map-Val* [*simp*]: *compxEs2 (map Val vs) pc d* = []

$\langle \text{proof} \rangle$

lemma *compE2-blocks1* [simp]:

$\text{compE2} (\text{blocks1 } n \text{ } Ts \text{ } \text{body}) = \text{compE2 } \text{body}$

$\langle \text{proof} \rangle$

lemma *compxE2-blocks1* [simp]:

$\text{compxE2} (\text{blocks1 } n \text{ } Ts \text{ } \text{body}) = \text{compxE2 } \text{body}$

$\langle \text{proof} \rangle$

lemma *fixes* $e :: 'addr \text{ expr1}$ **and** $es :: 'addr \text{ expr1 list}$

shows *compE2-not-Return*: $\text{Return} \notin \text{set} (\text{compE2 } e)$

and *compEs2-not-Return*: $\text{Return} \notin \text{set} (\text{compEs2 } es)$

$\langle \text{proof} \rangle$

primrec *max-stack* :: $'addr \text{ expr1} \Rightarrow \text{nat}$

and *max-stacks* :: $'addr \text{ expr1 list} \Rightarrow \text{nat}$

where

$\text{max-stack} (\text{new } C) = 1$

| $\text{max-stack} (\text{newA } T [e]) = \text{max-stack } e$

| $\text{max-stack} (\text{Cast } C \text{ } e) = \text{max-stack } e$

| $\text{max-stack} (e \text{ instanceof } T) = \text{max-stack } e$

| $\text{max-stack} (\text{Val } v) = 1$

| $\text{max-stack} (e1 \text{ «bop» } e2) = \max (\text{max-stack } e1) (\text{max-stack } e2) + 1$

| $\text{max-stack} (\text{Var } i) = 1$

| $\text{max-stack} (i := e) = \text{max-stack } e$

| $\text{max-stack} (a [i]) = \max (\text{max-stack } a) (\text{max-stack } i + 1)$

| $\text{max-stack} (a [i] := e) = \max (\max (\text{max-stack } a) (\text{max-stack } i + 1)) (\text{max-stack } e + 2)$

| $\text{max-stack} (a \cdot \text{length}) = \text{max-stack } a$

| $\text{max-stack} (e \cdot F \{D\}) = \text{max-stack } e$

| $\text{max-stack} (e1 \cdot F \{D\} := e2) = \max (\text{max-stack } e1) (\text{max-stack } e2) + 1$

| $\text{max-stack} (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) = \max (\max (\text{max-stack } e) (\text{max-stack } e' + 1)) (\text{max-stack } e'' + 2)$

| $\text{max-stack} (e \cdot M(es)) = \max (\text{max-stack } e) (\text{max-stacks } es) + 1$

| $\text{max-stack} (\{i:T=vo; e\}) = \text{max-stack } e$

| $\text{max-stack} (\text{sync}_V (o') \text{ } e) = \max (\text{max-stack } o') (\max (\text{max-stack } e) 2)$

| $\text{max-stack} (\text{insync}_V (a) \text{ } e) = 1$

| $\text{max-stack} (e1 ;; e2) = \max (\text{max-stack } e1) (\text{max-stack } e2)$

| $\text{max-stack} (\text{if } (e) \text{ } e1 \text{ else } e2) =$

$\max (\text{max-stack } e) (\max (\text{max-stack } e1) (\text{max-stack } e2))$

| $\text{max-stack} (\text{while } (e) \text{ } c) = \max (\text{max-stack } e) (\text{max-stack } c)$

| $\text{max-stack} (\text{throw } e) = \text{max-stack } e$

| $\text{max-stack} (\text{try } e1 \text{ catch}(C \text{ } i) \text{ } e2) = \max (\text{max-stack } e1) (\text{max-stack } e2)$

| $\text{max-stacks } [] = 0$

| $\text{max-stacks } (e \# es) = \max (\text{max-stack } e) (1 + \text{max-stacks } es)$

lemma *max-stack1*: $1 \leq \text{max-stack } e$ $\langle \text{proof} \rangle$

lemma *max-stacks-ge-length*: $\text{max-stacks } es \geq \text{length } es$

$\langle \text{proof} \rangle$

lemma *max-stack-blocks1* [simp]:

$\text{max-stack} (\text{blocks1 } n \text{ } Ts \text{ } \text{body}) = \text{max-stack } \text{body}$

$\langle \text{proof} \rangle$

definition $compMb2 :: 'addr\ expr1 \Rightarrow 'addr\ jvm-method$

where

$compMb2 \equiv \lambda body.$
 $let\ ins = compE2\ body\ @\ [Return];$
 $xt = compxE2\ body\ 0\ 0$
 $in\ (max-stack\ body,\ max-vars\ body,\ ins,\ xt)$

definition $compP2 :: 'addr\ J1-prog \Rightarrow 'addr\ jvm-prog$

where $compP2 \equiv compP\ (\lambda C\ M\ Ts\ T.\ compMb2)$

lemma $compMb2:$

$compMb2\ e = (max-stack\ e,\ max-vars\ e,\ (compE2\ e\ @\ [Return]),\ compxE2\ e\ 0\ 0)$
 $\langle proof \rangle$

end

7.10 Various Operations for Exception Tables

theory *Exception-Tables* **imports**

$Compiler2$
 $../Common/ExternalCallWF$
 $../JVM/JVMExceptions$

begin

definition $pcs :: ex-table \Rightarrow nat\ set$

where $pcs\ xt \equiv \bigcup (f,t,C,h,d) \in set\ xt.\ \{f\ ..< t\}$

lemma $pcs-subset:$

fixes $e :: 'addr\ expr1$ **and** $es :: 'addr\ expr1\ list$
shows $pcs(compxE2\ e\ pc\ d) \subseteq \{pc..<pc+size(compE2\ e)\}$
and $pcs(compxEs2\ es\ pc\ d) \subseteq \{pc..<pc+size(compEs2\ es)\}$
 $\langle proof \rangle$

lemma $pcs-Nil$ [*simp*]: $pcs\ [] = \{\}$

$\langle proof \rangle$

lemma $pcs-Cons$ [*simp*]: $pcs\ (x\#\ xt) = \{fst\ x\ ..< fst(snd\ x)\} \cup pcs\ xt$

$\langle proof \rangle$

lemma $pcs-append$ [*simp*]: $pcs(xt_1\ @\ xt_2) = pcs\ xt_1 \cup pcs\ xt_2$

$\langle proof \rangle$

lemma [*simp*]: $pc < pc_0 \vee pc_0 + size(compE2\ e) \leq pc \implies pc \notin pcs(compxE2\ e\ pc_0\ d)$

$\langle proof \rangle$

lemma [*simp*]: $pc < pc_0 \vee pc_0 + size(compEs2\ es) \leq pc \implies pc \notin pcs(compxEs2\ es\ pc_0\ d)$

$\langle proof \rangle$

lemma [*simp*]: $pc_1 + size(compE2\ e_1) \leq pc_2 \implies pcs(compxE2\ e_1\ pc_1\ d_1) \cap pcs(compxE2\ e_2\ pc_2\ d_2) = \{\}$

$\langle proof \rangle$

lemma [simp]: $pc_1 + size(compE2 e) \leq pc_2 \implies pcs(compxE2 e pc_1 d_1) \cap pcs(compxEs2 es pc_2 d_2) = \{\}$
 <proof>

lemma match-ex-table-append-not-pcs [simp]:
 $pc \notin pcs\ x\ t_0 \implies match\text{-}ex\text{-}table\ P\ C\ pc\ (x\ t_0\ @\ x\ t_1) = match\text{-}ex\text{-}table\ P\ C\ pc\ x\ t_1$
 <proof>

lemma outside-pcs-not-matches-entry [simp]:
 $\llbracket x \in set\ x\ t; pc \notin pcs\ x\ t \rrbracket \implies \neg matches\text{-}ex\text{-}entry\ P\ D\ pc\ x$
 <proof>

lemma outside-pcs-compxE2-not-matches-entry [simp]:
assumes $xe: xe \in set(compxE2 e pc d)$
and outside: $pc' < pc \vee pc + size(compE2 e) \leq pc'$
shows $\neg matches\text{-}ex\text{-}entry\ P\ C\ pc'\ xe$
 <proof>

lemma outside-pcs-compxEs2-not-matches-entry [simp]:
assumes $xe: xe \in set(compxEs2 es pc d)$
and outside: $pc' < pc \vee pc + size(compEs2 es) \leq pc'$
shows $\neg matches\text{-}ex\text{-}entry\ P\ C\ pc'\ xe$
 <proof>

lemma match-ex-table-app[simp]:
 $\forall x\ t_1. \neg matches\text{-}ex\text{-}entry\ P\ D\ pc\ x\ t_1 \implies match\text{-}ex\text{-}table\ P\ D\ pc\ (x\ t_1\ @\ x\ t) = match\text{-}ex\text{-}table\ P\ D\ pc\ x\ t$
 <proof>

lemma match-ex-table-eq-NoneI [simp]:
 $\forall x \in set\ x\ t. \neg matches\text{-}ex\text{-}entry\ P\ C\ pc\ x \implies match\text{-}ex\text{-}table\ P\ C\ pc\ x\ t = None$
 <proof>

lemma match-ex-table-not-pcs-None:
 $pc \notin pcs\ x\ t \implies match\text{-}ex\text{-}table\ P\ C\ pc\ x\ t = None$
 <proof>

lemma match-ex-entry:
fixes $start$ **shows**
 $matches\text{-}ex\text{-}entry\ P\ C\ pc\ (start, end, catch\text{-}type, handler) = (start \leq pc \wedge pc < end \wedge (case\ catch\text{-}type\ of\ None \Rightarrow True \mid \llbracket C' \rrbracket \Rightarrow P \vdash C \preceq^* C'))$
 <proof>

lemma pcs-compxE2D [dest]:
 $pc \in pcs\ (compxE2\ e\ pc'\ d) \implies pc' \leq pc \wedge pc < pc' + length\ (compE2\ e)$
 <proof>

lemma pcs-compxEs2D [dest]:
 $pc \in pcs\ (compxEs2\ es\ pc'\ d) \implies pc' \leq pc \wedge pc < pc' + length\ (compEs2\ es)$
 <proof>

definition shift :: $nat \Rightarrow ex\text{-}table \Rightarrow ex\text{-}table$
where

$shift\ n\ xt \equiv map\ (\lambda(from,to,C,handler,depth). (n+from,n+to,C,n+handler,depth))\ xt$

lemma *shift-0* [*simp*]: $shift\ 0\ xt = xt$
 ⟨*proof*⟩

lemma *shift-Nil* [*simp*]: $shift\ n\ [] = []$
 ⟨*proof*⟩

lemma *shift-Cons-tuple* [*simp*]:
 $shift\ n\ ((from, to, C, handler, depth) \# xt) = (from + n, to + n, C, handler + n, depth) \# shift\ n\ xt$
 ⟨*proof*⟩

lemma *shift-append* [*simp*]: $shift\ n\ (xt_1 @ xt_2) = shift\ n\ xt_1 @ shift\ n\ xt_2$
 ⟨*proof*⟩

lemma *shift-shift* [*simp*]: $shift\ m\ (shift\ n\ xt) = shift\ (m+n)\ xt$
 ⟨*proof*⟩

lemma *fixes* $e :: 'addr\ expr1$ **and** $es :: 'addr\ expr1\ list$
shows *shift-compxE2*: $shift\ pc\ (compxE2\ e\ pc'\ d) = compxE2\ e\ (pc' + pc)\ d$
and *shift-compxEs2*: $shift\ pc\ (compxEs2\ es\ pc'\ d) = compxEs2\ es\ (pc' + pc)\ d$
 ⟨*proof*⟩

lemma *compxE2-size-convs* [*simp*]: $n \neq 0 \implies compxE2\ e\ n\ d = shift\ n\ (compxE2\ e\ 0\ d)$
and *compxEs2-size-convs*: $n \neq 0 \implies compxEs2\ es\ n\ d = shift\ n\ (compxEs2\ es\ 0\ d)$
 ⟨*proof*⟩

lemma *pcs-shift-conv* [*simp*]: $pcs\ (shift\ n\ xt) = (+)\ n\ ' pcs\ xt$
 ⟨*proof*⟩

lemma *image-plus-const-conv* [*simp*]:
fixes $m :: nat$
shows $m \in (+)\ n\ ' A \longleftrightarrow m \geq n \wedge m - n \in A$
 ⟨*proof*⟩

lemma *match-ex-table-shift-eq-None-conv* [*simp*]:
 $match-ex-table\ P\ C\ pc\ (shift\ n\ xt) = None \longleftrightarrow pc < n \vee match-ex-table\ P\ C\ (pc - n)\ xt = None$
 ⟨*proof*⟩

lemma *match-ex-table-shift-pc-None*:
 $pc \geq n \implies match-ex-table\ P\ C\ pc\ (shift\ n\ xt) = None \longleftrightarrow match-ex-table\ P\ C\ (pc - n)\ xt = None$
 ⟨*proof*⟩

lemma *match-ex-table-shift-eq-Some-conv* [*simp*]:
 $match-ex-table\ P\ C\ pc\ (shift\ n\ xt) = \lfloor (pc', d) \rfloor \longleftrightarrow$
 $pc \geq n \wedge pc' \geq n \wedge match-ex-table\ P\ C\ (pc - n)\ xt = \lfloor (pc' - n, d) \rfloor$
 ⟨*proof*⟩

lemma *match-ex-table-shift*:
 $match-ex-table\ P\ C\ pc\ xt = \lfloor (pc', d) \rfloor \implies match-ex-table\ P\ C\ (n + pc)\ (shift\ n\ xt) = \lfloor (n + pc', d) \rfloor$
 ⟨*proof*⟩

lemma *match-ex-table-shift-pcD*:

$match\text{-}ex\text{-}table\ P\ C\ pc\ (shift\ n\ xt) = \lfloor (pc', d) \rfloor \implies pc \geq n \wedge pc' \geq n \wedge match\text{-}ex\text{-}table\ P\ C\ (pc - n)\ xt = \lfloor (pc' - n, d) \rfloor$
 ⟨proof⟩

lemma *match-ex-table-pcsD*: $match\text{-}ex\text{-}table\ P\ C\ pc\ xt = \lfloor (pc', D) \rfloor \implies pc \in pcs\ xt$
 ⟨proof⟩

definition *stack-xlift* :: $nat \Rightarrow ex\text{-}table \Rightarrow ex\text{-}table$

where $stack\text{-}xlift\ xt \equiv map\ (\lambda(from, to, C, handler, depth). (from, to, C, handler, n + depth))\ xt$

lemma *stack-xlift-0* [simp]: $stack\text{-}xlift\ 0\ xt = xt$
 ⟨proof⟩

lemma *stack-xlift-Nil* [simp]: $stack\text{-}xlift\ n\ [] = []$
 ⟨proof⟩

lemma *stack-xlift-Cons-tuple* [simp]:

$stack\text{-}xlift\ n\ ((from, to, C, handler, depth) \# xt) = (from, to, C, handler, depth + n) \# stack\text{-}xlift\ n\ xt$
 ⟨proof⟩

lemma *stack-xlift-append* [simp]: $stack\text{-}xlift\ n\ (xt @ xt') = stack\text{-}xlift\ n\ xt @ stack\text{-}xlift\ n\ xt'$
 ⟨proof⟩

lemma *stack-xlift-stack-xlift* [simp]: $stack\text{-}xlift\ n\ (stack\text{-}xlift\ m\ xt) = stack\text{-}xlift\ (n + m)\ xt$
 ⟨proof⟩

lemma *fixes e* :: 'addr expr1 **and** *es* :: 'addr expr1 list

shows *stack-xlift-compxE2*: $stack\text{-}xlift\ n\ (compxE2\ e\ pc\ d) = compxE2\ e\ pc\ (n + d)$
and *stack-xlift-compxEs2*: $stack\text{-}xlift\ n\ (compxEs2\ es\ pc\ d) = compxEs2\ es\ pc\ (n + d)$
 ⟨proof⟩

lemma *compxE2-stack-xlift-conv* [simp]: $d > 0 \implies compxE2\ e\ pc\ d = stack\text{-}xlift\ d\ (compxE2\ e\ pc\ 0)$

and *compxEs2-stack-xlift-conv* [simp]: $d > 0 \implies compxEs2\ es\ pc\ d = stack\text{-}xlift\ d\ (compxEs2\ es\ pc\ 0)$
 ⟨proof⟩

lemma *stack-xlift-shift* [simp]: $stack\text{-}xlift\ d\ (shift\ n\ xt) = shift\ n\ (stack\text{-}xlift\ d\ xt)$
 ⟨proof⟩

lemma *pcs-stack-xlift-conv* [simp]: $pcs\ (stack\text{-}xlift\ n\ xt) = pcs\ xt$
 ⟨proof⟩

lemma *match-ex-table-stack-xlift-eq-None-conv* [simp]:

$match\text{-}ex\text{-}table\ P\ C\ pc\ (stack\text{-}xlift\ d\ xt) = None \iff match\text{-}ex\text{-}table\ P\ C\ pc\ xt = None$
 ⟨proof⟩

lemma *match-ex-table-stack-xlift-eq-Some-conv* [simp]:

$match\text{-}ex\text{-}table\ P\ C\ pc\ (stack\text{-}xlift\ n\ xt) = \lfloor (pc', d) \rfloor \iff d \geq n \wedge match\text{-}ex\text{-}table\ P\ C\ pc\ xt = \lfloor (pc', d - n) \rfloor$
 ⟨proof⟩


```

lemma fixes  $e :: 'addr\ expr1$  and  $es :: 'addr\ expr1\ list$ 
  shows  $match\ ex\ table\ compxE2\ not\ same: match\ ex\ table\ P\ C\ pc\ (compxE2\ e\ n\ d) = [(pc', d')] \implies$ 
 $pc \neq pc'$ 
  and  $match\ ex\ table\ compxEs2\ not\ same: match\ ex\ table\ P\ C\ pc\ (compxEs2\ es\ n\ d) = [(pc', d')] \implies$ 
 $pc \neq pc'$ 
   $\langle proof \rangle$ 
end

```

7.11 Type rules for the intermediate language

```

theory  $J1WellType$  imports

```

```

   $J1State$ 

```

```

   $../Common/ExternalCallWF$ 

```

```

   $../Common/SemiType$ 

```

```

begin

```

```

declare  $Listn.lesub\ list\ impl\ same\ size[simp\ del]\ listE\ length\ [simp\ del]$ 

```

7.11.1 Well-Typedness

```

type-synonym

```

```

   $env1 = ty\ list$  — type environment indexed by variable number

```

```

inductive  $WT1 :: 'addr\ J1\ prog \Rightarrow env1 \Rightarrow 'addr\ expr1 \Rightarrow ty \Rightarrow bool$  ( $\langle -, - \vdash 1 - :: \rightarrow [51,0,0,51] 50 \rangle$ )

```

```

and  $WTs1 :: 'addr\ J1\ prog \Rightarrow env1 \Rightarrow 'addr\ expr1\ list \Rightarrow ty\ list \Rightarrow bool$  ( $\langle -, - \vdash 1 - [::] \rightarrow [51,0,0,51]50 \rangle$ )

```

```

for  $P :: 'addr\ J1\ prog$ 

```

```

where

```

```

   $WT1New:$ 

```

```

   $is\ class\ P\ C \implies$ 

```

```

   $P, E \vdash 1\ new\ C :: Class\ C$ 

```

```

|  $WT1NewArray:$ 

```

```

   $\llbracket P, E \vdash 1\ e :: Integer; is\ type\ P\ (T[]) \rrbracket \implies$ 

```

```

   $P, E \vdash 1\ newA\ T[e] :: T[]$ 

```

```

|  $WT1Cast:$ 

```

```

   $\llbracket P, E \vdash 1\ e :: T; P \vdash U \leq T \vee P \vdash T \leq U; is\ type\ P\ U \rrbracket$ 

```

```

   $\implies P, E \vdash 1\ Cast\ U\ e :: U$ 

```

```

|  $WT1InstanceOf:$ 

```

```

   $\llbracket P, E \vdash 1\ e :: T; P \vdash U \leq T \vee P \vdash T \leq U; is\ type\ P\ U; is\ refT\ U \rrbracket$ 

```

```

   $\implies P, E \vdash 1\ e\ instanceof\ U :: Boolean$ 

```

```

|  $WT1Val:$ 

```

```

   $typeof\ v = Some\ T \implies$ 

```

```

   $P, E \vdash 1\ Val\ v :: T$ 

```

```

|  $WT1Var:$ 

```

```

   $\llbracket E!V = T; V < size\ E \rrbracket \implies$ 

```

```

   $P, E \vdash 1\ Var\ V :: T$ 

```

```

|  $WT1BinOp:$ 

```

$$\begin{aligned} & \llbracket P, E \vdash_1 e_1 :: T_1; P, E \vdash_1 e_2 :: T_2; P \vdash T_1 \llbracket \text{bop} \rrbracket T_2 :: T \rrbracket \\ & \implies P, E \vdash_1 e_1 \llbracket \text{bop} \rrbracket e_2 :: T \end{aligned}$$

| *WT1LAss*:

$$\begin{aligned} & \llbracket E!i = T; i < \text{size } E; P, E \vdash_1 e :: T'; P \vdash T' \leq T \rrbracket \\ & \implies P, E \vdash_1 i := e :: \text{Void} \end{aligned}$$

| *WT1AAcc*:

$$\begin{aligned} & \llbracket P, E \vdash_1 a :: T[]; P, E \vdash_1 i :: \text{Integer} \rrbracket \\ & \implies P, E \vdash_1 a[i] :: T \end{aligned}$$

| *WT1AAss*:

$$\begin{aligned} & \llbracket P, E \vdash_1 a :: T[]; P, E \vdash_1 i :: \text{Integer}; P, E \vdash_1 e :: T'; P \vdash T' \leq T \rrbracket \\ & \implies P, E \vdash_1 a[i] := e :: \text{Void} \end{aligned}$$

| *WT1ALength*:

$$P, E \vdash_1 a :: T[] \implies P, E \vdash_1 a \cdot \text{length} :: \text{Integer}$$

| *WTFAcc1*:

$$\begin{aligned} & \llbracket P, E \vdash_1 e :: U; \text{class-type-of}' U = [C]; P \vdash C \text{ sees } F:T (fm) \text{ in } D \rrbracket \\ & \implies P, E \vdash_1 e \cdot F\{D\} :: T \end{aligned}$$

| *WTFAss1*:

$$\begin{aligned} & \llbracket P, E \vdash_1 e_1 :: U; \text{class-type-of}' U = [C]; P \vdash C \text{ sees } F:T (fm) \text{ in } D; P, E \vdash_1 e_2 :: T'; P \vdash T' \\ & \leq T \rrbracket \\ & \implies P, E \vdash_1 e_1 \cdot F\{D\} := e_2 :: \text{Void} \end{aligned}$$

| *WTCAS1*:

$$\begin{aligned} & \llbracket P, E \vdash_1 e_1 :: U; \text{class-type-of}' U = [C]; P \vdash C \text{ sees } F:T (fm) \text{ in } D; \text{volatile } fm; \\ & P, E \vdash_1 e_2 :: T'; P \vdash T' \leq T; P, E \vdash_1 e_3 :: T''; P \vdash T'' \leq T \rrbracket \\ & \implies P, E \vdash_1 e_1 \cdot \text{compareAndSwap}(D \cdot F, e_2, e_3) :: \text{Boolean} \end{aligned}$$

| *WT1Call*:

$$\begin{aligned} & \llbracket P, E \vdash_1 e :: U; \text{class-type-of}' U = [C]; P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D; \\ & P, E \vdash_1 es [::] Ts'; P \vdash Ts' \leq Ts \rrbracket \\ & \implies P, E \vdash_1 e \cdot M(es) :: T \end{aligned}$$

| *WT1Block*:

$$\begin{aligned} & \llbracket \text{is-type } P T; P, E@[T] \vdash_1 e :: T'; \text{case } vo \text{ of } None \Rightarrow \text{True} \mid [v] \Rightarrow \exists T'. \text{typeof } v = [T'] \wedge P \vdash \\ & T' \leq T \rrbracket \\ & \implies P, E \vdash_1 \{V:T=vo; e\} :: T' \end{aligned}$$

| *WT1Synchronized*:

$$\begin{aligned} & \llbracket P, E \vdash_1 o' :: T; \text{is-ref } T; T \neq NT; P, E@[Class \text{ Object}] \vdash_1 e :: T' \rrbracket \\ & \implies P, E \vdash_1 \text{sync}_V(o') e :: T' \end{aligned}$$

| *WT1Seq*:

$$\begin{aligned} & \llbracket P, E \vdash_1 e_1 :: T_1; P, E \vdash_1 e_2 :: T_2 \rrbracket \\ & \implies P, E \vdash_1 e_1; e_2 :: T_2 \end{aligned}$$

| *WT1Cond*:

$$\begin{aligned} & \llbracket P, E \vdash_1 e :: \text{Boolean}; P, E \vdash_1 e_1 :: T_1; P, E \vdash_1 e_2 :: T_2; P \vdash \text{lub}(T_1, T_2) = T \rrbracket \\ & \implies P, E \vdash_1 \text{if } (e) e_1 \text{ else } e_2 :: T \end{aligned}$$

| *WT1While*:
 $\llbracket P, E \vdash_1 e :: \text{Boolean}; P, E \vdash_1 c :: T \rrbracket$
 $\implies P, E \vdash_1 \text{while } (e) \ c :: \text{Void}$

| *WT1Throw*:
 $\llbracket P, E \vdash_1 e :: \text{Class } C; P \vdash C \preceq^* \text{Throwable} \rrbracket \implies$
 $P, E \vdash_1 \text{throw } e :: \text{Void}$

| *WT1Try*:
 $\llbracket P, E \vdash_1 e_1 :: T; P, E @ [\text{Class } C] \vdash_1 e_2 :: T; \text{is-class } P \ C \rrbracket$
 $\implies P, E \vdash_1 \text{try } e_1 \ \text{catch}(C \ V) \ e_2 :: T$

| *WT1Nil*: $P, E \vdash_1 [] [::] []$

| *WT1Cons*: $\llbracket P, E \vdash_1 e :: T; P, E \vdash_1 es [::] Ts \rrbracket \implies P, E \vdash_1 e \# es [::] T \# Ts$

declare *WT1-WTs1.intros*[*intro!*]

declare *WT1Nil*[*iff*]

inductive-cases *WT1-WTs1-cases*[*elim!*]:

$P, E \vdash_1 \text{Val } v :: T$
 $P, E \vdash_1 \text{Var } i :: T$
 $P, E \vdash_1 \text{Cast } D \ e :: T$
 $P, E \vdash_1 e \ \text{instanceof} \ U :: T$
 $P, E \vdash_1 i := e :: T$
 $P, E \vdash_1 \{i:U=v_0; e\} :: T$
 $P, E \vdash_1 e_1 ;; e_2 :: T$
 $P, E \vdash_1 \text{if } (e) \ e_1 \ \text{else} \ e_2 :: T$
 $P, E \vdash_1 \text{while } (e) \ c :: T$
 $P, E \vdash_1 \text{throw } e :: T$
 $P, E \vdash_1 \text{try } e_1 \ \text{catch}(C \ i) \ e_2 :: T$
 $P, E \vdash_1 e \cdot F\{D\} :: T$
 $P, E \vdash_1 e_1 \cdot F\{D\} := e_2 :: T$
 $P, E \vdash_1 e \cdot \text{compareAndSwap}(D \cdot F, \ e', \ e'') :: T$
 $P, E \vdash_1 e_1 \ \llbracket \text{bop} \rrbracket \ e_2 :: T$
 $P, E \vdash_1 \text{new } C :: T$
 $P, E \vdash_1 \text{newA } T' [e] :: T$
 $P, E \vdash_1 a [i] := e :: T$
 $P, E \vdash_1 a [i] :: T$
 $P, E \vdash_1 a \cdot \text{length} :: T$
 $P, E \vdash_1 e \cdot M(es) :: T$
 $P, E \vdash_1 \text{sync}_V(o') \ e :: T$
 $P, E \vdash_1 \text{insync}_V(a) \ e :: T$
 $P, E \vdash_1 [] [::] Ts$
 $P, E \vdash_1 e \# es [::] Ts$

lemma *WTs1-same-size*: $P, E \vdash_1 es [::] Ts \implies \text{size } es = \text{size } Ts$

<proof>

lemma *WTs1-snoc-cases*:

assumes *wt*: $P, E \vdash_1 es @ [e] [::] Ts$

obtains $T \ Ts'$ **where** $P, E \vdash_1 es [::] Ts' \ P, E \vdash_1 e :: T$

<proof>

lemma *WTs1-append*:

assumes *wt*: $P, Env \vdash 1 \text{ es} @ \text{ es}' [::] Ts$

obtains $Ts' Ts''$ **where** $P, Env \vdash 1 \text{ es} [::] Ts'$ $P, Env \vdash 1 \text{ es}' [::] Ts''$

<proof>

lemma *WT1-not-contains-insync*: $P, E \vdash 1 e :: T \implies \neg \text{contains-insync } e$

and *WTs1-not-contains-insyncs*: $P, E \vdash 1 \text{ es} [::] Ts \implies \neg \text{contains-insyncs } \text{es}$

<proof>

lemma *WT1-expr-locks*: $P, E \vdash 1 e :: T \implies \text{expr-locks } e = (\lambda a. 0)$

and *WTs1-expr-lockss*: $P, E \vdash 1 \text{ es} [::] Ts \implies \text{expr-lockss } \text{es} = (\lambda a. 0)$

<proof>

lemma **assumes** *wf*: *wf-prog wfmd* *P*

shows *WT1-unique*: $P, E \vdash 1 e :: T1 \implies P, E \vdash 1 e :: T2 \implies T1 = T2$

and *WTs1-unique*: $P, E \vdash 1 \text{ es} [::] Ts1 \implies P, E \vdash 1 \text{ es} [::] Ts2 \implies Ts1 = Ts2$

<proof>

lemma **assumes** *wf*: *wf-prog p* *P*

shows *WT1-is-type*: $P, E \vdash 1 e :: T \implies \text{set } E \subseteq \text{types } P \implies \text{is-type } P T$

and *WTs1-is-type*: $P, E \vdash 1 \text{ es} [::] Ts \implies \text{set } E \subseteq \text{types } P \implies \text{set } Ts \subseteq \text{types } P$

<proof>

lemma *blocks1-WT*:

$\llbracket P, Env @ Ts \vdash 1 \text{ body} :: T; \text{set } Ts \subseteq \text{types } P \rrbracket \implies P, Env \vdash 1 \text{ blocks1 } (\text{length } Env) Ts \text{ body} :: T$

<proof>

lemma *WT1-fv*: $\llbracket P, E \vdash 1 e :: T; \mathcal{B} e (\text{length } E); \text{syncvars } e \rrbracket \implies \text{fv } e \subseteq \{0..<\text{length } E\}$

and *WTs1-fvs*: $\llbracket P, E \vdash 1 \text{ es} [::] Ts; \mathcal{B} s \text{ es} (\text{length } E); \text{syncvarss } \text{es} \rrbracket \implies \text{fvs } \text{es} \subseteq \{0..<\text{length } E\}$

<proof>

end

7.12 Well-Formedness of Intermediate Language

theory *J1WellForm* **imports**

../J/DefAss

J1WellType

begin

7.12.1 Well-formedness

definition *wf-J1-mdecl* :: $'addr \text{ J1-prog} \Rightarrow \text{cname} \Rightarrow 'addr \text{ expr1 mdecl} \Rightarrow \text{bool}$

where

$\text{wf-J1-mdecl } P C \equiv \lambda(M, Ts, T, \text{body}).$

$(\exists T'. P, \text{Class } C \# Ts \vdash 1 \text{ body} :: T' \wedge P \vdash T' \leq T) \wedge$

$\mathcal{D} \text{ body } [\{\dots \text{size } Ts\}] \wedge \mathcal{B} \text{ body } (\text{size } Ts + 1) \wedge \text{syncvars } \text{body}$

lemma *wf-J1-mdecl[simp]*:

$\text{wf-J1-mdecl } P C (M, Ts, T, \text{body}) \equiv$

$((\exists T'. P, \text{Class } C \# Ts \vdash 1 \text{ body} :: T' \wedge P \vdash T' \leq T) \wedge$

$\mathcal{D} \text{ body } [\{\dots \text{size } Ts\}] \wedge \mathcal{B} \text{ body } (\text{size } Ts + 1) \wedge \text{syncvars } \text{body}$

<proof>

abbreviation *wf-J1-prog* :: 'addr J1-prog ⇒ bool
where *wf-J1-prog* == *wf-prog wf-J1-mdecl*

end

7.13 Preservation of Well-Typedness in Stage 2

theory *TypeComp*

imports

Exception-Tables

J1WellForm

../BV/BVSpec

HOL-Library.Prefix-Order

HOL-Library.Sublist

begin

locale *TC0* =

fixes *P* :: 'addr J1-prog **and** *mxl* :: nat

begin

definition *ty* :: ty list ⇒ 'addr expr1 ⇒ ty

where *ty E e* ≡ *THE T. P, E ⊢ 1 e :: T*

definition *ty_l* :: ty list ⇒ nat set ⇒ ty_l

where *ty_l E A'* ≡ *map (λi. if i ∈ A' ∧ i < size E then OK(E!i) else Err) [0..*mxl*]*

definition *ty_i'* :: ty list ⇒ ty list ⇒ nat set option ⇒ ty_i'

where *ty_i' ST E A* ≡ *case A of None ⇒ None | [A'] ⇒ Some(ST, ty_l E A')*

definition *after* :: ty list ⇒ nat set option ⇒ ty list ⇒ 'addr expr1 ⇒ ty_i'

where *after E A ST e* ≡ *ty_i' (ty E e # ST) E (A ⊔ A e)*

end

locale *TC1* = *TC0* +

fixes *wfmd*

assumes *wf-prog: wf-prog wfmd P*

begin

lemma *ty-def2* [*simp*]: *P, E ⊢ 1 e :: T* ⇒ *ty E e = T*

<proof>

end

context *TC0* **begin**

lemma *ty_i'-None* [*simp*]: *ty_i' ST E None = None*

<proof>

lemma *ty_l-app-diff* [*simp*]:

ty_l (E@[T]) (A - {size E}) = ty_l E A

<proof>

lemma *ty_i'-app-diff[simp]*:

$ty_i' ST (E @ [T]) (A \ominus size E) = ty_i' ST E A$
 $\langle proof \rangle$

lemma *ty_l-antimono*:

$A \subseteq A' \implies P \vdash ty_l E A' [\leq_{\top}] ty_l E A$
 $\langle proof \rangle$

lemma *ty_i'-antimono*:

$A \subseteq A' \implies P \vdash ty_i' ST E [A'] \leq' ty_i' ST E [A]$
 $\langle proof \rangle$

lemma *ty_l-env-antimono*:

$P \vdash ty_l (E @ [T]) A [\leq_{\top}] ty_l E A$
 $\langle proof \rangle$

lemma *ty_i'-env-antimono*:

$P \vdash ty_i' ST (E @ [T]) A \leq' ty_i' ST E A$
 $\langle proof \rangle$

lemma *ty_i'-incr*:

$P \vdash ty_i' ST (E @ [T]) [insert (size E) A] \leq' ty_i' ST E [A]$
 $\langle proof \rangle$

lemma *ty_l-incr*:

$P \vdash ty_l (E @ [T]) (insert (size E) A) [\leq_{\top}] ty_l E A$
 $\langle proof \rangle$

lemma *ty_l-in-types*:

$set E \subseteq types P \implies ty_l E A \in list mxl (err (types P))$
 $\langle proof \rangle$

function *compT* :: *ty list* \Rightarrow *nat hyperset* \Rightarrow *ty list* \Rightarrow 'addr *expr1* \Rightarrow *ty_i' list*

and *compTs* :: *ty list* \Rightarrow *nat hyperset* \Rightarrow *ty list* \Rightarrow 'addr *expr1 list* \Rightarrow *ty_i' list*

where

$compT E A ST (new C) = []$
 $| compT E A ST (newA T[e]) = compT E A ST e @ [after E A ST e]$
 $| compT E A ST (Cast C e) = compT E A ST e @ [after E A ST e]$
 $| compT E A ST (e instanceof T) = compT E A ST e @ [after E A ST e]$
 $| compT E A ST (Val v) = []$
 $| compT E A ST (e1 «bop» e2) =$
 $(let ST1 = ty E e1 # ST; A1 = A \sqcup \mathcal{A} e1 in$
 $compT E A ST e1 @ [after E A ST e1] @$
 $compT E A1 ST1 e2 @ [after E A1 ST1 e2])$
 $| compT E A ST (Var i) = []$
 $| compT E A ST (i := e) = compT E A ST e @ [after E A ST e, ty_i' ST E (A \sqcup \mathcal{A} e \sqcup [\{i\}])]$
 $| compT E A ST (a[i]) =$
 $(let ST1 = ty E a # ST; A1 = A \sqcup \mathcal{A} a$

$in\ compT\ E\ A\ ST\ a\ @\ [after\ E\ A\ ST\ a]\ @\ compT\ E\ A1\ ST1\ i\ @\ [after\ E\ A1\ ST1\ i]$
 $| compT\ E\ A\ ST\ (a[i] := e) =$
 $(let\ ST1 = ty\ E\ a\ \# ST; A1 = A\ \sqcup\ \mathcal{A}\ a;$
 $ST2 = ty\ E\ i\ \# ST1; A2 = A1\ \sqcup\ \mathcal{A}\ i; A3 = A2\ \sqcup\ \mathcal{A}\ e$
 $in\ compT\ E\ A\ ST\ a\ @\ [after\ E\ A\ ST\ a]\ @\ compT\ E\ A1\ ST1\ i\ @\ [after\ E\ A1\ ST1\ i]\ @\ compT\ E$
 $A2\ ST2\ e\ @\ [after\ E\ A2\ ST2\ e,\ ty_i'\ ST\ E\ A3])$
 $| compT\ E\ A\ ST\ (a.length) = compT\ E\ A\ ST\ a\ @\ [after\ E\ A\ ST\ a]$
 $| compT\ E\ A\ ST\ (e.F\{D\}) = compT\ E\ A\ ST\ e\ @\ [after\ E\ A\ ST\ e]$
 $| compT\ E\ A\ ST\ (e1.F\{D\} := e2) =$
 $(let\ ST1 = ty\ E\ e1\ \# ST; A1 = A\ \sqcup\ \mathcal{A}\ e1; A2 = A1\ \sqcup\ \mathcal{A}\ e2$
 $in\ compT\ E\ A\ ST\ e1\ @\ [after\ E\ A\ ST\ e1]\ @\ compT\ E\ A1\ ST1\ e2\ @\ [after\ E\ A1\ ST1\ e2]\ @\ [ty_i'$
 $ST\ E\ A2])$
 $| compT\ E\ A\ ST\ (e1.compareAndSwap(D.F, e2, e3)) =$
 $(let\ ST1 = ty\ E\ e1\ \# ST; A1 = A\ \sqcup\ \mathcal{A}\ e1; ST2 = ty\ E\ e2\ \# ST1; A2 = A1\ \sqcup\ \mathcal{A}\ e2; A3 = A2$
 $\sqcup\ \mathcal{A}\ e3$
 $in\ compT\ E\ A\ ST\ e1\ @\ [after\ E\ A\ ST\ e1]\ @\ compT\ E\ A1\ ST1\ e2\ @\ [after\ E\ A1\ ST1\ e2]\ @\ compT$
 $E\ A2\ ST2\ e3\ @\ [after\ E\ A2\ ST2\ e3])$
 $| compT\ E\ A\ ST\ (e.M(es)) =$
 $compT\ E\ A\ ST\ e\ @\ [after\ E\ A\ ST\ e]\ @$
 $compTs\ E\ (A\ \sqcup\ \mathcal{A}\ e)\ (ty\ E\ e\ \# ST)\ es$
 $| compT\ E\ A\ ST\ \{i:T=None; e\} = compT\ (E@[T])\ (A\ominus i)\ ST\ e$
 $| compT\ E\ A\ ST\ \{i:T=[v]; e\} =$
 $[after\ E\ A\ ST\ (Val\ v), ty_i'\ ST\ (E@[T])\ (A\ \sqcup\ [\{i\}])]\ @\ compT\ (E@[T])\ (A\ \sqcup\ [\{i\}])\ ST\ e$
 $| compT\ E\ A\ ST\ (sync_i\ (e1)\ e2) =$
 $(let\ A1 = A\ \sqcup\ \mathcal{A}\ e1\ \sqcup\ [\{i\}]; E1 = E\ @\ [Class\ Object]; ST2 = ty\ E1\ e2\ \# ST; A2 = A1\ \sqcup\ \mathcal{A}\ e2$
 $in\ compT\ E\ A\ ST\ e1\ @$
 $[after\ E\ A\ ST\ e1,$
 $ty_i'\ (Class\ Object\ \# Class\ Object\ \# ST)\ E\ (A\ \sqcup\ \mathcal{A}\ e1),$
 $ty_i'\ (Class\ Object\ \# ST)\ E1\ A1,$
 $ty_i'\ ST\ E1\ A1]\ @$
 $compT\ E1\ A1\ ST\ e2\ @$
 $[ty_i'\ ST2\ E1\ A2,\ ty_i'\ (Class\ Object\ \# ST2)\ E1\ A2,\ ty_i'\ ST2\ E1\ A2,$
 $ty_i'\ (Class\ Throwable\ \# ST)\ E1\ A1,$
 $ty_i'\ (Class\ Object\ \# Class\ Throwable\ \# ST)\ E1\ A1,$
 $ty_i'\ (Class\ Throwable\ \# ST)\ E1\ A1])$
 $| compT\ E\ A\ ST\ (insync_i\ (a)\ e) = []$
 $| compT\ E\ A\ ST\ (e1;;e2) =$
 $(let\ A1 = A\ \sqcup\ \mathcal{A}\ e1\ in$
 $compT\ E\ A\ ST\ e1\ @\ [after\ E\ A\ ST\ e1,\ ty_i'\ ST\ E\ A1]\ @$
 $compT\ E\ A1\ ST\ e2)$
 $| compT\ E\ A\ ST\ (if\ (e)\ e1\ else\ e2) =$
 $(let\ A0 = A\ \sqcup\ \mathcal{A}\ e; \tau = ty_i'\ ST\ E\ A0\ in$
 $compT\ E\ A\ ST\ e\ @\ [after\ E\ A\ ST\ e,\ \tau]\ @$
 $compT\ E\ A0\ ST\ e1\ @\ [after\ E\ A0\ ST\ e1,\ \tau]\ @$
 $compT\ E\ A0\ ST\ e2)$
 $| compT\ E\ A\ ST\ (while\ (e)\ c) =$
 $(let\ A0 = A\ \sqcup\ \mathcal{A}\ e; A1 = A0\ \sqcup\ \mathcal{A}\ c; \tau = ty_i'\ ST\ E\ A0\ in$
 $compT\ E\ A\ ST\ e\ @\ [after\ E\ A\ ST\ e,\ \tau]\ @$
 $compT\ E\ A0\ ST\ c\ @\ [after\ E\ A0\ ST\ c,\ ty_i'\ ST\ E\ A1,\ ty_i'\ ST\ E\ A0])$
 $| compT\ E\ A\ ST\ (throw\ e) = compT\ E\ A\ ST\ e\ @\ [after\ E\ A\ ST\ e]$
 $| compT\ E\ A\ ST\ (try\ e1\ catch(C\ i)\ e2) =$
 $compT\ E\ A\ ST\ e1\ @\ [after\ E\ A\ ST\ e1]\ @$

$[ty_i' (Class C \# ST) E A, ty_i' ST (E@[Class C]) (A \sqcup [\{i\}])] @$
 $compT (E@[Class C]) (A \sqcup [\{i\}]) ST e2$

| $compTs E A ST [] = []$
 | $compTs E A ST (e \# es) = compT E A ST e @ [after E A ST e] @$
 $compTs E (A \sqcup (\mathcal{A} e)) (ty E e \# ST) es$

$\langle proof \rangle$

termination

$\langle proof \rangle$

lemmas $compT-compTs-induct =$

$compT-compTs.induct[$
 $unfolded meta-all5-eq-conv meta-all4-eq-conv meta-all3-eq-conv meta-all2-eq-conv meta-all-eq-conv,$
 $case-names$
 $new NewArray Cast InstanceOf Val BinOp Var LAss AAcc AAss ALen FAcc FAss CompareAndSwap$
 $Call BlockNone BlockSome$
 $Synchronized InSynchronized Seq Cond While throw TryCatch$
 $Nil Cons]$

definition $compTa :: ty list \Rightarrow nat hyperset \Rightarrow ty list \Rightarrow 'addr expr1 \Rightarrow ty_i' list$

where $compTa E A ST e \equiv compT E A ST e @ [after E A ST e]$

lemmas $compE2-not-Nil = compE2-neq-Nil$

declare $compE2-not-Nil[simp]$

lemma $compT-sizes[simp]:$

shows $size(compT E A ST e) = size(compE2 e) - 1$

and $size(compTs E A ST es) = size(compEs2 es)$

$\langle proof \rangle$

lemma $compT-None-not-Some [simp]: [\tau] \notin set (compT E None ST e)$

and $compTs-None-not-Some [simp]: [\tau] \notin set (compTs E None ST es)$

$\langle proof \rangle$

lemma $pair-eq-ty_i'-conv:$

$([(ST, LT)] = ty_i' ST_0 E A) = (case A of None \Rightarrow False | Some A \Rightarrow (ST = ST_0 \wedge LT = ty_l E A))$

$\langle proof \rangle$

lemma $pair-conv-ty_i': [(ST, ty_l E A)] = ty_i' ST E [A]$

$\langle proof \rangle$

lemma $ty_i'-antimono2:$

$[[E \leq E'; A \subseteq A'] \Longrightarrow P \vdash ty_i' ST E' [A'] \leq' ty_i' ST E [A]$

$\langle proof \rangle$

declare $ty_i'-antimono [intro!] after-def[simp] pair-conv-ty_i'[simp] pair-eq-ty_i'-conv[simp]$

lemma $compT-LT-prefix:$

$[[[(ST,LT)] \in set(compT E A ST_0 e); B e (size E)] \Longrightarrow P \vdash [(ST,LT)] \leq' ty_i' ST E A$

and $compTs-LT-prefix:$

$[[[(ST,LT)] \in set(compTs E A ST_0 es); Bs es (size E)] \Longrightarrow P \vdash [(ST,LT)] \leq' ty_i' ST E A$

$\langle proof \rangle$

declare ty_i' -antimono [rule del] after-def[simp del] pair-conv- ty_i' [simp del] pair-eq- ty_i' -conv[simp del]

lemma OK-None-states [iff]: OK None \in states P mxs mxl
 ⟨proof⟩

end

context TC1 **begin**

lemma after-in-states:

$\llbracket P, E \vdash 1 e :: T; \text{set } E \subseteq \text{types } P; \text{set } ST \subseteq \text{types } P; \text{size } ST + \text{max-stack } e \leq \text{mxs} \rrbracket$
 \implies OK (after E A ST e) \in states P mxs mxl
 ⟨proof⟩

end

context TC0 **begin**

lemma OK- ty_i' -in-statesI [simp]:

$\llbracket \text{set } E \subseteq \text{types } P; \text{set } ST \subseteq \text{types } P; \text{size } ST \leq \text{mxs} \rrbracket$
 \implies OK (ty_i' ST E A) \in states P mxs mxl
 ⟨proof⟩

end

lemma is-class-type-aux: is-class P C \implies is-type P (Class C)
 ⟨proof⟩

context TC1 **begin**

declare is-type.simps[simp del] subsetI[rule del]

theorem

shows compT-states:

$\llbracket P, E \vdash 1 e :: T; \text{set } E \subseteq \text{types } P; \text{set } ST \subseteq \text{types } P;$
 $\text{size } ST + \text{max-stack } e \leq \text{mxs}; \text{size } E + \text{max-vars } e \leq \text{mxl} \rrbracket$
 \implies OK ' set(compT E A ST e) \subseteq states P mxs mxl
 (is PROP ?P e E T A ST)

and compTs-states:

$\llbracket P, E \vdash 1 es[::]Ts; \text{set } E \subseteq \text{types } P; \text{set } ST \subseteq \text{types } P;$
 $\text{size } ST + \text{max-stacks } es \leq \text{mxs}; \text{size } E + \text{max-varss } es \leq \text{mxl} \rrbracket$
 \implies OK ' set(compTs E A ST es) \subseteq states P mxs mxl
 (is PROP ?Ps es E Ts A ST)
 ⟨proof⟩

declare is-type.simps[simp] subsetI[intro!]

end

locale TC2 = TC0 +

fixes $T_r :: ty$ and mxs :: pc
begin

definition

$wt\text{-instrs} :: 'addr\ instr\ list \Rightarrow ex\text{-table} \Rightarrow ty_i' list \Rightarrow bool \langle (\vdash -, - / [::] / -) \rangle [0,0,51] 50)$

where

$\vdash is, xt [::] \tau s \equiv size\ is < size\ \tau s \wedge pcs\ xt \subseteq \{0..<size\ is\} \wedge (\forall pc < size\ is. P, T_r, m\&s, size\ \tau s, xt \vdash is!pc, pc :: \tau s)$

lemmas $wt\text{-defs} = wt\text{-instrs}\text{-def}\ wt\text{-instr}\text{-def}\ app\text{-def}\ eff\text{-def}\ norm\text{-eff}\text{-def}$

lemma $wt\text{-instrs}\text{-Nil}$ [simp]: $\tau s \neq [] \Longrightarrow \vdash [], [] [::] \tau s$

$\langle proof \rangle$

end

locale $TC3 = TC1 + TC2$

lemma $eff\text{-None}$ [simp]: $eff\ i\ P\ pc\ et\ None = []$

$\langle proof \rangle$

declare $split\text{-comp}\text{-eq}$ [simp del]

lemma $wt\text{-instr}\text{-appR}$:

$\llbracket P, T, m, mpc, xt \vdash is!pc, pc :: \tau s;$
 $pc < size\ is; size\ is < size\ \tau s; mpc \leq size\ \tau s; mpc \leq mpc' \rrbracket$
 $\Longrightarrow P, T, m, mpc', xt \vdash is!pc, pc :: \tau s @ \tau s'$

$\langle proof \rangle$

lemma $relevant\text{-entries}\text{-shift}$ [simp]:

$relevant\text{-entries}\ P\ i\ (pc+n)\ (shift\ n\ xt) = shift\ n\ (relevant\text{-entries}\ P\ i\ pc\ xt)$

$\langle proof \rangle$

lemma $xcpt\text{-eff}\text{-shift}$ [simp]:

$xcpt\text{-eff}\ i\ P\ (pc+n)\ \tau\ (shift\ n\ xt) =$
 $map\ (\lambda(pc, \tau). (pc + n, \tau))\ (xcpt\text{-eff}\ i\ P\ pc\ \tau\ xt)$

$\langle proof \rangle$

lemma $eff\text{-shift}$ [simp]:

$app_i\ (i, P, pc, m, T, \tau) \Longrightarrow$
 $eff\ i\ P\ (pc+n)\ (shift\ n\ xt)\ (Some\ \tau) =$
 $map\ (\lambda(pc, \tau). (pc+n, \tau))\ (eff\ i\ P\ pc\ xt\ (Some\ \tau))$

$\langle proof \rangle$

lemma $xcpt\text{-app}\text{-shift}$ [simp]:

$xcpt\text{-app}\ i\ P\ (pc+n)\ m\ (shift\ n\ xt)\ \tau = xcpt\text{-app}\ i\ P\ pc\ m\ xt\ \tau$

$\langle proof \rangle$

lemma $wt\text{-instr}\text{-appL}$:

$\llbracket P, T, m, mpc, xt \vdash i, pc :: \tau s; pc < size\ \tau s; mpc \leq size\ \tau s \rrbracket$

$\implies P, T, m, mpc + \text{size } \tau s', \text{shift } (\text{size } \tau s') \text{ } xt \vdash i, pc + \text{size } \tau s' :: \tau s' @ \tau s$
 ⟨proof⟩

lemma *wt-instr-Cons*:

$\llbracket P, T, m, mpc - 1, [] \vdash i, pc - 1 :: \tau s;$
 $0 < pc; 0 < mpc; pc < \text{size } \tau s + 1; mpc \leq \text{size } \tau s + 1 \rrbracket$
 $\implies P, T, m, mpc, [] \vdash i, pc :: \tau \# \tau s$
 ⟨proof⟩

lemma *wt-instr-append*:

$\llbracket P, T, m, mpc - \text{size } \tau s', [] \vdash i, pc - \text{size } \tau s' :: \tau s;$
 $\text{size } \tau s' \leq pc; \text{size } \tau s' \leq mpc; pc < \text{size } \tau s + \text{size } \tau s'; mpc \leq \text{size } \tau s + \text{size } \tau s' \rrbracket$
 $\implies P, T, m, mpc, [] \vdash i, pc :: \tau s' @ \tau s$
 ⟨proof⟩

lemma *xcpt-app-pcs*:

$pc \notin pcs \text{ } xt \implies \text{xcpt-app } i \ P \ pc \ m \ x \ \tau$
 ⟨proof⟩

lemma *xcpt-eff-pcs*:

$pc \notin pcs \text{ } xt \implies \text{xcpt-eff } i \ P \ pc \ \tau \text{ } xt = []$
 ⟨proof⟩

lemma *pcs-shift*:

$pc < n \implies pc \notin pcs \text{ } (\text{shift } n \text{ } xt)$
 ⟨proof⟩

lemma *xcpt-eff-shift-pc-ge-n*: **assumes** $x \in \text{set } (\text{xcpt-eff } i \ P \ pc \ \tau \text{ } (\text{shift } n \text{ } xt))$

shows $n \leq pc$
 ⟨proof⟩

lemma *wt-instr-appRx*:

$\llbracket P, T, m, mpc, xt \vdash \text{is!}pc, pc :: \tau s; pc < \text{size } is; \text{size } is < \text{size } \tau s; mpc \leq \text{size } \tau s \rrbracket$
 $\implies P, T, m, mpc, xt @ \text{shift } (\text{size } is) \text{ } xt' \vdash \text{is!}pc, pc :: \tau s$
 ⟨proof⟩

lemma *wt-instr-appLx*:

$\llbracket P, T, m, mpc, xt \vdash i, pc :: \tau s; pc \notin pcs \text{ } xt' \rrbracket$
 $\implies P, T, m, mpc, xt' @ xt \vdash i, pc :: \tau s$
 ⟨proof⟩

context *TC2* **begin**

lemma *wt-instrs-extR*:

$\vdash \text{is}, xt \text{ } [::] \ \tau s \implies \vdash \text{is}, xt \text{ } [::] \ \tau s @ \tau s'$
 ⟨proof⟩

lemma *wt-instrs-ext*:

$$\begin{aligned} & \llbracket \vdash is_1, xt_1 [::] \tau_{s_1} @ \tau_{s_2}; \vdash is_2, xt_2 [::] \tau_{s_2}; size \tau_{s_1} = size is_1 \rrbracket \\ & \implies \vdash is_1 @ is_2, xt_1 @ shift (size is_1) xt_2 [::] \tau_{s_1} @ \tau_{s_2} \\ & \langle proof \rangle \end{aligned}$$

corollary *wt-instrs-ext2*:

$$\begin{aligned} & \llbracket \vdash is_2, xt_2 [::] \tau_{s_2}; \vdash is_1, xt_1 [::] \tau_{s_1} @ \tau_{s_2}; size \tau_{s_1} = size is_1 \rrbracket \\ & \implies \vdash is_1 @ is_2, xt_1 @ shift (size is_1) xt_2 [::] \tau_{s_1} @ \tau_{s_2} \\ & \langle proof \rangle \end{aligned}$$

corollary *wt-instrs-ext-prefix* [trans]:

$$\begin{aligned} & \llbracket \vdash is_1, xt_1 [::] \tau_{s_1} @ \tau_{s_2}; \vdash is_2, xt_2 [::] \tau_{s_3}; \\ & \quad size \tau_{s_1} = size is_1; \tau_{s_3} \leq \tau_{s_2} \rrbracket \\ & \implies \vdash is_1 @ is_2, xt_1 @ shift (size is_1) xt_2 [::] \tau_{s_1} @ \tau_{s_2} \\ & \langle proof \rangle \end{aligned}$$

corollary *wt-instrs-app*:

$$\begin{aligned} & \text{assumes } is_1: \vdash is_1, xt_1 [::] \tau_{s_1} @ [\tau] \\ & \text{assumes } is_2: \vdash is_2, xt_2 [::] \tau \# \tau_{s_2} \\ & \text{assumes } s: size \tau_{s_1} = size is_1 \\ & \text{shows } \vdash is_1 @ is_2, xt_1 @ shift (size is_1) xt_2 [::] \tau_{s_1} @ \tau \# \tau_{s_2} \\ & \langle proof \rangle \end{aligned}$$

corollary *wt-instrs-app-last* [trans]:

$$\begin{aligned} & \llbracket \vdash is_2, xt_2 [::] \tau \# \tau_{s_2}; \vdash is_1, xt_1 [::] \tau_{s_1}; \\ & \quad last \tau_{s_1} = \tau; size \tau_{s_1} = size is_1 + 1 \rrbracket \\ & \implies \vdash is_1 @ is_2, xt_1 @ shift (size is_1) xt_2 [::] \tau_{s_1} @ \tau_{s_2} \\ & \langle proof \rangle \end{aligned}$$

corollary *wt-instrs-append-last* [trans]:

$$\begin{aligned} & \llbracket \vdash is, xt [::] \tau_s; P, T_r, mcs, mpc, [] \vdash i, pc :: \tau_s; \\ & \quad pc = size is; mpc = size \tau_s; size is + 1 < size \tau_s \rrbracket \\ & \implies \vdash is @ [i], xt [::] \tau_s \\ & \langle proof \rangle \end{aligned}$$

corollary *wt-instrs-app2*:

$$\begin{aligned} & \llbracket \vdash (is_2 :: 'b instr list), xt_2 [::] \tau' \# \tau_{s_2}; \vdash is_1, xt_1 [::] \tau \# \tau_{s_1} @ [\tau']; \\ & \quad xt' = xt_1 @ shift (size is_1) xt_2; size \tau_{s_1} + 1 = size is_1 \rrbracket \\ & \implies \vdash is_1 @ is_2, xt' [::] \tau \# \tau_{s_1} @ \tau' \# \tau_{s_2} \\ & \langle proof \rangle \end{aligned}$$

corollary *wt-instrs-app2-simp* [trans, simp]:

$$\begin{aligned} & \llbracket \vdash (is_2 :: 'b instr list), xt_2 [::] \tau' \# \tau_{s_2}; \vdash is_1, xt_1 [::] \tau \# \tau_{s_1} @ [\tau']; size \tau_{s_1} + 1 = size is_1 \rrbracket \\ & \implies \vdash is_1 @ is_2, xt_1 @ shift (size is_1) xt_2 [::] \tau \# \tau_{s_1} @ \tau' \# \tau_{s_2} \\ & \langle proof \rangle \end{aligned}$$

corollary *wt-instrs-Cons*[simp]:

$\llbracket \tau s \neq []; \vdash [i], [] \llbracket :: [\tau, \tau'] \vdash is, xt \llbracket :: \tau' \# \tau s \rrbracket \rrbracket$
 $\implies \vdash i \# is, shift\ 1\ xt \llbracket :: \tau \# \tau' \# \tau s \rrbracket$
 ⟨proof⟩

corollary *wt-instrs-Cons2*[trans]:

assumes $\tau s: \vdash is, xt \llbracket :: \tau s$
assumes $i: P, T_r, mxs, mpc, [] \vdash i, 0 :: \tau \# \tau s$
assumes $mpc: mpc = size\ \tau s + 1$
shows $\vdash i \# is, shift\ 1\ xt \llbracket :: \tau \# \tau s \rrbracket$
 ⟨proof⟩

lemma *wt-instrs-last-incr*[trans]:

$\llbracket \vdash is, xt \llbracket :: \tau s @ [\tau]; P \vdash \tau \leq' \tau' \rrbracket \implies \vdash is, xt \llbracket :: \tau s @ [\tau'] \rrbracket$
 ⟨proof⟩

end

lemma [iff]: *xcpt-app* $i\ P\ pc\ mxs\ []\ \tau$

⟨proof⟩

lemma [simp]: *xcpt-eff* $i\ P\ pc\ \tau\ [] = []$

⟨proof⟩

context *TC2* **begin**

lemma *wt-New*:

$\llbracket is-class\ P\ C; size\ ST < mxs \rrbracket \implies$
 $\vdash [New\ C], [] \llbracket :: [ty_i'\ ST\ E\ A, ty_i'\ (Class\ C \# ST)\ E\ A] \rrbracket$
 ⟨proof⟩

lemma *wt-Cast*:

is-type $P\ T \implies$
 $\vdash [Checkcast\ T], [] \llbracket :: [ty_i'\ (U \# ST)\ E\ A, ty_i'\ (T \# ST)\ E\ A] \rrbracket$
 ⟨proof⟩

lemma *wt-Instanceof*:

$\llbracket is-type\ P\ T; is-ref\ T\ U \rrbracket \implies$
 $\vdash [Instanceof\ T], [] \llbracket :: [ty_i'\ (U \# ST)\ E\ A, ty_i'\ (Boolean \# ST)\ E\ A] \rrbracket$
 ⟨proof⟩

lemma *wt-Push*:

$\llbracket size\ ST < mxs; typeof\ v = Some\ T \rrbracket$
 $\implies \vdash [Push\ v], [] \llbracket :: [ty_i'\ ST\ E\ A, ty_i'\ (T \# ST)\ E\ A] \rrbracket$
 ⟨proof⟩

lemma *wt-Pop*:

$\vdash [Pop], [] \llbracket :: (ty_i'\ (T \# ST)\ E\ A \# ty_i'\ ST\ E\ A \# \tau s) \rrbracket$
 ⟨proof⟩

lemma *wt-BinOpInstr*:

$P \vdash T1 \ll bop \gg T2 :: T \Longrightarrow \vdash [BinOpInstr\ bop], [] [::] [ty_i' (T2 \# T1 \# ST) E A, ty_i' (T \# ST) E A]$
 $\langle proof \rangle$

lemma *wt-Load*:

$\ll size\ ST < mxs; size\ E \leq mxl; i \in A; i < size\ E \gg$
 $\Longrightarrow \vdash [Load\ i], [] [::] [ty_i' ST E A, ty_i' (E!i \# ST) E A]$
 $\langle proof \rangle$

lemma *wt-Store*:

$\ll P \vdash T \leq E!i; i < size\ E; size\ E \leq mxl \gg \Longrightarrow$
 $\vdash [Store\ i], [] [::] [ty_i' (T \# ST) E A, ty_i' ST E (\{i\} \sqcup A)]$
 $\langle proof \rangle$

lemma *wt-Get*:

$\ll P \vdash C\ sees\ F:T\ (fm)\ in\ D; class\ type\ of'\ U = [C] \gg \Longrightarrow$
 $\vdash [Getfield\ F\ D], [] [::] [ty_i' (U \# ST) E A, ty_i' (T \# ST) E A]$
 $\langle proof \rangle$

lemma *wt-Put*:

$\ll P \vdash C\ sees\ F:T\ (fm)\ in\ D; class\ type\ of'\ U = [C]; P \vdash T' \leq T \gg \Longrightarrow$
 $\vdash [Putfield\ F\ D], [] [::] [ty_i' (T' \# U \# ST) E A, ty_i' ST E A]$
 $\langle proof \rangle$

lemma *wt-CAS*:

$\ll P \vdash C\ sees\ F:T\ (fm)\ in\ D; class\ type\ of'\ U' = [C]; volatile\ fm; P \vdash T2 \leq T; P \vdash T3 \leq T \gg \Longrightarrow$
 $\vdash [CAS\ F\ D], [] [::] [ty_i' (T3 \# T2 \# U' \# ST) E A, ty_i' (Boolean \# ST) E A]$
 $\langle proof \rangle$

lemma *wt-Throw*:

$P \vdash C \preceq^* Throwable \Longrightarrow \vdash [ThrowExc], [] [::] [ty_i' (Class\ C \# ST) E A, \tau]$
 $\langle proof \rangle$

lemma *wt-IfFalse*:

$\ll 2 \leq i; nat\ i < size\ \tau s + 2; P \vdash ty_i' ST E A \leq' \tau s ! nat(i - 2) \gg$
 $\Longrightarrow \vdash [IfFalse\ i], [] [::] ty_i' (Boolean \# ST) E A \# ty_i' ST E A \# \tau s$
 $\langle proof \rangle$

lemma *wt-Goto*:

$\ll 0 \leq int\ pc + i; nat\ (int\ pc + i) < size\ \tau s; size\ \tau s \leq mpc;$
 $P \vdash \tau s ! pc \leq' \tau s ! nat\ (int\ pc + i) \gg$
 $\Longrightarrow P, T, mxs, mpc, [] \vdash Goto\ i, pc :: \tau s$
 $\langle proof \rangle$

end

context *TC3* **begin**

lemma *wt-Invoke*:

$$\llbracket \text{size } es = \text{size } Ts'; \text{class-type-of}' U = \lfloor C \rfloor; P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D; P \vdash Ts' \leq Ts \rrbracket$$

$$\implies \vdash [\text{Invoke } M (\text{size } es), \llbracket \text{::} \rrbracket [ty_i' (\text{rev } Ts' @ U \# ST) E A, ty_i' (T \# ST) E A]$$

$\langle \text{proof} \rangle$

end

declare $\text{nth-append}[\text{simp del}]$

declare $\llbracket \text{simproc del: list-to-set-comprehension} \rrbracket$

context $TC2$ **begin**

corollary $\text{wt-instrs-app3}[\text{simp}]$:

$$\llbracket \vdash (is_2 :: 'b \text{ instr list}), \llbracket \text{::} \rrbracket (\tau' \# \tau s_2); \vdash is_1, xt_1 \llbracket \text{::} \rrbracket \tau \# \tau s_1 @ [\tau']; \text{size } \tau s_1 + 1 = \text{size } is_1 \rrbracket$$

$$\implies \vdash (is_1 @ is_2), xt_1 \llbracket \text{::} \rrbracket \tau \# \tau s_1 @ \tau' \# \tau s_2$$

$\langle \text{proof} \rangle$

corollary $\text{wt-instrs-Cons3}[\text{simp}]$:

$$\llbracket \tau s \neq \llbracket \text{::} \rrbracket; \vdash [i], \llbracket \text{::} \rrbracket [\tau, \tau']; \vdash is, \llbracket \text{::} \rrbracket \tau' \# \tau s \rrbracket$$

$$\implies \vdash (i \# is), \llbracket \text{::} \rrbracket \tau \# \tau' \# \tau s$$

$\langle \text{proof} \rangle$

lemma wt-instrs-xapp :

$$\llbracket \vdash is_1 @ is_2, xt \llbracket \text{::} \rrbracket \tau s_1 @ ty_i' (\text{Class } D \# ST) E A \# \tau s_2;$$

$$\forall \tau \in \text{set } \tau s_1. \forall ST' LT'. \tau = \text{Some}(ST', LT') \longrightarrow$$

$$\text{size } ST \leq \text{size } ST' \wedge P \vdash \text{Some}(\text{drop}(\text{size } ST' - \text{size } ST) ST', LT') \leq' ty_i' ST E A;$$

$$\text{size } is_1 = \text{size } \tau s_1; \text{size } ST < \text{maxs}; \text{case } Co \text{ of } None \Rightarrow D = \text{Throwable} \mid \text{Some } C \Rightarrow D = C \wedge$$

$$\text{is-class } P C \rrbracket \implies$$

$$\vdash is_1 @ is_2, xt @ [(0, \text{size } is_1 - \text{Suc } n, Co, \text{size } is_1, \text{size } ST)] \llbracket \text{::} \rrbracket \tau s_1 @ ty_i' (\text{Class } D \# ST) E A \#$$

$$\tau s_2$$

$\langle \text{proof} \rangle$

lemma $\text{wt-instrs-xapp-Some}[\text{trans}]$:

$$\llbracket \vdash is_1 @ is_2, xt \llbracket \text{::} \rrbracket \tau s_1 @ ty_i' (\text{Class } C \# ST) E A \# \tau s_2;$$

$$\forall \tau \in \text{set } \tau s_1. \forall ST' LT'. \tau = \text{Some}(ST', LT') \longrightarrow$$

$$\text{size } ST \leq \text{size } ST' \wedge P \vdash \text{Some}(\text{drop}(\text{size } ST' - \text{size } ST) ST', LT') \leq' ty_i' ST E A;$$

$$\text{size } is_1 = \text{size } \tau s_1; \text{is-class } P C; \text{size } ST < \text{maxs} \rrbracket \implies$$

$$\vdash is_1 @ is_2, xt @ [(0, \text{size } is_1 - \text{Suc } n, \text{Some } C, \text{size } is_1, \text{size } ST)] \llbracket \text{::} \rrbracket \tau s_1 @ ty_i' (\text{Class } C \# ST) E A$$

$$\# \tau s_2$$

$\langle \text{proof} \rangle$

lemma $\text{wt-instrs-xapp-Any}$:

$$\llbracket \vdash is_1 @ is_2, xt \llbracket \text{::} \rrbracket \tau s_1 @ ty_i' (\text{Class } \text{Throwable} \# ST) E A \# \tau s_2;$$

$$\forall \tau \in \text{set } \tau s_1. \forall ST' LT'. \tau = \text{Some}(ST', LT') \longrightarrow$$

$$\text{size } ST \leq \text{size } ST' \wedge P \vdash \text{Some}(\text{drop}(\text{size } ST' - \text{size } ST) ST', LT') \leq' ty_i' ST E A;$$

$$\text{size } is_1 = \text{size } \tau s_1; \text{size } ST < \text{maxs} \rrbracket \implies$$

$$\vdash is_1 @ is_2, xt @ [(0, \text{size } is_1 - \text{Suc } n, None, \text{size } is_1, \text{size } ST)] \llbracket \text{::} \rrbracket \tau s_1 @ ty_i' (\text{Class } \text{Throwable} \# ST)$$

$$E A \# \tau s_2$$

$\langle \text{proof} \rangle$

end

declare $\llbracket \text{simproc add: list-to-set-comprehension} \rrbracket$

declare $\text{nth-append}[\text{simp}]$

lemma *drop-Cons-Suc*:

$\bigwedge xs. \text{drop } n \text{ } xs = y \# ys \implies \text{drop } (\text{Suc } n) \text{ } xs = ys$
 ⟨proof⟩

lemma *drop-mess*:

$\llbracket \text{Suc } (\text{length } xs_0) \leq \text{length } xs; \text{drop } (\text{length } xs - \text{Suc } (\text{length } xs_0)) \text{ } xs = x \# xs_0 \rrbracket$
 $\implies \text{drop } (\text{length } xs - \text{length } xs_0) \text{ } xs = xs_0$
 ⟨proof⟩

lemma *drop-mess2*:

assumes *len*: $\text{Suc } (\text{Suc } (\text{length } xs_0)) \leq \text{length } xs$
and *drop*: $\text{drop } (\text{length } xs - \text{Suc } (\text{Suc } (\text{length } xs_0))) \text{ } xs = x_1 \# x_2 \# xs_0$
shows $\text{drop } (\text{length } xs - \text{length } xs_0) \text{ } xs = xs_0$
 ⟨proof⟩

abbreviation *postfix* :: 'a list \Rightarrow 'a list \Rightarrow bool ($\langle (-/ \gg= -) \rangle$ [51, 50] 50) **where**
postfix *xs ys* \equiv *suffix* *ys xs*

lemma *postfix-conv-eq-length-drop*:

$ST' \gg= ST \longleftrightarrow \text{length } ST \leq \text{length } ST' \wedge \text{drop } (\text{length } ST' - \text{length } ST) \text{ } ST' = ST$
 ⟨proof⟩

declare *suffix-ConsI*[simp]

context *TC0* **begin**

declare *after-def*[simp] *pair-eq-ty_i'-conv*[simp]

lemma

assumes $ST_0 \gg= ST'$
shows *compT-ST-prefix*:
 $\llbracket (ST, LT) \rrbracket \in \text{set}(\text{compT } E \ A \ ST_0 \ e) \implies ST \gg= ST'$

and *compTs-ST-prefix*:
 $\llbracket (ST, LT) \rrbracket \in \text{set}(\text{compTs } E \ A \ ST_0 \ es) \implies ST \gg= ST'$
 ⟨proof⟩

declare *after-def*[simp del] *pair-eq-ty_i'-conv*[simp del]

end

declare *suffix-ConsI*[simp del]

lemma *fun-of-simp* [simp]: *fun-of* *S* *x y* = ((*x, y*) \in *S*)
 ⟨proof⟩

declare *widens-refl* [iff]

context *TC3* **begin**

theorem *compT-wt-instrs*:

$\llbracket P, E \vdash 1 \ e :: T; \mathcal{D} \ e \ A; \mathcal{B} \ e \ (\text{size } E); \text{size } ST + \text{max-stack } e \leq \text{max}; \text{size } E + \text{max-vars } e \leq \text{max}; \text{set } E \subseteq \text{types } P \rrbracket$

$\implies \vdash \text{comp}E2\ e, \text{comp}xE2\ e\ 0\ (\text{size}\ ST)\ [\cdot] ty_i' ST\ E\ A\ \# \text{comp}T\ E\ A\ ST\ e\ @\ [\text{after}\ E\ A\ ST\ e]$
 (is PROP ?P e E T A ST)

and *compTs-wt-instrs*:

$\llbracket P, E \vdash 1\ es[\cdot] Ts; \mathcal{D}\ s\ es\ A; \mathcal{B}\ s\ es\ (\text{size}\ E); \text{size}\ ST + \text{max-stacks}\ es \leq \text{max}s; \text{size}\ E + \text{max-vars}\ es \leq \text{max}l; \text{set}\ E \subseteq \text{types}\ P \rrbracket$

$\implies \text{let } \tau s = ty_i' ST\ E\ A\ \# \text{comp}Ts\ E\ A\ ST\ es$

$\text{in } \vdash \text{comp}Es2\ es, \text{comp}xEs2\ es\ 0\ (\text{size}\ ST)\ [\cdot] \tau s \wedge \text{last}\ \tau s = ty_i' (\text{rev}\ Ts\ @\ ST)\ E\ (A \sqcup \mathcal{A}\ s\ es)$

(is PROP ?Ps es E Ts A ST)

$\langle \text{proof} \rangle$

end

lemma *states-compP [simp]*: $\text{states}\ (\text{comp}P\ f\ P)\ \text{max}s\ \text{max}l = \text{states}\ P\ \text{max}s\ \text{max}l$

$\langle \text{proof} \rangle$

lemma *[simp]*: $\text{app}_i\ (i, \text{comp}P\ f\ P, pc, mpc, T, \tau) = \text{app}_i\ (i, P, pc, mpc, T, \tau)$

$\langle \text{proof} \rangle$

lemma *[simp]*: $\text{is-relevant-entry}\ (\text{comp}P\ f\ P)\ i = \text{is-relevant-entry}\ P\ i$

$\langle \text{proof} \rangle$

lemma *[simp]*: $\text{relevant-entries}\ (\text{comp}P\ f\ P)\ i\ pc\ xt = \text{relevant-entries}\ P\ i\ pc\ xt$

$\langle \text{proof} \rangle$

lemma *[simp]*: $\text{app}\ i\ (\text{comp}P\ f\ P)\ mpc\ T\ pc\ \text{max}l\ xt\ \tau = \text{app}\ i\ P\ mpc\ T\ pc\ \text{max}l\ xt\ \tau$

$\langle \text{proof} \rangle$

lemma *[simp]*: $\text{app}\ i\ P\ mpc\ T\ pc\ \text{max}l\ xt\ \tau \implies \text{eff}\ i\ (\text{comp}P\ f\ P)\ pc\ xt\ \tau = \text{eff}\ i\ P\ pc\ xt\ \tau$

$\langle \text{proof} \rangle$

lemma *[simp]*: $\text{widen}\ (\text{comp}P\ f\ P) = \text{widen}\ P$

$\langle \text{proof} \rangle$

lemma *[simp]*: $\text{comp}P\ f\ P \vdash \tau \leq' \tau' = P \vdash \tau \leq' \tau'$

$\langle \text{proof} \rangle$

lemma *[simp]*: $\text{comp}P\ f\ P, T, mpc, \text{max}l, xt \vdash i, pc \:: \tau s = P, T, mpc, \text{max}l, xt \vdash i, pc \:: \tau s$

$\langle \text{proof} \rangle$

declare *TC0.compT-sizes[simp]* *TC1.ty-def2[OF TC1.intro, simp]*

lemma *compT-method*:

fixes *e* **and** *A* **and** *C* **and** *Ts* **and** *maxl₀*

defines *[simp]*: $E \equiv \text{Class}\ C\ \# Ts$

and *[simp]*: $A \equiv \llbracket \dots \text{size}\ Ts \rrbracket$

and *[simp]*: $A' \equiv A \sqcup \mathcal{A}\ e$

and *[simp]*: $\text{max}s \equiv \text{max-stack}\ e$

and *[simp]*: $\text{max}l_0 \equiv \text{max-vars}\ e$

and *[simp]*: $\text{max}l \equiv 1 + \text{size}\ Ts + \text{max}l_0$

assumes *wf-prog*: $\text{wf-prog}\ p\ P$

shows $\llbracket P, E \vdash 1\ e \:: T; \mathcal{D}\ e\ A; \mathcal{B}\ e\ (\text{size}\ E); \text{set}\ E \subseteq \text{types}\ P; P \vdash T \leq T' \rrbracket \implies$

$\text{wt-method}\ (\text{comp}P2\ P)\ C\ Ts\ T'\ \text{max}s\ \text{max}l_0\ (\text{comp}E2\ e\ @\ [\text{Return}])\ (\text{comp}xE2\ e\ 0\ 0)$

($TC0.ty_i'$ max [] $E A$ # $TC0.compTa P max E A$ [] e)
 ⟨proof⟩

definition $compTP :: 'addr J1-prog \Rightarrow ty_P$

where

$compTP P C M \equiv$
 let $(D, Ts, T, meth) = method P C M;$
 $e = the meth;$
 $E = Class C \# Ts;$
 $A = [\{..size Ts\}];$
 $max = 1 + size Ts + max-vars e$
 in $(TC0.ty_i'$ max [] $E A$ # $TC0.compTa P max E A$ [] e)

theorem $wt-compTP-compP2:$

$wf-J1-prog P \Longrightarrow wf-jvm-prog_{compTP P} (compP2 P)$
 ⟨proof⟩

theorem $wt-compP2:$

$wf-J1-prog P \Longrightarrow wf-jvm-prog (compP2 P)$
 ⟨proof⟩

end

7.14 Unobservable steps for the JVM

theory $JVMTau$ **imports**

$TypeComp$
 $../JVM/JVMThreaded$
 $../Framework/FWLTS$

begin

declare $nth-append$ [$simp del$]
declare $Listn.lesub-list-impl-same-size$ [$simp del$]
declare $listE-length$ [$simp del$]

declare $match-ex-table-append-not-pcs$ [$simp del$]
 $outside-pcs-not-matches-entry$ [$simp del$]
 $outside-pcs-compxE2-not-matches-entry$ [$simp del$]
 $outside-pcs-compxEs2-not-matches-entry$ [$simp del$]

context $JVM-heap-base$ **begin**

primrec $\tau instr :: 'm prog \Rightarrow 'heap \Rightarrow 'addr val list \Rightarrow 'addr instr \Rightarrow bool$

where

$\tau instr P h stk (Load n) = True$
 $\tau instr P h stk (Store n) = True$
 $\tau instr P h stk (Push v) = True$
 $\tau instr P h stk (New C) = False$
 $\tau instr P h stk (NewArray T) = False$
 $\tau instr P h stk ALoad = False$

```

|  $\tauinstr\ P\ h\ stk\ AStore = False$ 
|  $\tauinstr\ P\ h\ stk\ ALength = False$ 
|  $\tauinstr\ P\ h\ stk\ (Getfield\ F\ D) = False$ 
|  $\tauinstr\ P\ h\ stk\ (Putfield\ F\ D) = False$ 
|  $\tauinstr\ P\ h\ stk\ (CAS\ F\ D) = False$ 
|  $\tauinstr\ P\ h\ stk\ (Checkcast\ T) = True$ 
|  $\tauinstr\ P\ h\ stk\ (Instanceof\ T) = True$ 
|  $\tauinstr\ P\ h\ stk\ (Invoke\ M\ n) =$ 
  ( $n < length\ stk \wedge$ 
  ( $stk\ !\ n = Null \vee$ 
  ( $\forall T\ Ts\ Tr\ D.\ typeof\ addr\ h\ (the\ Addr\ (stk\ !\ n)) = [T] \longrightarrow P \vdash class\ type\ of\ T\ sees\ M:Ts \rightarrow Tr =$ 
  Native\ in\ D  $\longrightarrow \tauexternal\ defs\ D\ M)))$ )
|  $\tauinstr\ P\ h\ stk\ Return = True$ 
|  $\tauinstr\ P\ h\ stk\ Pop = True$ 
|  $\tauinstr\ P\ h\ stk\ Dup = True$ 
|  $\tauinstr\ P\ h\ stk\ Swap = True$ 
|  $\tauinstr\ P\ h\ stk\ (BinOpInstr\ bop) = True$ 
|  $\tauinstr\ P\ h\ stk\ (Goto\ i) = True$ 
|  $\tauinstr\ P\ h\ stk\ (IfFalse\ i) = True$ 
|  $\tauinstr\ P\ h\ stk\ ThrowExc = True$ 
|  $\tauinstr\ P\ h\ stk\ MEnter = False$ 
|  $\tauinstr\ P\ h\ stk\ MExit = False$ 

```

inductive $\tau move2 :: 'm\ prog \Rightarrow 'heap \Rightarrow 'addr\ val\ list \Rightarrow 'addr\ expr1 \Rightarrow nat \Rightarrow 'addr\ option \Rightarrow bool$
and $\tau moves2 :: 'm\ prog \Rightarrow 'heap \Rightarrow 'addr\ val\ list \Rightarrow 'addr\ expr1\ list \Rightarrow nat \Rightarrow 'addr\ option \Rightarrow bool$
for $P :: 'm\ prog$ **and** $h :: 'heap$ **and** $stk :: 'addr\ val\ list$

where

```

 $\tau move2xcp: pc < length\ (compE2\ e) \Longrightarrow \tau move2\ P\ h\ stk\ e\ pc\ [xcp]$ 

|  $\tau move2NewArray: \tau move2\ P\ h\ stk\ e\ pc\ xcp \Longrightarrow \tau move2\ P\ h\ stk\ (newA\ T[e])\ pc\ xcp$ 

|  $\tau move2Cast: \tau move2\ P\ h\ stk\ e\ pc\ xcp \Longrightarrow \tau move2\ P\ h\ stk\ (Cast\ T\ e)\ pc\ xcp$ 
|  $\tau move2CastRed: \tau move2\ P\ h\ stk\ (Cast\ T\ e)\ (length\ (compE2\ e))\ None$ 

|  $\tau move2InstanceOf: \tau move2\ P\ h\ stk\ e\ pc\ xcp \Longrightarrow \tau move2\ P\ h\ stk\ (e\ instanceof\ T)\ pc\ xcp$ 
|  $\tau move2InstanceOfRed: \tau move2\ P\ h\ stk\ (e\ instanceof\ T)\ (length\ (compE2\ e))\ None$ 

|  $\tau move2Val: \tau move2\ P\ h\ stk\ (Val\ v)\ 0\ None$ 

|  $\tau move2BinOp1:$ 
 $\tau move2\ P\ h\ stk\ e1\ pc\ xcp \Longrightarrow \tau move2\ P\ h\ stk\ (e1 \ll bop \gg e2)\ pc\ xcp$ 
|  $\tau move2BinOp2:$ 
 $\tau move2\ P\ h\ stk\ e2\ pc\ xcp \Longrightarrow \tau move2\ P\ h\ stk\ (e1 \ll bop \gg e2)\ (length\ (compE2\ e1) + pc)\ xcp$ 
|  $\tau move2BinOp:$ 
 $\tau move2\ P\ h\ stk\ (e1 \ll bop \gg e2)\ (length\ (compE2\ e1) + length\ (compE2\ e2))\ None$ 

|  $\tau move2Var:$ 
 $\tau move2\ P\ h\ stk\ (Var\ V)\ 0\ None$ 

|  $\tau move2LAss:$ 
 $\tau move2\ P\ h\ stk\ e\ pc\ xcp \Longrightarrow \tau move2\ P\ h\ stk\ (V := e)\ pc\ xcp$ 
|  $\tau move2LAssRed1:$ 
 $\tau move2\ P\ h\ stk\ (V := e)\ (length\ (compE2\ e))\ None$ 
|  $\tau move2LAssRed2: \tau move2\ P\ h\ stk\ (V := e)\ (Suc\ (length\ (compE2\ e)))\ None$ 

```

$\tau\text{move2AAcc1}: \tau\text{move2 } P \text{ h stk } a \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (a[i]) \text{ pc } xcp$
 $\tau\text{move2AAcc2}: \tau\text{move2 } P \text{ h stk } i \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (a[i]) \text{ (length (compE2 } a) + \text{pc) } xcp$

$\tau\text{move2AAss1}: \tau\text{move2 } P \text{ h stk } a \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (a[i] := e) \text{ pc } xcp$
 $\tau\text{move2AAss2}: \tau\text{move2 } P \text{ h stk } i \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (a[i] := e) \text{ (length (compE2 } a) + \text{pc) } xcp$
 $\tau\text{move2AAss3}: \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (a[i] := e) \text{ (length (compE2 } a) + \text{length (compE2 } i) + \text{pc) } xcp$
 $\tau\text{move2AAssRed}: \tau\text{move2 } P \text{ h stk } (a[i] := e) \text{ (Suc (length (compE2 } a) + \text{length (compE2 } i) + \text{length (compE2 } e))) \text{ None}$

$\tau\text{move2ALength}: \tau\text{move2 } P \text{ h stk } a \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (a \cdot \text{length}) \text{ pc } xcp$

$\tau\text{move2FAcc}: \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e \cdot F\{D\}) \text{ pc } xcp$

$\tau\text{move2FAss1}: \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e \cdot F\{D\} := e') \text{ pc } xcp$
 $\tau\text{move2FAss2}: \tau\text{move2 } P \text{ h stk } e' \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e \cdot F\{D\} := e') \text{ (length (compE2 } e) + \text{pc) } xcp$
 $\tau\text{move2FAssRed}: \tau\text{move2 } P \text{ h stk } (e \cdot F\{D\} := e') \text{ (Suc (length (compE2 } e) + \text{length (compE2 } e')) \text{ None}$

$\tau\text{move2CAS1}: \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) \text{ pc } xcp$
 $\tau\text{move2CAS2}: \tau\text{move2 } P \text{ h stk } e' \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) \text{ (length (compE2 } e) + \text{pc) } xcp$
 $\tau\text{move2CAS3}: \tau\text{move2 } P \text{ h stk } e'' \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) \text{ (length (compE2 } e) + \text{length (compE2 } e') + \text{pc) } xcp$

$\tau\text{move2CallObj}: \tau\text{move2 } P \text{ h stk } obj \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (obj \cdot M(ps)) \text{ pc } xcp$
 $\tau\text{move2CallParams}: \tau\text{moves2 } P \text{ h stk } ps \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (obj \cdot M(ps)) \text{ (length (compE2 } obj) + \text{pc) } xcp$
 $\tau\text{move2Call}: \llbracket \text{length } ps < \text{length } stk; \text{ stk ! length } ps = \text{Null} \vee (\forall T \text{ Ts Tr } D. \text{typeof-addr } h \text{ (the-Addr (stk ! length } ps)) = [T] \longrightarrow P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D \longrightarrow \tau\text{external-defs } D \text{ M}) \rrbracket \implies \tau\text{move2 } P \text{ h stk } (obj \cdot M(ps)) \text{ (length (compE2 } obj) + \text{length (compEs2 } ps)) \text{ None}$

$\tau\text{move2BlockSome1}: \tau\text{move2 } P \text{ h stk } \{V:T=[v]; e\} \text{ 0 None}$
 $\tau\text{move2BlockSome2}: \tau\text{move2 } P \text{ h stk } \{V:T=[v]; e\} \text{ (Suc 0) None}$
 $\tau\text{move2BlockSome}: \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } \{V:T=[v]; e\} \text{ (Suc (Suc pc)) } xcp$
 $\tau\text{move2BlockNone}: \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } \{V:T=None; e\} \text{ pc } xcp$

$\tau\text{move2Sync1}: \tau\text{move2 } P \text{ h stk } o' \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (\text{sync}_V(o') e) \text{ pc } xcp$
 $\tau\text{move2Sync2}: \tau\text{move2 } P \text{ h stk } (\text{sync}_V(o') e) \text{ (length (compE2 } o')) \text{ None}$
 $\tau\text{move2Sync3}: \tau\text{move2 } P \text{ h stk } (\text{sync}_V(o') e) \text{ (length (compE2 } o')) \text{ None}$

$\tau\text{move2 } P \text{ h stk } (\text{sync}_V (o') e) (\text{Suc } (\text{length } (\text{compE2 } o'))) \text{ None}$
| $\tau\text{move2Sync4:}$
 $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (\text{sync}_V (o') e) (\text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } o') + \text{pc})))) \text{ xcp}$
| $\tau\text{move2Sync5:}$
 $\tau\text{move2 } P \text{ h stk } (\text{sync}_V (o') e) (\text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } o') + \text{length } (\text{compE2 } e)))) \text{ None}$
| $\tau\text{move2Sync6:}$
 $\tau\text{move2 } P \text{ h stk } (\text{sync}_V (o') e) (5 + \text{length } (\text{compE2 } o') + \text{length } (\text{compE2 } e)) \text{ None}$
| $\tau\text{move2Sync7:}$
 $\tau\text{move2 } P \text{ h stk } (\text{sync}_V (o') e) (6 + \text{length } (\text{compE2 } o') + \text{length } (\text{compE2 } e)) \text{ None}$
| $\tau\text{move2Sync8:}$
 $\tau\text{move2 } P \text{ h stk } (\text{sync}_V (o') e) (8 + \text{length } (\text{compE2 } o') + \text{length } (\text{compE2 } e)) \text{ None}$

| $\tau\text{move2InSync: } \tau\text{move2 } P \text{ h stk } (\text{insync}_V (a) e) 0 \text{ None}$

| $\tau\text{move2Seq1:}$
 $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e;;e') \text{ pc } xcp$
| $\tau\text{move2SeqRed:}$
 $\tau\text{move2 } P \text{ h stk } (e;;e') (\text{length } (\text{compE2 } e)) \text{ None}$
| $\tau\text{move2Seq2:}$
 $\tau\text{move2 } P \text{ h stk } e' \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e;;e') (\text{Suc } (\text{length } (\text{compE2 } e) + \text{pc})) \text{ xcp}$

| $\tau\text{move2Cond:}$
 $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (\text{if } (e) e1 \text{ else } e2) \text{ pc } xcp$
| $\tau\text{move2CondRed:}$
 $\tau\text{move2 } P \text{ h stk } (\text{if } (e) e1 \text{ else } e2) (\text{length } (\text{compE2 } e)) \text{ None}$
| $\tau\text{move2CondThen:}$
 $\tau\text{move2 } P \text{ h stk } e1 \text{ pc } xcp$
 $\implies \tau\text{move2 } P \text{ h stk } (\text{if } (e) e1 \text{ else } e2) (\text{Suc } (\text{length } (\text{compE2 } e) + \text{pc})) \text{ xcp}$
| $\tau\text{move2CondThenExit:}$
 $\tau\text{move2 } P \text{ h stk } (\text{if } (e) e1 \text{ else } e2) (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } e1))) \text{ None}$
| $\tau\text{move2CondElse:}$
 $\tau\text{move2 } P \text{ h stk } e2 \text{ pc } xcp$
 $\implies \tau\text{move2 } P \text{ h stk } (\text{if } (e) e1 \text{ else } e2) (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } e1) + \text{pc}))) \text{ xcp}$

| $\tau\text{move2While1:}$
 $\tau\text{move2 } P \text{ h stk } c \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (\text{while } (c) e) \text{ pc } xcp$
| $\tau\text{move2While2:}$
 $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (\text{while } (c) e) (\text{Suc } (\text{length } (\text{compE2 } c) + \text{pc})) \text{ xcp}$
| $\tau\text{move2While3:}$ — Jump back to condition
 $\tau\text{move2 } P \text{ h stk } (\text{while } (c) e) (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e)))) \text{ None}$
| $\tau\text{move2While4:}$ — last instruction: Push Unit
 $\tau\text{move2 } P \text{ h stk } (\text{while } (c) e) (\text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e)))) \text{ None}$
| $\tau\text{move2While5:}$ — IfFalse instruction
 $\tau\text{move2 } P \text{ h stk } (\text{while } (c) e) (\text{length } (\text{compE2 } c)) \text{ None}$
| $\tau\text{move2While6:}$ — Pop instruction
 $\tau\text{move2 } P \text{ h stk } (\text{while } (c) e) (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e))) \text{ None}$

| $\tau\text{move2Throw1:}$
 $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (\text{throw } e) \text{ pc } xcp$
| $\tau\text{move2Throw2:}$
 $\tau\text{move2 } P \text{ h stk } (\text{throw } e) (\text{length } (\text{compE2 } e)) \text{ None}$

$\tau move2Try1:$
 $\tau move2 P h stk e pc xcp \implies \tau move2 P h stk (try e catch(C V) e') pc xcp$
 $\tau move2TryJump:$
 $\tau move2 P h stk (try e catch(C V) e') (length (compE2 e)) None$
 $\tau move2TryCatch2:$
 $\tau move2 P h stk (try e catch(C V) e') (Suc (length (compE2 e))) None$
 $\tau move2Try2:$
 $\tau move2 P h stk \{V:T=None; e'\} pc xcp$
 $\implies \tau move2 P h stk (try e catch(C V) e') (Suc (Suc (length (compE2 e) + pc))) xcp$
 $\tau moves2Hd:$
 $\tau move2 P h stk e pc xcp \implies \tau moves2 P h stk (e \# es) pc xcp$
 $\tau moves2Tl:$
 $\tau moves2 P h stk es pc xcp \implies \tau moves2 P h stk (e \# es) (length (compE2 e) + pc) xcp$

inductive-cases $\tau move2$ -cases:

$\tau move2 P h stk (new C) pc xcp$
 $\tau move2 P h stk (newA T[e]) pc xcp$
 $\tau move2 P h stk (Cast T e) pc xcp$
 $\tau move2 P h stk (e instanceof T) pc xcp$
 $\tau move2 P h stk (Val v) pc xcp$
 $\tau move2 P h stk (Var V) pc xcp$
 $\tau move2 P h stk (e1 \ll bop \gg e2) pc xcp$
 $\tau move2 P h stk (V := e) pc xcp$
 $\tau move2 P h stk (e1[e2]) pc xcp$
 $\tau move2 P h stk (e1[e2] := e3) pc xcp$
 $\tau move2 P h stk (e1 \cdot length) pc xcp$
 $\tau move2 P h stk (e1 \cdot F\{D\}) pc xcp$
 $\tau move2 P h stk (e1 \cdot F\{D\} := e3) pc xcp$
 $\tau move2 P h stk (e1 \cdot compareAndSwap(D, F, e2, e3)) pc xcp$
 $\tau move2 P h stk (e \cdot M(ps)) pc xcp$
 $\tau move2 P h stk \{V:T=vo; e\} pc xcp$
 $\tau move2 P h stk (sync_V (e1) e2) pc xcp$
 $\tau move2 P h stk (e1;;e2) pc xcp$
 $\tau move2 P h stk (if (e1) e2 else e3) pc xcp$
 $\tau move2 P h stk (while (e1) e2) pc xcp$
 $\tau move2 P h stk (try e1 catch(C V) e2) pc xcp$
 $\tau move2 P h stk (throw e) pc xcp$

lemma $\tau moves2xcp: pc < length (compEs2 es) \implies \tau moves2 P h stk es pc [xcp]$
 $\langle proof \rangle$

lemma $\tau move2$ -intros':

shows $\tau move2CastRed': pc = length (compE2 e) \implies \tau move2 P h stk (Cast T e) pc None$
and $\tau move2InstanceOfRed': pc = length (compE2 e) \implies \tau move2 P h stk (e instanceof T) pc None$
and $\tau move2BinOp2': \llbracket \tau move2 P h stk e2 pc xcp; pc' = length (compE2 e1) + pc \rrbracket \implies \tau move2 P h stk (e1 \ll bop \gg e2) pc' xcp$
and $\tau move2BinOp1': pc = length (compE2 e1) + length (compE2 e2) \implies \tau move2 P h stk (e1 \ll bop \gg e2) pc None$
and $\tau move2LAssRed1': pc = length (compE2 e) \implies \tau move2 P h stk (V:=e) pc None$
and $\tau move2LAssRed2': pc = Suc (length (compE2 e)) \implies \tau move2 P h stk (V:=e) pc None$
and $\tau move2AAcc2': \llbracket \tau move2 P h stk i pc xcp; pc' = length (compE2 a) + pc \rrbracket \implies \tau move2 P h stk (a[i]) pc' xcp$

and $\tau\text{move2AAss2}'$: $\llbracket \tau\text{move2 } P \text{ h stk } i \text{ pc } xcp; pc' = \text{length}(\text{compE2 } a) + pc \rrbracket \implies \tau\text{move2 } P \text{ h stk } (a[i] := e) \text{ pc}' \text{ xcp}$
and $\tau\text{move2AAss3}'$: $\llbracket \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp; pc' = \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc \rrbracket \implies \tau\text{move2 } P \text{ h stk } (a[i] := e) \text{ pc}' \text{ xcp}$
and $\tau\text{move2AAssRed}'$: $pc = \text{Suc}(\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e)) \implies \tau\text{move2 } P \text{ h stk } (a[i] := e) \text{ pc } \text{None}$
and $\tau\text{move2FAss2}'$: $\llbracket \tau\text{move2 } P \text{ h stk } e' \text{ pc } xcp; pc' = \text{length}(\text{compE2 } e) + pc \rrbracket \implies \tau\text{move2 } P \text{ h stk } (e \cdot F\{D\} := e') \text{ pc}' \text{ xcp}$
and $\tau\text{move2FAssRed}'$: $pc = \text{Suc}(\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e')) \implies \tau\text{move2 } P \text{ h stk } (e \cdot F\{D\} := e') \text{ pc } \text{None}$
and $\tau\text{move2CAS2}'$: $\llbracket \tau\text{move2 } P \text{ h stk } e2 \text{ pc } xcp; pc' = \text{length}(\text{compE2 } e1) + pc \rrbracket \implies \tau\text{move2 } P \text{ h stk } (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \text{ pc}' \text{ xcp}$
and $\tau\text{move2CAS3}'$: $\llbracket \tau\text{move2 } P \text{ h stk } e3 \text{ pc } xcp; pc' = \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) + pc \rrbracket \implies \tau\text{move2 } P \text{ h stk } (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \text{ pc}' \text{ xcp}$
and $\tau\text{move2CallParams}'$: $\llbracket \tau\text{moves2 } P \text{ h stk } ps \text{ pc } xcp; pc' = \text{length}(\text{compE2 } obj) + pc \rrbracket \implies \tau\text{move2 } P \text{ h stk } (obj \cdot M(ps)) \text{ pc}' \text{ xcp}$
and $\tau\text{move2Call}'$: $\llbracket pc = \text{length}(\text{compE2 } obj) + \text{length}(\text{compEs2 } ps); \text{length } ps < \text{length } stk;$
 $stk ! \text{length } ps = \text{Null} \vee$
 $(\forall T \text{ Ts Tr } D. \text{typeof-addr } h \text{ (the-Addr } (stk ! \text{length } ps)) = [T] \longrightarrow P \vdash \text{class-type-of}$
 $T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D \longrightarrow \tau\text{external-defs } D \text{ M}) \rrbracket$
 $\implies \tau\text{move2 } P \text{ h stk } (obj \cdot M(ps)) \text{ pc } \text{None}$
and $\tau\text{move2BlockSome2}$: $pc = \text{Suc } 0 \implies \tau\text{move2 } P \text{ h stk } \{V:T=[v]; e\} \text{ pc } \text{None}$
and $\tau\text{move2BlockSome}'$: $\llbracket \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp; pc' = \text{Suc}(\text{Suc } pc) \rrbracket \implies \tau\text{move2 } P \text{ h stk } \{V:T=[v]; e\} \text{ pc}' \text{ xcp}$
and $\tau\text{move2Sync2}'$: $pc = \text{length}(\text{compE2 } o') \implies \tau\text{move2 } P \text{ h stk } (\text{sync}_V(o') e) \text{ pc } \text{None}$
and $\tau\text{move2Sync3}'$: $pc = \text{Suc}(\text{length}(\text{compE2 } o')) \implies \tau\text{move2 } P \text{ h stk } (\text{sync}_V(o') e) \text{ pc } \text{None}$
and $\tau\text{move2Sync4}'$: $\llbracket \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp; pc' = \text{Suc}(\text{Suc}(\text{Suc}(\text{length}(\text{compE2 } o') + pc))) \rrbracket \implies \tau\text{move2 } P \text{ h stk } (\text{sync}_V(o') e) \text{ pc}' \text{ xcp}$
and $\tau\text{move2Sync5}'$: $pc = \text{Suc}(\text{Suc}(\text{Suc}(\text{length}(\text{compE2 } o') + \text{length}(\text{compE2 } e)))) \implies \tau\text{move2 } P \text{ h stk } (\text{sync}_V(o') e) \text{ pc } \text{None}$
and $\tau\text{move2Sync6}'$: $pc = 5 + \text{length}(\text{compE2 } o') + \text{length}(\text{compE2 } e) \implies \tau\text{move2 } P \text{ h stk } (\text{sync}_V(o') e) \text{ pc } \text{None}$
and $\tau\text{move2Sync7}'$: $pc = 6 + \text{length}(\text{compE2 } o') + \text{length}(\text{compE2 } e) \implies \tau\text{move2 } P \text{ h stk } (\text{sync}_V(o') e) \text{ pc } \text{None}$
and $\tau\text{move2Sync8}'$: $pc = 8 + \text{length}(\text{compE2 } o') + \text{length}(\text{compE2 } e) \implies \tau\text{move2 } P \text{ h stk } (\text{sync}_V(o') e) \text{ pc } \text{None}$
and $\tau\text{move2SeqRed}'$: $pc = \text{length}(\text{compE2 } e) \implies \tau\text{move2 } P \text{ h stk } (e;;e') \text{ pc } \text{None}$
and $\tau\text{move2Seq2}'$: $\llbracket \tau\text{move2 } P \text{ h stk } e' \text{ pc } xcp; pc' = \text{Suc}(\text{length}(\text{compE2 } e) + pc) \rrbracket \implies \tau\text{move2 } P \text{ h stk } (e;;e') \text{ pc}' \text{ xcp}$
and $\tau\text{move2CondRed}'$: $pc = \text{length}(\text{compE2 } e) \implies \tau\text{move2 } P \text{ h stk } (\text{if } (e) \text{ } e1 \text{ else } e2) \text{ pc } \text{None}$
and $\tau\text{move2CondThen}'$: $\llbracket \tau\text{move2 } P \text{ h stk } e1 \text{ pc } xcp; pc' = \text{Suc}(\text{length}(\text{compE2 } e) + pc) \rrbracket \implies \tau\text{move2 } P \text{ h stk } (\text{if } (e) \text{ } e1 \text{ else } e2) \text{ pc}' \text{ xcp}$
and $\tau\text{move2CondThenExit}'$: $pc = \text{Suc}(\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e1)) \implies \tau\text{move2 } P \text{ h stk } (\text{if } (e) \text{ } e1 \text{ else } e2) \text{ pc } \text{None}$
and $\tau\text{move2CondElse}'$: $\llbracket \tau\text{move2 } P \text{ h stk } e2 \text{ pc } xcp; pc' = \text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e1) + pc)) \rrbracket \implies \tau\text{move2 } P \text{ h stk } (\text{if } (e) \text{ } e1 \text{ else } e2) \text{ pc}' \text{ xcp}$
and $\tau\text{move2While2}'$: $\llbracket \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp; pc' = \text{Suc}(\text{length}(\text{compE2 } c) + pc) \rrbracket \implies \tau\text{move2 } P \text{ h stk } (\text{while } (c) \text{ } e) \text{ pc}' \text{ xcp}$
and $\tau\text{move2While3}'$: $pc = \text{Suc}(\text{Suc}(\text{length}(\text{compE2 } c) + \text{length}(\text{compE2 } e))) \implies \tau\text{move2 } P \text{ h stk } (\text{while } (c) \text{ } e) \text{ pc } \text{None}$
and $\tau\text{move2While4}'$: $pc = \text{Suc}(\text{Suc}(\text{Suc}(\text{length}(\text{compE2 } c) + \text{length}(\text{compE2 } e)))) \implies \tau\text{move2 } P \text{ h stk } (\text{while } (c) \text{ } e) \text{ pc } \text{None}$
and $\tau\text{move2While5}'$: $pc = \text{length}(\text{compE2 } c) \implies \tau\text{move2 } P \text{ h stk } (\text{while } (c) \text{ } e) \text{ pc } \text{None}$

and $\tau\text{move2While6}'$: $pc = \text{Suc} (\text{length} (\text{compE2 } c) + \text{length} (\text{compE2 } e)) \Longrightarrow \tau\text{move2 } P \text{ h stk } (\text{while } (c) \ e) \ pc \ \text{None}$
and $\tau\text{move2Throw2}'$: $pc = \text{length} (\text{compE2 } e) \Longrightarrow \tau\text{move2 } P \text{ h stk } (\text{throw } e) \ pc \ \text{None}$
and $\tau\text{move2TryJump}'$: $pc = \text{length} (\text{compE2 } e) \Longrightarrow \tau\text{move2 } P \text{ h stk } (\text{try } e \ \text{catch}(C \ V) \ e') \ pc \ \text{None}$
and $\tau\text{move2TryCatch2}'$: $pc = \text{Suc} (\text{length} (\text{compE2 } e)) \Longrightarrow \tau\text{move2 } P \text{ h stk } (\text{try } e \ \text{catch}(C \ V) \ e') \ pc \ \text{None}$
and $\tau\text{move2Try2}'$: $\llbracket \tau\text{move2 } P \text{ h stk } \{V:T=\text{None}; e'\} \ pc \ xcp; \ pc' = \text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e) + pc)) \rrbracket$
 $\Longrightarrow \tau\text{move2 } P \text{ h stk } (\text{try } e \ \text{catch}(C \ V) \ e') \ pc' \ xcp$
and $\tau\text{moves2Tl}'$: $\llbracket \tau\text{moves2 } P \text{ h stk } es \ pc \ xcp; \ pc' = \text{length} (\text{compE2 } e) + pc \rrbracket \Longrightarrow \tau\text{moves2 } P \text{ h stk } (e \ \# \ es) \ pc' \ xcp$
 $\langle \text{proof} \rangle$

lemma $\tau\text{move2-iff}$: $\tau\text{move2 } P \text{ h stk } e \ pc \ xcp \longleftrightarrow pc < \text{length} (\text{compE2 } e) \wedge (xcp = \text{None} \longrightarrow \tau\text{instr } P \text{ h stk } (\text{compE2 } e \ ! \ pc))$ (**is** $?lhs1 \longleftrightarrow ?rhs1$)
and $\tau\text{moves2-iff}$: $\tau\text{moves2 } P \text{ h stk } es \ pc \ xcp \longleftrightarrow pc < \text{length} (\text{compEs2 } es) \wedge (xcp = \text{None} \longrightarrow \tau\text{instr } P \text{ h stk } (\text{compEs2 } es \ ! \ pc))$ (**is** $?lhs2 \longleftrightarrow ?rhs2$)
 $\langle \text{proof} \rangle$

lemma $\tau\text{move2-pc-length-compE2}$: $\tau\text{move2 } P \text{ h stk } e \ pc \ xcp \Longrightarrow pc < \text{length} (\text{compE2 } e)$
and $\tau\text{moves2-pc-length-compEs2}$: $\tau\text{moves2 } P \text{ h stk } es \ pc \ xcp \Longrightarrow pc < \text{length} (\text{compEs2 } es)$
 $\langle \text{proof} \rangle$

lemma $\tau\text{move2-pc-length-compE2-conv}$: $pc \geq \text{length} (\text{compE2 } e) \Longrightarrow \neg \tau\text{move2 } P \text{ h stk } e \ pc \ xcp$
 $\langle \text{proof} \rangle$

lemma $\tau\text{moves2-pc-length-compEs2-conv}$: $pc \geq \text{length} (\text{compEs2 } es) \Longrightarrow \neg \tau\text{moves2 } P \text{ h stk } es \ pc \ xcp$
 $\langle \text{proof} \rangle$

lemma $\tau\text{moves2-append}$ [*elim*]:
 $\tau\text{moves2 } P \text{ h stk } es \ pc \ xcp \Longrightarrow \tau\text{moves2 } P \text{ h stk } (es \ @ \ es') \ pc \ xcp$
 $\langle \text{proof} \rangle$

lemma $\text{append-}\tau\text{moves2}$:
 $\tau\text{moves2 } P \text{ h stk } es \ pc \ xcp \Longrightarrow \tau\text{moves2 } P \text{ h stk } (es' \ @ \ es) (\text{length} (\text{compEs2 } es') + pc) \ xcp$
 $\langle \text{proof} \rangle$

lemma [*dest*]:
shows $\tau\text{move2-NewArrayD}$: $\llbracket \tau\text{move2 } P \text{ h stk } (\text{newA } T[e]) \ pc \ xcp; \ pc < \text{length} (\text{compE2 } e) \rrbracket \Longrightarrow \tau\text{move2 } P \text{ h stk } e \ pc \ xcp$
and $\tau\text{move2-CastD}$: $\llbracket \tau\text{move2 } P \text{ h stk } (\text{Cast } T \ e) \ pc \ xcp; \ pc < \text{length} (\text{compE2 } e) \rrbracket \Longrightarrow \tau\text{move2 } P \text{ h stk } e \ pc \ xcp$
and $\tau\text{move2-InstanceOfD}$: $\llbracket \tau\text{move2 } P \text{ h stk } (e \ \text{instanceof } T) \ pc \ xcp; \ pc < \text{length} (\text{compE2 } e) \rrbracket \Longrightarrow \tau\text{move2 } P \text{ h stk } e \ pc \ xcp$
and $\tau\text{move2-BinOp1D}$: $\llbracket \tau\text{move2 } P \text{ h stk } (e1 \ \llbracket \text{bop} \rrbracket \ e2) \ pc' \ xcp'; \ pc' < \text{length} (\text{compE2 } e1) \rrbracket \Longrightarrow \tau\text{move2 } P \text{ h stk } e1 \ pc' \ xcp'$
and $\tau\text{move2-BinOp2D}$:
 $\llbracket \tau\text{move2 } P \text{ h stk } (e1 \ \llbracket \text{bop} \rrbracket \ e2) (\text{length} (\text{compE2 } e1) + pc') \ xcp'; \ pc' < \text{length} (\text{compE2 } e2) \rrbracket \Longrightarrow \tau\text{move2 } P \text{ h stk } e2 \ pc' \ xcp'$
and $\tau\text{move2-LAssD}$: $\llbracket \tau\text{move2 } P \text{ h stk } (V := e) \ pc \ xcp; \ pc < \text{length} (\text{compE2 } e) \rrbracket \Longrightarrow \tau\text{move2 } P \text{ h stk } e \ pc \ xcp$
and $\tau\text{move2-AAccD1}$: $\llbracket \tau\text{move2 } P \text{ h stk } (a[i]) \ pc \ xcp; \ pc < \text{length} (\text{compE2 } a) \rrbracket \Longrightarrow \tau\text{move2 } P \text{ h stk } a \ pc \ xcp$
and $\tau\text{move2-AAccD2}$: $\llbracket \tau\text{move2 } P \text{ h stk } (a[i]) (\text{length} (\text{compE2 } a) + pc) \ xcp; \ pc < \text{length} (\text{compE2 } a) \rrbracket \Longrightarrow \tau\text{move2 } P \text{ h stk } a \ pc \ xcp$

$i) \]] \implies \tau\text{move2 } P \text{ h stk } i \text{ pc } xcp$
and $\tau\text{move2-AAssD1}$: $\llbracket \tau\text{move2 } P \text{ h stk } (a[i] := e) \text{ pc } xcp; \text{ pc} < \text{length} (\text{compE2 } a) \rrbracket \implies \tau\text{move2 } P \text{ h stk } a \text{ pc } xcp$
and $\tau\text{move2-AAssD2}$: $\llbracket \tau\text{move2 } P \text{ h stk } (a[i] := e) (\text{length} (\text{compE2 } a) + \text{pc}) \text{ xcp}; \text{ pc} < \text{length} (\text{compE2 } i) \rrbracket \implies \tau\text{move2 } P \text{ h stk } i \text{ pc } xcp$
and $\tau\text{move2-AAssD3}$:
 $\llbracket \tau\text{move2 } P \text{ h stk } (a[i] := e) (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{pc}) \text{ xcp}; \text{ pc} < \text{length} (\text{compE2 } e) \rrbracket \implies \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp$
and $\tau\text{move2-ALengthD}$: $\llbracket \tau\text{move2 } P \text{ h stk } (a.\text{length}) \text{ pc } xcp; \text{ pc} < \text{length} (\text{compE2 } a) \rrbracket \implies \tau\text{move2 } P \text{ h stk } a \text{ pc } xcp$
and $\tau\text{move2-FAccD}$: $\llbracket \tau\text{move2 } P \text{ h stk } (e.F\{D\}) \text{ pc } xcp; \text{ pc} < \text{length} (\text{compE2 } e) \rrbracket \implies \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp$
and $\tau\text{move2-FAssD1}$: $\llbracket \tau\text{move2 } P \text{ h stk } (e.F\{D\} := e') \text{ pc } xcp; \text{ pc} < \text{length} (\text{compE2 } e) \rrbracket \implies \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp$
and $\tau\text{move2-FAssD2}$: $\llbracket \tau\text{move2 } P \text{ h stk } (e.F\{D\} := e') (\text{length} (\text{compE2 } e) + \text{pc}) \text{ xcp}; \text{ pc} < \text{length} (\text{compE2 } e') \rrbracket \implies \tau\text{move2 } P \text{ h stk } e' \text{ pc } xcp$
and $\tau\text{move2-CASD1}$: $\llbracket \tau\text{move2 } P \text{ h stk } (e1.\text{compareAndSwap}(D.F, e2, e3)) \text{ pc } xcp; \text{ pc} < \text{length} (\text{compE2 } e1) \rrbracket \implies \tau\text{move2 } P \text{ h stk } e1 \text{ pc } xcp$
and $\tau\text{move2-CASD2}$: $\llbracket \tau\text{move2 } P \text{ h stk } (e1.\text{compareAndSwap}(D.F, e2, e3)) (\text{length} (\text{compE2 } e1) + \text{pc}) \text{ xcp}; \text{ pc} < \text{length} (\text{compE2 } e2) \rrbracket \implies \tau\text{move2 } P \text{ h stk } e2 \text{ pc } xcp$
and $\tau\text{move2-CASD3}$:
 $\llbracket \tau\text{move2 } P \text{ h stk } (e1.\text{compareAndSwap}(D.F, e2, e3)) (\text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + \text{pc}) \text{ xcp}; \text{ pc} < \text{length} (\text{compE2 } e3) \rrbracket \implies \tau\text{move2 } P \text{ h stk } e3 \text{ pc } xcp$
and $\tau\text{move2-CallObjD}$: $\llbracket \tau\text{move2 } P \text{ h stk } (e.M(es)) \text{ pc } xcp; \text{ pc} < \text{length} (\text{compE2 } e) \rrbracket \implies \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp$
and $\tau\text{move2-BlockNoneD}$: $\tau\text{move2 } P \text{ h stk } \{V:T=None; e\} \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp$
and $\tau\text{move2-BlockSomeD}$: $\tau\text{move2 } P \text{ h stk } \{V:T=[v]; e\} (\text{Suc } (\text{Suc } \text{pc})) \text{ xcp} \implies \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp$
and $\tau\text{move2-sync1D}$: $\llbracket \tau\text{move2 } P \text{ h stk } (\text{sync } V (o') e) \text{ pc } xcp; \text{ pc} < \text{length} (\text{compE2 } o') \rrbracket \implies \tau\text{move2 } P \text{ h stk } o' \text{ pc } xcp$
and $\tau\text{move2-sync2D}$:
 $\llbracket \tau\text{move2 } P \text{ h stk } (\text{sync } V (o') e) (\text{Suc } (\text{Suc } (\text{Suc } (\text{length} (\text{compE2 } o') + \text{pc})))) \text{ xcp}; \text{ pc} < \text{length} (\text{compE2 } e) \rrbracket \implies \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp$
and $\tau\text{move2-Seq1D}$: $\llbracket \tau\text{move2 } P \text{ h stk } (e1;; e2) \text{ pc } xcp; \text{ pc} < \text{length} (\text{compE2 } e1) \rrbracket \implies \tau\text{move2 } P \text{ h stk } e1 \text{ pc } xcp$
and $\tau\text{move2-Seq2D}$: $\tau\text{move2 } P \text{ h stk } (e1;; e2) (\text{Suc } (\text{length} (\text{compE2 } e1) + \text{pc}')) \text{ xcp}' \implies \tau\text{move2 } P \text{ h stk } e2 \text{ pc}' \text{ xcp}'$
and $\tau\text{move2-IfCondD}$: $\llbracket \tau\text{move2 } P \text{ h stk } (\text{if } (e) e1 \text{ else } e2) \text{ pc } xcp; \text{ pc} < \text{length} (\text{compE2 } e) \rrbracket \implies \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp$
and $\tau\text{move2-IfThenD}$:
 $\llbracket \tau\text{move2 } P \text{ h stk } (\text{if } (e) e1 \text{ else } e2) (\text{Suc } (\text{length} (\text{compE2 } e) + \text{pc}')) \text{ xcp}'; \text{ pc}' < \text{length} (\text{compE2 } e1) \rrbracket \implies \tau\text{move2 } P \text{ h stk } e1 \text{ pc}' \text{ xcp}'$
and $\tau\text{move2-IfElseD}$:
 $\tau\text{move2 } P \text{ h stk } (\text{if } (e) e1 \text{ else } e2) (\text{Suc } (\text{Suc } (\text{length} (\text{compE2 } e) + \text{length} (\text{compE2 } e1) + \text{pc}')) \text{ xcp}' \implies \tau\text{move2 } P \text{ h stk } e2 \text{ pc}' \text{ xcp}'$
and $\tau\text{move2-WhileCondD}$: $\llbracket \tau\text{move2 } P \text{ h stk } (\text{while } (c) b) \text{ pc } xcp; \text{ pc} < \text{length} (\text{compE2 } c) \rrbracket \implies \tau\text{move2 } P \text{ h stk } c \text{ pc } xcp$
and $\tau\text{move2-ThrowD}$: $\llbracket \tau\text{move2 } P \text{ h stk } (\text{throw } e) \text{ pc } xcp; \text{ pc} < \text{length} (\text{compE2 } e) \rrbracket \implies \tau\text{move2 } P \text{ h stk } e \text{ pc } xcp$
and $\tau\text{move2-Try1D}$: $\llbracket \tau\text{move2 } P \text{ h stk } (\text{try } e1 \text{ catch } (C' V) e2) \text{ pc } xcp; \text{ pc} < \text{length} (\text{compE2 } e1) \rrbracket \implies \tau\text{move2 } P \text{ h stk } e1 \text{ pc } xcp$
 $\langle \text{proof} \rangle$

lemma $\tau\text{move2-Invoke}$:

$\llbracket \tau \text{move2 } P \ h \ stk \ e \ pc \ None; \text{compE2 } e \ ! \ pc = \text{Invoke } M \ n \rrbracket$
 $\implies n < \text{length } stk \wedge (stk \ ! \ n = \text{Null} \vee (\forall T \ Ts \ Tr \ D. \text{typeof-addr } h \ (\text{the-Addr } (stk \ ! \ n)) = \lfloor T \rfloor \longrightarrow$
 $P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D \longrightarrow \tau \text{external-defs } D \ M))$

and $\tau \text{moves2-Invoke}$:

$\llbracket \tau \text{moves2 } P \ h \ stk \ es \ pc \ None; \text{compEs2 } es \ ! \ pc = \text{Invoke } M \ n \rrbracket$
 $\implies n < \text{length } stk \wedge (stk \ ! \ n = \text{Null} \vee (\forall T \ C \ Ts \ Tr \ D. \text{typeof-addr } h \ (\text{the-Addr } (stk \ ! \ n)) = \lfloor T \rfloor$
 $\longrightarrow P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D \longrightarrow \tau \text{external-defs } D \ M))$
 $\langle \text{proof} \rangle$

lemmas $\tau \text{move2-compE2-not-Invoke} = \tau \text{move2-Invoke}$

lemmas $\tau \text{moves2-compEs2-not-Invoke} = \tau \text{moves2-Invoke}$

lemma $\tau \text{move2-blocks1}$ [simp]:

$\tau \text{move2 } P \ h \ stk \ (\text{blocks1 } n \ Ts \ \text{body}) \ pc' \ xcp' = \tau \text{move2 } P \ h \ stk \ \text{body} \ pc' \ xcp'$
 $\langle \text{proof} \rangle$

lemma $\tau \text{instr-stk-append}$:

$\tau \text{instr } P \ h \ stk \ i \implies \tau \text{instr } P \ h \ (stk \ @ \ vs) \ i$
 $\langle \text{proof} \rangle$

lemma $\tau \text{move2-stk-append}$:

$\tau \text{move2 } P \ h \ stk \ e \ pc \ xcp \implies \tau \text{move2 } P \ h \ (stk \ @ \ vs) \ e \ pc \ xcp$
 $\langle \text{proof} \rangle$

lemma $\tau \text{moves2-stk-append}$:

$\tau \text{moves2 } P \ h \ stk \ es \ pc \ xcp \implies \tau \text{moves2 } P \ h \ (stk \ @ \ vs) \ es \ pc \ xcp$
 $\langle \text{proof} \rangle$

fun $\tau \text{Move2} :: 'addr \ \text{jvm-prog} \Rightarrow ('addr, 'heap) \ \text{jvm-state} \Rightarrow \text{bool}$

where

$\tau \text{Move2 } P \ (xcp, h, []) = \text{False}$
 $| \tau \text{Move2 } P \ (xcp, h, (stk, loc, C, M, pc) \ # \ \text{frs}) =$
 $\quad (\text{let } (-,-, \text{meth}) = \text{method } P \ C \ M; (-,-, \text{ins}, xt) = \text{the meth}$
 $\quad \text{in } (pc < \text{length } \text{ins} \wedge (xcp = \text{None} \longrightarrow \tau \text{instr } P \ h \ stk \ (\text{ins} \ ! \ pc))))$

lemma $\tau \text{Move2-iff}$:

$\tau \text{Move2 } P \ \sigma = (\text{let } (xcp, h, \text{frs}) = \sigma$
 $\quad \text{in case frs of } [] \Rightarrow \text{False}$
 $\quad | (stk, loc, C, M, pc) \ # \ \text{frs}' \Rightarrow$
 $\quad (\text{let } (-,-, \text{meth}) = \text{method } P \ C \ M; (-,-, \text{ins}, xt) = \text{the meth}$
 $\quad \text{in } (pc < \text{length } \text{ins} \wedge (xcp = \text{None} \longrightarrow \tau \text{instr } P \ h \ stk \ (\text{ins} \ ! \ pc))))$
 $\langle \text{proof} \rangle$

lemma $\tau \text{instr-compP}$ [simp]: $\tau \text{instr } (\text{compP } f \ P) \ h \ stk \ i \longleftrightarrow \tau \text{instr } P \ h \ stk \ i$

$\langle \text{proof} \rangle$

lemma [simp]: **fixes** $e :: 'addr \ \text{expr1}$ **and** $es :: 'addr \ \text{expr1 list}$

shows $\tau \text{move2-compP}: \tau \text{move2 } (\text{compP } f \ P) \ h \ stk \ e = \tau \text{move2 } P \ h \ stk \ e$
and $\tau \text{moves2-compP}: \tau \text{moves2 } (\text{compP } f \ P) \ h \ stk \ es = \tau \text{moves2 } P \ h \ stk \ es$
 $\langle \text{proof} \rangle$

lemma $\tau \text{Move2-compP2}$:

$P \vdash C \ \text{sees } M:Ts \rightarrow T = \lfloor \text{body} \rfloor \ \text{in } D \implies$

$\tau\text{Move2 } (\text{compP2 } P) (xcp, h, (\text{stk}, \text{loc}, C, M, pc) \# \text{frs}) =$
 $(\text{case } xcp \text{ of } \text{None} \Rightarrow \tau\text{move2 } P \ h \ \text{stk} \ \text{body} \ pc \ xcp \ \vee \ pc = \text{length } (\text{compE2 } \text{body}) \mid \text{Some } a \Rightarrow pc <$
 $\text{Suc } (\text{length } (\text{compE2 } \text{body})))$
 $\langle \text{proof} \rangle$

abbreviation $\tau\text{MOVE2} ::$

$'\text{addr } \text{jvm-prog} \Rightarrow (('\text{addr } \text{option} \times '\text{addr } \text{frame list}) \times '\text{heap}, ('\text{addr}, '\text{thread-id}, '\text{heap}) \text{jvm-thread-action})$
 trsys

where $\tau\text{MOVE2 } P \equiv \lambda((xcp, \text{frs}), h) \text{ ta s. } \tau\text{Move2 } P (xcp, h, \text{frs}) \wedge \text{ta} = \varepsilon$

lemma $\tau\text{jvmd-heap-unchanged}$:

$\llbracket P, t \vdash \text{Normal } (xcp, h, \text{frs}) \text{ --}\varepsilon\text{--jvmd} \rightarrow \text{Normal } (xcp', h', \text{frs}'); \tau\text{Move2 } P (xcp, h, \text{frs}) \rrbracket$
 $\implies h = h'$
 $\langle \text{proof} \rangle$

lemma $\text{mexecd-}\tau\text{mthr-wf}$:

$\tau\text{multithreaded-wf } \text{JVM-final } (\text{mexecd } P) (\tau\text{MOVE2 } P)$
 $\langle \text{proof} \rangle$

end

sublocale $\text{JVM-heap-base} < \text{execd-mthr}$:

$\tau\text{multithreaded-wf}$
 JVM-final
 $\text{mexecd } P$
 convert-RA
 $\tau\text{MOVE2 } P$

for P

$\langle \text{proof} \rangle$

context JVM-heap-base **begin**

lemma $\tau\text{exec-1-taD}$:

assumes $\text{exec: } \text{exec-1-d } P \ t \ (\text{Normal } (xcp, h, \text{frs})) \ \text{ta} \ (\text{Normal } (xcp', h', \text{frs}'))$
and $\tau: \tau\text{Move2 } P (xcp, h, \text{frs})$
shows $\text{ta} = \varepsilon$

$\langle \text{proof} \rangle$

end

end

7.15 JVM Semantics for the delay bisimulation proof from intermediate language to byte code

theory Execs **imports** JVMTau **begin**

declare $\text{match-ex-table-app}$ $[\text{simp del}]$

$\text{match-ex-table-eq-NoneI}$ $[\text{simp del}]$

compxE2-size-conv $[\text{simp del}]$

$\text{compxE2-stack-xlift-conv}$ $[\text{simp del}]$

$\text{compxEs2-stack-xlift-conv}$ $[\text{simp del}]$

type-synonym

$(\text{'addr}, \text{'heap}) \text{check-instr}' =$
 $\text{'addr instr} \Rightarrow \text{'addr jvm-prog} \Rightarrow \text{'heap} \Rightarrow \text{'addr val list} \Rightarrow \text{'addr val list} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{pc}$
 $\Rightarrow \text{'addr frame list} \Rightarrow \text{bool}$

primrec $\text{check-instr}' :: (\text{'addr}, \text{'heap}) \text{check-instr}'$

where

$\text{check-instr}'\text{-Load}:$

$\text{check-instr}' (\text{Load } n) P h \text{ stk } \text{loc } C M_0 \text{ pc } \text{frs} =$
 True

| $\text{check-instr}'\text{-Store}:$

$\text{check-instr}' (\text{Store } n) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$
 $(0 < \text{length } \text{stk})$

| $\text{check-instr}'\text{-Push}:$

$\text{check-instr}' (\text{Push } v) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$
 True

| $\text{check-instr}'\text{-New}:$

$\text{check-instr}' (\text{New } C) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$
 True

| $\text{check-instr}'\text{-NewArray}:$

$\text{check-instr}' (\text{NewArray } T) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$
 $(0 < \text{length } \text{stk})$

| $\text{check-instr}'\text{-ALoad}:$

$\text{check-instr}' \text{ALoad } P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$
 $(1 < \text{length } \text{stk})$

| $\text{check-instr}'\text{-AStore}:$

$\text{check-instr}' \text{AStore } P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$
 $(2 < \text{length } \text{stk})$

| $\text{check-instr}'\text{-ALength}:$

$\text{check-instr}' \text{ALength } P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$
 $(0 < \text{length } \text{stk})$

| $\text{check-instr}'\text{-Getfield}:$

$\text{check-instr}' (\text{Getfield } F C) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$
 $(0 < \text{length } \text{stk})$

| $\text{check-instr}'\text{-Putfield}:$

$\text{check-instr}' (\text{Putfield } F C) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$
 $(1 < \text{length } \text{stk})$

| $\text{check-instr}'\text{-CAS}:$

$\text{check-instr}' (\text{CAS } F C) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$
 $(2 < \text{length } \text{stk})$

| $\text{check-instr}'\text{-Checkcast}:$

$\text{check-instr}' (\text{Checkcast } T) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$
 $(0 < \text{length } \text{stk})$

- | *check-instr'-Instanceof*:
 $check_instr' (Instanceof\ T)\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(0 < length\ stk)$
- | *check-instr'-Invoke*:
 $check_instr' (Invoke\ M\ n)\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(n < length\ stk)$
- | *check-instr'-Return*:
 $check_instr' Return\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(0 < length\ stk)$
- | *check-instr'-Pop*:
 $check_instr' Pop\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(0 < length\ stk)$
- | *check-instr'-Dup*:
 $check_instr' Dup\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(0 < length\ stk)$
- | *check-instr'-Swap*:
 $check_instr' Swap\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(1 < length\ stk)$
- | *check-instr'-BinOpInstr*:
 $check_instr' (BinOpInstr\ bop)\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(1 < length\ stk)$
- | *check-instr'-IfFalse*:
 $check_instr' (IfFalse\ b)\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(0 < length\ stk \wedge 0 \leq int\ pc+b)$
- | *check-instr'-Goto*:
 $check_instr' (Goto\ b)\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(0 \leq int\ pc+b)$
- | *check-instr'-Throw*:
 $check_instr' ThrowExc\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(0 < length\ stk)$
- | *check-instr'-MEnter*:
 $check_instr' MEnter\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(0 < length\ stk)$
- | *check-instr'-MExit*:
 $check_instr' MExit\ P\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(0 < length\ stk)$

definition *ci-stk-offer* :: ('addr, 'heap) *check-instr'* \Rightarrow bool

where

ci-stk-offer *ci* =
 $(\forall ins\ P\ h\ stk\ stk'\ loc\ C\ M\ pc\ frs.\ ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs \longrightarrow ci\ ins\ P\ h\ (stk\ @\ stk')\ loc\ C\ M\ pc\ frs)$

lemma *ci-stk-offerI*:

$(\bigwedge ins P h stk stk' loc C M pc frs. ci ins P h stk loc C M pc frs \implies ci ins P h (stk @ stk') loc C M pc frs) \implies ci-stk-offer ci$
 $\langle proof \rangle$

lemma *ci-stk-offerD*:

$\llbracket ci-stk-offer ci; ci ins P h stk loc C M pc frs \rrbracket \implies ci ins P h (stk @ stk') loc C M pc frs$
 $\langle proof \rangle$

lemma *check-instr'-stk-offer*:

$ci-stk-offer check-instr'$
 $\langle proof \rangle$

context *JVM-heap-base* **begin**

lemma *check-instr-imp-check-instr'*:

$check-instr ins P h stk loc C M pc frs \implies check-instr' ins P h stk loc C M pc frs$
 $\langle proof \rangle$

lemma *check-instr-stk-offer*:

$ci-stk-offer check-instr$
 $\langle proof \rangle$

end

primrec *jump-ok* :: 'addr instr list \Rightarrow nat \Rightarrow nat \Rightarrow bool

where *jump-ok* [] n n' = True

| *jump-ok* (x # xs) n n' = (*jump-ok* xs (Suc n) n' \wedge
 (case x of *IfFalse* m \Rightarrow \neg int n \leq m \wedge m \leq int (n' + length xs)
 | *Goto* m \Rightarrow \neg int n \leq m \wedge m \leq int (n' + length xs)
 | - \Rightarrow True))

lemma *jump-ok-append* [*simp*]:

$jump-ok (xs @ xs') n n' \longleftrightarrow jump-ok xs n (n' + length xs') \wedge jump-ok xs' (n + length xs) n'$
 $\langle proof \rangle$

lemma *jump-ok-GotoD*:

$\llbracket jump-ok ins n n'; ins ! pc = Goto m; pc < length ins \rrbracket \implies \neg int (pc + n) \leq m \wedge m < int (length ins - pc + n')$
 $\langle proof \rangle$

lemma *jump-ok-IfFalseD*:

$\llbracket jump-ok ins n n'; ins ! pc = IfFalse m; pc < length ins \rrbracket \implies \neg int (pc + n) \leq m \wedge m < int (length ins - pc + n')$
 $\langle proof \rangle$

lemma *fixes e* :: 'addr expr1 **and** *es* :: 'addr expr1 list

shows *compE2-jump-ok* [*intro!*]: *jump-ok* (compE2 e) n (Suc n')

and *compEs2-jump-ok* [*intro!*]: *jump-ok* (compEs2 es) n (Suc n')

$\langle proof \rangle$

lemma fixes $e :: 'addr\ expr1$ **and** $es :: 'addr\ expr1\ list$
shows $compE1\text{-Goto-not-same}$: $\llbracket compE2\ e\ !\ pc = Goto\ i;\ pc < length\ (compE2\ e)\ \rrbracket \implies nat\ (int\ pc + i) \neq pc$
and $compEs2\text{-Goto-not-same}$: $\llbracket compEs2\ es\ !\ pc = Goto\ i;\ pc < length\ (compEs2\ es)\ \rrbracket \implies nat\ (int\ pc + i) \neq pc$
 $\langle proof \rangle$

fun $ins\text{-jump-ok} :: 'addr\ instr \Rightarrow nat \Rightarrow bool$

where

$ins\text{-jump-ok}\ (Goto\ m)\ l = (-\ (int\ l) \leq m)$
 $| ins\text{-jump-ok}\ (IfFalse\ m)\ l = (-\ (int\ l) \leq m)$
 $| ins\text{-jump-ok}\ _ = True$

definition $wf\text{-ci} :: ('addr, 'heap)\ check\text{-instr}' \Rightarrow bool$

where

$wf\text{-ci}\ ci \longleftrightarrow$
 $ci\text{-stk-offer}\ ci \wedge ci \leq check\text{-instr}' \wedge$
 $(\forall ins\ P\ h\ stk\ loc\ C\ M\ pc\ pc'\ frs.\ ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs \longrightarrow ins\text{-jump-ok}\ ins\ pc' \longrightarrow ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc'\ frs)$

lemma $wf\text{-ciI}$:

$\llbracket ci\text{-stk-offer}\ ci;$
 $\bigwedge ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs.\ ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs \implies check\text{-instr}'\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs;$
 $\bigwedge ins\ P\ h\ stk\ loc\ C\ M\ pc\ pc'\ frs.\ \llbracket ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs;\ ins\text{-jump-ok}\ ins\ pc' \rrbracket \implies ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc'\ frs \rrbracket$
 $\implies wf\text{-ci}\ ci$
 $\langle proof \rangle$

lemma $check\text{-instr}'\text{-pc}$:

$\llbracket check\text{-instr}'\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs;\ ins\text{-jump-ok}\ ins\ pc' \rrbracket \implies check\text{-instr}'\ ins\ P\ h\ stk\ loc\ C\ M\ pc'\ frs$
 $\langle proof \rangle$

lemma $wf\text{-ci-check-instr}'\ [iff]$:

$wf\text{-ci}\ check\text{-instr}'$
 $\langle proof \rangle$

lemma $jump\text{-ok-ins-jump-ok}$:

$\llbracket jump\text{-ok}\ ins\ n\ n';\ pc < length\ ins \rrbracket \implies ins\text{-jump-ok}\ (ins\ !\ pc)\ (pc + n)$
 $\langle proof \rangle$

context $JVM\text{-heap-base}\ \mathbf{begin}$

lemma $check\text{-instr}\text{-pc}$:

$\llbracket check\text{-instr}\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs;\ ins\text{-jump-ok}\ ins\ pc' \rrbracket \implies check\text{-instr}\ ins\ P\ h\ stk\ loc\ C\ M\ pc'\ frs$
 $\langle proof \rangle$

lemma $wf\text{-ci-check-instr}\ [iff]$:

$wf\text{-ci}\ check\text{-instr}$
 $\langle proof \rangle$

end

lemma *wf-ciD1*: $wf\text{-}ci\ ci \implies ci\text{-}stk\text{-}offer\ ci$
 ⟨proof⟩

lemma *wf-ciD2*: $\llbracket wf\text{-}ci\ ci; ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs \rrbracket \implies check\text{-}instr'\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs$
 ⟨proof⟩

lemma *wf-ciD3*: $\llbracket wf\text{-}ci\ ci; ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs; ins\text{-}jump\text{-}ok\ ins\ pc' \rrbracket \implies ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc'\ frs$
 ⟨proof⟩

lemma *check-instr'-ins-jump-ok*: $check\text{-}instr'\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs \implies ins\text{-}jump\text{-}ok\ ins\ pc$
 ⟨proof⟩

lemma *wf-ci-ins-jump-ok*:
assumes *wf*: $wf\text{-}ci\ ci$
and *ci*: $ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs$
and *pc'*: $pc \leq pc'$
shows $ins\text{-}jump\text{-}ok\ ins\ pc'$
 ⟨proof⟩

lemma *wf-ciD3'*: $\llbracket wf\text{-}ci\ ci; ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs; pc \leq pc' \rrbracket \implies ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc'\ frs$
 ⟨proof⟩

typedef (*'addr*, *'heap*) *check-instr* = *Collect wf-ci :: ('addr*, *'heap*) *check-instr' set*
morphisms *ci-app Abs-check-instr*
 ⟨proof⟩

lemma *ci-app-check-instr' [simp]*: $ci\text{-}app\ (Abs\text{-}check\text{-}instr\ check\text{-}instr') = check\text{-}instr'$
 ⟨proof⟩

lemma (**in** *JVM-heap-base*) *ci-app-check-instr [simp]*: $ci\text{-}app\ (Abs\text{-}check\text{-}instr\ check\text{-}instr) = check\text{-}instr$
 ⟨proof⟩

lemma *wf-ci-stk-offerD*:
 $ci\text{-}app\ ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs \implies ci\text{-}app\ ci\ ins\ P\ h\ (stk\ @\ stk')\ loc\ C\ M\ pc\ frs$
 ⟨proof⟩

lemma *wf-ciD2-ci-app*:
 $ci\text{-}app\ ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs \implies check\text{-}instr'\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs$
 ⟨proof⟩

lemma *wf-ciD3-ci-app*:
 $\llbracket ci\text{-}app\ ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs; ins\text{-}jump\text{-}ok\ ins\ pc' \rrbracket \implies ci\text{-}app\ ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc'\ frs$
 ⟨proof⟩

lemma *wf-ciD3'-ci-app*: $\llbracket ci\text{-}app\ ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs; pc \leq pc' \rrbracket \implies ci\text{-}app\ ci\ ins\ P\ h\ stk\ loc\ C\ M\ pc'\ frs$
 ⟨proof⟩

context *JVM-heap-base* **begin**

inductive *exec-meth* ::
 (*'addr*, *'heap*) *check-instr* \Rightarrow *'addr jvm-prog* \Rightarrow *'addr instr list* \Rightarrow *ex-table* \Rightarrow *'thread-id*
 \Rightarrow *'heap* \Rightarrow (*'addr val list* \times *'addr val list* \times *pc* \times *'addr option*) \Rightarrow (*'addr*, *'thread-id*, *'heap*)

jvm-thread-action

$\Rightarrow 'heap \Rightarrow ('addr\ val\ list \times 'addr\ val\ list \times pc \times 'addr\ option) \Rightarrow bool$

for $ci :: ('addr, 'heap)\ check_instr$ **and** $P :: 'addr\ jvm_prog$

and $ins :: 'addr\ instr\ list$ **and** $xt :: ex_table$ **and** $t :: 'thread_id$

where

exec-instr:

$\llbracket (ta, xcp, h', [(stk', loc', undefined, undefined, pc')]) \in exec_instr\ (ins\ !\ pc)\ P\ t\ h\ stk\ loc\ undefined\ undefined\ pc\ \square \rrbracket$;

$pc < length\ ins$;

$ci_app\ ci\ (ins\ !\ pc)\ P\ h\ stk\ loc\ undefined\ undefined\ pc\ \square \rrbracket$

$\Rightarrow exec_meth\ ci\ P\ ins\ xt\ t\ h\ (stk, loc, pc, None)\ ta\ h'\ (stk', loc', pc', xcp)$

| *exec-catch:*

$\llbracket match_ex_table\ P\ (cname_of\ h\ xcp)\ pc\ xt = \llbracket pc', d \rrbracket; d \leq length\ stk \rrbracket$

$\Rightarrow exec_meth\ ci\ P\ ins\ xt\ t\ h\ (stk, loc, pc, \llbracket xcp \rrbracket) \varepsilon h\ (Addr\ xcp\ \# drop\ (size\ stk - d)\ stk, loc, pc', None)$

lemma *exec-meth-instr:*

$exec_meth\ ci\ P\ ins\ xt\ t\ h\ (stk, loc, pc, None)\ ta\ h'\ (stk', loc', pc', xcp) \longleftrightarrow$

$(ta, xcp, h', [(stk', loc', undefined, undefined, pc')]) \in exec_instr\ (ins\ !\ pc)\ P\ t\ h\ stk\ loc\ undefined\ undefined\ pc\ \square \wedge pc < length\ ins \wedge ci_app\ ci\ (ins\ !\ pc)\ P\ h\ stk\ loc\ undefined\ undefined\ pc\ \square$

$\langle proof \rangle$

lemma *exec-meth-xcpt:*

$exec_meth\ ci\ P\ ins\ xt\ t\ h\ (stk, loc, pc, \llbracket xcp \rrbracket) ta\ h'\ (stk', loc', pc', xcp') \longleftrightarrow$

$(\exists d. match_ex_table\ P\ (cname_of\ h\ xcp)\ pc\ xt = \llbracket pc', d \rrbracket \wedge ta = \varepsilon \wedge stk' = (Addr\ xcp\ \# drop\ (size\ stk - d)\ stk) \wedge loc' = loc \wedge xcp' = None \wedge d \leq length\ stk)$

$\langle proof \rangle$

abbreviation *exec-meth-a*

where $exec_meth_a \equiv exec_meth\ (Abs_check_instr\ check_instr')$

abbreviation *exec-meth-d*

where $exec_meth_d \equiv exec_meth\ (Abs_check_instr\ check_instr)$

lemma *exec-meth-length-compE2D [dest]:*

$exec_meth\ ci\ P\ (compE2\ e)\ (compxE2\ e\ 0\ d)\ t\ h\ (stk, loc, pc, xcp)\ ta\ h'\ s' \Rightarrow pc < length\ (compE2\ e)$

$\langle proof \rangle$

lemma *exec-meth-length-compEs2D [dest]:*

$exec_meth\ ci\ P\ (compEs2\ es)\ (compxEs2\ es\ 0\ 0)\ t\ h\ (stk, loc, pc, xcp)\ ta\ h'\ s' \Rightarrow pc < length\ (compEs2\ es)$

$\langle proof \rangle$

lemma *exec-instr-stk-offer:*

assumes $check: check_instr'\ (ins\ !\ pc)\ P\ h\ stk\ loc\ C\ M\ pc\ frs$

and $exec: (ta', xcp', h', (stk', loc', C, M, pc') \# frs) \in exec_instr\ (ins\ !\ pc)\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$

shows $(ta', xcp', h', (stk' @ stk'', loc', C, M, pc') \# frs) \in exec_instr\ (ins\ !\ pc)\ P\ t\ h\ (stk @ stk'')$

$loc\ C\ M\ pc\ frs$

$\langle proof \rangle$

lemma *exec-meth-stk-offer:*

assumes $exec: exec_meth\ ci\ P\ ins\ xt\ t\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$

shows $\text{exec-meth ci } P \text{ ins } (\text{stack-xlift } (\text{length } \text{stk}'') \text{ } xt) \text{ t h } (\text{stk} @ \text{stk}'', \text{loc}, \text{pc}, \text{xcp}) \text{ ta h' } (\text{stk}' @ \text{stk}'', \text{loc}', \text{pc}', \text{xcp}')$
 ⟨proof⟩

lemma *exec-meth-append-xt* [intro]:
 $\text{exec-meth ci } P \text{ ins } xt \text{ t h } s \text{ ta h' } s'$
 $\implies \text{exec-meth ci } P (\text{ins} @ \text{ins}') (xt @ xt') \text{ t h } s \text{ ta h' } s'$
 ⟨proof⟩

lemma *exec-meth-append* [intro]:
 $\text{exec-meth ci } P \text{ ins } xt \text{ t h } s \text{ ta h' } s' \implies \text{exec-meth ci } P (\text{ins} @ \text{ins}') xt \text{ t h } s \text{ ta h' } s'$
 ⟨proof⟩

lemma *append-exec-meth-xt*:
assumes $\text{exec: exec-meth ci } P \text{ ins } xt \text{ t h } (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \text{ ta h' } (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$
and $\text{jump: jump-ok ins } 0 \ n$
and $\text{pcs: pcs } xt' \subseteq \{0..<\text{length ins}'\}$
shows $\text{exec-meth ci } P (\text{ins}' @ \text{ins}) (xt' @ \text{shift } (\text{length ins}') \text{ } xt) \text{ t h } (\text{stk}, \text{loc}, (\text{length ins}' + \text{pc}), \text{xcp})$
 $\text{ta h' } (\text{stk}', \text{loc}', (\text{length ins}' + \text{pc}'), \text{xcp}')$
 ⟨proof⟩

lemma *append-exec-meth*:
assumes $\text{exec: exec-meth ci } P \text{ ins } xt \text{ t h } (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \text{ ta h' } (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$
and $\text{jump: jump-ok ins } 0 \ n$
shows $\text{exec-meth ci } P (\text{ins}' @ \text{ins}) (\text{shift } (\text{length ins}') \text{ } xt) \text{ t h } (\text{stk}, \text{loc}, (\text{length ins}' + \text{pc}), \text{xcp}) \text{ ta h' } (\text{stk}', \text{loc}', (\text{length ins}' + \text{pc}'), \text{xcp}')$
 ⟨proof⟩

lemma *exec-meth-take-xt'*:
 [$\text{exec-meth ci } P (\text{ins} @ \text{ins}') (xt' @ xt) \text{ t h } (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \text{ ta h' } s'$;
 $\text{pc} < \text{length ins}; \text{pc} \notin \text{pcs } xt$]
 $\implies \text{exec-meth ci } P \text{ ins } xt' \text{ t h } (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \text{ ta h' } s'$
 ⟨proof⟩

lemma *exec-meth-take-xt*:
 [$\text{exec-meth ci } P (\text{ins} @ \text{ins}') (xt' @ \text{shift } (\text{length ins}) \text{ } xt) \text{ t h } (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \text{ ta h' } s'$;
 $\text{pc} < \text{length ins}$]
 $\implies \text{exec-meth ci } P \text{ ins } xt' \text{ t h } (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \text{ ta h' } s'$
 ⟨proof⟩

lemma *exec-meth-take*:
 [$\text{exec-meth ci } P (\text{ins} @ \text{ins}') xt \text{ t h } (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \text{ ta h' } s'$;
 $\text{pc} < \text{length ins}$]
 $\implies \text{exec-meth ci } P \text{ ins } xt \text{ t h } (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \text{ ta h' } s'$
 ⟨proof⟩

lemma *exec-meth-drop-xt*:
assumes $\text{exec: exec-meth ci } P (\text{ins} @ \text{ins}') (xt @ \text{shift } (\text{length ins}) \text{ } xt') \text{ t h } (\text{stk}, \text{loc}, (\text{length ins} + \text{pc}), \text{xcp}) \text{ ta h' } (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$
and $xt: \text{pcs } xt \subseteq \{..<\text{length ins}\}$
and $\text{jump: jump-ok ins}' 0 \ n$
shows $\text{exec-meth ci } P \text{ ins}' xt' \text{ t h } (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \text{ ta h' } (\text{stk}', \text{loc}', (\text{pc}' - \text{length ins}), \text{xcp}')$
 ⟨proof⟩

lemma *exec-meth-drop*:

$\llbracket \text{exec-meth } ci \ P \ (ins \ @ \ ins') \ (shift \ (length \ ins) \ xt) \ t \ h \ (stk, \ loc, \ (length \ ins + pc), \ xcp) \ ta \ h' \ (stk', \ loc', \ pc', \ xcp') \ ;$
 $\quad \text{jump-ok } ins' \ 0 \ b \ \rrbracket$
 $\implies \text{exec-meth } ci \ P \ ins' \ xt \ t \ h \ (stk, \ loc, \ pc, \ xcp) \ ta \ h' \ (stk', \ loc', \ (pc' - length \ ins), \ xcp')$
 $\langle \text{proof} \rangle$

lemma *exec-meth-drop-xt-pc*:

assumes *exec*: $\text{exec-meth } ci \ P \ (ins \ @ \ ins') \ (xt \ @ \ shift \ (length \ ins) \ xt') \ t \ h \ (stk, \ loc, \ pc, \ xcp) \ ta \ h' \ (stk', \ loc', \ pc', \ xcp')$
and *pc*: $pc \geq length \ ins$
and *pcs*: $pcs \ xt \subseteq \{..<length \ ins\}$
and *jump*: $\text{jump-ok } ins' \ 0 \ n'$
shows $pc' \geq length \ ins$
 $\langle \text{proof} \rangle$

lemmas *exec-meth-drop-pc* = *exec-meth-drop-xt-pc*[**where** $xt=[]$, *simplified*]

definition *exec-move* ::

$(\text{'addr}, \ \text{'heap}) \ \text{check-instr} \Rightarrow \ \text{'addr } J1\text{-prog} \Rightarrow \ \text{'thread-id} \Rightarrow \ \text{'addr } \text{expr1}$
 $\Rightarrow \ \text{'heap} \Rightarrow \ (\text{'addr } \text{val list} \times \ \text{'addr } \text{val list} \times \ pc \times \ \text{'addr } \text{option})$
 $\Rightarrow \ (\text{'addr}, \ \text{'thread-id}, \ \text{'heap}) \ \text{jvm-thread-action}$
 $\Rightarrow \ \text{'heap} \Rightarrow \ (\text{'addr } \text{val list} \times \ \text{'addr } \text{val list} \times \ pc \times \ \text{'addr } \text{option}) \Rightarrow \ \text{bool}$
where *exec-move* $ci \ P \ t \ e \equiv \text{exec-meth } ci \ (compP2 \ P) \ (compE2 \ e) \ (compxE2 \ e \ 0 \ 0) \ t$

definition *exec-moves* ::

$(\text{'addr}, \ \text{'heap}) \ \text{check-instr} \Rightarrow \ \text{'addr } J1\text{-prog} \Rightarrow \ \text{'thread-id} \Rightarrow \ \text{'addr } \text{expr1 list}$
 $\Rightarrow \ \text{'heap} \Rightarrow \ (\text{'addr } \text{val list} \times \ \text{'addr } \text{val list} \times \ pc \times \ \text{'addr } \text{option})$
 $\Rightarrow \ (\text{'addr}, \ \text{'thread-id}, \ \text{'heap}) \ \text{jvm-thread-action}$
 $\Rightarrow \ \text{'heap} \Rightarrow \ (\text{'addr } \text{val list} \times \ \text{'addr } \text{val list} \times \ pc \times \ \text{'addr } \text{option}) \Rightarrow \ \text{bool}$
where *exec-moves* $ci \ P \ t \ es \equiv \text{exec-meth } ci \ (compP2 \ P) \ (compEs2 \ es) \ (compxEs2 \ es \ 0 \ 0) \ t$

abbreviation *exec-move-a*

where *exec-move-a* $\equiv \text{exec-move} \ (Abs\text{-check-instr } \text{check-instr}')$

abbreviation *exec-move-d*

where *exec-move-d* $\equiv \text{exec-move} \ (Abs\text{-check-instr } \text{check-instr})$

abbreviation *exec-moves-a*

where *exec-moves-a* $\equiv \text{exec-moves} \ (Abs\text{-check-instr } \text{check-instr}')$

abbreviation *exec-moves-d*

where *exec-moves-d* $\equiv \text{exec-moves} \ (Abs\text{-check-instr } \text{check-instr})$

lemma *exec-move-newArrayI*:

$\text{exec-move } ci \ P \ t \ e \ h \ s \ ta \ h' \ s' \implies \text{exec-move } ci \ P \ t \ (\text{newA } T[e]) \ h \ s \ ta \ h' \ s'$
 $\langle \text{proof} \rangle$

lemma *exec-move-newArray*:

$pc < length \ (compE2 \ e) \implies \text{exec-move } ci \ P \ t \ (\text{newA } T[e]) \ h \ (stk, \ loc, \ pc, \ xcp) = \text{exec-move } ci \ P \ t \ e \ h \ (stk, \ loc, \ pc, \ xcp)$
 $\langle \text{proof} \rangle$

lemma *exec-move-CastI*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (Cast\ T\ e)\ h\ s\ ta\ h'\ s'$
 ⟨proof⟩

lemma *exec-move-Cast*:

$pc < length\ (compE2\ e) \implies exec-move\ ci\ P\ t\ (Cast\ T\ e)\ h\ (stk,\ loc,\ pc,\ xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)$
 ⟨proof⟩

lemma *exec-move-InstanceOfI*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e\ instanceof\ T)\ h\ s\ ta\ h'\ s'$
 ⟨proof⟩

lemma *exec-move-InstanceOf*:

$pc < length\ (compE2\ e) \implies exec-move\ ci\ P\ t\ (e\ instanceof\ T)\ h\ (stk,\ loc,\ pc,\ xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)$
 ⟨proof⟩

lemma *exec-move-BinOpI1*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e\ \llcorner bop \llcorner e')\ h\ s\ ta\ h'\ s'$
 ⟨proof⟩

lemma *exec-move-BinOp1*:

$pc < length\ (compE2\ e) \implies exec-move\ ci\ P\ t\ (e\ \llcorner bop \llcorner e')\ h\ (stk,\ loc,\ pc,\ xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)$
 ⟨proof⟩

lemma *exec-move-BinOpI2*:

assumes *exec*: $exec-move\ ci\ P\ t\ e2\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
shows $exec-move\ ci\ P\ t\ (e1\ \llcorner bop \llcorner e2)\ h\ (stk\ @\ [v],\ loc,\ length\ (compE2\ e1) + pc,\ xcp)\ ta\ h'\ (stk'\ @\ [v],\ loc',\ length\ (compE2\ e1) + pc',\ xcp')$
 ⟨proof⟩

lemma *exec-move-LAssI*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (V := e)\ h\ s\ ta\ h'\ s'$
 ⟨proof⟩

lemma *exec-move-LAss*:

$pc < length\ (compE2\ e) \implies exec-move\ ci\ P\ t\ (V := e)\ h\ (stk,\ loc,\ pc,\ xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)$
 ⟨proof⟩

lemma *exec-move-AAccI1*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e[e'])\ h\ s\ ta\ h'\ s'$
 ⟨proof⟩

lemma *exec-move-AAcc1*:

$pc < length\ (compE2\ e) \implies exec-move\ ci\ P\ t\ (e[e'])\ h\ (stk,\ loc,\ pc,\ xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)$
 ⟨proof⟩

lemma *exec-move-AAccI2*:

assumes *exec*: $exec-move\ ci\ P\ t\ e2\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
shows $exec-move\ ci\ P\ t\ (e1[e2])\ h\ (stk\ @\ [v],\ loc,\ length\ (compE2\ e1) + pc,\ xcp)\ ta\ h'\ (stk'\ @\ [v],\ loc',\ length\ (compE2\ e1) + pc',\ xcp')$

$loc', length (compE2 e1) + pc', xcp)$
 ⟨proof⟩

lemma *exec-move-AAssI1*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e[e'] := e'')\ h\ s\ ta\ h'\ s'$
 ⟨proof⟩

lemma *exec-move-AAss1*:

assumes $pc: pc < length (compE2 e)$
shows $exec-move\ ci\ P\ t\ (e[e'] := e'')\ h\ (stk, loc, pc, xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)$
 (is ?lhs = ?rhs)
 ⟨proof⟩

lemma *exec-move-AAssI2*:

assumes $exec: exec-move\ ci\ P\ t\ e2\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$
shows $exec-move\ ci\ P\ t\ (e1[e2] := e3)\ h\ (stk\ @\ [v], loc, length (compE2 e1) + pc, xcp)\ ta\ h'\ (stk'\ @\ [v], loc', length (compE2 e1) + pc', xcp')$
 ⟨proof⟩

lemma *exec-move-AAssI3*:

assumes $exec: exec-move\ ci\ P\ t\ e3\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$
shows $exec-move\ ci\ P\ t\ (e1[e2] := e3)\ h\ (stk\ @\ [v', v], loc, length (compE2 e1) + length (compE2 e2) + pc, xcp)\ ta\ h'\ (stk'\ @\ [v', v], loc', length (compE2 e1) + length (compE2 e2) + pc', xcp')$
 ⟨proof⟩

lemma *exec-move-ALengthI*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e.length)\ h\ s\ ta\ h'\ s'$
 ⟨proof⟩

lemma *exec-move-ALength*:

$pc < length (compE2 e) \implies exec-move\ ci\ P\ t\ (e.length)\ h\ (stk, loc, pc, xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)$
 ⟨proof⟩

lemma *exec-move-FAccI*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e.F\{D\})\ h\ s\ ta\ h'\ s'$
 ⟨proof⟩

lemma *exec-move-FAcc*:

$pc < length (compE2 e) \implies exec-move\ ci\ P\ t\ (e.F\{D\})\ h\ (stk, loc, pc, xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)$
 ⟨proof⟩

lemma *exec-move-FAssI1*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e.F\{D\} := e')\ h\ s\ ta\ h'\ s'$
 ⟨proof⟩

lemma *exec-move-FAss1*:

$pc < length (compE2 e) \implies exec-move\ ci\ P\ t\ (e.F\{D\} := e')\ h\ (stk, loc, pc, xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)$
 ⟨proof⟩

lemma *exec-move-FAssI2*:

assumes $exec: exec-move\ ci\ P\ t\ e2\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$

shows $exec-move\ ci\ P\ t\ (e1 \cdot F\{D\} := e2)\ h\ (stk\ @\ [v],\ loc,\ length\ (compE2\ e1) + pc,\ xcp)\ ta\ h'$
 $(stk'\ @\ [v],\ loc',\ length\ (compE2\ e1) + pc',\ xcp')$
 ⟨proof⟩

lemma *exec-move-CASI1*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e \cdot compareAndSwap(D \cdot F,\ e',\ e''))\ h\ s\ ta\ h'\ s'$
 ⟨proof⟩

lemma *exec-move-CASI*:

assumes $pc: pc < length\ (compE2\ e)$
shows $exec-move\ ci\ P\ t\ (e \cdot compareAndSwap(D \cdot F,\ e',\ e''))\ h\ (stk,\ loc,\ pc,\ xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)$
 (is ?lhs = ?rhs)
 ⟨proof⟩

lemma *exec-move-CASI2*:

assumes $exec: exec-move\ ci\ P\ t\ e2\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
shows $exec-move\ ci\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F,\ e2,\ e3))\ h\ (stk\ @\ [v],\ loc,\ length\ (compE2\ e1) + pc,\ xcp)\ ta\ h'\ (stk'\ @\ [v],\ loc',\ length\ (compE2\ e1) + pc',\ xcp')$
 ⟨proof⟩

lemma *exec-move-CASI3*:

assumes $exec: exec-move\ ci\ P\ t\ e3\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
shows $exec-move\ ci\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F,\ e2,\ e3))\ h\ (stk\ @\ [v',\ v],\ loc,\ length\ (compE2\ e1) + length\ (compE2\ e2) + pc,\ xcp)\ ta\ h'\ (stk'\ @\ [v',\ v],\ loc',\ length\ (compE2\ e1) + length\ (compE2\ e2) + pc',\ xcp')$
 ⟨proof⟩

lemma *exec-move-CallI1*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e \cdot M(es))\ h\ s\ ta\ h'\ s'$
 ⟨proof⟩

lemma *exec-move-Call1*:

$pc < length\ (compE2\ e) \implies exec-move\ ci\ P\ t\ (e \cdot M(es))\ h\ (stk,\ loc,\ pc,\ xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)$
 ⟨proof⟩

lemma *exec-move-CallI2*:

assumes $exec: exec-moves\ ci\ P\ t\ es\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
shows $exec-move\ ci\ P\ t\ (e \cdot M(es))\ h\ (stk\ @\ [v],\ loc,\ length\ (compE2\ e) + pc,\ xcp)\ ta\ h'\ (stk'\ @\ [v],\ loc',\ length\ (compE2\ e) + pc',\ xcp')$
 ⟨proof⟩

lemma *exec-move-BlockNoneI*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ \{V:T=None; e\}\ h\ s\ ta\ h'\ s'$
 ⟨proof⟩

lemma *exec-move-BlockNone*:

$exec-move\ ci\ P\ t\ \{V:T=None; e\} = exec-move\ ci\ P\ t\ e$
 ⟨proof⟩

lemma *exec-move-BlockSomeI*:

assumes $exec: exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
shows $exec-move\ ci\ P\ t\ \{V:T=[v]; e\}\ h\ (stk,\ loc,\ Suc\ (Suc\ pc),\ xcp)\ ta\ h'\ (stk',\ loc',\ Suc\ (Suc\ pc'),\ xcp')$

xcp'
 $\langle \text{proof} \rangle$

lemma *exec-move-BlockSome*:

$exec-move\ ci\ P\ t\ \{V:T=[v];\ e\} h\ (stk,\ loc,\ Suc\ (Suc\ pc),\ xcp)\ ta\ h'\ (stk',\ loc',\ Suc\ (Suc\ pc'),\ xcp')$
 $=$
 $exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$ (**is** $?lhs = ?rhs$)
 $\langle \text{proof} \rangle$

lemma *exec-move-SyncI1*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (sync_V\ (e)\ e')\ h\ s\ ta\ h'\ s'$
 $\langle \text{proof} \rangle$

lemma *exec-move-Sync1*:

assumes $pc: pc < length\ (compE2\ e)$
shows $exec-move\ ci\ P\ t\ (sync_V\ (e)\ e')\ h\ (stk,\ loc,\ pc,\ xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)$
 (**is** $?lhs = ?rhs$)
 $\langle \text{proof} \rangle$

lemma *exec-move-SyncI2*:

assumes $exec: exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
shows $exec-move\ ci\ P\ t\ (sync_V\ (o')\ e)\ h\ (stk,\ loc,\ (Suc\ (Suc\ (Suc\ (length\ (compE2\ o') + pc))))),$
 $xcp)\ ta\ h'\ (stk',\ loc',\ (Suc\ (Suc\ (Suc\ (length\ (compE2\ o') + pc')))),\ xcp')$
 $\langle \text{proof} \rangle$

lemma *exec-move-SeqI1*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e;;e')\ h\ s\ ta\ h'\ s'$
 $\langle \text{proof} \rangle$

lemma *exec-move-Seq1*:

assumes $pc: pc < length\ (compE2\ e)$
shows $exec-move\ ci\ P\ t\ (e;;e')\ h\ (stk,\ loc,\ pc,\ xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)$
 (**is** $?lhs = ?rhs$)
 $\langle \text{proof} \rangle$

lemma *exec-move-SeqI2*:

assumes $exec: exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
shows $exec-move\ ci\ P\ t\ (e';;e)\ h\ (stk,\ loc,\ (Suc\ (length\ (compE2\ e') + pc)),\ xcp)\ ta\ h'\ (stk',\ loc',$
 $(Suc\ (length\ (compE2\ e') + pc')),\ xcp')$
 $\langle \text{proof} \rangle$

lemma *exec-move-Seq2*:

assumes $pc: pc < length\ (compE2\ e)$
shows $exec-move\ ci\ P\ t\ (e';;e)\ h\ (stk,\ loc,\ Suc\ (length\ (compE2\ e') + pc),\ xcp)\ ta$
 $h'\ (stk',\ loc',\ Suc\ (length\ (compE2\ e') + pc'),\ xcp') =$
 $exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
 (**is** $?lhs = ?rhs$)
 $\langle \text{proof} \rangle$

lemma *exec-move-CondI1*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (if\ (e)\ e1\ else\ e2)\ h\ s\ ta\ h'\ s'$
 $\langle \text{proof} \rangle$

lemma *exec-move-Cond1*:

assumes $pc: pc < \text{length}(\text{compE2 } e)$
shows $\text{exec-move } ci P t \text{ (if } (e) e1 \text{ else } e2) h (stk, loc, pc, xcp) = \text{exec-move } ci P t e h (stk, loc, pc, xcp)$
(is ?lhs = ?rhs)
 ⟨proof⟩

lemma *exec-move-CondI2*:

assumes $\text{exec}: \text{exec-move } ci P t e1 h (stk, loc, pc, xcp) \text{ ta } h' (stk', loc', pc', xcp')$
shows $\text{exec-move } ci P t \text{ (if } (e) e1 \text{ else } e2) h (stk, loc, (\text{Suc } (\text{length}(\text{compE2 } e) + pc)), xcp) \text{ ta } h' (stk', loc', (\text{Suc } (\text{length}(\text{compE2 } e) + pc')), xcp')$
 ⟨proof⟩

lemma *exec-move-Cond2*:

assumes $pc: pc < \text{length}(\text{compE2 } e1)$
shows $\text{exec-move } ci P t \text{ (if } (e) e1 \text{ else } e2) h (stk, loc, (\text{Suc } (\text{length}(\text{compE2 } e) + pc)), xcp) \text{ ta } h' (stk', loc', (\text{Suc } (\text{length}(\text{compE2 } e) + pc')), xcp') = \text{exec-move } ci P t e1 h (stk, loc, pc, xcp) \text{ ta } h' (stk', loc', pc', xcp')$
(is ?lhs = ?rhs)
 ⟨proof⟩

lemma *exec-move-CondI3*:

assumes $\text{exec}: \text{exec-move } ci P t e2 h (stk, loc, pc, xcp) \text{ ta } h' (stk', loc', pc', xcp')$
shows $\text{exec-move } ci P t \text{ (if } (e) e1 \text{ else } e2) h (stk, loc, \text{Suc } (\text{Suc } (\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e1) + pc)), xcp) \text{ ta } h' (stk', loc', \text{Suc } (\text{Suc } (\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e1) + pc')), xcp')$
 ⟨proof⟩

lemma *exec-move-Cond3*:

$\text{exec-move } ci P t \text{ (if } (e) e1 \text{ else } e2) h (stk, loc, \text{Suc } (\text{Suc } (\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e1) + pc)), xcp) \text{ ta}$

$$h' (stk', loc', \text{Suc } (\text{Suc } (\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e1) + pc')), xcp') =$$
 $\text{exec-move } ci P t e2 h (stk, loc, pc, xcp) \text{ ta } h' (stk', loc', pc', xcp')$
(is ?lhs = ?rhs)
 ⟨proof⟩

lemma *exec-move-WhileI1*:

$\text{exec-move } ci P t e h s \text{ ta } h' s' \implies \text{exec-move } ci P t \text{ (while } (e) e') h s \text{ ta } h' s'$
 ⟨proof⟩

lemma (*in ab-group-add*) *uminus-minus-left-commute*:

$- a - (b + c) = - b - (a + c)$
 ⟨proof⟩

lemma *exec-move-While1*:

assumes $pc: pc < \text{length}(\text{compE2 } e)$
shows $\text{exec-move } ci P t \text{ (while } (e) e') h (stk, loc, pc, xcp) = \text{exec-move } ci P t e h (stk, loc, pc, xcp)$
(is ?lhs = ?rhs)
 ⟨proof⟩

lemma *exec-move-WhileI2*:

assumes $\text{exec}: \text{exec-move } ci P t e1 h (stk, loc, pc, xcp) \text{ ta } h' (stk', loc', pc', xcp')$
shows $\text{exec-move } ci P t \text{ (while } (e) e1) h (stk, loc, (\text{Suc } (\text{length}(\text{compE2 } e) + pc)), xcp) \text{ ta } h' (stk', loc', (\text{Suc } (\text{length}(\text{compE2 } e) + pc')), xcp')$
 ⟨proof⟩

lemma *exec-move-While2*:

assumes *pc*: $pc < \text{length} (\text{compE2 } e')$

shows $\text{exec-move } ci P t (\text{while } (e) e') h (stk, loc, (\text{Suc } (\text{length} (\text{compE2 } e) + pc)), xcp) ta$
 $h' (stk', loc', (\text{Suc } (\text{length} (\text{compE2 } e) + pc')), xcp') =$

$\text{exec-move } ci P t e' h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')$

(**is** ?lhs = ?rhs)

<proof>

lemma *exec-move-ThrowI*:

$\text{exec-move } ci P t e h s ta h' s' \implies \text{exec-move } ci P t (\text{throw } e) h s ta h' s'$

<proof>

lemma *exec-move-Throw*:

$pc < \text{length} (\text{compE2 } e) \implies \text{exec-move } ci P t (\text{throw } e) h (stk, loc, pc, xcp) = \text{exec-move } ci P t e h$
 (stk, loc, pc, xcp)

<proof>

lemma *exec-move-TryI1*:

$\text{exec-move } ci P t e h s ta h' s' \implies \text{exec-move } ci P t (\text{try } e \text{ catch } (C V) e') h s ta h' s'$

<proof>

lemma *exec-move-TryI2*:

assumes *exec*: $\text{exec-move } ci P t e h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')$

shows $\text{exec-move } ci P t (\text{try } e' \text{ catch } (C V) e) h (stk, loc, \text{Suc } (\text{Suc } (\text{length} (\text{compE2 } e') + pc)), xcp)$
 $ta h' (stk', loc', \text{Suc } (\text{Suc } (\text{length} (\text{compE2 } e') + pc')), xcp')$

<proof>

lemma *exec-move-Try2*:

$\text{exec-move } ci P t (\text{try } e \text{ catch } (C V) e') h (stk, loc, \text{Suc } (\text{Suc } (\text{length} (\text{compE2 } e) + pc)), xcp) ta$
 $h' (stk', loc', \text{Suc } (\text{Suc } (\text{length} (\text{compE2 } e) + pc')), xcp') =$

$\text{exec-move } ci P t e' h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')$

(**is** ?lhs = ?rhs)

<proof>

lemma *exec-move-raise-xcp-pcD*:

$\text{exec-move } ci P t E h (stk, loc, pc, \text{None}) ta h' (stk', loc', pc', \text{Some } a) \implies pc' = pc$

<proof>

definition $\tau \text{exec-meth} ::$

$(\text{'addr}, \text{'heap}) \text{check-instr} \Rightarrow \text{'addr jvm-prog} \Rightarrow \text{'addr instr list} \Rightarrow \text{ex-table} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap}$

$\Rightarrow (\text{'addr val list} \times \text{'addr val list} \times pc \times \text{'addr option})$

$\Rightarrow (\text{'addr val list} \times \text{'addr val list} \times pc \times \text{'addr option}) \Rightarrow \text{bool}$

where

$\tau \text{exec-meth } ci P \text{ ins } xt t h s s' \longleftrightarrow$

$\text{exec-meth } ci P \text{ ins } xt t h s \varepsilon h s' \wedge (\text{snd } (\text{snd } (\text{snd } s)) = \text{None} \longrightarrow \tau \text{instr } P h (\text{fst } s) (\text{ins } ! \text{fst } (\text{snd } (\text{snd } s))))$

abbreviation $\tau \text{exec-meth-a}$

where $\tau \text{exec-meth-a} \equiv \tau \text{exec-meth } (\text{Abs-check-instr check-instr}')$

abbreviation $\tau \text{exec-meth-d}$

where $\tau \text{exec-meth-d} \equiv \tau \text{exec-meth } (\text{Abs-check-instr check-instr})$

lemma $\tau exec\text{-}methI$ [intro]:

$\llbracket exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ \varepsilon\ h\ s';\ xcp = None \implies \tau instr\ P\ h\ stk\ (ins\ !\ pc) \rrbracket$
 $\implies \tau exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ s'$

$\langle proof \rangle$

lemma $\tau exec\text{-}methE$ [elim]:

assumes $\tau exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ s\ s'$

obtains $stk\ loc\ pc\ xcp$

where $s = (stk,\ loc,\ pc,\ xcp)$

and $exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ \varepsilon\ h\ s'$

and $xcp = None \implies \tau instr\ P\ h\ stk\ (ins\ !\ pc)$

$\langle proof \rangle$

abbreviation $\tau Exec\text{-}methr$::

$(\text{'addr},\ \text{'heap})\ check\text{-}instr \Rightarrow \text{'addr}\ jvm\text{-}prog \Rightarrow \text{'addr}\ instr\ list \Rightarrow ex\text{-}table \Rightarrow \text{'thread}\text{-}id \Rightarrow \text{'heap}$
 $\Rightarrow (\text{'addr}\ val\ list \times \text{'addr}\ val\ list \times pc \times \text{'addr}\ option)$
 $\Rightarrow (\text{'addr}\ val\ list \times \text{'addr}\ val\ list \times pc \times \text{'addr}\ option) \Rightarrow bool$

where

$\tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h == (\tau exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h)^{\wedge**}$

abbreviation $\tau Exec\text{-}methht$::

$(\text{'addr},\ \text{'heap})\ check\text{-}instr \Rightarrow \text{'addr}\ jvm\text{-}prog \Rightarrow \text{'addr}\ instr\ list \Rightarrow ex\text{-}table \Rightarrow \text{'thread}\text{-}id \Rightarrow \text{'heap}$
 $\Rightarrow (\text{'addr}\ val\ list \times \text{'addr}\ val\ list \times pc \times \text{'addr}\ option)$
 $\Rightarrow (\text{'addr}\ val\ list \times \text{'addr}\ val\ list \times pc \times \text{'addr}\ option) \Rightarrow bool$

where

$\tau Exec\text{-}methht\ ci\ P\ ins\ xt\ t\ h == (\tau exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h)^{\wedge++}$

abbreviation $\tau Exec\text{-}methr\text{-}a$

where $\tau Exec\text{-}methr\text{-}a \equiv \tau Exec\text{-}methr\ (Abs\text{-}check\text{-}instr\ check\text{-}instr')$

abbreviation $\tau Exec\text{-}methr\text{-}d$

where $\tau Exec\text{-}methr\text{-}d \equiv \tau Exec\text{-}methr\ (Abs\text{-}check\text{-}instr\ check\text{-}instr)$

abbreviation $\tau Exec\text{-}methht\text{-}a$

where $\tau Exec\text{-}methht\text{-}a \equiv \tau Exec\text{-}methht\ (Abs\text{-}check\text{-}instr\ check\text{-}instr')$

abbreviation $\tau Exec\text{-}methht\text{-}d$

where $\tau Exec\text{-}methht\text{-}d \equiv \tau Exec\text{-}methht\ (Abs\text{-}check\text{-}instr\ check\text{-}instr)$

lemma $\tau Exec\text{-}methr\text{-}refl$: $\tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h\ s\ s\ \langle proof \rangle$

lemma $\tau Exec\text{-}methr\text{-}step'$:

$\llbracket \tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h\ s\ (stk',\ loc',\ pc',\ xcp');$
 $\tau exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk',\ loc',\ pc',\ xcp')\ s' \rrbracket$
 $\implies \tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h\ s\ s'$

$\langle proof \rangle$

lemma $\tau Exec\text{-}methr\text{-}step$:

$\llbracket \tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h\ s\ (stk',\ loc',\ pc',\ xcp');$
 $exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk',\ loc',\ pc',\ xcp')\ \varepsilon\ h\ s';$
 $xcp' = None \implies \tau instr\ P\ h\ stk'\ (ins\ !\ pc') \rrbracket$
 $\implies \tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h\ s\ s'$

$\langle proof \rangle$

lemmas $\tauExec\text{-methr}\text{-intros} = \tauExec\text{-methr}\text{-refl} \ \tauExec\text{-methr}\text{-step}$

lemmas $\tauExec\text{-methr}\text{-1step} = \tauExec\text{-methr}\text{-step}[OF \ \tauExec\text{-methr}\text{-refl}]$

lemmas $\tauExec\text{-methr}\text{-2step} = \tauExec\text{-methr}\text{-step}[OF \ \tauExec\text{-methr}\text{-step}, OF \ \tauExec\text{-methr}\text{-refl}]$

lemmas $\tauExec\text{-methr}\text{-3step} = \tauExec\text{-methr}\text{-step}[OF \ \tauExec\text{-methr}\text{-step}, OF \ \tauExec\text{-methr}\text{-step}, OF \ \tauExec\text{-methr}\text{-refl}]$

lemma $\tauExec\text{-methr}\text{-cases}$ [consumes 1, case-names refl step]:

assumes $\tauExec\text{-methr} \ ci \ P \ ins \ xt \ t \ h \ s \ s'$

obtains $s = s'$

| $stk' \ loc' \ pc' \ xcp'$

where $\tauExec\text{-methr} \ ci \ P \ ins \ xt \ t \ h \ s \ (stk', \ loc', \ pc', \ xcp')$

$exec\text{-meth} \ ci \ P \ ins \ xt \ t \ h \ (stk', \ loc', \ pc', \ xcp') \ \varepsilon \ h \ s'$

$xcp' = None \implies \tauinstr \ P \ h \ stk' \ (ins \ ! \ pc')$

$\langle proof \rangle$

lemma $\tauExec\text{-methr}\text{-induct}$ [consumes 1, case-names refl step]:

$\llbracket \tauExec\text{-methr} \ ci \ P \ ins \ xt \ t \ h \ s \ s';$

$Q \ s;$

$\bigwedge stk \ loc \ pc \ xcp \ s'. \llbracket \tauExec\text{-methr} \ ci \ P \ ins \ xt \ t \ h \ s \ (stk, \ loc, \ pc, \ xcp); \ exec\text{-meth} \ ci \ P \ ins \ xt \ t \ h \ (stk, \ loc, \ pc, \ xcp) \ \varepsilon \ h \ s';$

$xcp = None \implies \tauinstr \ P \ h \ stk \ (ins \ ! \ pc); Q \ (stk, \ loc, \ pc, \ xcp) \rrbracket \implies Q \ s' \rrbracket$

$\implies Q \ s'$

$\langle proof \rangle$

lemma $\tauExec\text{-methr}\text{-trans}$:

$\llbracket \tauExec\text{-methr} \ ci \ P \ ins \ xt \ t \ h \ s \ s'; \tauExec\text{-methr} \ ci \ P \ ins \ xt \ t \ h \ s' \ s'' \rrbracket \implies \tauExec\text{-methr} \ ci \ P \ ins \ xt \ t \ h \ s \ s''$

$\langle proof \rangle$

lemmas $\tauExec\text{-meth}\text{-induct}\text{-split} = \tauExec\text{-methr}\text{-induct}[split\text{-format} \ (complete), \ consumes \ 1, \ case\text{-names} \ \tauExec\text{-refl} \ \tauExec\text{-step}]$

lemma $\tauExec\text{-methr}\text{-converse}\text{-cases}$ [consumes 1, case-names refl step]:

assumes $\tauExec\text{-methr} \ ci \ P \ ins \ xt \ t \ h \ s \ s'$

obtains $s = s'$

| $stk \ loc \ pc \ xcp \ s''$

where $s = (stk, \ loc, \ pc, \ xcp)$

$exec\text{-meth} \ ci \ P \ ins \ xt \ t \ h \ (stk, \ loc, \ pc, \ xcp) \ \varepsilon \ h \ s''$

$xcp = None \implies \tauinstr \ P \ h \ stk \ (ins \ ! \ pc)$

$\tauExec\text{-methr} \ ci \ P \ ins \ xt \ t \ h \ s'' \ s'$

$\langle proof \rangle$

definition $\tauexec\text{-move} \ ::$

$(addr, heap) \ check\text{-instr} \ \Rightarrow \ 'addr \ J1\text{-prog} \ \Rightarrow \ 'thread\text{-id} \ \Rightarrow \ 'addr \ expr1 \ \Rightarrow \ 'heap$

$\Rightarrow \ ('addr \ val \ list \ \times \ 'addr \ val \ list \ \times \ pc \ \times \ 'addr \ option)$

$\Rightarrow \ ('addr \ val \ list \ \times \ 'addr \ val \ list \ \times \ pc \ \times \ 'addr \ option) \ \Rightarrow \ bool$

where

$\tauexec\text{-move} \ ci \ P \ t \ e \ h =$

$(\lambda(stk, \ loc, \ pc, \ xcp) \ s'. \ exec\text{-move} \ ci \ P \ t \ e \ h \ (stk, \ loc, \ pc, \ xcp) \ \varepsilon \ h \ s' \ \wedge \ \taumove2 \ P \ h \ stk \ e \ pc \ xcp)$

definition $\tauexec\text{-moves} \ ::$

$(addr, heap) \ check\text{-instr} \ \Rightarrow \ 'addr \ J1\text{-prog} \ \Rightarrow \ 'thread\text{-id} \ \Rightarrow \ 'addr \ expr1 \ list \ \Rightarrow \ 'heap$

$\Rightarrow \ ('addr \ val \ list \ \times \ 'addr \ val \ list \ \times \ pc \ \times \ 'addr \ option)$

$\Rightarrow \ ('addr \ val \ list \ \times \ 'addr \ val \ list \ \times \ pc \ \times \ 'addr \ option) \ \Rightarrow \ bool$

where

$\tau exec\text{-moves} \textit{ ci P t es h} =$
 $(\lambda(stk, loc, pc, xcp) s'. exec\text{-moves} \textit{ ci P t es h} (stk, loc, pc, xcp) \varepsilon h s' \wedge \tau moves2 P h stk es pc xcp)$

lemma $\tau exec\text{-move}I$:

$\llbracket exec\text{-move} \textit{ ci P t e h} (stk, loc, pc, xcp) \varepsilon h s'; \tau move2 P h stk e pc xcp \rrbracket$
 $\implies \tau exec\text{-move} \textit{ ci P t e h} (stk, loc, pc, xcp) s'$

$\langle proof \rangle$

lemma $\tau exec\text{-move}E$:

assumes $\tau exec\text{-move} \textit{ ci P t e h} (stk, loc, pc, xcp) s'$
obtains $exec\text{-move} \textit{ ci P t e h} (stk, loc, pc, xcp) \varepsilon h s' \tau move2 P h stk e pc xcp$

$\langle proof \rangle$

lemma $\tau exec\text{-moves}I$:

$\llbracket exec\text{-moves} \textit{ ci P t es h} (stk, loc, pc, xcp) \varepsilon h s'; \tau moves2 P h stk es pc xcp \rrbracket$
 $\implies \tau exec\text{-moves} \textit{ ci P t es h} (stk, loc, pc, xcp) s'$

$\langle proof \rangle$

lemma $\tau exec\text{-moves}E$:

assumes $\tau exec\text{-moves} \textit{ ci P t es h} (stk, loc, pc, xcp) s'$
obtains $exec\text{-moves} \textit{ ci P t es h} (stk, loc, pc, xcp) \varepsilon h s' \tau moves2 P h stk es pc xcp$

$\langle proof \rangle$

lemma $\tau exec\text{-move-conv-}\tau exec\text{-meth}$:

$\tau exec\text{-move} \textit{ ci P t e} = \tau exec\text{-meth} \textit{ ci} (compP2 P) (compE2 e) (compxE2 e 0 0) t$

$\langle proof \rangle$

lemma $\tau exec\text{-moves-conv-}\tau exec\text{-meth}$:

$\tau exec\text{-moves} \textit{ ci P t es} = \tau exec\text{-meth} \textit{ ci} (compP2 P) (compEs2 es) (compxEs2 es 0 0) t$

$\langle proof \rangle$

abbreviation $\tau Exec\text{-mover}$

where $\tau Exec\text{-mover} \textit{ ci P t e h} == (\tau exec\text{-move} \textit{ ci P t e h})^{\wedge**}$

abbreviation $\tau Exec\text{-movet}$

where $\tau Exec\text{-movet} \textit{ ci P t e h} == (\tau exec\text{-move} \textit{ ci P t e h})^{\wedge++}$

abbreviation $\tau Exec\text{-mover-a}$

where $\tau Exec\text{-mover-a} \equiv \tau Exec\text{-mover} (Abs\text{-check-instr} \textit{ check-instr}')$

abbreviation $\tau Exec\text{-mover-d}$

where $\tau Exec\text{-mover-d} \equiv \tau Exec\text{-mover} (Abs\text{-check-instr} \textit{ check-instr})$

abbreviation $\tau Exec\text{-movet-a}$

where $\tau Exec\text{-movet-a} \equiv \tau Exec\text{-movet} (Abs\text{-check-instr} \textit{ check-instr}')$

abbreviation $\tau Exec\text{-movet-d}$

where $\tau Exec\text{-movet-d} \equiv \tau Exec\text{-movet} (Abs\text{-check-instr} \textit{ check-instr})$

abbreviation $\tau Exec\text{-movesr}$

where $\tau Exec\text{-movesr} \textit{ ci P t e h} == (\tau exec\text{-moves} \textit{ ci P t e h})^{\wedge**}$

abbreviation $\tau Exec\text{-movest}$

where $\tau Exec\text{-movest } ci P t e h == (\tau exec\text{-moves } ci P t e h)^{++}$

abbreviation $\tau Exec\text{-movesr-a}$

where $\tau Exec\text{-movesr-a} \equiv \tau Exec\text{-movesr } (Abs\text{-check-instr } check\text{-instr}')$

abbreviation $\tau Exec\text{-movesr-d}$

where $\tau Exec\text{-movesr-d} \equiv \tau Exec\text{-movesr } (Abs\text{-check-instr } check\text{-instr})$

abbreviation $\tau Exec\text{-movest-a}$

where $\tau Exec\text{-movest-a} \equiv \tau Exec\text{-movest } (Abs\text{-check-instr } check\text{-instr}')$

abbreviation $\tau Exec\text{-movest-d}$

where $\tau Exec\text{-movest-d} \equiv \tau Exec\text{-movest } (Abs\text{-check-instr } check\text{-instr})$

lemma $\tau Execr\text{-refl}$: $\tau Exec\text{-mover } ci P t e h s s$

$\langle proof \rangle$

lemma $\tau Execsr\text{-refl}$: $\tau Exec\text{-movesr } ci P t e h s s$

$\langle proof \rangle$

lemma $\tau Execr\text{-step}$:

$\llbracket \tau Exec\text{-mover } ci P t e h s (stk', loc', pc', xcp');$
 $exec\text{-move } ci P t e h (stk', loc', pc', xcp') \varepsilon h s';$
 $\tau move2 P h stk' e pc' xcp' \rrbracket$

$\implies \tau Exec\text{-mover } ci P t e h s s'$

$\langle proof \rangle$

lemma $\tau Execsr\text{-step}$:

$\llbracket \tau Exec\text{-movesr } ci P t e s h s (stk', loc', pc', xcp');$
 $exec\text{-moves } ci P t e s h (stk', loc', pc', xcp') \varepsilon h s';$
 $\tau moves2 P h stk' es pc' xcp' \rrbracket$

$\implies \tau Exec\text{-movesr } ci P t e s h s s'$

$\langle proof \rangle$

lemma $\tau Exec\text{-step}$:

$\llbracket \tau Exec\text{-movet } ci P t e h s (stk', loc', pc', xcp');$
 $exec\text{-move } ci P t e h (stk', loc', pc', xcp') \varepsilon h s';$
 $\tau move2 P h stk' e pc' xcp' \rrbracket$

$\implies \tau Exec\text{-movet } ci P t e h s s'$

$\langle proof \rangle$

lemma $\tau Execst\text{-step}$:

$\llbracket \tau Exec\text{-movest } ci P t e s h s (stk', loc', pc', xcp');$
 $exec\text{-moves } ci P t e s h (stk', loc', pc', xcp') \varepsilon h s';$
 $\tau moves2 P h stk' es pc' xcp' \rrbracket$

$\implies \tau Exec\text{-movest } ci P t e s h s s'$

$\langle proof \rangle$

lemmas $\tau Execr1step = \tau Execr\text{-step}[OF \tau Execr\text{-refl}]$

lemmas $\tau Execr2step = \tau Execr\text{-step}[OF \tau Execr\text{-step}, OF \tau Execr\text{-refl}]$

lemmas $\tau Execr3step = \tau Execr\text{-step}[OF \tau Execr\text{-step}, OF \tau Execr\text{-step}, OF \tau Execr\text{-refl}]$

lemmas $\tau Execsr1step = \tau Execsr\text{-step}[OF \tau Execsr\text{-refl}]$

lemmas $\tau Execsr2step = \tau Execsr\text{-step}[OF \tau Execsr\text{-step}, OF \tau Execsr\text{-refl}]$

lemmas $\tau Execsr3step = \tau Execsr-step[OF \tau Execsr-step, OF \tau Execsr-step, OF \tau Execsr-refl]$

lemma $\tau Exec1step$:

$\llbracket exec-move \text{ ci } P \text{ t e h s } \varepsilon \text{ h s}' ;$
 $\tau move2 \text{ P h (fst s) e (fst (snd (snd s))) (snd (snd (snd s))) } \rrbracket$
 $\implies \tau Exec-movet \text{ ci } P \text{ t e h s s}'$

$\langle proof \rangle$

lemmas $\tau Exec2step = \tau Exec-step[OF \tau Exec1step]$

lemmas $\tau Exec3step = \tau Exec-step[OF \tau Exec-step, OF \tau Exec1step]$

lemma $\tau Execst1step$:

$\llbracket exec-moves \text{ ci } P \text{ t es h s } \varepsilon \text{ h s}' ;$
 $\tau moves2 \text{ P h (fst s) es (fst (snd (snd s))) (snd (snd (snd s))) } \rrbracket$
 $\implies \tau Exec-movest \text{ ci } P \text{ t es h s s}'$

$\langle proof \rangle$

lemmas $\tau Execst2step = \tau Execst-step[OF \tau Execst1step]$

lemmas $\tau Execst3step = \tau Execst-step[OF \tau Execst-step, OF \tau Execst1step]$

lemma $\tau Execr-induct$ [*consumes 1, case-names refl step*]:

assumes *major*: $\tau Exec-mover \text{ ci } P \text{ t e h (stk, loc, pc, xcp) (stk'', loc'', pc'', xcp'')$

and *refl*: $Q \text{ stk loc pc xcp}$

and *step*: $\bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''$.

$\llbracket \tau Exec-mover \text{ ci } P \text{ t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp') ;$

$\tau exec-move \text{ ci } P \text{ t e h (stk', loc', pc', xcp') (stk'', loc'', pc'', xcp'') ; Q \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rrbracket$

$\implies Q \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''$

shows $Q \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''$

$\langle proof \rangle$

lemma $\tau Execsr-induct$ [*consumes 1, case-names refl step*]:

assumes *major*: $\tau Exec-movesr \text{ ci } P \text{ t es h (stk, loc, pc, xcp) (stk'', loc'', pc'', xcp'')$

and *refl*: $Q \text{ stk loc pc xcp}$

and *step*: $\bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''$.

$\llbracket \tau Exec-movesr \text{ ci } P \text{ t es h (stk, loc, pc, xcp) (stk', loc', pc', xcp') ;$

$\tau exec-moves \text{ ci } P \text{ t es h (stk', loc', pc', xcp') (stk'', loc'', pc'', xcp'') ; Q \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rrbracket$

$\implies Q \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''$

shows $Q \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''$

$\langle proof \rangle$

lemma $\tau Exec-induct$ [*consumes 1, case-names base step*]:

assumes *major*: $\tau Exec-movet \text{ ci } P \text{ t e h (stk, loc, pc, xcp) (stk'', loc'', pc'', xcp'')$

and *base*: $\bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$. $\tau exec-move \text{ ci } P \text{ t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \implies Q \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$

and *step*: $\bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''$.

$\llbracket \tau Exec-movet \text{ ci } P \text{ t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp') ;$

$\tau exec-move \text{ ci } P \text{ t e h (stk', loc', pc', xcp') (stk'', loc'', pc'', xcp'') ; Q \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rrbracket$

$\implies Q \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''$

shows $Q \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''$

$\langle proof \rangle$

lemma $\tau Execst-induct$ [*consumes 1, case-names base step*]:

assumes *major*: $\tau Exec-movest \text{ ci } P \text{ t es h (stk, loc, pc, xcp) (stk'', loc'', pc'', xcp'')$

and *base*: $\bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$. $\tau exec-moves \text{ ci } P \text{ t es h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \implies Q$

$stk' loc' pc' xcp'$

and step: $\bigwedge stk' loc' pc' xcp' stk'' loc'' pc'' xcp''$.

$\llbracket \tau Exec\text{-}movest\ ci\ P\ t\ es\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp');$
 $\tau exec\text{-}moves\ ci\ P\ t\ es\ h\ (stk', loc', pc', xcp')\ (stk'', loc'', pc'', xcp'');\ Q\ stk' loc' pc' xcp' \rrbracket$
 $\implies Q\ stk'' loc'' pc'' xcp''$

shows $Q\ stk'' loc'' pc'' xcp''$

$\langle proof \rangle$

lemma $\tau Exec\text{-}mover\text{-}\tau Exec\text{-}methr$:

$\tau Exec\text{-}mover\ ci\ P\ t\ e = \tau Exec\text{-}methr\ ci\ (compP2\ P)\ (compE2\ e)\ (compxE2\ e\ 0\ 0)\ t$

$\langle proof \rangle$

lemma $\tau Exec\text{-}movesr\text{-}\tau Exec\text{-}methr$:

$\tau Exec\text{-}movesr\ ci\ P\ t\ es = \tau Exec\text{-}methr\ ci\ (compP2\ P)\ (compEs2\ es)\ (compxEs2\ es\ 0\ 0)\ t$

$\langle proof \rangle$

lemma $\tau Exec\text{-}movet\text{-}\tau Exec\text{-}methht$:

$\tau Exec\text{-}movet\ ci\ P\ t\ e = \tau Exec\text{-}methht\ ci\ (compP2\ P)\ (compE2\ e)\ (compxE2\ e\ 0\ 0)\ t$

$\langle proof \rangle$

lemma $\tau Exec\text{-}movest\text{-}\tau Exec\text{-}methht$:

$\tau Exec\text{-}movest\ ci\ P\ t\ es = \tau Exec\text{-}methht\ ci\ (compP2\ P)\ (compEs2\ es)\ (compxEs2\ es\ 0\ 0)\ t$

$\langle proof \rangle$

lemma $\tau Exec\text{-}mover\text{-}trans$:

$\llbracket \tau Exec\text{-}mover\ ci\ P\ t\ e\ h\ s\ s'; \tau Exec\text{-}mover\ ci\ P\ t\ e\ h\ s'\ s'' \rrbracket \implies \tau Exec\text{-}mover\ ci\ P\ t\ e\ h\ s\ s''$

$\langle proof \rangle$

lemma $\tau Exec\text{-}movesr\text{-}trans$:

$\llbracket \tau Exec\text{-}movesr\ ci\ P\ t\ es\ h\ s\ s'; \tau Exec\text{-}movesr\ ci\ P\ t\ es\ h\ s'\ s'' \rrbracket \implies \tau Exec\text{-}movesr\ ci\ P\ t\ es\ h\ s\ s''$

$\langle proof \rangle$

lemma $\tau Exec\text{-}movet\text{-}trans$:

$\llbracket \tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ s\ s'; \tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ s'\ s'' \rrbracket \implies \tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ s\ s''$

$\langle proof \rangle$

lemma $\tau Exec\text{-}movest\text{-}trans$:

$\llbracket \tau Exec\text{-}movest\ ci\ P\ t\ es\ h\ s\ s'; \tau Exec\text{-}movest\ ci\ P\ t\ es\ h\ s'\ s'' \rrbracket \implies \tau Exec\text{-}movest\ ci\ P\ t\ es\ h\ s\ s''$

$\langle proof \rangle$

lemma $\tau exec\text{-}move\text{-}into\text{-}\tau exec\text{-}moves$:

$\tau exec\text{-}move\ ci\ P\ t\ e\ h\ s\ s' \implies \tau exec\text{-}moves\ ci\ P\ t\ (e\ \# \ es)\ h\ s\ s'$

$\langle proof \rangle$

lemma $\tau Exec\text{-}mover\text{-}\tau Exec\text{-}movesr$:

$\tau Exec\text{-}mover\ ci\ P\ t\ e\ h\ s\ s' \implies \tau Exec\text{-}movesr\ ci\ P\ t\ (e\ \# \ es)\ h\ s\ s'$

$\langle proof \rangle$

lemma $\tau Exec\text{-}movet\text{-}\tau Exec\text{-}movest$:

$\tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ s\ s' \implies \tau Exec\text{-}movest\ ci\ P\ t\ (e\ \# \ es)\ h\ s\ s'$

$\langle proof \rangle$

lemma $exec\text{-}moves\text{-}append$: $exec\text{-}moves\ ci\ P\ t\ es\ h\ s\ ta\ h'\ s' \implies exec\text{-}moves\ ci\ P\ t\ (es\ @\ es')\ h\ s\ ta\ h'\ s'$

<proof>

lemma $\tau exec\text{-moves}\text{-append}$: $\tau exec\text{-moves}\ ci\ P\ t\ es\ h\ s\ s' \implies \tau exec\text{-moves}\ ci\ P\ t\ (es\ @\ es')\ h\ s\ s'$
<proof>

lemma $\tau Exec\text{-movesr}\text{-append}$ [intro]:

$\tau Exec\text{-movesr}\ ci\ P\ t\ es\ h\ s\ s' \implies \tau Exec\text{-movesr}\ ci\ P\ t\ (es\ @\ es')\ h\ s\ s'$
<proof>

lemma $\tau Exec\text{-movest}\text{-append}$ [intro]:

$\tau Exec\text{-movest}\ ci\ P\ t\ es\ h\ s\ s' \implies \tau Exec\text{-movest}\ ci\ P\ t\ (es\ @\ es')\ h\ s\ s'$
<proof>

lemma $append\text{-exec}\text{-moves}$:

assumes len : $length\ vs = length\ es'$

and $exec$: $exec\text{-moves}\ ci\ P\ t\ es\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$

shows $exec\text{-moves}\ ci\ P\ t\ (es'\ @\ es)\ h\ ((stk\ @\ vs),\ loc,\ (length\ (compEs2\ es') + pc),\ xcp)\ ta\ h'\ ((stk'\ @\ vs),\ loc',\ (length\ (compEs2\ es') + pc'),\ xcp')$

<proof>

lemma $append\text{-}\tau exec\text{-moves}$:

$\llbracket length\ vs = length\ es';$

$\tau exec\text{-moves}\ ci\ P\ t\ es\ h\ (stk,\ loc,\ pc,\ xcp)\ (stk',\ loc',\ pc',\ xcp') \rrbracket$

$\implies \tau exec\text{-moves}\ ci\ P\ t\ (es'\ @\ es)\ h\ ((stk\ @\ vs),\ loc,\ (length\ (compEs2\ es') + pc),\ xcp)\ ((stk'\ @\ vs),\ loc',\ (length\ (compEs2\ es') + pc'),\ xcp')$

<proof>

lemma $append\text{-}\tau Exec\text{-movesr}$:

assumes len : $length\ vs = length\ es'$

shows $\tau Exec\text{-movesr}\ ci\ P\ t\ es\ h\ (stk,\ loc,\ pc,\ xcp)\ (stk',\ loc',\ pc',\ xcp')$

$\implies \tau Exec\text{-movesr}\ ci\ P\ t\ (es'\ @\ es)\ h\ ((stk\ @\ vs),\ loc,\ (length\ (compEs2\ es') + pc),\ xcp)\ ((stk'\ @\ vs),\ loc',\ (length\ (compEs2\ es') + pc'),\ xcp')$

<proof>

lemma $append\text{-}\tau Exec\text{-movest}$:

assumes len : $length\ vs = length\ es'$

shows $\tau Exec\text{-movest}\ ci\ P\ t\ es\ h\ (stk,\ loc,\ pc,\ xcp)\ (stk',\ loc',\ pc',\ xcp')$

$\implies \tau Exec\text{-movest}\ ci\ P\ t\ (es'\ @\ es)\ h\ ((stk\ @\ vs),\ loc,\ (length\ (compEs2\ es') + pc),\ xcp)\ ((stk'\ @\ vs),\ loc',\ (length\ (compEs2\ es') + pc'),\ xcp')$

<proof>

lemma $NewArray\text{-}\tau execI$:

$\tau exec\text{-move}\ ci\ P\ t\ e\ h\ s\ s' \implies \tau exec\text{-move}\ ci\ P\ t\ (newA\ T[e])\ h\ s\ s'$
<proof>

lemma $Cast\text{-}\tau execI$:

$\tau exec\text{-move}\ ci\ P\ t\ e\ h\ s\ s' \implies \tau exec\text{-move}\ ci\ P\ t\ (Cast\ T\ e)\ h\ s\ s'$
<proof>

lemma $InstanceOf\text{-}\tau execI$:

$\tau exec\text{-move}\ ci\ P\ t\ e\ h\ s\ s' \implies \tau exec\text{-move}\ ci\ P\ t\ (e\ instanceof\ T)\ h\ s\ s'$
<proof>

lemma *BinOp- τ execI1*:

$\tau\text{exec-move } ci P t e h s s' \implies \tau\text{exec-move } ci P t (e \llbracket bop \rrbracket e') h s s'$
 $\langle \text{proof} \rangle$

lemma *BinOp- τ execI2*:

$\tau\text{exec-move } ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau\text{exec-move } ci P t (e \llbracket bop \rrbracket e') h ((stk @ [v]), loc, (length (compE2 e) + pc), xcp) ((stk' @ [v]),$
 $loc', (length (compE2 e) + pc'), xcp')$
 $\langle \text{proof} \rangle$

lemma *LAss- τ execI*:

$\tau\text{exec-move } ci P t e h s s' \implies \tau\text{exec-move } ci P t (V := e) h s s'$
 $\langle \text{proof} \rangle$

lemma *AAcc- τ execI1*:

$\tau\text{exec-move } ci P t e h s s' \implies \tau\text{exec-move } ci P t (e[i]) h s s'$
 $\langle \text{proof} \rangle$

lemma *AAcc- τ execI2*:

$\tau\text{exec-move } ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau\text{exec-move } ci P t (e[e']) h ((stk @ [v]), loc, (length (compE2 e) + pc), xcp) ((stk' @ [v]), loc',$
 $(length (compE2 e) + pc'), xcp')$
 $\langle \text{proof} \rangle$

lemma *AAss- τ execI1*:

$\tau\text{exec-move } ci P t e h s s' \implies \tau\text{exec-move } ci P t (e[i] := e') h s s'$
 $\langle \text{proof} \rangle$

lemma *AAss- τ execI2*:

$\tau\text{exec-move } ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau\text{exec-move } ci P t (e[e'] := e'') h ((stk @ [v]), loc, (length (compE2 e) + pc), xcp) ((stk' @ [v]),$
 $loc', (length (compE2 e) + pc'), xcp')$
 $\langle \text{proof} \rangle$

lemma *AAss- τ execI3*:

$\tau\text{exec-move } ci P t e'' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau\text{exec-move } ci P t (e[e'] := e'') h ((stk @ [v, v']), loc, (length (compE2 e) + length (compE2 e')$
 $+ pc), xcp) ((stk' @ [v, v']), loc', (length (compE2 e) + length (compE2 e') + pc'), xcp')$
 $\langle \text{proof} \rangle$

lemma *ALength- τ execI*:

$\tau\text{exec-move } ci P t e h s s' \implies \tau\text{exec-move } ci P t (e \cdot \text{length}) h s s'$
 $\langle \text{proof} \rangle$

lemma *FAcc- τ execI*:

$\tau\text{exec-move } ci P t e h s s' \implies \tau\text{exec-move } ci P t (e \cdot F\{D\}) h s s'$
 $\langle \text{proof} \rangle$

lemma *FAss- τ execI1*:

$\tau\text{exec-move } ci P t e h s s' \implies \tau\text{exec-move } ci P t (e \cdot F\{D\} := e') h s s'$
 $\langle \text{proof} \rangle$

lemma *FAss- τ execI2*:

$$\begin{aligned} & \tau_{exec-move} \text{ ci } P \text{ t } e' \text{ h } (stk, loc, pc, xcp) (stk', loc', pc', xcp') \\ & \implies \tau_{exec-move} \text{ ci } P \text{ t } (e \cdot F\{D\} := e') \text{ h } ((stk \text{ @ } [v]), loc, (length (compE2 e) + pc), xcp) ((stk' \text{ @ } \\ & [v]), loc', (length (compE2 e) + pc'), xcp') \\ & \langle proof \rangle \end{aligned}$$

lemma CAS- τ_{execI1} :

$$\tau_{exec-move} \text{ ci } P \text{ t } e \text{ h } s \text{ s}' \implies \tau_{exec-move} \text{ ci } P \text{ t } (e \cdot compareAndSwap(D \cdot F, e', e'')) \text{ h } s \text{ s}'$$

$\langle proof \rangle$

lemma CAS- τ_{execI2} :

$$\begin{aligned} & \tau_{exec-move} \text{ ci } P \text{ t } e' \text{ h } (stk, loc, pc, xcp) (stk', loc', pc', xcp') \\ & \implies \tau_{exec-move} \text{ ci } P \text{ t } (e \cdot compareAndSwap(D \cdot F, e', e'')) \text{ h } ((stk \text{ @ } [v]), loc, (length (compE2 e) + \\ & pc), xcp) ((stk' \text{ @ } [v]), loc', (length (compE2 e) + pc'), xcp') \\ & \langle proof \rangle \end{aligned}$$

lemma CAS- τ_{execI3} :

$$\begin{aligned} & \tau_{exec-move} \text{ ci } P \text{ t } e'' \text{ h } (stk, loc, pc, xcp) (stk', loc', pc', xcp') \\ & \implies \tau_{exec-move} \text{ ci } P \text{ t } (e \cdot compareAndSwap(D \cdot F, e', e'')) \text{ h } ((stk \text{ @ } [v, v']), loc, (length (compE2 e) \\ & + length (compE2 e') + pc), xcp) ((stk' \text{ @ } [v, v']), loc', (length (compE2 e) + length (compE2 e') + \\ & pc'), xcp') \\ & \langle proof \rangle \end{aligned}$$

lemma Call- τ_{execI1} :

$$\tau_{exec-move} \text{ ci } P \text{ t } e \text{ h } s \text{ s}' \implies \tau_{exec-move} \text{ ci } P \text{ t } (e \cdot M(es)) \text{ h } s \text{ s}'$$

$\langle proof \rangle$

lemma Call- τ_{execI2} :

$$\begin{aligned} & \tau_{exec-moves} \text{ ci } P \text{ t } es \text{ h } (stk, loc, pc, xcp) (stk', loc', pc', xcp') \\ & \implies \tau_{exec-move} \text{ ci } P \text{ t } (e \cdot M(es)) \text{ h } ((stk \text{ @ } [v]), loc, (length (compE2 e) + pc), xcp) ((stk' \text{ @ } [v]), \\ & loc', (length (compE2 e) + pc'), xcp') \\ & \langle proof \rangle \end{aligned}$$

lemma Block- $\tau_{execI-Some}$:

$$\begin{aligned} & \tau_{exec-move} \text{ ci } P \text{ t } e \text{ h } (stk, loc, pc, xcp) (stk', loc', pc', xcp') \\ & \implies \tau_{exec-move} \text{ ci } P \text{ t } \{V:T=[v]; e\} \text{ h } (stk, loc, Suc (Suc pc), xcp) (stk', loc', Suc (Suc pc'), xcp') \\ & \langle proof \rangle \end{aligned}$$

lemma Block- $\tau_{execI-None}$:

$$\tau_{exec-move} \text{ ci } P \text{ t } e \text{ h } s \text{ s}' \implies \tau_{exec-move} \text{ ci } P \text{ t } \{V:T=None; e\} \text{ h } s \text{ s}'$$

$\langle proof \rangle$

lemma Sync- τ_{execI} :

$$\tau_{exec-move} \text{ ci } P \text{ t } e \text{ h } s \text{ s}' \implies \tau_{exec-move} \text{ ci } P \text{ t } (sync_V (e) e') \text{ h } s \text{ s}'$$

$\langle proof \rangle$

lemma Insync- τ_{execI} :

$$\begin{aligned} & \tau_{exec-move} \text{ ci } P \text{ t } e' \text{ h } (stk, loc, pc, xcp) (stk', loc', pc', xcp') \\ & \implies \tau_{exec-move} \text{ ci } P \text{ t } (sync_V (e) e') \text{ h } (stk, loc, Suc (Suc (Suc (length (compE2 e) + pc))), xcp) \\ & (stk', loc', Suc (Suc (Suc (length (compE2 e) + pc'))), xcp') \\ & \langle proof \rangle \end{aligned}$$

lemma Seq- τ_{execI1} :

$$\tau_{exec-move} \text{ ci } P \text{ t } e \text{ h } s \text{ s}' \implies \tau_{exec-move} \text{ ci } P \text{ t } (e;; e') \text{ h } s \text{ s}'$$

$\langle proof \rangle$

lemma *Seq- τ execI2*:

$$\begin{aligned} & \tau\text{exec-move } ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \\ & \implies \tau\text{exec-move } ci P t (e;; e') h (stk, loc, Suc (\text{length } (compE2 e) + pc), xcp) (stk', loc', Suc (\text{length } \\ & (\text{compE2 } e) + pc'), xcp') \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma *Cond- τ execI1*:

$$\begin{aligned} & \tau\text{exec-move } ci P t e h s s' \implies \tau\text{exec-move } ci P t (\text{if } (e) e' \text{ else } e'') h s s' \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma *Cond- τ execI2*:

$$\begin{aligned} & \tau\text{exec-move } ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \\ & \implies \tau\text{exec-move } ci P t (\text{if } (e) e' \text{ else } e'') h (stk, loc, Suc (\text{length } (compE2 e) + pc), xcp) (stk', loc', \\ & Suc (\text{length } (compE2 e) + pc'), xcp') \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma *Cond- τ execI3*:

$$\begin{aligned} & \tau\text{exec-move } ci P t e'' h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \\ & \implies \tau\text{exec-move } ci P t (\text{if } (e) e' \text{ else } e'') h (stk, loc, Suc (Suc (\text{length } (compE2 e) + \text{length } (compE2 \\ & e') + pc)), xcp) (stk', loc', Suc (Suc (\text{length } (compE2 e) + \text{length } (compE2 e') + pc')), xcp') \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma *While- τ execI1*:

$$\begin{aligned} & \tau\text{exec-move } ci P t e h s s' \implies \tau\text{exec-move } ci P t (\text{while } (e) e') h s s' \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma *While- τ execI2*:

$$\begin{aligned} & \tau\text{exec-move } ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \\ & \implies \tau\text{exec-move } ci P t (\text{while } (e) e') h (stk, loc, Suc (\text{length } (compE2 e) + pc), xcp) (stk', loc', Suc \\ & (\text{length } (compE2 e) + pc'), xcp') \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma *Throw- τ execI*:

$$\begin{aligned} & \tau\text{exec-move } ci P t e h s s' \implies \tau\text{exec-move } ci P t (\text{throw } e) h s s' \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma *Try- τ execI1*:

$$\begin{aligned} & \tau\text{exec-move } ci P t e h s s' \implies \tau\text{exec-move } ci P t (\text{try } e \text{ catch } (C V) e') h s s' \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma *Try- τ execI2*:

$$\begin{aligned} & \tau\text{exec-move } ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \\ & \implies \tau\text{exec-move } ci P t (\text{try } e \text{ catch } (C V) e') h (stk, loc, Suc (Suc (\text{length } (compE2 e) + pc)), xcp) \\ & (stk', loc', Suc (Suc (\text{length } (compE2 e) + pc')), xcp') \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma *NewArray- τ ExecrI*:

$$\begin{aligned} & \tau\text{Exec-mover } ci P t e h s s' \implies \tau\text{Exec-mover } ci P t (\text{newA } T[e]) h s s' \\ & \langle \text{proof} \rangle \end{aligned}$$

lemma *Cast- τ ExecrI*:

$\tauExec\text{-mover } ci P t e h s s' \Longrightarrow \tauExec\text{-mover } ci P t (Cast T e) h s s'$
 \langle proof \rangle

lemma *InstanceOf- \tauExecrI :*

$\tauExec\text{-mover } ci P t e h s s' \Longrightarrow \tauExec\text{-mover } ci P t (e \text{ instanceof } T) h s s'$
 \langle proof \rangle

lemma *BinOp- $\tauExecrI1$:*

$\tauExec\text{-mover } ci P t e1 h s s' \Longrightarrow \tauExec\text{-mover } ci P t (e1 \ll bop \gg e2) h s s'$
 \langle proof \rangle

lemma *BinOp- $\tauExecrI2$:*

$\tauExec\text{-mover } ci P t e2 h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\Longrightarrow \tauExec\text{-mover } ci P t (e \ll bop \gg e2) h ((stk @ [v]), loc, (length (compE2 e) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 e) + pc'), xcp')$
 \langle proof \rangle

lemma *LAss- \tauExecrI :*

$\tauExec\text{-mover } ci P t e h s s' \Longrightarrow \tauExec\text{-mover } ci P t (V := e) h s s'$
 \langle proof \rangle

lemma *AAcc- $\tauExecrI1$:*

$\tauExec\text{-mover } ci P t e h s s' \Longrightarrow \tauExec\text{-mover } ci P t (e[i]) h s s'$
 \langle proof \rangle

lemma *AAcc- $\tauExecrI2$:*

$\tauExec\text{-mover } ci P t i h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\Longrightarrow \tauExec\text{-mover } ci P t (a[i]) h ((stk @ [v]), loc, (length (compE2 a) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 a) + pc'), xcp')$
 \langle proof \rangle

lemma *AAss- $\tauExecrI1$:*

$\tauExec\text{-mover } ci P t e h s s' \Longrightarrow \tauExec\text{-mover } ci P t (e[i] := e') h s s'$
 \langle proof \rangle

lemma *AAss- $\tauExecrI2$:*

$\tauExec\text{-mover } ci P t i h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\Longrightarrow \tauExec\text{-mover } ci P t (a[i] := e) h ((stk @ [v]), loc, (length (compE2 a) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 a) + pc'), xcp')$
 \langle proof \rangle

lemma *AAss- $\tauExecrI3$:*

$\tauExec\text{-mover } ci P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\Longrightarrow \tauExec\text{-mover } ci P t (a[i] := e) h ((stk @ [v, v']), loc, (length (compE2 a) + length (compE2 i) + pc), xcp) ((stk' @ [v, v']), loc', (length (compE2 a) + length (compE2 i) + pc'), xcp')$
 \langle proof \rangle

lemma *ALength- \tauExecrI :*

$\tauExec\text{-mover } ci P t e h s s' \Longrightarrow \tauExec\text{-mover } ci P t (e \cdot length) h s s'$
 \langle proof \rangle

lemma *FAcc- \tauExecrI :*

$\tauExec\text{-mover } ci P t e h s s' \Longrightarrow \tauExec\text{-mover } ci P t (e \cdot F\{D\}) h s s'$
 \langle proof \rangle

lemma *FAss- τ ExecrI1*:

τ Exec-mover ci P t e h s s' \implies τ Exec-mover ci P t (e.F{D} := e') h s s'

\langle proof \rangle

lemma *FAss- τ ExecrI2*:

τ Exec-mover ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')

\implies τ Exec-mover ci P t (e.F{D} := e') h ((stk @ [v]), loc, (length (compE2 e) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 e) + pc'), xcp')

\langle proof \rangle

lemma *CAS- τ ExecrI1*:

τ Exec-mover ci P t e h s s' \implies τ Exec-mover ci P t (e.compareAndSwap(D.F, e', e'')) h s s'

\langle proof \rangle

lemma *CAS- τ ExecrI2*:

τ Exec-mover ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')

\implies τ Exec-mover ci P t (e.compareAndSwap(D.F, e', e'')) h ((stk @ [v]), loc, (length (compE2 e) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 e) + pc'), xcp')

\langle proof \rangle

lemma *CAS- τ ExecrI3*:

τ Exec-mover ci P t e'' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')

\implies τ Exec-mover ci P t (e.compareAndSwap(D.F, e', e'')) h ((stk @ [v, v']), loc, (length (compE2 e) + length (compE2 e') + pc), xcp) ((stk' @ [v, v']), loc', (length (compE2 e) + length (compE2 e') + pc'), xcp')

\langle proof \rangle

lemma *Call- τ ExecrI1*:

τ Exec-mover ci P t obj h s s' \implies τ Exec-mover ci P t (obj.M'(es)) h s s'

\langle proof \rangle

lemma *Call- τ ExecrI2*:

τ Exec-movesr ci P t es h (stk, loc, pc, xcp) (stk', loc', pc', xcp')

\implies τ Exec-mover ci P t (obj.M'(es)) h ((stk @ [v]), loc, (length (compE2 obj) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 obj) + pc'), xcp')

\langle proof \rangle

lemma *Block- τ ExecrI-Some*:

τ Exec-mover ci P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp')

\implies τ Exec-mover ci P t {V:T=[v]; e} h (stk, loc, (Suc (Suc pc)), xcp) (stk', loc', (Suc (Suc pc')), xcp')

\langle proof \rangle

lemma *Block- τ ExecrI-None*:

τ Exec-mover ci P t e h s s' \implies τ Exec-mover ci P t {V:T=None; e} h s s'

\langle proof \rangle

lemma *Sync- τ ExecrI*:

τ Exec-mover ci P t e h s s' \implies τ Exec-mover ci P t (sync_V (e) e') h s s'

\langle proof \rangle

lemma *Insync- τ ExecrI*:

τ Exec-mover ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')

$\implies \tauExec\text{-mover } ci P t (sync_V (e) e') h (stk, loc, (Suc (Suc (Suc (length (compE2 e) + pc))))), xcp) (stk', loc', (Suc (Suc (Suc (length (compE2 e) + pc')))), xcp')$
 \langle proof \rangle

lemma *Seq- $\tauExecrI1$* :

$\tauExec\text{-mover } ci P t e h s s' \implies \tauExec\text{-mover } ci P t (e;;e') h s s'$
 \langle proof \rangle

lemma *Seq- $\tauExecrI2$* :

$\tauExec\text{-mover } ci P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \implies$
 $\tauExec\text{-mover } ci P t (e';;e) h (stk, loc, (Suc (length (compE2 e') + pc)), xcp) (stk', loc', (Suc (length (compE2 e') + pc'))), xcp')$
 \langle proof \rangle

lemma *Cond- $\tauExecrI1$* :

$\tauExec\text{-mover } ci P t e h s s' \implies \tauExec\text{-mover } ci P t (if (e) e1 else e2) h s s'$
 \langle proof \rangle

lemma *Cond- $\tauExecrI2$* :

$\tauExec\text{-mover } ci P t e1 h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \implies$
 $\tauExec\text{-mover } ci P t (if (e) e1 else e2) h (stk, loc, (Suc (length (compE2 e) + pc)), xcp) (stk', loc', (Suc (length (compE2 e) + pc'))), xcp')$
 \langle proof \rangle

lemma *Cond- $\tauExecrI3$* :

$\tauExec\text{-mover } ci P t e2 h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \implies$
 $\tauExec\text{-mover } ci P t (if (e) e1 else e2) h (stk, loc, (Suc (Suc (length (compE2 e) + length (compE2 e1) + pc))), xcp) (stk', loc', (Suc (Suc (length (compE2 e) + length (compE2 e1) + pc'))), xcp')$
 \langle proof \rangle

lemma *While- $\tauExecrI1$* :

$\tauExec\text{-mover } ci P t c h s s' \implies \tauExec\text{-mover } ci P t (while (c) e) h s s'$
 \langle proof \rangle

lemma *While- $\tauExecrI2$* :

$\tauExec\text{-mover } ci P t E h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tauExec\text{-mover } ci P t (while (c) E) h (stk, loc, (Suc (length (compE2 c) + pc)), xcp) (stk', loc', (Suc (length (compE2 c) + pc'))), xcp')$
 \langle proof \rangle

lemma *Throw- \tauExecrI* :

$\tauExec\text{-mover } ci P t e h s s' \implies \tauExec\text{-mover } ci P t (throw e) h s s'$
 \langle proof \rangle

lemma *Try- $\tauExecrI1$* :

$\tauExec\text{-mover } ci P t E h s s' \implies \tauExec\text{-mover } ci P t (try E catch (C' V) e) h s s'$
 \langle proof \rangle

lemma *Try- $\tauExecrI2$* :

$\tauExec\text{-mover } ci P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tauExec\text{-mover } ci P t (try E catch (C' V) e) h (stk, loc, (Suc (Suc (length (compE2 E) + pc))), xcp) (stk', loc', (Suc (Suc (length (compE2 E) + pc'))), xcp')$
 \langle proof \rangle

lemma *NewArray- τ ExecI*:

$\tauExec\text{-movet}\ ci\ Pt\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ Pt\ (newA\ T[e])\ h\ s\ s'$
 ⟨proof⟩

lemma *Cast- τ ExecI*:

$\tauExec\text{-movet}\ ci\ Pt\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ Pt\ (Cast\ T\ e)\ h\ s\ s'$
 ⟨proof⟩

lemma *InstanceOf- τ ExecI*:

$\tauExec\text{-movet}\ ci\ Pt\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ Pt\ (e\ instanceof\ T)\ h\ s\ s'$
 ⟨proof⟩

lemma *BinOp- τ ExecI1*:

$\tauExec\text{-movet}\ ci\ Pt\ e1\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ Pt\ (e1\ \llcorner bop \llcorner e2)\ h\ s\ s'$
 ⟨proof⟩

lemma *BinOp- τ ExecI2*:

$\tauExec\text{-movet}\ ci\ Pt\ e2\ h\ (stk,\ loc,\ pc,\ xcp)\ (stk',\ loc',\ pc',\ xcp')$
 $\implies \tauExec\text{-movet}\ ci\ Pt\ (e\ \llcorner bop \llcorner e2)\ h\ ((stk\ @\ [v]),\ loc,\ (length\ (compE2\ e) + pc),\ xcp)\ ((stk'\ @\ [v]),\ loc',\ (length\ (compE2\ e) + pc'),\ xcp')$
 ⟨proof⟩

lemma *LAss- τ ExecI*:

$\tauExec\text{-movet}\ ci\ Pt\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ Pt\ (V := e)\ h\ s\ s'$
 ⟨proof⟩

lemma *AAcc- τ ExecI1*:

$\tauExec\text{-movet}\ ci\ Pt\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ Pt\ (e[i])\ h\ s\ s'$
 ⟨proof⟩

lemma *AAcc- τ ExecI2*:

$\tauExec\text{-movet}\ ci\ Pt\ i\ h\ (stk,\ loc,\ pc,\ xcp)\ (stk',\ loc',\ pc',\ xcp')$
 $\implies \tauExec\text{-movet}\ ci\ Pt\ (a[i])\ h\ ((stk\ @\ [v]),\ loc,\ (length\ (compE2\ a) + pc),\ xcp)\ ((stk'\ @\ [v]),\ loc',\ (length\ (compE2\ a) + pc'),\ xcp')$
 ⟨proof⟩

lemma *AAss- τ ExecI1*:

$\tauExec\text{-movet}\ ci\ Pt\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ Pt\ (e[i] := e')\ h\ s\ s'$
 ⟨proof⟩

lemma *AAss- τ ExecI2*:

$\tauExec\text{-movet}\ ci\ Pt\ i\ h\ (stk,\ loc,\ pc,\ xcp)\ (stk',\ loc',\ pc',\ xcp')$
 $\implies \tauExec\text{-movet}\ ci\ Pt\ (a[i] := e)\ h\ ((stk\ @\ [v]),\ loc,\ (length\ (compE2\ a) + pc),\ xcp)\ ((stk'\ @\ [v]),\ loc',\ (length\ (compE2\ a) + pc'),\ xcp')$
 ⟨proof⟩

lemma *AAss- τ ExecI3*:

$\tauExec\text{-movet}\ ci\ Pt\ e\ h\ (stk,\ loc,\ pc,\ xcp)\ (stk',\ loc',\ pc',\ xcp')$
 $\implies \tauExec\text{-movet}\ ci\ Pt\ (a[i] := e)\ h\ ((stk\ @\ [v,\ v']),\ loc,\ (length\ (compE2\ a) + length\ (compE2\ i) + pc),\ xcp)\ ((stk'\ @\ [v,\ v']),\ loc',\ (length\ (compE2\ a) + length\ (compE2\ i) + pc'),\ xcp')$
 ⟨proof⟩

lemma *ALength- τ ExecI*:

$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ (e\cdot\text{length})\ h\ s\ s'$
 \langle proof \rangle

lemma *FAcc- \tauExec I:*

$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ (e\cdot F\{D\})\ h\ s\ s'$
 \langle proof \rangle

lemma *FAss- \tauExec I1:*

$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ (e\cdot F\{D\} := e')\ h\ s\ s'$
 \langle proof \rangle

lemma *FAss- \tauExec I2:*

$\tauExec\text{-movet}\ ci\ P\ t\ e' h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$
 $\implies \tauExec\text{-movet}\ ci\ P\ t\ (e\cdot F\{D\} := e')\ h\ ((stk\ @\ [v]), loc, (\text{length}\ (compE2\ e) + pc), xcp)\ ((stk'\ @\ [v]), loc', (\text{length}\ (compE2\ e) + pc'), xcp')$
 \langle proof \rangle

lemma *CAS- \tauExec I1:*

$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ (e\cdot\text{compareAndSwap}(D\cdot F, e', e''))\ h\ s\ s'$
 \langle proof \rangle

lemma *CAS- \tauExec I2:*

$\tauExec\text{-movet}\ ci\ P\ t\ e' h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$
 $\implies \tauExec\text{-movet}\ ci\ P\ t\ (e\cdot\text{compareAndSwap}(D\cdot F, e', e''))\ h\ ((stk\ @\ [v]), loc, (\text{length}\ (compE2\ e) + pc), xcp)\ ((stk'\ @\ [v]), loc', (\text{length}\ (compE2\ e) + pc'), xcp')$
 \langle proof \rangle

lemma *CAS- \tauExec I3:*

$\tauExec\text{-movet}\ ci\ P\ t\ e'' h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$
 $\implies \tauExec\text{-movet}\ ci\ P\ t\ (e\cdot\text{compareAndSwap}(D\cdot F, e', e''))\ h\ ((stk\ @\ [v, v']), loc, (\text{length}\ (compE2\ e) + \text{length}\ (compE2\ e') + pc), xcp)\ ((stk'\ @\ [v, v']), loc', (\text{length}\ (compE2\ e) + \text{length}\ (compE2\ e') + pc'), xcp')$
 \langle proof \rangle

lemma *Call- \tauExec I1:*

$\tauExec\text{-movet}\ ci\ P\ t\ obj\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ (obj\cdot M'(es))\ h\ s\ s'$
 \langle proof \rangle

lemma *Call- \tauExec I2:*

$\tauExec\text{-movet}\ ci\ P\ t\ es\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$
 $\implies \tauExec\text{-movet}\ ci\ P\ t\ (obj\cdot M'(es))\ h\ ((stk\ @\ [v]), loc, (\text{length}\ (compE2\ obj) + pc), xcp)\ ((stk'\ @\ [v]), loc', (\text{length}\ (compE2\ obj) + pc'), xcp')$
 \langle proof \rangle

lemma *Block- \tauExec I-Some:*

$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$
 $\implies \tauExec\text{-movet}\ ci\ P\ t\ \{V:T=[v]; e\}\ h\ (stk, loc, (\text{Suc}\ (\text{Suc}\ pc)), xcp)\ (stk', loc', (\text{Suc}\ (\text{Suc}\ pc'))), xcp'$
 \langle proof \rangle

lemma *Block- \tauExec I-None:*

$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ \{V:T=None; e\}\ h\ s\ s'$
 \langle proof \rangle

lemma *Sync- τ ExecI1:*

$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ (sync\ \vee\ (e)\ e')\ h\ s\ s'$
 \langle proof \rangle

lemma *Insync- τ ExecI1:*

$\tauExec\text{-movet}\ ci\ P\ t\ e'\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp') \implies$
 $\tauExec\text{-movet}\ ci\ P\ t\ (sync\ \vee\ (e)\ e')\ h\ (stk, loc, (Suc\ (Suc\ (Suc\ (length\ (compE2\ e)\ +\ pc))))),$
 $xcp)\ (stk', loc', (Suc\ (Suc\ (Suc\ (length\ (compE2\ e)\ +\ pc')))), xcp')$
 \langle proof \rangle

lemma *Seq- τ ExecI1:*

$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ (e;;e')\ h\ s\ s'$
 \langle proof \rangle

lemma *Seq- τ ExecI2:*

$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp') \implies$
 $\tauExec\text{-movet}\ ci\ P\ t\ (e';;e)\ h\ (stk, loc, (Suc\ (length\ (compE2\ e')\ +\ pc)), xcp)\ (stk', loc', (Suc\ (length\ (compE2\ e')\ +\ pc'))), xcp')$
 \langle proof \rangle

lemma *Cond- τ ExecI1:*

$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ (if\ (e)\ e1\ else\ e2)\ h\ s\ s'$
 \langle proof \rangle

lemma *Cond- τ ExecI2:*

$\tauExec\text{-movet}\ ci\ P\ t\ e1\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp') \implies$
 $\tauExec\text{-movet}\ ci\ P\ t\ (if\ (e)\ e1\ else\ e2)\ h\ (stk, loc, (Suc\ (length\ (compE2\ e)\ +\ pc)), xcp)\ (stk', loc', (Suc\ (length\ (compE2\ e)\ +\ pc'))), xcp')$
 \langle proof \rangle

lemma *Cond- τ ExecI3:*

$\tauExec\text{-movet}\ ci\ P\ t\ e2\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp') \implies$
 $\tauExec\text{-movet}\ ci\ P\ t\ (if\ (e)\ e1\ else\ e2)\ h\ (stk, loc, (Suc\ (Suc\ (length\ (compE2\ e)\ +\ length\ (compE2\ e1)\ +\ pc))), xcp)\ (stk', loc', (Suc\ (Suc\ (length\ (compE2\ e)\ +\ length\ (compE2\ e1)\ +\ pc'))), xcp')$
 \langle proof \rangle

lemma *While- τ ExecI1:*

$\tauExec\text{-movet}\ ci\ P\ t\ c\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ (while\ (c)\ e)\ h\ s\ s'$
 \langle proof \rangle

lemma *While- τ ExecI2:*

$\tauExec\text{-movet}\ ci\ P\ t\ E\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp') \implies$
 $\tauExec\text{-movet}\ ci\ P\ t\ (while\ (c)\ E)\ h\ (stk, loc, (Suc\ (length\ (compE2\ c)\ +\ pc)), xcp)\ (stk', loc', (Suc\ (length\ (compE2\ c)\ +\ pc'))), xcp')$
 \langle proof \rangle

lemma *Throw- τ ExecI:*

$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ (throw\ e)\ h\ s\ s'$
 \langle proof \rangle

lemma *Try- τ ExecI1:*

$\tauExec\text{-movet}\ ci\ P\ t\ E\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ (try\ E\ catch\ (C'\ V)\ e)\ h\ s\ s'$
 \langle proof \rangle

lemma *Try- τ ExecI2*:

$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$
 $\implies \tauExec\text{-movet}\ ci\ P\ t\ (try\ E\ catch(C'\ V)\ e)\ h\ (stk, loc, (Suc\ (Suc\ (length\ (compE2\ E) + pc))),$
 $xcp)\ (stk', loc', (Suc\ (Suc\ (length\ (compE2\ E) + pc'))), xcp')$
 $\langle proof \rangle$

lemma *$\tauExec\text{-movesr-map-Val}$* :

$\tauExec\text{-movesr-a}\ P\ t\ (map\ Val\ vs)\ h\ ([], xs, 0, None)\ ((rev\ vs), xs, (length\ (compEs2\ (map\ Val\ vs))),$
 $None)$
 $\langle proof \rangle$

lemma *$\tauExec\text{-mover-blocks1}$ [simp]*:

$\tauExec\text{-mover}\ ci\ P\ t\ (blocks1\ n\ Ts\ body)\ h\ s\ s' = \tauExec\text{-mover}\ ci\ P\ t\ body\ h\ s\ s'$
 $\langle proof \rangle$

lemma *$\tauExec\text{-movet-blocks1}$ [simp]*:

$\tauExec\text{-movet}\ ci\ P\ t\ (blocks1\ n\ Ts\ body)\ h\ s\ s' = \tauExec\text{-movet}\ ci\ P\ t\ body\ h\ s\ s'$
 $\langle proof \rangle$

definition $\tauexec\text{-1} :: 'addr\ jvm\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow ('addr, 'heap)\ jvm\text{-state} \Rightarrow ('addr, 'heap)\ jvm\text{-state}$
 $\Rightarrow bool$

where $\tauexec\text{-1}\ P\ t\ \sigma\ \sigma' \longleftrightarrow exec\text{-1}\ P\ t\ \sigma\ \varepsilon\ \sigma' \wedge \tauMove2\ P\ \sigma$

lemma *$\tauexec\text{-1I}$ [intro]*:

$\llbracket exec\text{-1}\ P\ t\ \sigma\ \varepsilon\ \sigma'; \tauMove2\ P\ \sigma \rrbracket \implies \tauexec\text{-1}\ P\ t\ \sigma\ \sigma'$
 $\langle proof \rangle$

lemma *$\tauexec\text{-1E}$ [elim]*:

assumes $\tauexec\text{-1}\ P\ t\ \sigma\ \sigma'$

obtains $exec\text{-1}\ P\ t\ \sigma\ \varepsilon\ \sigma'\ \tauMove2\ P\ \sigma$

$\langle proof \rangle$

abbreviation $\tauExec\text{-1r} :: 'addr\ jvm\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow ('addr, 'heap)\ jvm\text{-state} \Rightarrow ('addr, 'heap)$
 $jvm\text{-state} \Rightarrow bool$

where $\tauExec\text{-1r}\ P\ t == (\tauexec\text{-1}\ P\ t)^{\hat{**}}$

abbreviation $\tauExec\text{-1t} :: 'addr\ jvm\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow ('addr, 'heap)\ jvm\text{-state} \Rightarrow ('addr, 'heap)$
 $jvm\text{-state} \Rightarrow bool$

where $\tauExec\text{-1t}\ P\ t == (\tauexec\text{-1}\ P\ t)^{\hat{++}}$

definition $\tauexec\text{-1-d} :: 'addr\ jvm\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow ('addr, 'heap)\ jvm\text{-state} \Rightarrow ('addr, 'heap)$
 $jvm\text{-state} \Rightarrow bool$

where $\tauexec\text{-1-d}\ P\ t\ \sigma\ \sigma' \longleftrightarrow exec\text{-1}\ P\ t\ \sigma\ \varepsilon\ \sigma' \wedge \tauMove2\ P\ \sigma \wedge check\ P\ \sigma$

lemma *$\tauexec\text{-1-dI}$ [intro]*:

$\llbracket exec\text{-1}\ P\ t\ \sigma\ \varepsilon\ \sigma'; check\ P\ \sigma; \tauMove2\ P\ \sigma \rrbracket \implies \tauexec\text{-1-d}\ P\ t\ \sigma\ \sigma'$
 $\langle proof \rangle$

lemma *$\tauexec\text{-1-dE}$ [elim]*:

assumes $\tauexec\text{-1-d}\ P\ t\ \sigma\ \sigma'$

obtains $exec\text{-1}\ P\ t\ \sigma\ \varepsilon\ \sigma'\ check\ P\ \sigma\ \tauMove2\ P\ \sigma$

$\langle proof \rangle$

abbreviation $\tau Exec\text{-}1\text{-}dr :: 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow bool$

where $\tau Exec\text{-}1\text{-}dr\ P\ t == (\tau exec\text{-}1\text{-}d\ P\ t) \hat{**}$

abbreviation $\tau Exec\text{-}1\text{-}dt :: 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow bool$

where $\tau Exec\text{-}1\text{-}dt\ P\ t == (\tau exec\text{-}1\text{-}d\ P\ t) \hat{+++}$

declare $compxE2\text{-}size\text{-}convus[simp\ del]\ compxEs2\text{-}size\text{-}convus[simp\ del]$

declare $compxE2\text{-}stack\text{-}xlift\text{-}convus[simp\ del]\ compxEs2\text{-}stack\text{-}xlift\text{-}convus[simp\ del]$

lemma $exec\text{-}instr\text{-}frs\text{-}offer$:

$(ta, xcp', h', (stk', loc', C, M, pc') \# frs) \in exec\text{-}instr\ ins\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$
 $\implies (ta, xcp', h', (stk', loc', C, M, pc') \# frs @ frs') \in exec\text{-}instr\ ins\ P\ t\ h\ stk\ loc\ C\ M\ pc\ (frs @ frs')$
 $\langle proof \rangle$

lemma $check\text{-}instr\text{-}frs\text{-}offer$:

$\llbracket check\text{-}instr\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs; ins \neq Return \rrbracket$
 $\implies check\text{-}instr\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ (frs @ frs')$
 $\langle proof \rangle$

lemma $exec\text{-}instr\text{-}CM\text{-}change$:

$(ta, xcp', h', (stk', loc', C, M, pc') \# frs) \in exec\text{-}instr\ ins\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$
 $\implies (ta, xcp', h', (stk', loc', C', M', pc') \# frs) \in exec\text{-}instr\ ins\ P\ t\ h\ stk\ loc\ C'\ M'\ pc\ frs$
 $\langle proof \rangle$

lemma $check\text{-}instr\text{-}CM\text{-}change$:

$\llbracket check\text{-}instr\ ins\ P\ h\ stk\ loc\ C\ M\ pc\ frs; ins \neq Return \rrbracket$
 $\implies check\text{-}instr\ ins\ P\ h\ stk\ loc\ C'\ M'\ pc\ frs$
 $\langle proof \rangle$

lemma $exec\text{-}move\text{-}exec\text{-}1$:

assumes $exec: exec\text{-}move\ ci\ P\ t\ body\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$
and sees: $P \vdash C\ sees\ M : Ts \rightarrow T = \lfloor body \rfloor\ in\ D$
shows $exec\text{-}1\ (compP2\ P)\ t\ (xcp, h, (stk, loc, C, M, pc) \# frs)\ ta\ (xcp', h', (stk', loc', C, M, pc') \# frs)$
 $\langle proof \rangle$

lemma $\tau exec\text{-}move\text{-}\tau exec\text{-}1$:

assumes $exec: \tau exec\text{-}move\ ci\ P\ t\ body\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$
and sees: $P \vdash C\ sees\ M : Ts \rightarrow T = \lfloor body \rfloor\ in\ D$
shows $\tau exec\text{-}1\ (compP2\ P)\ t\ (xcp, h, (stk, loc, C, M, pc) \# frs)\ (xcp', h, (stk', loc', C, M, pc') \# frs)$
 $\langle proof \rangle$

lemma $\tau Exec\text{-}mover\text{-}\tau Exec\text{-}1r$:

assumes $move: \tau Exec\text{-}mover\ ci\ P\ t\ body\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$
and sees: $P \vdash C\ sees\ M : Ts \rightarrow T = \lfloor body \rfloor\ in\ D$
shows $\tau Exec\text{-}1r\ (compP2\ P)\ t\ (xcp, h, (stk, loc, C, M, pc) \# frs')\ (xcp', h, (stk', loc', C, M, pc') \# frs')$
 $\langle proof \rangle$

lemma $\tau Exec\text{-}mover\text{-}\tau Exec\text{-}1t$:

assumes *move*: $\tau Exec\text{-}movet\ ci\ P\ t\ body\ h\ (stk,\ loc,\ pc,\ xcp)\ (stk',\ loc',\ pc',\ xcp')$
and *sees*: $P \vdash C\ sees\ M : Ts \rightarrow T = [body]$ in *D*
shows $\tau Exec\text{-}1t\ (compP2\ P)\ t\ (xcp,\ h,\ (stk,\ loc,\ C,\ M,\ pc)\ \# \text{ frs}')\ (xcp',\ h,\ (stk',\ loc',\ C,\ M,\ pc')\ \# \text{ frs}')$
 $\langle proof \rangle$

lemma $\tau Exec\text{-}1r\text{-}rtranclpD$:

$\tau Exec\text{-}1r\ P\ t\ (xcp,\ h,\ frs)\ (xcp',\ h',\ frs')$
 $\implies (\lambda((xcp,\ frs),\ h)\ ((xcp',\ frs'),\ h')).\ exec\text{-}1\ P\ t\ (xcp,\ h,\ frs)\ \varepsilon\ (xcp',\ h',\ frs') \wedge \tau Move2\ P\ (xcp,\ h,\ frs)\ \hat{\ }^{**}\ ((xcp,\ frs),\ h)\ ((xcp',\ frs'),\ h')$
 $\langle proof \rangle$

lemma $\tau Exec\text{-}1t\text{-}rtranclpD$:

$\tau Exec\text{-}1t\ P\ t\ (xcp,\ h,\ frs)\ (xcp',\ h',\ frs')$
 $\implies (\lambda((xcp,\ frs),\ h)\ ((xcp',\ frs'),\ h')).\ exec\text{-}1\ P\ t\ (xcp,\ h,\ frs)\ \varepsilon\ (xcp',\ h',\ frs') \wedge \tau Move2\ P\ (xcp,\ h,\ frs)\ \hat{\ }^{++}\ ((xcp,\ frs),\ h)\ ((xcp',\ frs'),\ h')$
 $\langle proof \rangle$

lemma *exec-meth-length-compE2-stack-xliftD*:

exec-meth ci P (compE2 e) (stack-xlift d (compxE2 e 0 0)) t h (stk, loc, pc, xcp) ta h' s'
 $\implies pc < length\ (compE2\ e)$
 $\langle proof \rangle$

lemma *exec-meth-length-pc-xt-Nil*:

exec-meth ci P ins [] t h (stk, loc, pc, xcp) ta h' s' $\implies pc < length\ ins$
 $\langle proof \rangle$

lemma *BinOp-exec2D*:

assumes *exec*: *exec-meth ci (compP2 P) (compE2 (e1 «bop» e2)) (compxE2 (e1 «bop» e2) 0 0) t h (stk @ [v1], loc, length (compE2 e1) + pc, xcp) ta h' (stk', loc', pc', xcp')*
and *pc*: $pc < length\ (compE2\ e2)$
shows *exec-meth ci (compP2 P) (compE2 e2) (stack-xlift (length [v1]) (compxE2 e2 0 0)) t h (stk @ [v1], loc, pc, xcp) ta h' (stk', loc', pc' - length (compE2 e1), xcp') $\wedge pc' \geq length\ (compE2\ e1)$*
 $\langle proof \rangle$

lemma *Call-execParamD*:

assumes *exec*: *exec-meth ci (compP2 P) (compE2 (obj.M'(ps))) (compxE2 (obj.M'(ps)) 0 0) t h (stk @ [v], loc, length (compE2 obj) + pc, xcp) ta h' (stk', loc', pc', xcp')*
and *pc*: $pc < length\ (compEs2\ ps)$
shows *exec-meth ci (compP2 P) (compEs2 ps) (stack-xlift (length [v]) (compxEs2 ps 0 0)) t h (stk @ [v], loc, pc, xcp) ta h' (stk', loc', pc' - length (compE2 obj), xcp') $\wedge pc' \geq length\ (compE2\ obj)$*
 $\langle proof \rangle$

lemma *exec-move-length-compE2D [dest]*:

exec-move ci P t e h (stk, loc, pc, xcp) ta h' s' $\implies pc < length\ (compE2\ e)$
 $\langle proof \rangle$

lemma *exec-moves-length-compEs2D [dest]*:

exec-moves ci P t es h (stk, loc, pc, xcp) ta h' s' $\implies pc < length\ (compEs2\ es)$
 $\langle proof \rangle$

lemma *exec-meth-ci-appD*:

$\llbracket exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk,\ loc,\ pc,\ None)\ ta\ h'\ fr' \rrbracket$
 $\implies ci\text{-}app\ ci\ (ins\ !\ pc)\ P\ h\ stk\ loc\ undefined\ undefined\ pc\ \llbracket$

<proof>

lemma *exec-move-ci-appD:*

exec-move ci P t E h (stk, loc, pc, None) ta h' fr'

\implies *ci-app ci (compE2 E ! pc) (compP2 P) h stk loc undefined undefined pc []*

<proof>

lemma *exec-moves-ci-appD:*

exec-moves ci P t Es h (stk, loc, pc, None) ta h' fr'

\implies *ci-app ci (compEs2 Es ! pc) (compP2 P) h stk loc undefined undefined pc []*

<proof>

lemma *τ instr-stk-append-check:*

check-instr' i P h stk loc C M pc frs \implies τ instr P h (stk @ vs) i = τ instr P h stk i

<proof>

lemma *τ instr-stk-drop-exec-move:*

exec-move ci P t e h (stk, loc, pc, None) ta h' fr'

\implies *τ instr (compP2 P) h (stk @ vs) (compE2 e ! pc) = τ instr (compP2 P) h stk (compE2 e ! pc)*

<proof>

lemma *τ instr-stk-drop-exec-moves:*

exec-moves ci P t es h (stk, loc, pc, None) ta h' fr'

\implies *τ instr (compP2 P) h (stk @ vs) (compEs2 es ! pc) = τ instr (compP2 P) h stk (compEs2 es !*

pc)

<proof>

end

end

7.16 The delay bisimulation between intermediate language and JVM

theory *J1JVMBisim imports*

Execs

../BV/BVNoTypeError

J1

begin

declare *Listn.lesub-list-impl-same-size[simp del]*

lemma (**in** *JVM-heap-conf-base'*) *τ exec-1- τ exec-1-d:*

\llbracket *wf-jvm-prog Φ P; τ exec-1 P t σ σ' ; $\Phi \mid - t:\sigma$ [ok] $\rrbracket \implies \tau$ exec-1-d P t σ σ'*

<proof>

context *JVM-conf-read begin*

lemma *τ Exec-1r-preserves-correct-state:*

assumes *wf: wf-jvm-prog Φ P*

and *exec: τ Exec-1r P t σ σ'*

shows *$\Phi \mid - t:\sigma$ [ok] $\implies \Phi \mid - t:\sigma'$ [ok]*

<proof>

lemma $\tau Exec\text{-}1t\text{-preserves-correct-state}$:

assumes $wf: wf\text{-jvm-prog}_{\Phi} P$
and $exec: \tau Exec\text{-}1t P t \sigma \sigma'$
shows $\Phi \mid- t:\sigma [ok] \implies \Phi \mid- t:\sigma' [ok]$

$\langle proof \rangle$

lemma $\tau Exec\text{-}1r\text{-}\tau Exec\text{-}1\text{-dr}$:

assumes $wf: wf\text{-jvm-prog}_{\Phi} P$
shows $\llbracket \tau Exec\text{-}1r P t \sigma \sigma'; \Phi \mid- t:\sigma [ok] \rrbracket \implies \tau Exec\text{-}1\text{-dr} P t \sigma \sigma'$

$\langle proof \rangle$

lemma $\tau Exec\text{-}1t\text{-}\tau Exec\text{-}1\text{-dt}$:

assumes $wf: wf\text{-jvm-prog}_{\Phi} P$
shows $\llbracket \tau Exec\text{-}1t P t \sigma \sigma'; \Phi \mid- t:\sigma [ok] \rrbracket \implies \tau Exec\text{-}1\text{-dt} P t \sigma \sigma'$

$\langle proof \rangle$

lemma $\tau Exec\text{-}1\text{-dr-preserves-correct-state}$:

assumes $wf: wf\text{-jvm-prog}_{\Phi} P$
and $exec: \tau Exec\text{-}1\text{-dr} P t \sigma \sigma'$
shows $\Phi \mid- t:\sigma [ok] \implies \Phi \mid- t:\sigma' [ok]$

$\langle proof \rangle$

lemma $\tau Exec\text{-}1\text{-dt-preserves-correct-state}$:

assumes $wf: wf\text{-jvm-prog}_{\Phi} P$
and $exec: \tau Exec\text{-}1\text{-dt} P t \sigma \sigma'$
shows $\Phi \mid- t:\sigma [ok] \implies \Phi \mid- t:\sigma' [ok]$

$\langle proof \rangle$

end

locale $J1\text{-JVM-heap-base} =$

$J1\text{-heap-base} +$
 $JVM\text{-heap-base} +$
constrains $addr2thread\text{-}id :: ('addr :: addr) \Rightarrow 'thread\text{-}id$
and $thread\text{-}id2addr :: 'thread\text{-}id \Rightarrow 'addr$
and $spurious\text{-}wakeups :: bool$
and $empty\text{-}heap :: 'heap$
and $allocate :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) set$
and $typeof\text{-}addr :: 'heap \Rightarrow 'addr \rightarrow htype$
and $heap\text{-}read :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-}loc \Rightarrow 'addr val \Rightarrow bool$
and $heap\text{-}write :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-}loc \Rightarrow 'addr val \Rightarrow 'heap \Rightarrow bool$

begin

inductive $bisim1 ::$

$'m prog \Rightarrow 'heap \Rightarrow 'addr expr1 \Rightarrow ('addr expr1 \times 'addr locals1)$
 $\Rightarrow ('addr val list \times 'addr val list \times pc \times 'addr option) \Rightarrow bool$

and $bisims1 ::$

$'m prog \Rightarrow 'heap \Rightarrow 'addr expr1 list \Rightarrow ('addr expr1 list \times 'addr locals1)$
 $\Rightarrow ('addr val list \times 'addr val list \times pc \times 'addr option) \Rightarrow bool$

and $bisim1\text{-}syntax ::$

$'m prog \Rightarrow 'addr expr1 \Rightarrow 'heap \Rightarrow ('addr expr1 \times 'addr locals1)$

$\Rightarrow ('addr\ val\ list \times 'addr\ val\ list \times pc \times 'addr\ option) \Rightarrow bool$
 $(\langle -, -, \vdash - \leftrightarrow - \rangle \rightarrow [50, 0, 0, 0, 50] 100)$

and *bisims1-syntax* ::

$'m\ prog \Rightarrow 'addr\ expr1\ list \Rightarrow 'heap \Rightarrow ('addr\ expr1\ list \times 'addr\ locals1)$

$\Rightarrow ('addr\ val\ list \times 'addr\ val\ list \times pc \times 'addr\ option) \Rightarrow bool$

$(\langle -, -, \vdash - [\leftrightarrow] - \rangle \rightarrow [50, 0, 0, 0, 50] 100)$

for $P :: 'm\ prog$ **and** $h :: 'heap$

where

$P, e, h \vdash es \leftrightarrow s \equiv bisim1\ P\ h\ e\ es\ s$

| $P, es, h \vdash esxs [\leftrightarrow] s \equiv bisims1\ P\ h\ es\ esxs\ s$

| *bisim1Val2*:

$pc = length\ (compE2\ e) \Longrightarrow P, e, h \vdash (Val\ v, xs) \leftrightarrow (v\ \#\ [] , xs, pc, None)$

| *bisim1New*:

$P, new\ C, h \vdash (new\ C, xs) \leftrightarrow ([], xs, 0, None)$

| *bisim1NewThrow*:

$P, new\ C, h \vdash (THROW\ OutOfMemory, xs) \leftrightarrow ([], xs, 0, [addr-of-sys-xcpt\ OutOfMemory])$

| *bisim1NewArray*:

$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \Longrightarrow P, newA\ T[e], h \vdash (newA\ T[e], xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1NewArrayThrow*:

$P, e, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \Longrightarrow P, newA\ T[e], h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$

| *bisim1NewArrayFail*:

$P, newA\ T[e], h \vdash (Throw\ a, xs) \leftrightarrow ([v], xs, length\ (compE2\ e), [a])$

| *bisim1Cast*:

$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \Longrightarrow P, Cast\ T\ e, h \vdash (Cast\ T\ e', xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1CastThrow*:

$P, e, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \Longrightarrow P, Cast\ T\ e, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$

| *bisim1CastFail*:

$P, Cast\ T\ e, h \vdash (THROW\ ClassCast, xs) \leftrightarrow ([v], xs, length\ (compE2\ e), [addr-of-sys-xcpt\ ClassCast])$

| *bisim1InstanceOf*:

$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \Longrightarrow P, e\ instanceof\ T, h \vdash (e'\ instanceof\ T, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1InstanceOfThrow*:

$P, e, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \Longrightarrow P, e\ instanceof\ T, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$

- | *bisim1Val*: $P, \text{Val } v, h \vdash (\text{Val } v, xs) \leftrightarrow ([], xs, 0, \text{None})$
- | *bisim1Var*: $P, \text{Var } V, h \vdash (\text{Var } V, xs) \leftrightarrow ([], xs, 0, \text{None})$
- | *bisim1BinOp1*:
 $P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (e' \llbracket \text{bop} \rrbracket e2, xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1BinOp2*:
 $P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Val } v1 \llbracket \text{bop} \rrbracket e', xs) \leftrightarrow (stk @ [v1], loc, \text{length}(\text{compE2 } e1) + pc, xcp)$
- | *bisim1BinOpThrow1*:
 $P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$
 $\implies P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$
- | *bisim1BinOpThrow2*:
 $P, e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$
 $\implies P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk @ [v1], loc, \text{length}(\text{compE2 } e1) + pc, [a])$
- | *bisim1BinOpThrow*:
 $P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Throw } a, xs) \leftrightarrow ([v1, v2], xs, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), [a])$
- | *bisim1LAss1*:
 $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, V := e, h \vdash (V := e', xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1LAss2*:
 $P, V := e, h \vdash (\text{unit}, xs) \leftrightarrow ([], xs, \text{Suc}(\text{length}(\text{compE2 } e)), \text{None})$
- | *bisim1LAssThrow*:
 $P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \implies P, V := e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$
- | *bisim1AAcc1*:
 $P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, a[i], h \vdash (a'[i], xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1AAcc2*:
 $P, i, h \vdash (i', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, a[i], h \vdash (\text{Val } v[i'], xs) \leftrightarrow (stk @ [v], loc, \text{length}(\text{compE2 } a) + pc, xcp)$
- | *bisim1AAccThrow1*:
 $P, a, h \vdash (\text{Throw } ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, a[i], h \vdash (\text{Throw } ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
- | *bisim1AAccThrow2*:
 $P, i, h \vdash (\text{Throw } ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, a[i], h \vdash (\text{Throw } ad, xs) \leftrightarrow (stk @ [v], loc, \text{length}(\text{compE2 } a) + pc, [ad])$
- | *bisim1AAccFail*:
 $P, a[i], h \vdash (\text{Throw } ad, xs) \leftrightarrow ([v, v'], xs, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i), [ad])$
- | *bisim1AAss1*:

$P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, a[i] := e, h \vdash (a'[i] := e, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1AAss2*:

$P, i, h \vdash (i', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, a[i] := e, h \vdash (Val\ v\ [i'] := e, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ a) + pc, xcp)$

| *bisim1AAss3*:

$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, a[i] := e, h \vdash (Val\ v\ [Val\ v'] := e', xs) \leftrightarrow (stk\ @\ [v', v], loc, length\ (compE2\ a) + length\ (compE2\ i) + pc, xcp)$

| *bisim1AAssThrow1*:

$P, a, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, a[i] := e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$

| *bisim1AAssThrow2*:

$P, i, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, a[i] := e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ a) + pc, [ad])$

| *bisim1AAssThrow3*:

$P, e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, a[i] := e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v', v], loc, length\ (compE2\ a) + length\ (compE2\ i) + pc, [ad])$

| *bisim1AAssFail*:

$P, a[i] := e, h \vdash (Throw\ ad, xs) \leftrightarrow ([v', v, v'], xs, length\ (compE2\ a) + length\ (compE2\ i) + length\ (compE2\ e), [ad])$

| *bisim1AAss4*:

$P, a[i] := e, h \vdash (unit, xs) \leftrightarrow ([], xs, Suc\ (length\ (compE2\ a) + length\ (compE2\ i) + length\ (compE2\ e)), None)$

| *bisim1ALength*:

$P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, a \cdot length, h \vdash (a' \cdot length, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1ALengthThrow*:

$P, a, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, a \cdot length, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$

| *bisim1ALengthNull*:

$P, a \cdot length, h \vdash (THROW\ NullPointer, xs) \leftrightarrow ([Null], xs, length\ (compE2\ a), [addr-of-sys-xcpt\ NullPointer])$

| *bisim1FAcc*:

$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, e \cdot F\{D\}, h \vdash (e' \cdot F\{D\}, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1FAccThrow*:

$P, e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$

$$\implies P, e \cdot F\{D\}, h \vdash (\text{Throw } ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$$

| *bisim1FAccNull*:

$$P, e \cdot F\{D\}, h \vdash (\text{THROW } \text{NullPointer}, xs) \leftrightarrow ([\text{Null}], xs, \text{length}(\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt } \text{NullPointer} \rfloor)$$

| *bisim1FAss1*:

$$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, e \cdot F\{D\} := e2, h \vdash (e' \cdot F\{D\} := e2, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1FAss2*:

$$P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, e \cdot F\{D\} := e2, h \vdash (\text{Val } v \cdot F\{D\} := e', xs) \leftrightarrow (stk @ [v], loc, \text{length}(\text{compE2 } e) + pc, xcp)$$

| *bisim1FAssThrow1*:

$$P, e, h \vdash (\text{Throw } ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor) \\ \implies P, e \cdot F\{D\} := e2, h \vdash (\text{Throw } ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$$

| *bisim1FAssThrow2*:

$$P, e2, h \vdash (\text{Throw } ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor) \\ \implies P, e \cdot F\{D\} := e2, h \vdash (\text{Throw } ad, xs) \leftrightarrow (stk @ [v], loc, \text{length}(\text{compE2 } e) + pc, \lfloor ad \rfloor)$$

| *bisim1FAssNull*:

$$P, e \cdot F\{D\} := e2, h \vdash (\text{THROW } \text{NullPointer}, xs) \leftrightarrow ([v, \text{Null}], xs, \text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e2), \lfloor \text{addr-of-sys-xcpt } \text{NullPointer} \rfloor)$$

| *bisim1FAss3*:

$$P, e \cdot F\{D\} := e2, h \vdash (\text{unit}, xs) \leftrightarrow ([], xs, \text{Suc}(\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e2)), \text{None})$$

| *bisim1CAS1*:

$$P, e1, h \vdash (e1', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (e1' \cdot \text{compareAndSwap}(D \cdot F, e2, e3), xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1CAS2*:

$$P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e2', e3), xs) \leftrightarrow (stk @ [v], loc, \text{length}(\text{compE2 } e1) + pc, xcp)$$

| *bisim1CAS3*:

$$P, e3, h \vdash (e3', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e3'), xs) \leftrightarrow (stk @ [v', v], loc, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) + pc, xcp)$$

| *bisim1CASThrow1*:

$$P, e1, h \vdash (\text{Throw } ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor) \\ \implies P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{Throw } ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$$

| *bisim1CASThrow2*:

$$P, e2, h \vdash (\text{Throw } ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor) \\ \implies P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{Throw } ad, xs) \leftrightarrow (stk @ [v], loc, \text{length}(\text{compE2 } e1) + pc, \lfloor ad \rfloor)$$

| *bisim1CASThrow3*:

$P, e3, h \vdash (\text{Throw } ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$
 $\implies P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{Throw } ad, xs) \leftrightarrow (stk \ @ \ [v', v], loc, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) + pc, \lfloor ad \rfloor)$

| *bisim1CASFail*:

$P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{Throw } ad, xs) \leftrightarrow ([v', v, v'], xs, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) + \text{length}(\text{compE2 } e3), \lfloor ad \rfloor)$

| *bisim1Call1*:

$P, obj, h \vdash (obj', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, obj \cdot M(ps), h \vdash (obj' \cdot M(ps), xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1CallParams*:

$P, ps, h \vdash (ps', xs) \llbracket \leftrightarrow \rrbracket (stk, loc, pc, xcp)$
 $\implies P, obj \cdot M(ps), h \vdash (\text{Val } v \cdot M(ps'), xs) \leftrightarrow (stk \ @ \ [v], loc, \text{length}(\text{compE2 } obj) + pc, xcp)$

| *bisim1CallThrowObj*:

$P, obj, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$
 $\implies P, obj \cdot M(ps), h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$

| *bisim1CallThrowParams*:

$P, ps, h \vdash (\text{map Val } vs \ @ \ \text{Throw } a \ \# \ ps', xs) \llbracket \leftrightarrow \rrbracket (stk, loc, pc, \lfloor a \rfloor)$
 $\implies P, obj \cdot M(ps), h \vdash (\text{Throw } a, xs) \leftrightarrow (stk \ @ \ [v], loc, \text{length}(\text{compE2 } obj) + pc, \lfloor a \rfloor)$

| *bisim1CallThrow*:

$\text{length } ps = \text{length } vs$
 $\implies P, obj \cdot M(ps), h \vdash (\text{Throw } a, xs) \leftrightarrow (vs \ @ \ [v], xs, \text{length}(\text{compE2 } obj) + \text{length}(\text{compEs2 } ps), \lfloor a \rfloor)$

| *bisim1BlockSome1*:

$P, \{V:T=[v]; e\}, h \vdash (\{V:T=[v]; e\}, xs) \leftrightarrow ([], xs, 0, \text{None})$

| *bisim1BlockSome2*:

$P, \{V:T=[v]; e\}, h \vdash (\{V:T=[v]; e\}, xs) \leftrightarrow ([v], xs, \text{Suc } 0, \text{None})$

| *bisim1BlockSome4*:

$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, \{V:T=[v]; e\}, h \vdash (\{V:T=\text{None}; e'\}, xs) \leftrightarrow (stk, loc, \text{Suc}(\text{Suc } pc), xcp)$

| *bisim1BlockThrowSome*:

$P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$
 $\implies P, \{V:T=[v]; e\}, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, \text{Suc}(\text{Suc } pc), \lfloor a \rfloor)$

| *bisim1BlockNone*:

$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, \{V:T=\text{None}; e\}, h \vdash (\{V:T=\text{None}; e'\}, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1BlockThrowNone*:

$P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$
 $\implies P, \{V:T=\text{None}; e\}, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$

- | *bisim1Sync1*:
 $P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, sync_V (e1) e2, h \vdash (sync_V (e') e2, xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1Sync2*:
 $P, sync_V (e1) e2, h \vdash (sync_V (Val v) e2, xs) \leftrightarrow ([v, v], xs, Suc (length (compE2 e1)), None)$
- | *bisim1Sync3*:
 $P, sync_V (e1) e2, h \vdash (sync_V (Val v) e2, xs) \leftrightarrow ([v], xs[V := v], Suc (Suc (length (compE2 e1))), None)$
- | *bisim1Sync4*:
 $P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, sync_V (e1) e2, h \vdash (insync_V (a) e', xs) \leftrightarrow (stk, loc, Suc (Suc (Suc (length (compE2 e1) + pc))), xcp)$
- | *bisim1Sync5*:
 $P, sync_V (e1) e2, h \vdash (insync_V (a) Val v, xs) \leftrightarrow ([xs ! V, v], xs, 4 + length (compE2 e1) + length (compE2 e2), None)$
- | *bisim1Sync6*:
 $P, sync_V (e1) e2, h \vdash (Val v, xs) \leftrightarrow ([v], xs, 5 + length (compE2 e1) + length (compE2 e2), None)$
- | *bisim1Sync7*:
 $P, sync_V (e1) e2, h \vdash (insync_V (a) Throw a', xs) \leftrightarrow ([Addr a'], xs, 6 + length (compE2 e1) + length (compE2 e2), None)$
- | *bisim1Sync8*:
 $P, sync_V (e1) e2, h \vdash (insync_V (a) Throw a', xs) \leftrightarrow ([xs ! V, Addr a'], xs, 7 + length (compE2 e1) + length (compE2 e2), None)$
- | *bisim1Sync9*:
 $P, sync_V (e1) e2, h \vdash (Throw a, xs) \leftrightarrow ([Addr a], xs, 8 + length (compE2 e1) + length (compE2 e2), None)$
- | *bisim1Sync10*:
 $P, sync_V (e1) e2, h \vdash (Throw a, xs) \leftrightarrow ([Addr a], xs, 8 + length (compE2 e1) + length (compE2 e2), [a])$
- | *bisim1Sync11*:
 $P, sync_V (e1) e2, h \vdash (THROW NullPointer, xs) \leftrightarrow ([Null], xs, Suc (Suc (length (compE2 e1))), [addr-of-sys-xcpt NullPointer])$
- | *bisim1Sync12*:
 $P, sync_V (e1) e2, h \vdash (Throw a, xs) \leftrightarrow ([v, v'], xs, 4 + length (compE2 e1) + length (compE2 e2), [a])$
- | *bisim1Sync14*:
 $P, sync_V (e1) e2, h \vdash (Throw a, xs) \leftrightarrow ([v, Addr a'], xs, 7 + length (compE2 e1) + length (compE2 e2), [a])$
- | *bisim1SyncThrow*:
 $P, e1, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a])$

$\implies P, \text{sync}_V (e1) e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$

| *bisim1InSync*: — This rule only exists such that $P, e, h \vdash (e, xs) \leftrightarrow (\lfloor \rfloor, xs, 0, \text{None})$ holds for all e
 $P, \text{insync}_V (a) e, h \vdash (\text{insync}_V (a) e, xs) \leftrightarrow (\lfloor \rfloor, xs, 0, \text{None})$

| *bisim1Seq1*:

$P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, e1;;e2, h \vdash (e';;e2, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1SeqThrow1*:

$P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \implies P, e1;;e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$

| *bisim1Seq2*:

$P, e2, h \vdash \text{exs} \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, e1;;e2, h \vdash \text{exs} \leftrightarrow (stk, loc, \text{Suc} (\text{length} (\text{compE2 } e1) + pc), xcp)$

| *bisim1Cond1*:

$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, \text{if } (e) e1 \text{ else } e2, h \vdash (\text{if } (e) e1 \text{ else } e2, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1CondThen*:

$P, e1, h \vdash \text{exs} \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, \text{if } (e) e1 \text{ else } e2, h \vdash \text{exs} \leftrightarrow (stk, loc, \text{Suc} (\text{length} (\text{compE2 } e) + pc), xcp)$

| *bisim1CondElse*:

$P, e2, h \vdash \text{exs} \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, \text{if } (e) e1 \text{ else } e2, h \vdash \text{exs} \leftrightarrow (stk, loc, \text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e) + \text{length} (\text{compE2 } e1) + pc)), xcp)$

| *bisim1CondThrow*:

$P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$
 $\implies P, \text{if } (e) e1 \text{ else } e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$

| *bisim1While1*:

$P, \text{while } (c) e, h \vdash (\text{while } (c) e, xs) \leftrightarrow (\lfloor \rfloor, xs, 0, \text{None})$

| *bisim1While3*:

$P, c, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, \text{while } (c) e, h \vdash (\text{if } (e') (e;; \text{while } (c) e) \text{ else } \text{unit}, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1While4*:

$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, \text{while } (c) e, h \vdash (e';; \text{while } (c) e, xs) \leftrightarrow (stk, loc, \text{Suc} (\text{length} (\text{compE2 } c) + pc), xcp)$

| *bisim1While6*:

$P, \text{while } (c) e, h \vdash (\text{while } (c) e, xs) \leftrightarrow (\lfloor \rfloor, xs, \text{Suc} (\text{Suc} (\text{length} (\text{compE2 } c) + \text{length} (\text{compE2 } e))), \text{None})$

| *bisim1While7*:

$P, \text{while } (c) e, h \vdash (\text{unit}, xs) \leftrightarrow (\lfloor \rfloor, xs, \text{Suc} (\text{Suc} (\text{Suc} (\text{length} (\text{compE2 } c) + \text{length} (\text{compE2 } e))), \text{None})$

| *bisim1WhileThrow1*:

$$P, c, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \\ \implies P, \text{while } (c) e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$$

| *bisim1WhileThrow2*:

$$P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \\ \implies P, \text{while } (c) e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, \text{Suc } (\text{length } (\text{compE2 } c) + pc), [a])$$

| *bisim1Throw1*:

$$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, \text{throw } e, h \vdash (\text{throw } e', xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1Throw2*:

$$P, \text{throw } e, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], xs, \text{length } (\text{compE2 } e), [a])$$

| *bisim1ThrowNull*:

$$P, \text{throw } e, h \vdash (\text{THROW } \text{NullPointer}, xs) \leftrightarrow ([\text{Null}], xs, \text{length } (\text{compE2 } e), [\text{addr-of-sys-xcpt } \text{NullPointer}])$$

| *bisim1ThrowThrow*:

$$P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \implies P, \text{throw } e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$$

| *bisim1Try*:

$$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, \text{try } e \text{ catch}(C V) e2, h \vdash (\text{try } e' \text{ catch}(C V) e2, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1TryCatch1*:

$$\llbracket P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]); \text{typeof-addr } h a = [\text{Class-type } C']; P \vdash C' \preceq^* C \rrbracket \\ \implies P, \text{try } e \text{ catch}(C V) e2, h \vdash (\{V:\text{Class } C=\text{None}; e2\}, xs[V := \text{Addr } a]) \leftrightarrow ([\text{Addr } a], loc, \text{Suc } (\text{length } (\text{compE2 } e)), \text{None})$$

| *bisim1TryCatch2*:

$$P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, \text{try } e \text{ catch}(C V) e2, h \vdash (\{V:\text{Class } C=\text{None}; e'\}, xs) \leftrightarrow (stk, loc, \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + pc)), xcp)$$

| *bisim1TryFail*:

$$\llbracket P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]); \text{typeof-addr } h a = [\text{Class-type } C']; \neg P \vdash C' \preceq^* C \rrbracket \\ \implies P, \text{try } e \text{ catch}(C V) e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$$

| *bisim1TryCatchThrow*:

$$P, e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \\ \implies P, \text{try } e \text{ catch}(C V) e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + pc)), [a])$$

| *bisims1Nil*: $P, [], h \vdash ([], xs) [\leftrightarrow] ([], xs, 0, \text{None})$

| *bisims1List1*:

$$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, e\#es, h \vdash (e'\#es, xs) [\leftrightarrow] (stk, loc, pc, xcp)$$

| *bisims1List2*:

$$P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp)$$

$$\implies P, e \# es, h \vdash (\text{Val } v \# es', xs) [\leftrightarrow] (stk \ @ \ [v], loc, length \ (\text{compE2 } e) + pc, xcp)$$

inductive-cases *bisim1-cases*:

$$P, e, h \vdash (\text{Val } v, xs) \leftrightarrow (stk, loc, pc, xcp)$$

lemma *bisim1-refl*: $P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, \text{None})$

and *bisims1-refl*: $P, es, h \vdash (es, xs) [\leftrightarrow] ([], xs, 0, \text{None})$

<proof>

lemma *bisims1-lengthD*: $P, es, h \vdash (es', xs) [\leftrightarrow] s \implies length \ es = length \ es'$

<proof>

Derive an alternative induction rule for *bisim1* such that (i) induction hypothesis are generated for all subexpressions and (ii) the number of surrounding blocks is passed through.

inductive *bisim1'* ::

$$'m \ prog \Rightarrow 'heap \Rightarrow 'addr \ expr1 \Rightarrow nat \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \\ \Rightarrow ('addr \ val \ list \times 'addr \ val \ list \times pc \times 'addr \ option) \Rightarrow bool$$

and *bisims1'* ::

$$'m \ prog \Rightarrow 'heap \Rightarrow 'addr \ expr1 \ list \Rightarrow nat \Rightarrow ('addr \ expr1 \ list \times 'addr \ locals1) \\ \Rightarrow ('addr \ val \ list \times 'addr \ val \ list \times pc \times 'addr \ option) \Rightarrow bool$$

and *bisim1'-syntax* ::

$$'m \ prog \Rightarrow 'addr \ expr1 \Rightarrow nat \Rightarrow 'heap \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \\ \Rightarrow ('addr \ val \ list \times 'addr \ val \ list \times pc \times 'addr \ option) \Rightarrow bool \\ (\langle -, -, -, - \vdash'' - \leftrightarrow - \rangle [50, 0, 0, 0, 0, 50] \ 100)$$

and *bisims1'-syntax* ::

$$'m \ prog \Rightarrow 'addr \ expr1 \ list \Rightarrow nat \Rightarrow 'heap \Rightarrow ('addr \ expr1 \ list \times 'addr \ val \ list) \\ \Rightarrow ('addr \ val \ list \times 'addr \ val \ list \times pc \times 'addr \ option) \Rightarrow bool \\ (\langle -, -, -, - \vdash'' - [\leftrightarrow] - \rangle [50, 0, 0, 0, 0, 50] \ 100)$$

for *P* :: *'m prog* **and** *h* :: *'heap*

where

$$P, e, n, h \vdash' \text{exs} \leftrightarrow s \equiv \text{bisim1}' \ P \ h \ e \ n \ \text{exs} \ s$$

$$| \ P, es, n, h \vdash' \text{esxs} [\leftrightarrow] s \equiv \text{bisims1}' \ P \ h \ es \ n \ \text{esxs} \ s$$

| *bisim1Val2'*:

$$P, e, n, h \vdash' (\text{Val } v, xs) \leftrightarrow (v \ \# \ [], xs, length \ (\text{compE2 } e), \text{None})$$

| *bisim1New'*:

$$P, \text{new } C, n, h \vdash' (\text{new } C, xs) \leftrightarrow ([], xs, 0, \text{None})$$

| *bisim1NewThrow'*:

$$P, \text{new } C, n, h \vdash' (\text{THROW } \text{OutOfMemory}, xs) \leftrightarrow ([], xs, 0, [\text{addr-of-sys-xcpt } \text{OutOfMemory}])$$

| *bisim1NewArray'*:

$$P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, \text{newA } T[e], n, h \vdash' (\text{newA } T[e], xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1NewArrayThrow'*:

$$P, e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$$

$$\implies P, \text{newA } T[e], n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$$

| *bisim1NewArrayFail'*:

$$(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, \text{None}))$$

$$\implies P, \text{newA } T[e], n, h \vdash' (\text{Throw } a, xs) \leftrightarrow ([v], xs, \text{length}(\text{compE2 } e), [a])$$

| *bisim1Cast'*:

$$P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp)$$

$$\implies P, \text{Cast } T e, n, h \vdash' (\text{Cast } T e', xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1CastThrow'*:

$$P, e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$$

$$\implies P, \text{Cast } T e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$$

| *bisim1CastFail'*:

$$(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, \text{None}))$$

$$\implies P, \text{Cast } T e, n, h \vdash' (\text{THROW } \text{ClassCast}, xs) \leftrightarrow ([v], xs, \text{length}(\text{compE2 } e), [\text{addr-of-sys-xcpt } \text{ClassCast}])$$

| *bisim1InstanceOf'*:

$$P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp)$$

$$\implies P, e \text{ instanceof } T, n, h \vdash' (e' \text{ instanceof } T, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1InstanceOfThrow'*:

$$P, e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$$

$$\implies P, e \text{ instanceof } T, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$$

| *bisim1Val'*: $P, \text{Val } v, n, h \vdash' (\text{Val } v, xs) \leftrightarrow ([], xs, 0, \text{None})$

| *bisim1Var'*: $P, \text{Var } V, n, h \vdash' (\text{Var } V, xs) \leftrightarrow ([], xs, 0, \text{None})$

| *bisim1BinOp1'*:

$$[[P, e1, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$$

$$\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, \text{None})]]$$

$$\implies P, e1 \llbracket \text{bop} \rrbracket e2, n, h \vdash' (e' \llbracket \text{bop} \rrbracket e2, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1BinOp2'*:

$$[[P, e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$$

$$\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, \text{None})]]$$

$$\implies P, e1 \llbracket \text{bop} \rrbracket e2, n, h \vdash' (\text{Val } v1 \llbracket \text{bop} \rrbracket e', xs) \leftrightarrow (stk @ [v1], loc, \text{length}(\text{compE2 } e1) + pc, xcp)$$

| *bisim1BinOpThrow1'*:

$$[[P, e1, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]);$$

$$\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, \text{None})]]$$

$$\implies P, e1 \llbracket \text{bop} \rrbracket e2, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$$

| *bisim1BinOpThrow2'*:

$$[[P, e2, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]);$$

$$\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, \text{None})]]$$

$$\implies P, e1 \llbracket \text{bop} \rrbracket e2, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk @ [v1], loc, \text{length}(\text{compE2 } e1) + pc, [a])$$

| *bisim1BinOpThrow'*:
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e1 \langle\text{bop}\rangle e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow ([v1, v2], xs, \text{length}(\text{compE2}\ e1) + \text{length}(\text{compE2}\ e2), [a])$

| *bisim1LAss1'*:
 $P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, V:=e, n, h \vdash' (V:=e', xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1LAss2'*:
 $(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None))$
 $\implies P, V:=e, n, h \vdash' (unit, xs) \leftrightarrow ([], xs, \text{Suc}(\text{length}(\text{compE2}\ e)), None)$

| *bisim1LAssThrow'*:
 $P, e, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$
 $\implies P, V:=e, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$

| *bisim1AAcc1'*:
 $\llbracket P, a, n, h \vdash' (a', xs) \leftrightarrow (stk, loc, pc, xcp); \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, a[i], n, h \vdash' (a'[i], xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1AAcc2'*:
 $\llbracket P, i, n, h \vdash' (i', xs) \leftrightarrow (stk, loc, pc, xcp); \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, a[i], n, h \vdash' (Val\ v[i'], xs) \leftrightarrow (stk\ @\ [v], loc, \text{length}(\text{compE2}\ a) + pc, xcp)$

| *bisim1AAccThrow1'*:
 $\llbracket P, a, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad]);$
 $\bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, a[i], n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$

| *bisim1AAccThrow2'*:
 $\llbracket P, i, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad]);$
 $\bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, a[i], n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v], loc, \text{length}(\text{compE2}\ a) + pc, [ad])$

| *bisim1AAccFail'*:
 $\llbracket \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow ([], xs, 0, None); \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, a[i], n, h \vdash' (Throw\ ad, xs) \leftrightarrow ([v, v'], xs, \text{length}(\text{compE2}\ a) + \text{length}(\text{compE2}\ i), [ad])$

| *bisim1AAss1'*:
 $\llbracket P, a, n, h \vdash' (a', xs) \leftrightarrow (stk, loc, pc, xcp);$
 $\bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, a[i] := e, n, h \vdash' (a'[i] := e, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1AAss2'*:
 $\llbracket P, i, n, h \vdash' (i', xs) \leftrightarrow (stk, loc, pc, xcp);$
 $\bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, a[i] := e, n, h \vdash' (Val\ v[i'] := e, xs) \leftrightarrow (stk\ @\ [v], loc, \text{length}(\text{compE2}\ a) + pc, xcp)$

| *bisim1AAss3'*:

$$\llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$$

$$\bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None);$$

$$\bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket$$

$$\implies P, a[i] := e, n, h \vdash' (Val v \llbracket Val v \rrbracket := e', xs) \leftrightarrow (stk @ [v', v], loc, length (compE2 a) + length (compE2 i) + pc, xcp)$$

| *bisim1AAssThrow1'*:

$$\llbracket P, a, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, \llbracket ad \rrbracket);$$

$$\bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None);$$

$$\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket$$

$$\implies P, a[i] := e, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, \llbracket ad \rrbracket)$$

| *bisim1AAssThrow2'*:

$$\llbracket P, i, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, \llbracket ad \rrbracket);$$

$$\bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None);$$

$$\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket$$

$$\implies P, a[i] := e, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk @ [v], loc, length (compE2 a) + pc, \llbracket ad \rrbracket)$$

| *bisim1AAssThrow3'*:

$$\llbracket P, e, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, \llbracket ad \rrbracket);$$

$$\bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None);$$

$$\bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket$$

$$\implies P, a[i] := e, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk @ [v', v], loc, length (compE2 a) + length (compE2 i) + pc, \llbracket ad \rrbracket)$$

| *bisim1AAssFail'*:

$$\llbracket \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None);$$

$$\bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None);$$

$$\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket$$

$$\implies P, a[i] := e, n, h \vdash' (Throw ad, xs) \leftrightarrow ([v', v, v'], xs, length (compE2 a) + length (compE2 i) + length (compE2 e), \llbracket ad \rrbracket)$$

| *bisim1AAss4'*:

$$\llbracket \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None);$$

$$\bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None);$$

$$\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket$$

$$\implies P, a[i] := e, n, h \vdash' (unit, xs) \leftrightarrow (\llbracket, xs, Suc (length (compE2 a) + length (compE2 i) + length (compE2 e)), None)$$

| *bisim1ALength'*:

$$P, a, n, h \vdash' (a', xs) \leftrightarrow (stk, loc, pc, xcp)$$

$$\implies P, a \cdot length, n, h \vdash' (a' \cdot length, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1ALengthThrow'*:

$$P, a, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, \llbracket ad \rrbracket)$$

$$\implies P, a \cdot length, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, \llbracket ad \rrbracket)$$

| *bisim1ALengthNull'*:

$$(\bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None))$$

$$\implies P, a \cdot length, n, h \vdash' (THROW NullPointer, xs) \leftrightarrow ([Null], xs, length (compE2 a), \llbracket addr-of-sys-xcpt$$

NullPointer])

| *bisim1FAcc'*:

$$P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, e \cdot F\{D\}, n, h \vdash' (e' \cdot F\{D\}, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1FAccThrow'*:

$$P, e, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor) \\ \implies P, e \cdot F\{D\}, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$$

| *bisim1FAccNull'*:

$$(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None)) \\ \implies P, e \cdot F\{D\}, n, h \vdash' (THROW\ NullPointer, xs) \leftrightarrow ([Null], xs, length\ (compE2\ e), \lfloor addr-of-sys-xcpt\ NullPointer \rfloor)$$

| *bisim1FAss1'*:

$$\llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ \implies P, e \cdot F\{D\} := e2, n, h \vdash' (e' \cdot F\{D\} := e2, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1FAss2'*:

$$\llbracket P, e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ \implies P, e \cdot F\{D\} := e2, n, h \vdash' (Val\ v \cdot F\{D\} := e', xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ e) + pc, xcp)$$

| *bisim1FAssThrow1'*:

$$\llbracket P, e, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor); \\ \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ \implies P, e \cdot F\{D\} := e2, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$$

| *bisim1FAssThrow2'*:

$$\llbracket P, e2, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor); \\ \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ \implies P, e \cdot F\{D\} := e2, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ e) + pc, \lfloor ad \rfloor)$$

| *bisim1FAssNull'*:

$$\llbracket \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None); \\ \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ \implies P, e \cdot F\{D\} := e2, n, h \vdash' (THROW\ NullPointer, xs) \leftrightarrow ([v, Null], xs, length\ (compE2\ e) + \\ length\ (compE2\ e2), \lfloor addr-of-sys-xcpt\ NullPointer \rfloor)$$

| *bisim1FAss3'*:

$$\llbracket \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None); \\ \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ \implies P, e \cdot F\{D\} := e2, n, h \vdash' (unit, xs) \leftrightarrow ([], xs, Suc\ (length\ (compE2\ e) + length\ (compE2\ e2)), \\ None)$$

| *bisim1CAS1'*:

$$\llbracket P, e1, n, h \vdash' (e1', xs) \leftrightarrow (stk, loc, pc, xcp); \\ \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None); \rrbracket$$

$$\begin{aligned} & \bigwedge xs. P, e3, n, h \vdash' (e3, xs) \leftrightarrow ([], xs, 0, None) \big] \\ \implies & P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), n, h \vdash' (e1' \cdot \text{compareAndSwap}(D \cdot F, e2, e3), xs) \leftrightarrow (stk, \\ & loc, pc, xcp) \end{aligned}$$

| *bisim1CAS2'*:

$$\begin{aligned} & \big[P, e2, n, h \vdash' (e2', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \bigwedge xs. P, e3, n, h \vdash' (e3, xs) \leftrightarrow ([], xs, 0, None) \big] \\ \implies & P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), n, h \vdash' (Val v \cdot \text{compareAndSwap}(D \cdot F, e2', e3), xs) \leftrightarrow \\ & (stk @ [v], loc, length (compE2 e1) + pc, xcp) \end{aligned}$$

| *bisim1CAS3'*:

$$\begin{aligned} & \big[P, e3, n, h \vdash' (e3', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \big] \\ \implies & P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), n, h \vdash' (Val v \cdot \text{compareAndSwap}(D \cdot F, Val v', e3'), xs) \leftrightarrow \\ & (stk @ [v', v], loc, length (compE2 e1) + length (compE2 e2) + pc, xcp) \end{aligned}$$

| *bisim1CASThrow1'*:

$$\begin{aligned} & \big[P, e1, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, [ad]); \\ & \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None); \\ & \bigwedge xs. P, e3, n, h \vdash' (e3, xs) \leftrightarrow ([], xs, 0, None) \big] \\ \implies & P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, [ad]) \end{aligned}$$

| *bisim1CASThrow2'*:

$$\begin{aligned} & \big[P, e2, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, [ad]); \\ & \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \bigwedge xs. P, e3, n, h \vdash' (e3, xs) \leftrightarrow ([], xs, 0, None) \big] \\ \implies & P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), n, h \vdash' (Throw ad, xs) \leftrightarrow (stk @ [v], loc, length (compE2 \\ & e1) + pc, [ad]) \end{aligned}$$

| *bisim1CASThrow3'*:

$$\begin{aligned} & \big[P, e3, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, [ad]); \\ & \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \big] \\ \implies & P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), n, h \vdash' (Throw ad, xs) \leftrightarrow (stk @ [v', v], loc, length \\ & (compE2 e1) + length (compE2 e2) + pc, [ad]) \end{aligned}$$

| *bisim1CASFail'*:

$$\begin{aligned} & \big[\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None); \\ & \bigwedge xs. P, e3, n, h \vdash' (e3, xs) \leftrightarrow ([], xs, 0, None) \big] \\ \implies & P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), n, h \vdash' (Throw ad, xs) \leftrightarrow ([v', v, v'], xs, length (compE2 \\ & e1) + length (compE2 e2) + length (compE2 e3), [ad]) \end{aligned}$$

| *bisim1Call1'*:

$$\begin{aligned} & \big[P, obj, n, h \vdash' (obj', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \bigwedge xs. P, ps, n, h \vdash' (ps, xs) [\leftrightarrow] ([], xs, 0, None) \big] \\ \implies & P, obj \cdot M(ps), n, h \vdash' (obj' \cdot M(ps), xs) \leftrightarrow (stk, loc, pc, xcp) \end{aligned}$$

| *bisim1CallParams'*:

$$\begin{aligned} & \big[P, ps, n, h \vdash' (ps', xs) [\leftrightarrow] (stk, loc, pc, xcp); ps \neq []; \\ & \bigwedge xs. P, obj, n, h \vdash' (obj, xs) \leftrightarrow ([], xs, 0, None) \big] \end{aligned}$$

$$\Longrightarrow P, \text{obj} \cdot M(ps), n, h \vdash' (Val v \cdot M(ps'), xs) \leftrightarrow (stk @ [v], loc, length (compE2 \text{obj}) + pc, xcp)$$

| *bisim1CallThrowObj'*:

$$\begin{aligned} & \llbracket P, \text{obj}, n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]); \\ & \quad \wedge xs. P, ps, n, h \vdash' (ps, xs) [\leftrightarrow] ([], xs, 0, None) \rrbracket \\ \Longrightarrow & P, \text{obj} \cdot M(ps), n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \end{aligned}$$

| *bisim1CallThrowParams'*:

$$\begin{aligned} & \llbracket P, ps, n, h \vdash' (map Val vs @ Throw a \# ps', xs) [\leftrightarrow] (stk, loc, pc, [a]); \\ & \quad \wedge xs. P, \text{obj}, n, h \vdash' (\text{obj}, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ \Longrightarrow & P, \text{obj} \cdot M(ps), n, h \vdash' (Throw a, xs) \leftrightarrow (stk @ [v], loc, length (compE2 \text{obj}) + pc, [a]) \end{aligned}$$

| *bisim1CallThrow'*:

$$\begin{aligned} & \llbracket length ps = length vs; \\ & \quad \wedge xs. P, \text{obj}, n, h \vdash' (\text{obj}, xs) \leftrightarrow ([], xs, 0, None); \wedge xs. P, ps, n, h \vdash' (ps, xs) [\leftrightarrow] ([], xs, 0, None) \rrbracket \\ \Longrightarrow & P, \text{obj} \cdot M(ps), n, h \vdash' (Throw a, xs) \leftrightarrow (vs @ [v], xs, length (compE2 \text{obj}) + length (compEs2 ps), [a]) \end{aligned}$$

| *bisim1BlockSome1'*:

$$\begin{aligned} & (\wedge xs. P, e, Suc n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None)) \\ \Longrightarrow & P, \{V:T=[v]; e\}, n, h \vdash' (\{V:T=[v]; e\}, xs) \leftrightarrow ([], xs, 0, None) \end{aligned}$$

| *bisim1BlockSome2'*:

$$\begin{aligned} & (\wedge xs. P, e, Suc n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None)) \\ \Longrightarrow & P, \{V:T=[v]; e\}, n, h \vdash' (\{V:T=[v]; e\}, xs) \leftrightarrow ([v], xs, Suc 0, None) \end{aligned}$$

| *bisim1BlockSome4'*:

$$\begin{aligned} & P, e, Suc n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \Longrightarrow & P, \{V:T=[v]; e\}, n, h \vdash' (\{V:T=None; e'\}, xs) \leftrightarrow (stk, loc, Suc (Suc pc), xcp) \end{aligned}$$

| *bisim1BlockThrowSome'*:

$$\begin{aligned} & P, e, Suc n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \\ \Longrightarrow & P, \{V:T=[v]; e\}, n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, Suc (Suc pc), [a]) \end{aligned}$$

| *bisim1BlockNone'*:

$$\begin{aligned} & P, e, Suc n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \Longrightarrow & P, \{V:T=None; e\}, n, h \vdash' (\{V:T=None; e'\}, xs) \leftrightarrow (stk, loc, pc, xcp) \end{aligned}$$

| *bisim1BlockThrowNone'*:

$$\begin{aligned} & P, e, Suc n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \\ \Longrightarrow & P, \{V:T=None; e\}, n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \end{aligned}$$

| *bisim1Sync1'*:

$$\begin{aligned} & \llbracket P, e1, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \wedge xs. P, e2, Suc n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ \Longrightarrow & P, sync_V (e1) e2, n, h \vdash' (sync_V (e') e2, xs) \leftrightarrow (stk, loc, pc, xcp) \end{aligned}$$

| *bisim1Sync2'*:

$$\begin{aligned} & \llbracket \wedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \wedge xs. P, e2, Suc n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ \Longrightarrow & P, sync_V (e1) e2, n, h \vdash' (sync_V (Val v) e2, xs) \leftrightarrow ([v, v], xs, Suc (length (compE2 e1)), None) \end{aligned}$$

| *bisim1Sync3'*:
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (sync_V (Val\ v)\ e2, xs) \leftrightarrow ([v], xs[V := v], Suc\ (Suc\ (length\ (compE2\ e1))), None)$

| *bisim1Sync4'*:
 $\llbracket P, e2, Suc\ n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$
 $\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (insync_V (a)\ e', xs) \leftrightarrow (stk, loc, Suc\ (Suc\ (Suc\ (length\ (compE2\ e1) + pc))), xcp)$

| *bisim1Sync5'*:
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (insync_V (a)\ Val\ v, xs) \leftrightarrow ([xs\ !\ V, v], xs, 4 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$

| *bisim1Sync6'*:
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (Val\ v, xs) \leftrightarrow ([v], xs, 5 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$

| *bisim1Sync7'*:
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (insync_V (a)\ Throw\ a', xs) \leftrightarrow ([Addr\ a'], xs, 6 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$

| *bisim1Sync8'*:
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (insync_V (a)\ Throw\ a', xs) \leftrightarrow ([xs\ !\ V, Addr\ a'], xs, 7 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$

| *bisim1Sync9'*:
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow ([Addr\ a], xs, 8 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$

| *bisim1Sync10'*:
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow ([Addr\ a], xs, 8 + length\ (compE2\ e1) + length\ (compE2\ e2), [a])$

| *bisim1Sync11'*:
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (THROW\ NullPointer, xs) \leftrightarrow ([Null], xs, Suc\ (Suc\ (length\ (compE2\ e1))), [addr-of-sys-xcpt\ NullPointer])$

| *bisim1Sync12'*:

$$\begin{aligned} & \llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, sync_V (e1) e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow ([v, v'], xs, 4 + length\ (compE2\ e1) + length \\ & (compE2\ e2), [a]) \end{aligned}$$

| *bisim1Sync14'*:

$$\begin{aligned} & \llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, sync_V (e1) e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow \\ & ([v, Addr\ a], xs, 7 + length\ (compE2\ e1) + length\ (compE2\ e2), [a]) \end{aligned}$$

| *bisim1SyncThrow'*:

$$\begin{aligned} & \llbracket P, e1, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]); \\ & \quad \bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, sync_V (e1) e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \end{aligned}$$

| *bisim1InSync'*:

$$P, insync_V (a) e, n, h \vdash' (insync_V (a) e, xs) \leftrightarrow ([], xs, 0, None)$$

| *bisim1Seq1'*:

$$\begin{aligned} & \llbracket P, e1, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, e1;;e2, n, h \vdash' (e';e2, xs) \leftrightarrow (stk, loc, pc, xcp) \end{aligned}$$

| *bisim1SeqThrow1'*:

$$\begin{aligned} & \llbracket P, e1, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]); \\ & \quad \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, e1;;e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \end{aligned}$$

| *bisim1Seq2'*:

$$\begin{aligned} & \llbracket P, e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, e1;;e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, Suc\ (length\ (compE2\ e1) + pc), xcp) \end{aligned}$$

| *bisim1Cond1'*:

$$\begin{aligned} & \llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, if\ (e)\ e1\ else\ e2, n, h \vdash' (if\ (e')\ e1\ else\ e2, xs) \leftrightarrow (stk, loc, pc, xcp) \end{aligned}$$

| *bisim1CondThen'*:

$$\begin{aligned} & \llbracket P, e1, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, if\ (e)\ e1\ else\ e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, Suc\ (length\ (compE2\ e) + pc), xcp) \end{aligned}$$

| *bisim1CondElse'*:

$$\begin{aligned} & \llbracket P, e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \end{aligned}$$

$\implies P, \text{if } (e) e1 \text{ else } e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, Suc (Suc (length (compE2 e) + length (compE2 e1) + pc)), xcp)$

| *bisim1CondThrow'*:

$\llbracket P, e, n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor);$
 $\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket);$
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket$
 $\implies P, \text{if } (e) e1 \text{ else } e2, n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$

| *bisim1While1'*:

$\llbracket \bigwedge xs. P, c, n, h \vdash' (c, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket);$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket$
 $\implies P, \text{while } (c) e, n, h \vdash' (\text{while } (c) e, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket)$

| *bisim1While3'*:

$\llbracket P, c, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket$
 $\implies P, \text{while } (c) e, n, h \vdash' (\text{if } (e') (e;; \text{while } (c) e) \text{ else unit}, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1While4'*:

$\llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$
 $\bigwedge xs. P, c, n, h \vdash' (c, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket$
 $\implies P, \text{while } (c) e, n, h \vdash' (e';; \text{while } (c) e, xs) \leftrightarrow (stk, loc, Suc (length (compE2 c) + pc), xcp)$

| *bisim1While6'*:

$\llbracket \bigwedge xs. P, c, n, h \vdash' (c, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket);$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket \implies$
 $P, \text{while } (c) e, n, h \vdash' (\text{while } (c) e, xs) \leftrightarrow (\llbracket, xs, Suc (Suc (length (compE2 c) + length (compE2 e))), None \rrbracket)$

| *bisim1While7'*:

$\llbracket \bigwedge xs. P, c, n, h \vdash' (c, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket);$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket \implies$
 $P, \text{while } (c) e, n, h \vdash' (\text{unit}, xs) \leftrightarrow (\llbracket, xs, Suc (Suc (Suc (length (compE2 c) + length (compE2 e))))), None \rrbracket)$

| *bisim1WhileThrow1'*:

$\llbracket P, c, n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor);$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket$
 $\implies P, \text{while } (c) e, n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$

| *bisim1WhileThrow2'*:

$\llbracket P, e, n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor);$
 $\bigwedge xs. P, c, n, h \vdash' (c, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket$
 $\implies P, \text{while } (c) e, n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, Suc (length (compE2 c) + pc), \lfloor a \rfloor)$

| *bisim1Throw1'*:

$P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, \text{throw } e, n, h \vdash' (\text{throw } e', xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1Throw2'*:

$(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket))$

$\implies P, \text{throw } e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], xs, \text{length}(\text{compE2 } e), [a])$

| *bisim1ThrowNull'*:

$(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, \text{None}))$
 $\implies P, \text{throw } e, n, h \vdash' (\text{THROW } \text{NullPointer}, xs) \leftrightarrow ([\text{Null}], xs, \text{length}(\text{compE2 } e), [\text{addr-of-sys-xcpt } \text{NullPointer}])$

| *bisim1ThrowThrow'*:

$P, e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$
 $\implies P, \text{throw } e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$

| *bisim1Try'*:

$\llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp});$
 $\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rrbracket$
 $\implies P, \text{try } e \text{ catch}(C \ V) \ e2, n, h \vdash' (\text{try } e' \text{ catch}(C \ V) \ e2, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$

| *bisim1TryCatch1'*:

$\llbracket P, e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]); \text{typeof-addr } h \ a = [\text{Class-type } C']; P \vdash C' \preceq^* C;$
 $\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rrbracket$
 $\implies P, \text{try } e \text{ catch}(C \ V) \ e2, n, h \vdash' (\{V:\text{Class } C=\text{None}; e2\}, xs[V := \text{Addr } a]) \leftrightarrow ([\text{Addr } a], \text{loc}, \text{Suc}(\text{length}(\text{compE2 } e)), \text{None})$

| *bisim1TryCatch2'*:

$\llbracket P, e2, \text{Suc } n, h \vdash' (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp});$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, \text{None}) \rrbracket$
 $\implies P, \text{try } e \text{ catch}(C \ V) \ e2, n, h \vdash' (\{V:\text{Class } C=\text{None}; e'\}, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e) + \text{pc})), \text{xcp})$

| *bisim1TryFail'*:

$\llbracket P, e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]); \text{typeof-addr } h \ a = [\text{Class-type } C']; \neg P \vdash C' \preceq^* C;$
 $\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rrbracket$
 $\implies P, \text{try } e \text{ catch}(C \ V) \ e2, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$

| *bisim1TryCatchThrow'*:

$\llbracket P, e2, \text{Suc } n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]);$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, \text{None}) \rrbracket$
 $\implies P, \text{try } e \text{ catch}(C \ V) \ e2, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e) + \text{pc})), [a])$

| *bisims1Nil'*: $P, [], n, h \vdash' ([], xs) [\leftrightarrow] ([], xs, 0, \text{None})$

| *bisims1List1'*:

$\llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp});$
 $\bigwedge xs. P, es, n, h \vdash' (es, xs) [\leftrightarrow] ([], xs, 0, \text{None}) \rrbracket$
 $\implies P, e\#es, n, h \vdash' (e'\#es, xs) [\leftrightarrow] (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$

| *bisims1List2'*:

$\llbracket P, es, n, h \vdash' (es', xs) [\leftrightarrow] (\text{stk}, \text{loc}, \text{pc}, \text{xcp});$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, \text{None}) \rrbracket$
 $\implies P, e\#es, n, h \vdash' (\text{Val } v \# es', xs) [\leftrightarrow] (\text{stk} @ [v], \text{loc}, \text{length}(\text{compE2 } e) + \text{pc}, \text{xcp})$

lemma *bisim1'-refl*: $P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, \text{None})$

and *bisims1'-refl*: $P, es, n, h \vdash' (es, xs) [\leftrightarrow] ([], xs, 0, None)$
 ⟨proof⟩

lemma *bisim1-imp-bisim1'*: $P, e, h \vdash' es \leftrightarrow s \implies P, e, n, h \vdash' es \leftrightarrow s$
and *bisims1-imp-bisims1'*: $P, es, h \vdash' esxs [\leftrightarrow] s \implies P, es, n, h \vdash' esxs [\leftrightarrow] s$
 ⟨proof⟩

lemma *bisim1'-imp-bisim1*: $P, e, n, h \vdash' es \leftrightarrow s \implies P, e, h \vdash' es \leftrightarrow s$
and *bisims1'-imp-bisims1*: $P, es, n, h \vdash' esxs [\leftrightarrow] s \implies P, es, h \vdash' esxs [\leftrightarrow] s$
 ⟨proof⟩

lemma *bisim1'-eq-bisim1*: $bisim1' P h e n = bisim1 P h e$
and *bisims1'-eq-bisims1*: $bisims1' P h es n = bisims1 P h es$
 ⟨proof⟩

end

lemmas *bisim1-bisims1-inducts* =
J1-JVM-heap-base.bisim1'-bisims1'.inducts
 [simplified *J1-JVM-heap-base.bisim1'-eq-bisim1* *J1-JVM-heap-base.bisims1'-eq-bisims1*,
 consumes 1,
 case-names *bisim1Val2 bisim1New bisim1NewThrow*
bisim1NewArray bisim1NewArrayThrow bisim1NewArrayFail bisim1Cast bisim1CastThrow bisim1CastFail
bisim1InstanceOf bisim1InstanceOfThrow
bisim1Val bisim1Var bisim1BinOp1 bisim1BinOp2 bisim1BinOpThrow1 bisim1BinOpThrow2 bisim1BinOpThrow
bisim1LAss1 bisim1LAss2 bisim1LAssThrow
bisim1AAcc1 bisim1AAcc2 bisim1AAccThrow1 bisim1AAccThrow2 bisim1AAccFail
bisim1AAss1 bisim1AAss2 bisim1AAss3 bisim1AAssThrow1 bisim1AAssThrow2
bisim1AAssThrow3 bisim1AAssFail bisim1AAss4
bisim1ALength bisim1ALengthThrow bisim1ALengthNull
bisim1FAcc bisim1FAccThrow bisim1FAccNull
bisim1FAss1 bisim1FAss2 bisim1FAssThrow1 bisim1FAssThrow2 bisim1FAssNull bisim1FAss3
bisim1CAS1 bisim1CAS2 bisim1CAS3 bisim1CASThrow1 bisim1CASThrow2
bisim1CASThrow3 bisim1CASFail
bisim1Call1 bisim1CallParams bisim1CallThrowObj bisim1CallThrowParams
bisim1CallThrow
bisim1BlockSome1 bisim1BlockSome2 bisim1BlockSome4 bisim1BlockThrowSome
bisim1BlockNone bisim1BlockThrowNone
bisim1Sync1 bisim1Sync2 bisim1Sync3 bisim1Sync4 bisim1Sync5 bisim1Sync6
bisim1Sync7 bisim1Sync8 bisim1Sync9 bisim1Sync10 bisim1Sync11 bisim1Sync12
bisim1Sync14 bisim1SyncThrow bisim1InSync
bisim1Seq1 bisim1SeqThrow1 bisim1Seq2
bisim1Cond1 bisim1CondThen bisim1CondElse bisim1CondThrow
bisim1While1 bisim1While3 bisim1While4
bisim1While6 bisim1While7 bisim1WhileThrow1 bisim1WhileThrow2
bisim1Throw1 bisim1Throw2 bisim1ThrowNull bisim1ThrowThrow
bisim1Try bisim1TryCatch1 bisim1TryCatch2 bisim1TryFail bisim1TryCatchThrow
bisims1Nil bisims1List1 bisims1List2]

lemmas *bisim1-bisims1-inducts-split* = *bisim1-bisims1-inducts[split-format (complete)]*

context *J1-JVM-heap-base* **begin**

lemma *bisim1-pc-length-compE2*: $P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \implies pc \leq \text{length} (\text{compE2 } E)$
and *bisims1-pc-length-compEs2*: $P, Es, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, xcp) \implies pc \leq \text{length} (\text{compEs2 } Es)$

$\langle \text{proof} \rangle$

lemma *bisim1-pc-length-compE2D*:

$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, \text{length} (\text{compE2 } e), xcp)$
 $\implies xcp = \text{None} \wedge \text{call1 } e' = \text{None} \wedge (\exists v. stk = [v] \wedge (\text{is-val } e' \implies e' = \text{Val } v \wedge xs = loc))$

and *bisims1-pc-length-compEs2D*:

$P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, \text{length} (\text{compEs2 } es), xcp)$
 $\implies xcp = \text{None} \wedge \text{calls1 } es' = \text{None} \wedge (\exists vs. stk = rev \ vs \wedge \text{length } vs = \text{length } es \wedge (\text{is-vals } es' \implies es' = \text{map } \text{Val } vs \wedge xs = loc))$

$\langle \text{proof} \rangle$

corollary *bisim1-call-pcD*: $\llbracket P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp); \text{call1 } e' = [aMvs] \rrbracket \implies pc < \text{length} (\text{compE2 } e)$

and *bisims1-calls-pcD*: $\llbracket P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp); \text{calls1 } es' = [aMvs] \rrbracket \implies pc < \text{length} (\text{compEs2 } es)$

$\langle \text{proof} \rangle$

lemma *bisim1-length-xs*: $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies \text{length } xs = \text{length } loc$

and *bisims1-length-xs*: $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp) \implies \text{length } xs = \text{length } loc$

$\langle \text{proof} \rangle$

lemma *bisim1-Val-length-compE2D*:

$P, e, h \vdash (\text{Val } v, xs) \leftrightarrow (stk, loc, \text{length} (\text{compE2 } e), xcp) \implies stk = [v] \wedge xs = loc \wedge xcp = \text{None}$

and *bisims1-Val-length-compEs2D*:

$P, es, h \vdash (\text{map } \text{Val } vs, xs) [\leftrightarrow] (stk, loc, \text{length} (\text{compEs2 } es), xcp) \implies stk = rev \ vs \wedge xs = loc \wedge xcp = \text{None}$

$\langle \text{proof} \rangle$

lemma *bisim-Val-loc-eq-xcp-None*:

$P, e, h \vdash (\text{Val } v, xs) \leftrightarrow (stk, loc, pc, xcp) \implies xs = loc \wedge xcp = \text{None}$

and *bisims-Val-loc-eq-xcp-None*:

$P, es, h \vdash (\text{map } \text{Val } vs, xs) [\leftrightarrow] (stk, loc, pc, xcp) \implies xs = loc \wedge xcp = \text{None}$

$\langle \text{proof} \rangle$

lemma *bisim-Val-pc-not-Invoke*:

$\llbracket P, e, h \vdash (\text{Val } v, xs) \leftrightarrow (stk, loc, pc, xcp); pc < \text{length} (\text{compE2 } e) \rrbracket \implies \text{compE2 } e ! pc \neq \text{Invoke } M \ n'$

and *bisims-Val-pc-not-Invoke*:

$\llbracket P, es, h \vdash (\text{map } \text{Val } vs, xs) [\leftrightarrow] (stk, loc, pc, xcp); pc < \text{length} (\text{compEs2 } es) \rrbracket \implies \text{compEs2 } es ! pc \neq \text{Invoke } M \ n'$

$\langle \text{proof} \rangle$

lemma *bisim1-VarD*: $P, E, h \vdash (\text{Var } V, xs) \leftrightarrow (stk, loc, pc, xcp) \implies xs = loc$

and $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp) \implies \text{True}$

$\langle \text{proof} \rangle$

lemma *bisim1-ThrowD*:

$P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$
 $\implies \text{pc} < \text{length} (\text{compE2 } e) \wedge (\text{xcp} = [a] \vee \text{xcp} = \text{None}) \wedge xs = \text{loc}$

and *bisims1-ThrowD*:

$P, es, h \vdash (\text{map Val } vs @ \text{Throw } a \# es', xs) [\leftrightarrow] (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$
 $\implies \text{pc} < \text{length} (\text{compEs2 } es) \wedge (\text{xcp} = [a] \vee \text{xcp} = \text{None}) \wedge xs = \text{loc}$
 ⟨proof⟩

lemma *fixes P :: 'addr J1-prog*

shows *bisim1-Invoke-stkD*:

$\llbracket P, e, h \vdash \text{exs} \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{None}); \text{pc} < \text{length} (\text{compE2 } e); \text{compE2 } e ! \text{pc} = \text{Invoke } M \ n' \rrbracket$
 $\implies \exists vs \ v \ \text{stk}'. \text{stk} = vs @ v \# \text{stk}' \wedge \text{length } vs = n'$

and *bisims1-Invoke-stkD*:

$\llbracket P, es, h \vdash \text{esxs} [\leftrightarrow] (\text{stk}, \text{loc}, \text{pc}, \text{None}); \text{pc} < \text{length} (\text{compEs2 } es); \text{compEs2 } es ! \text{pc} = \text{Invoke } M \ n' \rrbracket$
 $\implies \exists vs \ v \ \text{stk}'. \text{stk} = vs @ v \# \text{stk}' \wedge \text{length } vs = n'$
 ⟨proof⟩

lemma *fixes P :: 'addr J1-prog*

shows *bisim1-call-xcpNone*: $P, e, h \vdash (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]) \implies \text{call1 } e' = \text{None}$

and *bisims1-calls-xcpNone*: $P, es, h \vdash (es', xs) [\leftrightarrow] (\text{stk}, \text{loc}, \text{pc}, [a]) \implies \text{calls1 } es' = \text{None}$

⟨proof⟩

lemma *bisims1-map-Val-append*:

assumes *bisim*: $P, es', h \vdash (es'', xs) [\leftrightarrow] (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$

shows $\text{length } es = \text{length } vs$

$\implies P, es @ es', h \vdash (\text{map Val } vs @ es'', xs) [\leftrightarrow] (\text{stk} @ \text{rev } vs, \text{loc}, \text{length} (\text{compEs2 } es) + \text{pc}, \text{xcp})$

⟨proof⟩

lemma *bisim1-hext-mono*: $\llbracket P, e, h \vdash \text{exs} \leftrightarrow s; \text{hext } h \ h' \rrbracket \implies P, e, h' \vdash \text{exs} \leftrightarrow s$ (**is** *PROP* *?thesis1*)

and *bisims1-hext-mono*: $\llbracket P, es, h \vdash \text{esxs} [\leftrightarrow] s; \text{hext } h \ h' \rrbracket \implies P, es, h' \vdash \text{esxs} [\leftrightarrow] s$ (**is** *PROP* *?thesis2*)

⟨proof⟩

declare *match-ex-table-append-not-pcs* [simp]

match-ex-table-eq-NoneI [simp]

outside-pcs-compxE2-not-matches-entry [simp]

outside-pcs-compxEs2-not-matches-entry [simp]

lemma *bisim1-xcp-Some-not-caught*:

$P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$

$\implies \text{match-ex-table} (\text{compP } f \ P) (\text{cname-of } h \ a) (\text{pc}' + \text{pc}) (\text{compxE2 } e \ \text{pc}' \ d) = \text{None}$

and *bisims1-xcp-Some-not-caught*:

$P, es, h \vdash (\text{map Val } vs @ \text{Throw } a \# es', xs) [\leftrightarrow] (\text{stk}, \text{loc}, \text{pc}, [a])$

$\implies \text{match-ex-table} (\text{compP } f \ P) (\text{cname-of } h \ a) (\text{pc}' + \text{pc}) (\text{compxEs2 } es \ \text{pc}' \ d) = \text{None}$

⟨proof⟩

declare *match-ex-table-append-not-pcs* [simp del]

match-ex-table-eq-NoneI [simp del]

outside-pcs-compxE2-not-matches-entry [simp del]

outside-pcs-compxEs2-not-matches-entry [simp del]

lemma *bisim1-xcp-pcD*: $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \implies pc < length (compE2 e)$
and *bisims1-xcp-pcD*: $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, \lfloor a \rfloor) \implies pc < length (compEs2 es)$
 ⟨proof⟩

declare *nth-append* [simp]

lemma *bisim1-Val-τExec-move*:

$\llbracket P, E, h \vdash (Val v, xs) \leftrightarrow (stk, loc, pc, xcp); pc < length (compE2 E) \rrbracket$
 $\implies xs = loc \wedge xcp = None \wedge$
 $\tauExec-mover-a P t E h (stk, xs, pc, None) ([v], xs, length (compE2 E), None)$

and *bisims1-Val-τExec-moves*:

$\llbracket P, Es, h \vdash (map Val vs, xs) [\leftrightarrow] (stk, loc, pc, xcp); pc < length (compEs2 Es) \rrbracket$
 $\implies xs = loc \wedge xcp = None \wedge$
 $\tauExec-movesr-a P t Es h (stk, xs, pc, None) (rev vs, xs, length (compEs2 Es), None)$
 ⟨proof⟩

lemma *bisim1Val2D1*:

assumes *bisim*: $P, e, h \vdash (Val v, xs) \leftrightarrow (stk, loc, pc, xcp)$
shows $xcp = None \wedge xs = loc \wedge \tauExec-mover-a P t e h (stk, loc, pc, xcp) ([v], loc, length (compE2 e), None)$
 ⟨proof⟩

lemma *bisim1-Throw-τExec-movet*:

$\llbracket P, e, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, None) \rrbracket$
 $\implies \exists pc'. \tauExec-movet-a P t e h (stk, loc, pc, None) ([Addr a], loc, pc', \lfloor a \rfloor) \wedge$
 $P, e, h \vdash (Throw a, xs) \leftrightarrow ([Addr a], loc, pc', \lfloor a \rfloor) \wedge xs = loc$

and *bisims1-Throw-τExec-movest*:

$\llbracket P, es, h \vdash (map Val vs @ Throw a \# es', xs) [\leftrightarrow] (stk, loc, pc, None) \rrbracket$
 $\implies \exists pc'. \tauExec-movest-a P t es h (stk, loc, pc, None) (Addr a \# rev vs, loc, pc', \lfloor a \rfloor) \wedge$
 $P, es, h \vdash (map Val vs @ Throw a \# es', xs) [\leftrightarrow] (Addr a \# rev vs, loc, pc', \lfloor a \rfloor) \wedge xs = loc$
 ⟨proof⟩

lemma *bisim1-Throw-τExec-mover*:

$\llbracket P, e, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, None) \rrbracket$
 $\implies \exists pc'. \tauExec-mover-a P t e h (stk, loc, pc, None) ([Addr a], loc, pc', \lfloor a \rfloor) \wedge$
 $P, e, h \vdash (Throw a, xs) \leftrightarrow ([Addr a], loc, pc', \lfloor a \rfloor) \wedge xs = loc$
 ⟨proof⟩

lemma *bisims1-Throw-τExec-movesr*:

$\llbracket P, es, h \vdash (map Val vs @ Throw a \# es', xs) [\leftrightarrow] (stk, loc, pc, None) \rrbracket$
 $\implies \exists pc'. \tauExec-movesr-a P t es h (stk, loc, pc, None) (Addr a \# rev vs, loc, pc', \lfloor a \rfloor) \wedge$
 $P, es, h \vdash (map Val vs @ Throw a \# es', xs) [\leftrightarrow] (Addr a \# rev vs, loc, pc', \lfloor a \rfloor) \wedge xs = loc$
 ⟨proof⟩

declare *split-beta* [simp]

lemma *bisim1-inline-call-Throw*:

$\llbracket P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None); call1 e' = \lfloor (a, M, vs) \rfloor;$
 $compE2 e ! pc = Invoke M n0; pc < length (compE2 e) \rrbracket$
 $\implies n0 = length vs \wedge P, e, h \vdash (inline-call (Throw A) e', xs) \leftrightarrow (stk, loc, pc, \lfloor A \rfloor)$
 (is $\llbracket -; -; - \rrbracket \implies ?concl e n e' xs pc stk loc$)

and *bisims1-inline-calls-Throw*:

$\llbracket P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, None); calls1\ es' = \llbracket (a, M, vs) \rrbracket;$
 $\quad compEs2\ es\ !\ pc = Invoke\ M\ n0; pc < length\ (compEs2\ es) \rrbracket$
 $\implies n0 = length\ vs \wedge P, es, h \vdash (inline-calls\ (Throw\ A)\ es', xs) [\leftrightarrow] (stk, loc, pc, \llbracket A \rrbracket)$
 $(is\ \llbracket -; -; - \rrbracket \implies ?concls\ es\ n\ es'\ xs\ pc\ stk\ loc)$
 $\langle proof \rangle$

lemma *bisim1-max-stack*: $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies length\ stk \leq max-stack\ e$

and *bisims1-max-stacks*: $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp) \implies length\ stk \leq max-stacks\ es$
 $\langle proof \rangle$

inductive *bisim1-fr* :: 'addr J1-prog \Rightarrow 'heap \Rightarrow 'addr expr1 \times 'addr locals1 \Rightarrow 'addr frame \Rightarrow bool
for $P :: 'addr\ J1-prog$ **and** $h :: 'heap$

where

$\llbracket P \vdash C\ sees\ M:Ts \rightarrow T = \llbracket body \rrbracket\ in\ D;$
 $\quad P, blocks1\ 0\ (Class\ D\ \# Ts)\ body, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, None);$
 $\quad call1\ e = \llbracket (a, M', vs) \rrbracket;$
 $\quad max-vars\ e \leq length\ xs \rrbracket$
 $\implies bisim1-fr\ P\ h\ (e, xs)\ (stk, loc, C, M, pc)$

declare *bisim1-fr.intros* [intro]

declare *bisim1-fr.cases* [elim]

lemma *bisim1-fr-heap-mono*:

$\llbracket bisim1-fr\ P\ h\ exs\ fr; heap\ h\ h' \rrbracket \implies bisim1-fr\ P\ h'\ exs\ fr$
 $\langle proof \rangle$

lemma *bisim1-max-vars*: $P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \implies max-vars\ E \geq max-vars\ e$

and *bisims1-max-varss*: $P, Es, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, xcp) \implies max-varss\ Es \geq max-varss\ es$
 $\langle proof \rangle$

lemma *bisim1-call-Exec-move*:

$\llbracket P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None); call1\ e' = \llbracket (a, M', vs) \rrbracket; n + max-vars\ e' \leq length\ xs; \neg$
 $contains-insync\ e \rrbracket$
 $\implies \exists pc'\ loc'\ stk'. \tauExec-mover-a\ P\ t\ e\ h\ (stk, loc, pc, None)\ (rev\ vs\ @\ Addr\ a\ \#\ stk', loc', pc',$
 $None) \wedge$

$\quad pc' < length\ (compE2\ e) \wedge compE2\ e\ !\ pc' = Invoke\ M'\ (length\ vs) \wedge$
 $\quad P, e, h \vdash (e', xs) \leftrightarrow (rev\ vs\ @\ Addr\ a\ \#\ stk', loc', pc', None)$

$(is\ \llbracket -; -; - \rrbracket \implies ?concl\ e\ n\ e'\ xs\ pc\ stk\ loc)$

and *bisims1-calls-Exec-moves*:

$\llbracket P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, None); calls1\ es' = \llbracket (a, M', vs) \rrbracket;$
 $\quad n + max-varss\ es' \leq length\ xs; \neg\ contains-insyncs\ es \rrbracket$
 $\implies \exists pc'\ stk'\ loc'. \tauExec-movesr-a\ P\ t\ es\ h\ (stk, loc, pc, None)\ (rev\ vs\ @\ Addr\ a\ \#\ stk', loc', pc',$
 $None) \wedge$

$\quad pc' < length\ (compEs2\ es) \wedge compEs2\ es\ !\ pc' = Invoke\ M'\ (length\ vs) \wedge$
 $\quad P, es, h \vdash (es', xs) [\leftrightarrow] (rev\ vs\ @\ Addr\ a\ \#\ stk', loc', pc', None)$

$(is\ \llbracket -; -; - \rrbracket \implies ?concls\ es\ n\ es'\ xs\ pc\ stk\ loc)$

$\langle proof \rangle$

lemma *fixes P* :: 'addr J1-prog

shows *bisim1-inline-call-Val*:

$\llbracket P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None); call1\ e' = \llbracket (a, M, vs) \rrbracket;$

$\text{compE2 } e ! pc = \text{Invoke } M \ n0 \]$
 $\implies \text{length } stk \geq \text{Suc } (\text{length } vs) \wedge n0 = \text{length } vs \wedge$
 $P, e, h \vdash (\text{inline-call } (\text{Val } v) \ e', \ xs) \leftrightarrow (v \ \# \ \text{drop } (\text{Suc } (\text{length } vs)) \ stk, \ loc, \ \text{Suc } pc, \ \text{None})$
 $(\text{is } [\ -; \ -; \ - \] \implies ?\text{concl } e \ n \ e' \ xs \ pc \ stk \ loc)$

and *bisims1-inline-calls-Val*:

$[[P, es, h \vdash (es', xs) \ [\leftrightarrow] \ (stk, loc, pc, None); \ \text{calls1 } es' = [(a, M, vs)];$
 $\text{compEs2 } es ! pc = \text{Invoke } M \ n0 \]$
 $\implies \text{length } stk \geq \text{Suc } (\text{length } vs) \wedge n0 = \text{length } vs \wedge$
 $P, es, h \vdash (\text{inline-calls } (\text{Val } v) \ es', \ xs) \ [\leftrightarrow] \ (v \ \# \ \text{drop } (\text{Suc } (\text{length } vs)) \ stk, loc, \ \text{Suc } pc, \ \text{None})$
 $(\text{is } [\ -; \ -; \ - \] \implies ?\text{concls } es \ n \ es' \ xs \ pc \ stk \ loc)$
 $\langle \text{proof} \rangle$

lemma *bisim1-fv*: $P, e, h \vdash (e', xs) \leftrightarrow s \implies \text{fv } e' \subseteq \text{fv } e$

and *bisims1-fvs*: $P, es, h \vdash (es', xs) \ [\leftrightarrow] \ s \implies \text{fvs } es' \subseteq \text{fvs } es$

$\langle \text{proof} \rangle$

lemma *bisim1-syncvars*: $[[P, e, h \vdash (e', xs) \leftrightarrow s; \ \text{syncvars } e \] \implies \text{syncvars } e'$

and *bisims1-syncvarss*: $[[P, es, h \vdash (es', xs) \ [\leftrightarrow] \ s; \ \text{syncvarss } es \] \implies \text{syncvarss } es'$

$\langle \text{proof} \rangle$

declare *pcs-stack-xlift* [*simp*]

lemma *bisim1-Val- τ red1r*:

$[[P, E, h \vdash (e, xs) \leftrightarrow ([v], loc, \text{length } (\text{compE2 } E), \ \text{None}); \ n + \text{max-vars } e \leq \text{length } xs; \ \mathcal{B} \ E \ n \]$
 $\implies \tau\text{red1r } P \ t \ h \ (e, \ xs) \ (\text{Val } v, \ loc)$

and *bisims1-Val- τ Reds1r*:

$[[P, Es, h \vdash (es, xs) \ [\leftrightarrow] \ (\text{rev } vs, \ loc, \ \text{length } (\text{compEs2 } Es), \ \text{None}); \ n + \text{max-varss } es \leq \text{length } xs;$
 $\mathcal{B}s \ Es \ n \]$

$\implies \tau\text{reds1r } P \ t \ h \ (es, \ xs) \ (\text{map } \text{Val } vs, \ loc)$

$\langle \text{proof} \rangle$

lemma *exec-meth-stk-split*:

$[[P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp);$
 $\text{exec-meth-d } (\text{compP2 } P) \ (\text{compE2 } E) \ (\text{stack-xlift } (\text{length } STK) \ (\text{compxE2 } E \ 0 \ 0)) \ t$
 $h \ (stk \ @ \ STK, \ loc, \ pc, \ xcp) \ \text{ta} \ h' \ (stk', \ loc', \ pc', \ xcp') \]$
 $\implies \exists \ stk''. \ stk' = stk'' \ @ \ STK \wedge \text{exec-meth-d } (\text{compP2 } P) \ (\text{compE2 } E) \ (\text{compxE2 } E \ 0 \ 0) \ t$
 $h \ (stk, \ loc, \ pc, \ xcp) \ \text{ta} \ h' \ (stk'', \ loc', \ pc', \ xcp')$

$(\text{is } [\ -; \ ?\text{exec } E \ stk \ STK \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp' \] \implies ?\text{concl } E \ stk \ STK \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp')$

and *exec-meth-stk-splits*:

$[[P, Es, h \vdash (es, xs) \ [\leftrightarrow] \ (stk, loc, pc, xcp);$
 $\text{exec-meth-d } (\text{compP2 } P) \ (\text{compEs2 } Es) \ (\text{stack-xlift } (\text{length } STK) \ (\text{compxEs2 } Es \ 0 \ 0)) \ t$
 $h \ (stk \ @ \ STK, \ loc, \ pc, \ xcp) \ \text{ta} \ h' \ (stk', \ loc', \ pc', \ xcp') \]$
 $\implies \exists \ stk''. \ stk' = stk'' \ @ \ STK \wedge \text{exec-meth-d } (\text{compP2 } P) \ (\text{compEs2 } Es) \ (\text{compxEs2 } Es \ 0 \ 0) \ t$
 $h \ (stk, \ loc, \ pc, \ xcp) \ \text{ta} \ h' \ (stk'', \ loc', \ pc', \ xcp')$

$(\text{is } [\ -; \ ?\text{execs } Es \ stk \ STK \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp' \] \implies ?\text{concls } Es \ stk \ STK \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp')$

$\langle \text{proof} \rangle$

lemma shows *bisim1-callD*:

```

[[ P,e,h ⊢ (e', xs) ↔ (stk, loc, pc, xcp); call1 e' = [(a, M, vs)];
   compE2 e ! pc = Invoke M' n0 ]]
⇒ M = M'

```

and *bisims1-callD*:

```

[[ P,es,h ⊢ (es',xs) [↔] (stk,loc,pc, xcp); calls1 es' = [(a, M, vs)];
   compEs2 es ! pc = Invoke M' n0 ]]
⇒ M = M'

```

⟨proof⟩

lemma *bisim1-xcpD*: $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, [a]) \implies pc < \text{length} (compE2 e)$

and *bisims1-xcpD*: $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, [a]) \implies pc < \text{length} (compEs2 es)$

⟨proof⟩

lemma *bisim1-match-Some-stk-length*:

```

[[ P,E,h ⊢ (e, xs) ↔ (stk, loc, pc, [a]);
   match-ex-table (compP2 P) (cname-of h a) pc (compxE2 E 0 0) = [(pc', d)] ]]
⇒ d ≤ length stk

```

and *bisims1-match-Some-stk-length*:

```

[[ P,Es,h ⊢ (es, xs) [↔] (stk, loc, pc, [a]);
   match-ex-table (compP2 P) (cname-of h a) pc (compxEs2 Es 0 0) = [(pc', d)] ]]
⇒ d ≤ length stk

```

⟨proof⟩

end

locale *J1-JVM-heap-conf-base* =

```

J1-JVM-heap-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write

```

+

```

J1-heap-conf-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf P

```

+

```

JVM-heap-conf-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf compP2 P

```

for *addr2thread-id* :: ('addr :: addr) ⇒ 'thread-id

and *thread-id2addr* :: 'thread-id ⇒ 'addr

and *spurious-wakeups* :: bool

and *empty-heap* :: 'heap

and *allocate* :: 'heap ⇒ htype ⇒ ('heap × 'addr) set

and *typeof-addr* :: 'heap ⇒ 'addr → htype

and *heap-read* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool

and *heap-write* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool

and *hconf* :: 'heap ⇒ bool

and *P* :: 'addr J1-prog

begin

inductive *bisim1-list1* ::

'thread-id ⇒ 'heap ⇒ 'addr expr1 × 'addr locals1 ⇒ ('addr expr1 × 'addr locals1) list
⇒ 'addr option ⇒ 'addr frame list ⇒ bool

for *t* :: 'thread-id **and** *h* :: 'heap

where

bl1-Normal:

[[*compTP* *P* ⊢ *t*:(*xcp*, *h*, (*stk*, *loc*, *C*, *M*, *pc*) # *frs*) √;
 P ⊢ *C* sees *M* : *Ts*→*T* = [*body*] in *D*;
 P,*blocks1* 0 (*Class D*#*Ts*) *body*, *h* ⊢ (*e*, *xs*) ↔ (*stk*, *loc*, *pc*, *xcp*); *max-vars* *e* ≤ *length* *xs*;
 list-all2 (*bisim1-fr P h*) *exs frs*]]
⇒ *bisim1-list1 t h* (*e*, *xs*) *exs xcp* ((*stk*, *loc*, *C*, *M*, *pc*) # *frs*)

| *bl1-finalVal*:

[[*hconf h*; *preallocated h*]] ⇒ *bisim1-list1 t h* (*Val v*, *xs*) [] *None* []

| *bl1-finalThrow*:

[[*hconf h*; *preallocated h*]] ⇒ *bisim1-list1 t h* (*Throw a*, *xs*) [] [*a*] []

fun *wbisim1* ::

'thread-id

⇒ (((('addr expr1 × 'addr locals1) × ('addr expr1 × 'addr locals1) list) × 'heap,
('addr option × 'addr frame list) × 'heap) *bisim*

where *wbisim1 t* ((*ex*, *exs*), *h*) ((*xcp*, *frs*), *h'*) ↔ *h* = *h'* ∧ *bisim1-list1 t h ex exs xcp frs*

lemma *new-thread-conf-compTP*:

assumes *hconf*: *hconf h preallocated h*

and *ha*: *typeof-addr h a* = [*Class-type C*]

and *sub*: *typeof-addr h (thread-id2addr t)* = [*Class-type C'*] *P* ⊢ *C'* ≼* *Thread*

and *sees*: *P* ⊢ *C* sees *M*: []→*T* = [*meth*] in *D*

shows *compTP P* ⊢ *t*:(*None*, *h*, [([], *Addr a* # *replicate (max-vars meth) undefined-value*, *D*, *M*, 0)]) √

⟨*proof*⟩

lemma *ta-bisim12-extTA2J1-extTA2JVM*:

assumes *nt*: ∧*n T C M a h*. [[*n* < *length* {*ta*}_{*t*}; {*ta*}_{*t*} ! *n* = *NewThread T* (*C*, *M*, *a*) *h*]]

⇒ *typeof-addr h a* = [*Class-type C*] ∧ (∃ *C'*. *typeof-addr h (thread-id2addr T)* = [*Class-type C'*] ∧ *P* ⊢ *C'* ≼* *Thread*) ∧

(∃ *T meth D*. *P* ⊢ *C* sees *M*: []→*T* = [*meth*] in *D*) ∧ *hconf h* ∧ *preallocated h*

shows *ta-bisim wbisim1 (extTA2J1 P ta) (extTA2JVM (compP2 P) ta)*

⟨*proof*⟩

end

definition *no-call2* :: 'addr expr1 ⇒ *pc* ⇒ bool

where *no-call2 e pc* ↔ (*pc* ≤ *length (compE2 e)*) ∧ (*pc* < *length (compE2 e)* → (∀ *M n*. *compE2 e* ! *pc* ≠ *Invoke M n*))

definition *no-calls2* :: 'addr expr1 list ⇒ *pc* ⇒ bool

where *no-calls2 es pc* ↔ (*pc* ≤ *length (compEs2 es)*) ∧ (*pc* < *length (compEs2 es)* → (∀ *M n*. *compEs2 es* ! *pc* ≠ *Invoke M n*))

locale *J1-JVM-conf-read* =

```

J1-JVM-heap-conf-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf P
+
JVM-conf-read
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf compP2 P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr J1-prog

locale J1-JVM-heap-conf =
  J1-JVM-heap-conf-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf P
+
JVM-heap-conf
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf compP2 P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr J1-prog
begin

lemma red-external-ta-bisim21:
  [[ wf-prog wf-md P; P, t ⊢ ⟨a·M(vs), h⟩ −ta→ext ⟨va, h'⟩; hconf h'; preallocated h' ] ]
  ⇒ ta-bisim wbisim1 (extTA2J1 P ta) (extTA2JVM (compP2 P) ta)
  ⟨proof⟩

lemma ta-bisim-red-extTA2J1-extTA2JVM:
  assumes wf: wf-prog wf-md P
  and red: uf, P, t' ⊢ 1 ⟨e, s⟩ −ta→ ⟨e', s'⟩

```

and $hconf: hconf (hp s') \text{ preallocated } (hp s')$
shows $ta\text{-bisim } wbisim1 (extTA2J1 P ta) (extTA2JVM (compP2 P) ta)$
 $\langle proof \rangle$

end

sublocale $J1\text{-JVM-conf-read} < heap\text{-conf?}: J1\text{-JVM-heap-conf}$
 $\langle proof \rangle$

sublocale $J1\text{-JVM-conf-read} < heap?: J1\text{-heap}$
 $\langle proof \rangle$

end

7.17 Correctness of Stage: From intermediate language to JVM

theory $J1JVM$ **imports** $J1JVMBisim$ **begin**

context $J1\text{-JVM-heap-base}$ **begin**

declare $\tau move1.simps [simp del]$ $\tau moves1.simps [simp del]$

lemma $bisim1\text{-insync-Throw-exec}$:

assumes $bisim2: P, e2, h \vdash (Throw ad, xs) \leftrightarrow (stk, loc, pc, xcp)$
shows $\tau Exec\text{-movet-a } P t (sync_V (e1) e2) h (stk, loc, Suc (Suc (Suc (length (compE2 e1) + pc))),$
 $xcp) ([Addr ad], loc, 6 + length (compE2 e1) + length (compE2 e2), None)$
 $\langle proof \rangle$

end

primrec $sim12\text{-size} :: ('a, 'b, 'addr) exp \Rightarrow nat$
and $sim12\text{-sizes} :: ('a, 'b, 'addr) exp list \Rightarrow nat$

where

$sim12\text{-size} (new C) = 0$
 $sim12\text{-size} (newA T [e]) = Suc (sim12\text{-size } e)$
 $sim12\text{-size} (Cast T e) = Suc (sim12\text{-size } e)$
 $sim12\text{-size} (e \text{ instanceof } T) = Suc (sim12\text{-size } e)$
 $sim12\text{-size} (e \ll bop \gg e') = Suc (sim12\text{-size } e + sim12\text{-size } e')$
 $sim12\text{-size} (Val v) = 0$
 $sim12\text{-size} (Var V) = 0$
 $sim12\text{-size} (V := e) = Suc (sim12\text{-size } e)$
 $sim12\text{-size} (a [i]) = Suc (sim12\text{-size } a + sim12\text{-size } i)$
 $sim12\text{-size} (a [i] := e) = Suc (sim12\text{-size } a + sim12\text{-size } i + sim12\text{-size } e)$
 $sim12\text{-size} (a \cdot length) = Suc (sim12\text{-size } a)$
 $sim12\text{-size} (e \cdot F \{D\}) = Suc (sim12\text{-size } e)$
 $sim12\text{-size} (e \cdot F \{D\} := e') = Suc (sim12\text{-size } e + sim12\text{-size } e')$
 $sim12\text{-size} (e \cdot compareAndSwap (D \cdot F, e', e'')) = Suc (sim12\text{-size } e + sim12\text{-size } e' + sim12\text{-size } e'')$
 $sim12\text{-size} (e \cdot M (es)) = Suc (sim12\text{-size } e + sim12\text{-sizes } es)$
 $sim12\text{-size} (\{V: T=vo; e\}) = Suc (sim12\text{-size } e)$
 $sim12\text{-size} (sync_V (e) e') = Suc (sim12\text{-size } e + sim12\text{-size } e')$
 $sim12\text{-size} (insync_V (a) e) = Suc (sim12\text{-size } e)$
 $sim12\text{-size} (e;; e') = Suc (sim12\text{-size } e + sim12\text{-size } e')$
 $sim12\text{-size} (if (e) e1 else e2) = Suc (sim12\text{-size } e)$

| $\text{sim12-size } (\text{while}(b) c) = \text{Suc } (\text{Suc } (\text{sim12-size } b))$
| $\text{sim12-size } (\text{throw } e) = \text{Suc } (\text{sim12-size } e)$
| $\text{sim12-size } (\text{try } e \text{ catch}(C V) e') = \text{Suc } (\text{sim12-size } e)$

| $\text{sim12-sizes } [] = 0$
| $\text{sim12-sizes } (e \# es) = \text{sim12-size } e + \text{sim12-sizes } es$

lemma $\text{sim12-sizes-map-Val}$ [simp]:

$\text{sim12-sizes } (\text{map Val } vs) = 0$

$\langle \text{proof} \rangle$

lemma $\text{sim12-sizes-append}$ [simp]:

$\text{sim12-sizes } (es @ es') = \text{sim12-sizes } es + \text{sim12-sizes } es'$

$\langle \text{proof} \rangle$

context JVM-heap-base **begin**

lemma $\tau\text{Exec-mover-length-compE2-conv}$ [simp]:

assumes $pc: pc \geq \text{length } (\text{compE2 } e)$

shows $\tau\text{Exec-mover } ci P t e h (stk, loc, pc, xcp) s \longleftrightarrow s = (stk, loc, pc, xcp)$

$\langle \text{proof} \rangle$

lemma $\tau\text{Exec-movesr-length-compE2-conv}$ [simp]:

assumes $pc: pc \geq \text{length } (\text{compEs2 } es)$

shows $\tau\text{Exec-movesr } ci P t es h (stk, loc, pc, xcp) s \longleftrightarrow s = (stk, loc, pc, xcp)$

$\langle \text{proof} \rangle$

end

context J1-JVM-heap-base **begin**

lemma **assumes** $wf: wf\text{-J1-prog } P$

defines [simp]: $\text{sim-move} \equiv \lambda e e'. \text{if } \text{sim12-size } e' < \text{sim12-size } e \text{ then } \tau\text{Exec-mover-a} \text{ else } \tau\text{Exec-movet-a}$

and [simp]: $\text{sim-moves} \equiv \lambda es es'. \text{if } \text{sim12-sizes } es' < \text{sim12-sizes } es \text{ then } \tau\text{Exec-movesr-a} \text{ else } \tau\text{Exec-movest-a}$

shows $\text{exec-instr-simulates-red1}$:

$\llbracket P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp); \text{True}, P, t \vdash 1 \langle e, (h, xs) \rangle \text{-ta} \rightarrow \langle e', (h', xs') \rangle; \text{bsok } E n \rrbracket$

$\implies \exists pc'' stk'' loc'' xcp''. P, E, h' \vdash (e', xs') \leftrightarrow (stk'', loc'', pc'', xcp'') \wedge$

(if $\tau\text{move1 } P h e$

then $h = h' \wedge \text{sim-move } e e' P t E h (stk, loc, pc, xcp) (stk'', loc'', pc'', xcp'')$

else $\exists pc' stk' loc' xcp'. \tau\text{Exec-mover-a } P t E h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \wedge$

$\text{exec-move-a } P t E h (stk', loc', pc', xcp') (\text{extTA2JVM } (\text{compP2 } P) \text{ ta}) h'$

$(stk'', loc'', pc'', xcp'') \wedge$

$\neg \tau\text{move2 } (\text{compP2 } P) h stk' E pc' xcp' \wedge$

$(\text{call1 } e = \text{None} \vee \text{no-call2 } E pc \vee pc' = pc \wedge stk' = stk \wedge loc' = loc \wedge$

$xcp' = xcp))$

(**is** $\llbracket -; -; - \rrbracket$

$\implies \exists pc'' stk'' loc'' xcp''. - \wedge ?\text{exec ta } E e e' h stk loc pc xcp h' pc'' stk'' loc'' xcp''$)

and $\text{exec-instr-simulates-reds1}$:

$\llbracket P, Es, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, xcp); \text{True}, P, t \vdash 1 \langle es, (h, xs) \rangle \text{-ta} \rightarrow \langle es', (h', xs') \rangle; \text{bsoks } Es n \rrbracket$

$\implies \exists pc'' stk'' loc'' xcp''. P, Es, h' \vdash (es', xs') [\leftrightarrow] (stk'', loc'', pc'', xcp'') \wedge$

(if $\tau\text{moves1 } P h es$

then $h = h' \wedge \text{sim-moves } es \text{ } es' \text{ } P \text{ } t \text{ } Es \text{ } h \text{ } (stk, loc, pc, xcp) \text{ } (stk'', loc'', pc'', xcp'')$
 else $\exists pc' \text{ } stk' \text{ } loc' \text{ } xcp'. \tau \text{Exec-moves-r-a } P \text{ } t \text{ } Es \text{ } h \text{ } (stk, loc, pc, xcp) \text{ } (stk', loc', pc', xcp') \wedge$
 $\text{exec-moves-a } P \text{ } t \text{ } Es \text{ } h \text{ } (stk', loc', pc', xcp') \text{ } (\text{extTA2JVM } (compP2 \text{ } P) \text{ } ta)$
 $h' \text{ } (stk'', loc'', pc'', xcp'') \wedge$
 $\neg \tau \text{moves2 } (compP2 \text{ } P) \text{ } h \text{ } stk' \text{ } Es \text{ } pc' \text{ } xcp' \wedge$
 $(\text{calls1 } es = \text{None} \vee \text{no-calls2 } Es \text{ } pc \vee pc' = pc \wedge stk' = stk \wedge loc' = loc \wedge$
 $xcp' = xcp)$
 (is [-; -; -]
 $\implies \exists pc'' \text{ } stk'' \text{ } loc'' \text{ } xcp''. - \wedge ?\text{execs } ta \text{ } Es \text{ } es \text{ } es' \text{ } h \text{ } stk \text{ } loc \text{ } pc \text{ } xcp \text{ } h' \text{ } pc'' \text{ } stk'' \text{ } loc'' \text{ } xcp''$)
 <proof>

end

context J1-JVM-conf-read begin

lemma exec-1-simulates-Red1- τ :

assumes $wf: wf\text{-}J1\text{-prog } P$
and $Red1: True, P, t \vdash 1 \langle (e, xs)/exs, h \rangle -ta \rightarrow \langle (e', xs')/exs', h \rangle$
and $bisim: bisim1\text{-list1 } t \text{ } h \text{ } (e, xs) \text{ } exs \text{ } xcp \text{ } frs$
and $\tau: \tau \text{Move1 } P \text{ } h \text{ } ((e, xs), exs)$
shows $\exists xcp' \text{ } frs'. (\text{if } sim12\text{-size } e' < sim12\text{-size } e \text{ then } \tau \text{Exec-1-dr} \text{ else } \tau \text{Exec-1-dt}) (compP2 \text{ } P) \text{ } t$
 $(xcp, h, frs) \text{ } (xcp', h, frs') \wedge bisim1\text{-list1 } t \text{ } h \text{ } (e', xs') \text{ } exs' \text{ } xcp' \text{ } frs'$
 <proof>

lemma exec-1-simulates-Red1-not- τ :

assumes $wf: wf\text{-}J1\text{-prog } P$
and $Red1: True, P, t \vdash 1 \langle (e, xs)/exs, h \rangle -ta \rightarrow \langle (e', xs')/exs', h \rangle$
and $bisim: bisim1\text{-list1 } t \text{ } h \text{ } (e, xs) \text{ } exs \text{ } xcp \text{ } frs$
and $\tau: \neg \tau \text{Move1 } P \text{ } h \text{ } ((e, xs), exs)$
shows $\exists xcp' \text{ } frs'. \tau \text{Exec-1-dr } (compP2 \text{ } P) \text{ } t \text{ } (xcp, h, frs) \text{ } (xcp', h, frs') \wedge$
 $(\exists ta' \text{ } xcp'' \text{ } frs''. \text{exec-1-d } (compP2 \text{ } P) \text{ } t \text{ } (\text{Normal } (xcp', h, frs')) \text{ } ta' \text{ } (\text{Normal } (xcp'', h', frs''))$
 \wedge
 $\neg \tau \text{Move2 } (compP2 \text{ } P) \text{ } (xcp', h, frs') \wedge ta\text{-bisim } wbisim1 \text{ } ta \text{ } ta' \wedge$
 $bisim1\text{-list1 } t \text{ } h' \text{ } (e', xs') \text{ } exs' \text{ } xcp'' \text{ } frs'') \wedge$
 $(\text{call1 } e = \text{None} \vee$
 $(\text{case } frs \text{ of } Nil \Rightarrow \text{False} \mid (stk, loc, C, M, pc) \# \text{FRS} \Rightarrow \forall M' \text{ } n. \text{instrs-of } (compP2 \text{ } P) \text{ } C$
 $M \text{ } ! \text{ } pc \neq \text{Invoke } M' \text{ } n) \vee$
 $xcp' = xcp \wedge frs' = frs)$
 <proof>

end

end

7.18 Correctness of Stage 2: From JVM to intermediate language

theory JVMJ1 imports

J1JVMBisim

begin

declare *split-paired-Ex*[simp del]

lemma *rec-option-is-case-option*: *rec-option* = *case-option*
 ⟨*proof*⟩

context *J1-JVM-heap-base* **begin**

lemma **assumes** *ha*: *typeof-addr h a* = [*Class-type D*]
and *subclsObj*: $P \vdash D \preceq^* \text{Throwable}$
shows *bisim1-xcp-τRed*:
 [$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, [a])$;
 match-ex-table (*compP f P*) (*cname-of h a*) *pc* (*compxE2 e 0 0*) = *None*;
 $\exists n. n + \text{max-vars } e' \leq \text{length } xs \wedge \mathcal{B} e n$]
 $\implies \tau\text{red1r } P \text{ t h } (e', xs) (\text{Throw } a, loc) \wedge P, e, h \vdash (\text{Throw } a, loc) \leftrightarrow (stk, loc, pc, [a])$

and *bisims1-xcp-τReds*:
 [$P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, [a])$;
 match-ex-table (*compP f P*) (*cname-of h a*) *pc* (*compxEs2 es 0 0*) = *None*;
 $\exists n. n + \text{max-varss } es' \leq \text{length } xs \wedge \mathcal{B} s es n$]
 $\implies \exists vs es''. \tau\text{reds1r } P \text{ t h } (es', xs) (\text{map Val vs } @ \text{Throw } a \# es'', loc) \wedge$
 $P, es, h \vdash (\text{map Val vs } @ \text{Throw } a \# es'', loc) [\leftrightarrow] (stk, loc, pc, [a])$

⟨*proof*⟩

primrec *conf-xcp'* :: '*m prog* \Rightarrow '*heap* \Rightarrow '*addr option* \Rightarrow *bool* **where**
 conf-xcp' P h None = *True*
 | *conf-xcp' P h [a]* = ($\exists D. \text{typeof-addr } h a = [\text{Class-type } D] \wedge P \vdash D \preceq^* \text{Throwable}$)

lemma *conf-xcp-conf-xcp'*:
 conf-xcp P h xcp i \implies *conf-xcp' P h xcp*
 ⟨*proof*⟩

lemma *conf-xcp'-compP [simp]*: *conf-xcp' (compP f P)* = *conf-xcp' P*
 ⟨*proof*⟩

end

context *J1-heap-base* **begin**

lemmas *τred1-Val-simps [simp]* =
 τred1r-Val τred1t-Val τreds1r-map-Val τreds1t-map-Val

end

context *J1-JVM-conf-read* **begin**

lemma **assumes** *wf*: *wf-J1-prog P*
and *hconf*: *hconf h preallocated h*
and *tconf*: $P, h \vdash t \sqrt{t}$
shows *red1-simulates-exec-instr*:
 [$P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp)$;
 exec-move-d P t E h (*stk, loc, pc, xcp*) *ta h'* (*stk', loc', pc', xcp'*);
 $n + \text{max-vars } e \leq \text{length } xs$; *bsok E n*; $P, h \vdash stk [\leq] ST$; *conf-xcp' (compP2 P) h xcp*]
 $\implies \exists e'' xs''. P, E, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp') \wedge$
 (*if* $\tau\text{move2 (compP2 P) h stk E pc xcp}$
 then $h' = h \wedge (\text{if } xcp' = \text{None} \longrightarrow pc < pc' \text{ then } \tau\text{red1r} \text{ else } \tau\text{red1t}) P \text{ t h } (e, xs) (e'', xs'')$)

else $\exists ta' e' xs'. \tau red1r P t h (e, xs) (e', xs') \wedge True, P, t \vdash 1 \langle e', (h, xs') \rangle -ta' \rightarrow \langle e'', (h', xs'') \rangle$
 $\wedge ta\text{-bisim } w\text{bisim1 } (extTA2J1 P ta') ta \wedge \neg \tau move1 P h e' \wedge (call1 e = None \vee no\text{-call2 } E pc \vee e' = e \wedge xs' = xs)$

(is $\llbracket -; ?exec E stk loc pc xcp stk' loc' pc' xcp'; -; -; - \rrbracket$
 $\implies \exists e'' xs''. P, E, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp') \wedge ?red e xs e'' xs'' E stk pc pc' xcp xcp'$)

and *reds1-simulates-exec-instr*:

$\llbracket P, Es, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, xcp);$
exec-moves-d $P t Es h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')$;
 $n + max\text{-varss } es \leq length xs; bsoks Es n; P, h \vdash stk [:\leq] ST; conf\text{-xcp}' (compP2 P) h xcp \rrbracket$
 $\implies \exists es'' xs''. P, Es, h' \vdash (es'', xs'') [\leftrightarrow] (stk', loc', pc', xcp') \wedge$
 (if $\tau moves2 (compP2 P) h stk Es pc xcp$
 then $h' = h \wedge (if xcp' = None \longrightarrow pc < pc' \text{ then } \tau reds1r \text{ else } \tau reds1t) P t h (es, xs) (es'', xs'')$
 else $\exists ta' es' xs'. \tau reds1r P t h (es, xs) (es', xs') \wedge True, P, t \vdash 1 \langle es', (h, xs') \rangle [-ta' \rightarrow] \langle es'', (h', xs'') \rangle$) $\wedge ta\text{-bisim } w\text{bisim1 } (extTA2J1 P ta') ta \wedge \neg \tau moves1 P h es' \wedge (calls1 es = None \vee no\text{-calls2 } Es pc \vee es' = es \wedge xs' = xs)$
 (is $\llbracket -; ?execs Es stk loc pc xcp stk' loc' pc' xcp'; -; -; - \rrbracket$
 $\implies \exists es'' xs''. P, Es, h' \vdash (es'', xs'') [\leftrightarrow] (stk', loc', pc', xcp') \wedge ?reds es xs es'' xs'' Es stk pc pc' xcp xcp'$)
 $\langle proof \rangle$

end

inductive *sim21-size-aux* :: $nat \Rightarrow (pc \times 'addr \text{ option}) \Rightarrow (pc \times 'addr \text{ option}) \Rightarrow bool$

for *len* :: nat

where

$\llbracket pc1 \leq len; pc2 \leq len; xcp1 \neq None \wedge xcp2 = None \wedge pc1 = pc2 \vee xcp1 = None \wedge pc1 > pc2 \rrbracket$
 $\implies sim21\text{-size-aux } len (pc1, xcp1) (pc2, xcp2)$

definition *sim21-size* :: $'addr \text{ jvm-prog} \Rightarrow 'addr \text{ jvm-thread-state} \Rightarrow 'addr \text{ jvm-thread-state} \Rightarrow bool$

where

$sim21\text{-size } P xcpfrs xcpfrs' \longleftrightarrow$
 $(xcpfrs, xcpfrs') \in$
 $inv\text{-image } (less\text{-than } < *lex* > \text{ same-fst } (\lambda n. True) (\lambda n. \{(pcxcp, pcxcp'). sim21\text{-size-aux } n pcxcp pcxcp'\}))$
 $(\lambda(xcp, frs). (length frs, case frs of [] \Rightarrow undefined$
 $\quad | (stk, loc, C, M, pc) \# frs \Rightarrow (length (fst (snd (snd (the (snd (snd (snd (method P C M))))))))), pc, xcp)))$

lemma *wfP-sim21-size-aux*: $wfP (sim21\text{-size-aux } n)$

$\langle proof \rangle$

lemma *Collect-split-mem*: $\{(x, y). (x, y) \in Q\} = Q \langle proof \rangle$

lemma *wfP-sim21-size*: $wfP (sim21\text{-size } P)$

$\langle proof \rangle$

declare *split-beta*[*simp*]

context *J1-JVM-heap-base* **begin**

lemma *bisim1-Invoke- τ Red*:

$\llbracket P, E, h \vdash (e, xs) \leftrightarrow (rev vs @ Addr a \# stk', loc, pc, None); pc < length (compE2 E);$

$\text{compE2 } E ! pc = \text{Invoke } M (\text{length } vs); n + \text{max-vars } e \leq \text{length } xs; \text{bsok } E n \llbracket$
 $\implies \exists e' xs'. \tau \text{red1r } P t h (e, xs) (e', xs') \wedge P, E, h \vdash (e', xs') \leftrightarrow (\text{rev } vs @ \text{Addr } a \# \text{stk}', \text{loc}, pc,$
 $\text{None}) \wedge \text{call1 } e' = \llbracket (a, M, vs) \rrbracket$
 $(\text{is } \llbracket -; -; -; - \rrbracket \implies ?\text{concl } e \text{ xs } E n \text{ pc } \text{stk}' \text{ loc})$

and *bisims1-Invoke- τ Reds:*

$\llbracket P, Es, h \vdash (es, xs) [\leftrightarrow] (\text{rev } vs @ \text{Addr } a \# \text{stk}', \text{loc}, pc, \text{None}); pc < \text{length } (\text{compEs2 } Es);$
 $\text{compEs2 } Es ! pc = \text{Invoke } M (\text{length } vs); n + \text{max-varss } es \leq \text{length } xs; \text{bsoks } Es n \llbracket$
 $\implies \exists es' xs'. \tau \text{reds1r } P t h (es, xs) (es', xs') \wedge P, Es, h \vdash (es', xs') [\leftrightarrow] (\text{rev } vs @ \text{Addr } a \# \text{stk}', \text{loc},$
 $pc, \text{None}) \wedge \text{calls1 } es' = \llbracket (a, M, vs) \rrbracket$
 $(\text{is } \llbracket -; -; -; - \rrbracket \implies ?\text{concls } es \text{ xs } Es n \text{ pc } \text{stk}' \text{ loc})$
 $\langle \text{proof} \rangle$

end

declare *split-beta* [*simp del*]

context *J1-JVM-conf-read* **begin**

lemma *τ Red1-simulates-exec-1- τ :*

assumes *wf: wf-J1-prog P*

and *exec: exec-1-d (compP2 P) t (Normal (xcp, h, frs)) ta (Normal (xcp', h', frs'))*

and *bisim: bisim1-list1 t h (e, xs) exs xcp frs*

and *τ : τ Move2 (compP2 P) (xcp, h, frs)*

shows $h = h' \wedge (\exists e' xs' exs'. (\text{if } \text{sim21-size } (\text{compP2 } P) (xcp', frs') (xcp, frs) \text{ then } \tau \text{Red1r else } \tau \text{Red1t}) P t h ((e, xs), exs) ((e', xs'), exs') \wedge \text{bisim1-list1 } t h (e', xs') exs' xcp' frs')$
 $\langle \text{proof} \rangle$

lemma *τ Red1-simulates-exec-1-not- τ :*

assumes *wf: wf-J1-prog P*

and *exec: exec-1-d (compP2 P) t (Normal (xcp, h, frs)) ta (Normal (xcp', h', frs'))*

and *bisim: bisim1-list1 t h (e, xs) exs xcp frs*

and *τ : $\neg \tau$ Move2 (compP2 P) (xcp, h, frs)*

shows $\exists e' xs' exs' ta' e'' xs'' exs''. \tau \text{Red1r } P t h ((e, xs), exs) ((e', xs'), exs') \wedge$
 $\text{True}, P, t \vdash 1 \langle (e', xs') / exs', h \rangle - ta' \rightarrow \langle (e'', xs'') / exs'', h \rangle \wedge$
 $\neg \tau \text{Move1 } P h ((e', xs'), exs') \wedge ta\text{-bisim } w\text{bisim1 } ta' ta \wedge$
 $\text{bisim1-list1 } t h' (e'', xs'') exs'' xcp' frs' \wedge$
 $(\text{call1 } e = \text{None} \vee$

$(\text{case } frs \text{ of } Nil \Rightarrow \text{False} \mid (\text{stk}, \text{loc}, C, M, pc) \# \text{FRS} \Rightarrow \forall M' n.$
 $\text{instrs-of } (\text{compP2 } P) C M ! pc \neq \text{Invoke } M' n) \vee$
 $e' = e \wedge xs' = xs \wedge exs' = exs)$

$\langle \text{proof} \rangle$

end

end

7.19 Correctness of Stage 2: The multithreaded setting

theory *Correctness2*

imports

J1JVM

```

JVMJ1
../BV/BVProgressThreaded
begin

declare Listn.lesub-list-impl-same-size[simp del]

context J1-JVM-heap-conf-base begin

lemma bisim1-list1-has-methodD: bisim1-list1 t h ex exs xcp ((stk, loc, C, M, pc) # frs)  $\implies$  P  $\vdash$  C
has M
⟨proof⟩

end

declare compP-has-method [simp]

sublocale J1-JVM-heap-conf-base < Red1-exec:
  delay-bisimulation-base mred1 P t mexec (compP2 P) t wbisim1 t ta-bisim wbisim1  $\tau$ MOVE1 P
 $\tau$ MOVE2 (compP2 P)
  for t
⟨proof⟩

sublocale J1-JVM-heap-conf-base < Red1-execd: delay-bisimulation-base
  mred1 P t
  mexecd (compP2 P) t
  wbisim1 t
  ta-bisim wbisim1
   $\tau$ MOVE1 P
   $\tau$ MOVE2 (compP2 P)
  for t
⟨proof⟩

context JVM-heap-base begin

lemma  $\tau$ exec-1-d-silent-move:
   $\tau$ exec-1-d P t (xcp, h, frs) (xcp', h', frs')
 $\implies$   $\tau$ trsys.silent-move (mexecd P t) ( $\tau$ MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')
⟨proof⟩

lemma silent-move- $\tau$ exec-1-d:
   $\tau$ trsys.silent-move (mexecd P t) ( $\tau$ MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')
 $\implies$   $\tau$ exec-1-d P t (xcp, h, frs) (xcp', h', frs')
⟨proof⟩

lemma  $\tau$ Exec-1-dr-rtranclpD:
   $\tau$ Exec-1-dr P t (xcp, h, frs) (xcp', h', frs')
 $\implies$   $\tau$ trsys.silent-moves (mexecd P t) ( $\tau$ MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')
⟨proof⟩

lemma  $\tau$ Exec-1-dt-tranclpD:
   $\tau$ Exec-1-dt P t (xcp, h, frs) (xcp', h', frs')
 $\implies$   $\tau$ trsys.silent-movet (mexecd P t) ( $\tau$ MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')
⟨proof⟩

```

lemma *rtranclp- τ Exec-1-dr*:

τ trsys.silent-moves (mexecd P t) (τ MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')

$\implies \tau$ Exec-1-dr P t (xcp, h, frs) (xcp', h', frs')

<proof>

lemma *tranclp- τ Exec-1-dt*:

τ trsys.silent-movet (mexecd P t) (τ MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')

$\implies \tau$ Exec-1-dt P t (xcp, h, frs) (xcp', h', frs')

<proof>

lemma *τ Exec-1-dr-conv-rtranclp*:

τ Exec-1-dr P t (xcp, h, frs) (xcp', h', frs') =

τ trsys.silent-moves (mexecd P t) (τ MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')

<proof>

lemma *τ Exec-1-dt-conv-tranclp*:

τ Exec-1-dt P t (xcp, h, frs) (xcp', h', frs') =

τ trsys.silent-movet (mexecd P t) (τ MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')

<proof>

end

context *J1-JVM-conf-read begin*

lemma *Red1-execd-weak-bisim*:

assumes wf: wf-J1-prog P

shows delay-bisimulation-measure (mred1 P t) (mexecd (compP2 P) t) (wbisim1 t) (ta-bisim wbisim1) (τ MOVE1 P) (τ MOVE2 (compP2 P)) ($\lambda((e, xs), exs), h$) (((e', xs'), exs'), h'). sim12-size e < sim12-size e' ($\lambda(xcpfrs, h)$) (xcpfrs', h). sim21-size (compP2 P) xcpfrs xcpfrs')

<proof>

lemma *Red1-execd-delay-bisim*:

assumes wf: wf-J1-prog P

shows delay-bisimulation-diverge (mred1 P t) (mexecd (compP2 P) t) (wbisim1 t) (ta-bisim wbisim1) (τ MOVE1 P) (τ MOVE2 (compP2 P))

<proof>

end

definition *bisim-wait1JVM* ::

'addr jvm-prog \implies ('addr expr1 \times 'addr locals1) \times ('addr expr1 \times 'addr locals1) list \implies 'addr jvm-thread-state \implies bool

where

bisim-wait1JVM P \equiv

$\lambda((e1, xs1), exs1) (xcp, frs). call1 e1 \neq None \wedge$

(case frs of Nil \implies False | (stk, loc, C, M, pc) # frs' $\implies \exists M' n. instrs-of P C M ! pc = Invoke M' n$)

sublocale *J1-JVM-heap-conf-base* < *Red1-execd*:

FWbisimulation-base

final-expr1

mred1 P

JVM-final

mexecd (compP2 P)

convert-RA
wbisim1
bisim-wait1JVM (compP2 P)
 ⟨*proof*⟩

sublocale *JVM-heap-base* < *execd-mthr*:

τ multithreaded
JVM-final
mexecd P
convert-RA
τ MOVE2 P
for *P*
 ⟨*proof*⟩

sublocale *J1-JVM-heap-conf-base* < *Red1-execd*:

FWdelay-bisimulation-base
final-expr1
mred1 P
JVM-final
mexecd (compP2 P)
convert-RA
wbisim1
bisim-wait1JVM (compP2 P)
τ MOVE1 P
τ MOVE2 (compP2 P)
 ⟨*proof*⟩

context *J1-JVM-conf-read* **begin**

theorem *Red1-exec1-FWwbisim*:

assumes *wf: wf-J1-prog P*
shows *FWdelay-bisimulation-diverge final-expr1 (mred1 P) JVM-final (mexecd (compP2 P)) wbisim1*
(bisim-wait1JVM (compP2 P)) (τ MOVE1 P) (τ MOVE2 (compP2 P))
 ⟨*proof*⟩

end

sublocale *J1-JVM-heap-conf-base* < *Red1-mexecd*:

FWbisimulation-base
final-expr1
mred1 P
JVM-final
mexecd (compP2 P)
convert-RA
wbisim1
bisim-wait1JVM (compP2 P)
 ⟨*proof*⟩

context *J1-JVM-heap-conf* **begin**

lemma *bisim-J1-JVM-start*:

assumes *wf: wf-J1-prog P*
and *wf-start: wf-start-state P C M vs*
shows *Red1-execd.mbisim (J1-start-state P C M vs) (JVM-start-state (compP2 P) C M vs)*

<proof>

lemmas $\tau red1\text{-}Val\text{-}simps = \tau red1r\text{-}Val \ \tau red1t\text{-}Val \ \tau reds1r\text{-}map\text{-}Val \ \tau reds1t\text{-}map\text{-}Val$

end

end

7.20 Indexing variables in variable lists

theory *ListIndex* **imports** *Main* **begin**

In order to support local variables and arbitrarily nested blocks, the local variables are arranged as an indexed list. The outermost local variable (“this”) is the first element in the list, the most recently created local variable the last element. When descending into a block structure, a corresponding list *Vs* of variable names is maintained. To find the index of some variable *V*, we have to find the index of the *last* occurrence of *V* in *Vs*. This is what *index* does:

primrec *index* :: 'a list \Rightarrow 'a \Rightarrow nat

where

$index [] y = 0$
 $| index (x\#xs) y =$
(if x=y then if x \in set xs then index xs y + 1 else 0 else index xs y + 1)

definition *hidden* :: 'a list \Rightarrow nat \Rightarrow bool

where *hidden* xs *i* $\equiv i < size\ xs \wedge xs!i \in set(drop\ (i+1)\ xs)$

7.20.1 *index*

lemma [*simp*]: $index\ (xs\ @\ [x])\ x = size\ xs$ *<proof>*

lemma [*simp*]: $(index\ (xs\ @\ [x])\ y = size\ xs) = (x = y)$ *<proof>*

lemma [*simp*]: $x \in set\ xs \Longrightarrow xs\ !\ index\ xs\ x = x$ *<proof>*

lemma [*simp*]: $x \notin set\ xs \Longrightarrow index\ xs\ x = size\ xs$ *<proof>*

lemma *index-size-conv*[*simp*]: $(index\ xs\ x = size\ xs) = (x \notin set\ xs)$ *<proof>*

lemma *size-index-conv*[*simp*]: $(size\ xs = index\ xs\ x) = (x \notin set\ xs)$ *<proof>*

lemma $(index\ xs\ x < size\ xs) = (x \in set\ xs)$ *<proof>*

lemma [*simp*]: $\llbracket y \in set\ xs; x \neq y \rrbracket \Longrightarrow index\ (xs\ @\ [x])\ y = index\ xs\ y$ *<proof>*

lemma *index-less-size*[*simp*]: $x \in set\ xs \Longrightarrow index\ xs\ x < size\ xs$ *<proof>*

lemma *index-less-aux*: $\llbracket x \in set\ xs; size\ xs \leq n \rrbracket \Longrightarrow index\ xs\ x < n$ *<proof>*

lemma [*simp*]: $x \in set\ xs \vee y \in set\ xs \Longrightarrow (index\ xs\ x = index\ xs\ y) = (x = y)$ *<proof>*

lemma *inj-on-index*: $inj\text{-}on\ (index\ xs)\ (set\ xs)$ *<proof>*

lemma *index-drop*: $\bigwedge x\ i. \llbracket x \in set\ xs; index\ xs\ x < i \rrbracket \Longrightarrow x \notin set(drop\ i\ xs)$ *<proof>*

7.20.2 hidden

lemma *hidden-index*: $x \in \text{set } xs \implies \text{hidden } (xs @ [x]) (\text{index } xs \ x) \langle \text{proof} \rangle$

lemma *hidden-inacc*: $\text{hidden } xs \ i \implies \text{index } xs \ x \neq i \langle \text{proof} \rangle$

lemma [*simp*]: $\text{hidden } xs \ i \implies \text{hidden } (xs @ [x]) \ i \langle \text{proof} \rangle$

lemma *fun-upds-apply*: $\bigwedge m \ ys.$
 $(m(xs[\mapsto]ys)) \ x =$
 $(\text{let } xs' = \text{take } (\text{size } ys) \ xs$
 $\text{in if } x \in \text{set } xs' \text{ then } \text{Some}(ys \ ! \ \text{index } xs' \ x) \text{ else } m \ x) \langle \text{proof} \rangle$

lemma *map-upds-apply-eq-Some*:
 $((m(xs[\mapsto]ys)) \ x = \text{Some } y) =$
 $(\text{let } xs' = \text{take } (\text{size } ys) \ xs$
 $\text{in if } x \in \text{set } xs' \text{ then } ys \ ! \ \text{index } xs' \ x = y \text{ else } m \ x = \text{Some } y) \langle \text{proof} \rangle$

lemma *map-upds-upd-conv-index*:
 $\llbracket x \in \text{set } xs; \text{size } xs \leq \text{size } ys \rrbracket$
 $\implies m(xs[\mapsto]ys, \ x \mapsto y) = m(xs[\mapsto]ys[\text{index } xs \ x := y]) \langle \text{proof} \rangle$

lemma *image-index*:
 $A \subseteq \text{set}(xs @ [x]) \implies \text{index } (xs @ [x]) \ 'A =$
 $(\text{if } x \in A \text{ then } \text{insert } (\text{size } xs) \ (\text{index } xs \ ' (A - \{x\})) \text{ else } \text{index } xs \ ' A) \langle \text{proof} \rangle$

lemma *index-le-lengthD*: $\text{index } xs \ x < \text{length } xs \implies x \in \text{set } xs$
 $\langle \text{proof} \rangle$

lemma *not-hidden-index-nth*: $\llbracket i < \text{length } Vs; \neg \text{hidden } Vs \ i \rrbracket \implies \text{index } Vs \ (Vs \ ! \ i) = i$
 $\langle \text{proof} \rangle$

lemma *hidden-snoc-nth*:
assumes *len*: $i < \text{length } Vs$
shows $\text{hidden } (Vs @ [Vs \ ! \ i]) \ i$
 $\langle \text{proof} \rangle$

lemma *map-upds-Some-eq-nth-index*:
assumes $[Vs \ [\mapsto] \ vs] \ V = \text{Some } v \ \text{length } Vs \leq \text{length } vs$
shows $vs \ ! \ \text{index } Vs \ V = v$
 $\langle \text{proof} \rangle$

end

7.21 Compilation Stage 1

theory *Compiler1* **imports**

PCompiler

J1State

ListIndex

begin

definition *fresh-var* :: $vname \ list \Rightarrow vname$
where $\text{fresh-var } Vs = \text{sum-list } (\text{STR } "V" \ \# \ Vs)$

lemma *fresh-var-fresh*: $\text{fresh-var } Vs \notin \text{set } Vs$

<proof>

Replacing variable names by indices.

function *compE1* :: *vname list* ⇒ *'addr expr* ⇒ *'addr expr1*
and *compEs1* :: *vname list* ⇒ *'addr expr list* ⇒ *'addr expr1 list*

where

compE1 *Vs* (*new C*) = *new C*
| *compE1* *Vs* (*newA T[e]*) = *newA T[compE1 Vs e]*
| *compE1* *Vs* (*Cast T e*) = *Cast T (compE1 Vs e)*
| *compE1* *Vs* (*e instanceof T*) = (*compE1 Vs e*) *instanceof T*
| *compE1* *Vs* (*Val v*) = *Val v*
| *compE1* *Vs* (*Var V*) = *Var(index Vs V)*
| *compE1* *Vs* (*e«bop»e'*) = (*compE1 Vs e*)«bop»(*compE1 Vs e'*)
| *compE1* *Vs* (*V:=e*) = (*index Vs V*):= (*compE1 Vs e*)
| *compE1* *Vs* (*a[i]*) = (*compE1 Vs a*)_[*compE1 Vs i*]
| *compE1* *Vs* (*a[i]:=e*) = (*compE1 Vs a*)_[*compE1 Vs i*]:=*compE1 Vs e*
| *compE1* *Vs* (*a.length*) = *compE1 Vs a.length*
| *compE1* *Vs* (*e.F{D}*) = *compE1 Vs e.F{D}*
| *compE1* *Vs* (*e.F{D}:=e'*) = *compE1 Vs e.F{D}:=compE1 Vs e'*
| *compE1* *Vs* (*e.compareAndSwap(D.F, e', e'')*) = *compE1 Vs e.compareAndSwap(D.F, compE1 Vs e', compE1 Vs e'')*
| *compE1* *Vs* (*e.M(es)*) = (*compE1 Vs e*)·*M(compEs1 Vs es)*
| *compE1* *Vs* {*V:T=vo; e*} = {(*size Vs*):*T=vo; compE1 (Vs@[V]) e*}
| *compE1* *Vs* (*sync_U (o') e*) = *sync_{length Vs} (compE1 Vs o') (compE1 (Vs@[fresh-var Vs]) e)*
| *compE1* *Vs* (*insync_U (a) e*) = *insync_{length Vs} (a) (compE1 (Vs@[fresh-var Vs]) e)*
| *compE1* *Vs* (*e1;;e2*) = (*compE1 Vs e1*)*;;*(*compE1 Vs e2*)
| *compE1* *Vs* (*if (b) e1 else e2*) = (*if (compE1 Vs b) (compE1 Vs e1) else (compE1 Vs e2)*)
| *compE1* *Vs* (*while (b) e*) = (*while (compE1 Vs b) (compE1 Vs e)*)
| *compE1* *Vs* (*throw e*) = *throw (compE1 Vs e)*
| *compE1* *Vs* (*try e1 catch(C V) e2*) = *try(compE1 Vs e1) catch(C (size Vs)) (compE1 (Vs@[V]) e2)*

| *compEs1* *Vs* [] = []
| *compEs1* *Vs* (*e#es*) = *compE1 Vs e # compEs1 Vs es*

<proof>

termination

<proof>

lemmas *compE1-compEs1-induct* =

compE1-compEs1.induct[*case-names New NewArray Cast InstanceOf Val Var BinOp LAss AAcc AAss ALen FAcc FAss CAS Call Block Synchronized InSynchronized Seq Cond While throw TryCatch Nil Cons*]

lemma *compEs1-conv-map* [*simp*]: *compEs1 Vs es* = *map (compE1 Vs) es*

<proof>

lemmas *compEs1-map-Val* = *compEs1-conv-map*

lemma *compE1-eq-Val* [*simp*]: *compE1 Vs e* = *Val v* ⇔ *e* = *Val v*

<proof>

lemma *Val-eq-compE1* [*simp*]: *Val v* = *compE1 Vs e* ⇔ *e* = *Val v*

<proof>

lemma *compEs1-eq-map-Val* [*simp*]: *compEs1 Vs es* = *map Val vs* ⇔ *es* = *map Val vs*

<proof>

lemma *compE1-eq-Var* [simp]: $\text{compE1 } Vs \ e = \text{Var } V \longleftrightarrow (\exists V'. e = \text{Var } V' \wedge V = \text{index } Vs \ V')$
<proof>

lemma *compE1-eq-Call* [simp]:
 $\text{compE1 } Vs \ e = \text{obj} \cdot M(\text{params}) \longleftrightarrow (\exists \text{obj}' \ \text{params}'. e = \text{obj}' \cdot M(\text{params}') \wedge \text{compE1 } Vs \ \text{obj}' = \text{obj} \wedge \text{compEs1 } Vs \ \text{params}' = \text{params})$
<proof>

lemma *length-compEs2* [simp]:
 $\text{length } (\text{compEs1 } Vs \ es) = \text{length } es$
<proof>

lemma *fixes* $e :: 'addr \ \text{expr}$ **and** $es :: 'addr \ \text{expr list}$
shows *expr-locks-compE1* [simp]: $\text{expr-locks } (\text{compE1 } Vs \ e) = \text{expr-locks } e$
and *expr-lockss-compEs1* [simp]: $\text{expr-lockss } (\text{compEs1 } Vs \ es) = \text{expr-lockss } es$
<proof>

lemma *fixes* $e :: 'addr \ \text{expr}$ **and** $es :: 'addr \ \text{expr list}$
shows *contains-insync-compE1* [simp]: $\text{contains-insync } (\text{compE1 } Vs \ e) = \text{contains-insync } e$
and *contains-insyncs-compEs1* [simp]: $\text{contains-insyncs } (\text{compEs1 } Vs \ es) = \text{contains-insyncs } es$
<proof>

lemma *fixes* $e :: 'addr \ \text{expr}$ **and** $es :: 'addr \ \text{expr list}$
shows *max-vars-compE1*: $\text{max-vars } (\text{compE1 } Vs \ e) = \text{max-vars } e$
and *max-varss-compEs1*: $\text{max-varss } (\text{compEs1 } Vs \ es) = \text{max-varss } es$
<proof>

lemma *fixes* $e :: 'addr \ \text{expr}$ **and** $es :: 'addr \ \text{expr list}$
shows \mathcal{B} : $\text{size } Vs = n \implies \mathcal{B} (\text{compE1 } Vs \ e) \ n$
and $\mathcal{B}s$: $\text{size } Vs = n \implies \mathcal{B}s (\text{compEs1 } Vs \ es) \ n$
<proof>

lemma *fixes* $e :: 'addr \ \text{expr}$ **and** $es :: 'addr \ \text{expr list}$
shows *fv-compE1*: $\text{fv } e \subseteq \text{set } Vs \implies \text{fv } (\text{compE1 } Vs \ e) = (\text{index } Vs) \ ' (\text{fv } e)$
and *fvs-compEs1*: $\text{fvs } es \subseteq \text{set } Vs \implies \text{fvs } (\text{compEs1 } Vs \ es) = (\text{index } Vs) \ ' (\text{fvs } es)$
<proof>

lemma *fixes* $e :: 'addr \ \text{expr}$ **and** $es :: 'addr \ \text{expr list}$
shows *syncvars-compE1*: $\text{fv } e \subseteq \text{set } Vs \implies \text{syncvars } (\text{compE1 } Vs \ e)$
and *syncvarss-compEs1*: $\text{fvs } es \subseteq \text{set } Vs \implies \text{syncvarss } (\text{compEs1 } Vs \ es)$
<proof>

lemma (*in heap-base*) *synthesized-call-compP* [simp]:
 $\text{synthesized-call } (\text{compP } f \ P) \ h \ aMvs = \text{synthesized-call } P \ h \ aMvs$
<proof>

primrec *fin1* :: $'addr \ \text{expr} \Rightarrow 'addr \ \text{expr1}$
where
 $\text{fin1 } (\text{Val } v) = \text{Val } v$
 $\text{fin1 } (\text{throw } e) = \text{throw } (\text{fin1 } e)$

lemma *comp-final*: $final\ e \implies compE1\ Vs\ e = fin1\ e$
 ⟨*proof*⟩

lemma *fixes* $e :: 'addr\ expr$ **and** $es :: 'addr\ expr\ list$
shows [*simp*]: $max\ vars\ (compE1\ Vs\ e) = max\ vars\ e$
and $max\ varss\ (compEs1\ Vs\ es) = max\ varss\ es$
 ⟨*proof*⟩

Compiling programs:

definition *compP1* :: $'addr\ J\ prog \Rightarrow 'addr\ J1\ prog$

where

$compP1 \equiv compP\ (\lambda C\ M\ Ts\ T\ (pns, body). compE1\ (this\#pns)\ body)$

declare *compP1-def*[*simp*]

end

7.22 The bisimulation relation between source and intermediate language

theory *J0J1Bisim* **imports**

J1
J1WellForm
Compiler1
 ../J/JWellForm
J0

begin

7.22.1 Correctness of program compilation

primrec *unmod* :: $'addr\ expr1 \Rightarrow nat \Rightarrow bool$

and *unmods* :: $'addr\ expr1\ list \Rightarrow nat \Rightarrow bool$

where

$unmod\ (new\ C)\ i = True$
 $unmod\ (newA\ T[e])\ i = unmod\ e\ i$
 $unmod\ (Cast\ C\ e)\ i = unmod\ e\ i$
 $unmod\ (e\ instanceof\ T)\ i = unmod\ e\ i$
 $unmod\ (Val\ v)\ i = True$
 $unmod\ (e1\ \ll bop \gg\ e2)\ i = (unmod\ e1\ i \wedge unmod\ e2\ i)$
 $unmod\ (Var\ i)\ j = True$
 $unmod\ (i:=e)\ j = (i \neq j \wedge unmod\ e\ j)$
 $unmod\ (a[i])\ j = (unmod\ a\ j \wedge unmod\ i\ j)$
 $unmod\ (a[i]:=e)\ j = (unmod\ a\ j \wedge unmod\ i\ j \wedge unmod\ e\ j)$
 $unmod\ (a.length)\ j = unmod\ a\ j$
 $unmod\ (e.F\{D\})\ i = unmod\ e\ i$
 $unmod\ (e1.F\{D\}:=e2)\ i = (unmod\ e1\ i \wedge unmod\ e2\ i)$
 $unmod\ (e1.compareAndSwap(D.F, e2, e3))\ i = (unmod\ e1\ i \wedge unmod\ e2\ i \wedge unmod\ e3\ i)$
 $unmod\ (e.M(es))\ i = (unmod\ e\ i \wedge unmods\ es\ i)$
 $unmod\ \{j:T=vo; e\}\ i = ((i = j \longrightarrow vo = None) \wedge unmod\ e\ i)$
 $unmod\ (sync_V\ (o')\ e)\ i = (unmod\ o'\ i \wedge unmod\ e\ i \wedge i \neq V)$
 $unmod\ (insync_V\ (a)\ e)\ i = unmod\ e\ i$
 $unmod\ (e1;;e2)\ i = (unmod\ e1\ i \wedge unmod\ e2\ i)$
 $unmod\ (if\ (e)\ e1\ else\ e2)\ i = (unmod\ e\ i \wedge unmod\ e1\ i \wedge unmod\ e2\ i)$

| $\text{unmod } (\text{while } (e) \ c) \ i = (\text{unmod } e \ i \wedge \text{unmod } c \ i)$
| $\text{unmod } (\text{throw } e) \ i = \text{unmod } e \ i$
| $\text{unmod } (\text{try } e_1 \ \text{catch}(C \ i) \ e_2) \ j = (\text{unmod } e_1 \ j \wedge (\text{if } i=j \ \text{then } \text{False} \ \text{else } \text{unmod } e_2 \ j))$

| $\text{unmods } ([]) \ i = \text{True}$
| $\text{unmods } (e\#es) \ i = (\text{unmod } e \ i \wedge \text{unmods } es \ i)$

lemma *unmods-map-Val* [simp]: $\text{unmods } (\text{map } \text{Val } vs) \ V$
⟨proof⟩

lemma *fixes* $e :: 'addr \ \text{expr}$ **and** $es :: 'addr \ \text{expr list}$
shows *hidden-unmod*: $\text{hidden } Vs \ i \implies \text{unmod } (\text{compE1 } Vs \ e) \ i$
and *hidden-unmods*: $\text{hidden } Vs \ i \implies \text{unmods } (\text{compEs1 } Vs \ es) \ i$
⟨proof⟩

lemma *unmod-extRet2J* [simp]: $\text{unmod } e \ i \implies \text{unmod } (\text{extRet2J } e \ va) \ i$
⟨proof⟩

lemma *max-dest*: $(n :: \text{nat}) + \max \ a \ b \leq c \implies n + a \leq c \wedge n + b \leq c$
⟨proof⟩

declare *max-dest* [dest!]

lemma *fixes* $e :: 'addr \ \text{expr}$ **and** $es :: 'addr \ \text{expr list}$
shows *fv-unmod-compE1*: $\llbracket i < \text{length } Vs; Vs \ ! \ i \notin \text{fv } e \rrbracket \implies \text{unmod } (\text{compE1 } Vs \ e) \ i$
and *fvs-unmods-compEs1*: $\llbracket i < \text{length } Vs; Vs \ ! \ i \notin \text{fvs } es \rrbracket \implies \text{unmods } (\text{compEs1 } Vs \ es) \ i$
⟨proof⟩

lemma *hidden-lengthD*: $\text{hidden } Vs \ i \implies i < \text{length } Vs$
⟨proof⟩

lemma *fixes* $e :: 'addr \ \text{expr1}$ **and** $es :: 'addr \ \text{expr1 list}$
shows *fv-B-unmod*: $\llbracket V \notin \text{fv } e; \mathcal{B} \ e \ n; V < n \rrbracket \implies \text{unmod } e \ V$
and *fvs-Bs-unmods*: $\llbracket V \notin \text{fvs } es; \mathcal{B}s \ es \ n; V < n \rrbracket \implies \text{unmods } es \ V$
⟨proof⟩

lemma *assumes* *fin*: *final* e'
shows *unmod-inline-call*: $\text{unmod } (\text{inline-call } e' \ e) \ V \longleftrightarrow \text{unmod } e \ V$
and *unmods-inline-calls*: $\text{unmods } (\text{inline-calls } e' \ es) \ V \longleftrightarrow \text{unmods } es \ V$
⟨proof⟩

7.22.2 The delay bisimulation relation

Delay bisimulation for expressions

inductive *bisim* :: $vname \ list \Rightarrow 'addr \ \text{expr} \Rightarrow 'addr \ \text{expr1} \Rightarrow 'addr \ \text{val list} \Rightarrow \text{bool}$
and *bisims* :: $vname \ list \Rightarrow 'addr \ \text{expr list} \Rightarrow 'addr \ \text{expr1 list} \Rightarrow 'addr \ \text{val list} \Rightarrow \text{bool}$
where

bisimNew: $\text{bisim } Vs \ (\text{new } C) \ (\text{new } C) \ xs$
| *bisimNewArray*: $\text{bisim } Vs \ e \ e' \ xs \implies \text{bisim } Vs \ (\text{newA } T[e]) \ (\text{newA } T[e']) \ xs$
| *bisimCast*: $\text{bisim } Vs \ e \ e' \ xs \implies \text{bisim } Vs \ (\text{Cast } T \ e) \ (\text{Cast } T \ e') \ xs$
| *bisimInstanceOf*: $\text{bisim } Vs \ e \ e' \ xs \implies \text{bisim } Vs \ (e \ \text{instanceof } T) \ (e' \ \text{instanceof } T) \ xs$
| *bisimVal*: $\text{bisim } Vs \ (\text{Val } v) \ (\text{Val } v) \ xs$
| *bisimBinOp1*:
 $\llbracket \text{bisim } Vs \ e \ e' \ xs; \neg \text{is-val } e; \neg \text{contains-insync } e'' \rrbracket \implies \text{bisim } Vs \ (e \ \llbracket \text{bop} \rrbracket \ e'') \ (e' \ \llbracket \text{bop} \rrbracket \ \text{compE1})$

$Vs\ e''\ xs$
 $| \text{bisimBinOp2: } \text{bisim } Vs\ e\ e'\ xs \implies \text{bisim } Vs\ (\text{Val } v \ll \text{bop} \gg e)\ (\text{Val } v \ll \text{bop} \gg e')\ xs$
 $| \text{bisimVar: } \text{bisim } Vs\ (\text{Var } V)\ (\text{Var } (\text{index } Vs\ V))\ xs$
 $| \text{bisimLAss: } \text{bisim } Vs\ e\ e'\ xs \implies \text{bisim } Vs\ (V := e)\ (\text{index } Vs\ V := e')\ xs$
 $| \text{bisimAAcc1: } \llbracket \text{bisim } Vs\ a\ a'\ xs; \neg \text{is-val } a; \neg \text{contains-insync } i \rrbracket \implies \text{bisim } Vs\ (a[i])\ (a'[\text{compE1 } Vs\ i])\ xs$
 $| \text{bisimAAcc2: } \text{bisim } Vs\ i\ i'\ xs \implies \text{bisim } Vs\ (\text{Val } v[i])\ (\text{Val } v[i'])\ xs$
 $| \text{bisimAAss1:}$
 $\llbracket \text{bisim } Vs\ a\ a'\ xs; \neg \text{is-val } a; \neg \text{contains-insync } i; \neg \text{contains-insync } e \rrbracket$
 $\implies \text{bisim } Vs\ (a[i] := e)\ (a'[\text{compE1 } Vs\ i] := \text{compE1 } Vs\ e)\ xs$
 $| \text{bisimAAss2: } \llbracket \text{bisim } Vs\ i\ i'\ xs; \neg \text{is-val } i; \neg \text{contains-insync } e \rrbracket \implies \text{bisim } Vs\ (\text{Val } v[i] := e)\ (\text{Val } v[i'] := \text{compE1 } Vs\ e)\ xs$
 $| \text{bisimAAss3: } \text{bisim } Vs\ e\ e'\ xs \implies \text{bisim } Vs\ (\text{Val } v[\text{Val } i] := e)\ (\text{Val } v[\text{Val } i'] := e')\ xs$
 $| \text{bisimALength: } \text{bisim } Vs\ a\ a'\ xs \implies \text{bisim } Vs\ (a \cdot \text{length})\ (a' \cdot \text{length})\ xs$
 $| \text{bisimFAcc: } \text{bisim } Vs\ e\ e'\ xs \implies \text{bisim } Vs\ (e \cdot F\{D\})\ (e' \cdot F\{D\})\ xs$
 $| \text{bisimFAss1: } \llbracket \text{bisim } Vs\ e\ e'\ xs; \neg \text{is-val } e; \neg \text{contains-insync } e'' \rrbracket \implies \text{bisim } Vs\ (e \cdot F\{D\} := e'')\ (e' \cdot F\{D\} := \text{compE1 } Vs\ e'')\ xs$
 $| \text{bisimFAss2: } \text{bisim } Vs\ e\ e'\ xs \implies \text{bisim } Vs\ (\text{Val } v \cdot F\{D\} := e)\ (\text{Val } v \cdot F\{D\} := e')\ xs$
 $| \text{bisimCAS1: } \llbracket \text{bisim } Vs\ e\ e'\ xs; \neg \text{is-val } e; \neg \text{contains-insync } e2; \neg \text{contains-insync } e3 \rrbracket$
 $\implies \text{bisim } Vs\ (e \cdot \text{compareAndSwap}(D \cdot F, e2, e3))\ (e' \cdot \text{compareAndSwap}(D \cdot F, \text{compE1 } Vs\ e2, \text{compE1 } Vs\ e3))\ xs$
 $| \text{bisimCAS2: } \llbracket \text{bisim } Vs\ e\ e'\ xs; \neg \text{is-val } e; \neg \text{contains-insync } e3 \rrbracket$
 $\implies \text{bisim } Vs\ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e, e3))\ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e', \text{compE1 } Vs\ e3))\ xs$
 $| \text{bisimCAS3: } \text{bisim } Vs\ e\ e'\ xs \implies \text{bisim } Vs\ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e))\ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e'))\ xs$
 $| \text{bisimCallObj: } \llbracket \text{bisim } Vs\ e\ e'\ xs; \neg \text{is-val } e; \neg \text{contains-insyncs } es \rrbracket \implies \text{bisim } Vs\ (e \cdot M(es))\ (e' \cdot M(\text{compEs1 } Vs\ es))\ xs$
 $| \text{bisimCallParams: } \text{bisims } Vs\ es\ es'\ xs \implies \text{bisim } Vs\ (\text{Val } v \cdot M(es))\ (\text{Val } v \cdot M(es'))\ xs$
 $| \text{bisimBlockNone: } \text{bisim } (Vs@[V])\ e\ e'\ xs \implies \text{bisim } Vs\ \{V:T=None; e\} \{(length\ Vs):T=None; e'\}\ xs$
 $| \text{bisimBlockSome: } \llbracket \text{bisim } (Vs@[V])\ e\ e'\ (xs[length\ Vs := v]) \rrbracket \implies \text{bisim } Vs\ \{V:T=[v]; e\} \{(length\ Vs):T=[v]; e'\}\ xs$
 $| \text{bisimBlockSomeNone: } \llbracket \text{bisim } (Vs@[V])\ e\ e'\ xs; xs ! (length\ Vs) = v \rrbracket \implies \text{bisim } Vs\ \{V:T=[v]; e\} \{(length\ Vs):T=None; e'\}\ xs$
 $| \text{bisimSynchronized:}$
 $\llbracket \text{bisim } Vs\ o'\ o''\ xs; \neg \text{contains-insync } e \rrbracket$
 $\implies \text{bisim } Vs\ (\text{sync}(o')\ e)\ (\text{sync}_{length\ Vs}(o'')\ (\text{compE1 } (Vs@[fresh-var\ Vs])\ e))\ xs$
 $| \text{bisimInSynchronized:}$
 $\llbracket \text{bisim } (Vs@[fresh-var\ Vs])\ e\ e'\ xs; xs ! length\ Vs = \text{Addr } a \rrbracket \implies \text{bisim } Vs\ (\text{insync}(a)\ e)\ (\text{insync}_{length\ Vs}(a)\ e')\ xs$
 $| \text{bisimSeq: } \llbracket \text{bisim } Vs\ e\ e'\ xs; \neg \text{contains-insync } e'' \rrbracket \implies \text{bisim } Vs\ (e;;e'')\ (e';;\text{compE1 } Vs\ e'')\ xs$
 $| \text{bisimCond:}$
 $\llbracket \text{bisim } Vs\ e\ e'\ xs; \neg \text{contains-insync } e1; \neg \text{contains-insync } e2 \rrbracket$
 $\implies \text{bisim } Vs\ (\text{if } (e)\ e1\ \text{else } e2)\ (\text{if } (e')\ (\text{compE1 } Vs\ e1)\ \text{else } (\text{compE1 } Vs\ e2))\ xs$
 $| \text{bisimWhile:}$
 $\llbracket \neg \text{contains-insync } b; \neg \text{contains-insync } e \rrbracket \implies \text{bisim } Vs\ (\text{while } (b)\ e)\ (\text{while } (\text{compE1 } Vs\ b)\ (\text{compE1 } Vs\ e))\ xs$
 $| \text{bisimThrow: } \text{bisim } Vs\ e\ e'\ xs \implies \text{bisim } Vs\ (\text{throw } e)\ (\text{throw } e')\ xs$
 $| \text{bisimTryCatch:}$
 $\llbracket \text{bisim } Vs\ e\ e'\ xs; \neg \text{contains-insync } e'' \rrbracket$
 $\implies \text{bisim } Vs\ (\text{try } e\ \text{catch}(C\ V)\ e'')\ (\text{try } e'\ \text{catch}(C\ (length\ Vs))\ \text{compE1 } (Vs@[V])\ e'')\ xs$
 $| \text{bisimsNil: } \text{bisims } Vs\ []\ []\ xs$
 $| \text{bisimsCons1: } \llbracket \text{bisim } Vs\ e\ e'\ xs; \neg \text{is-val } e; \neg \text{contains-insyncs } es \rrbracket \implies \text{bisims } Vs\ (e \# es)\ (e' \#$

compEs1 $Vs\ es\ xs$
| *bisimsCons2*: *bisims* $Vs\ es\ es'\ xs \implies bisims\ Vs\ (Val\ v\ \# \ es)\ (Val\ v\ \# \ es')\ xs$

declare *bisimNew* [*iff*]
declare *bisimVal* [*iff*]
declare *bisimVar* [*iff*]
declare *bisimWhile* [*iff*]
declare *bisimsNil* [*iff*]

declare *bisim-bisims.intros* [*intro!*]
declare *bisimsCons1* [*rule del, intro*] *bisimsCons2* [*rule del, intro*]
bisimBinOp1 [*rule del, intro*] *bisimAAcc1* [*rule del, intro*]
bisimAAss1 [*rule del, intro*] *bisimAAss2* [*rule del, intro*]
bisimFAss1 [*rule del, intro*]
bisimCAS1 [*rule del, intro*] *bisimCAS2* [*rule del, intro*]
bisimCallObj [*rule del, intro*]

inductive-cases *bisim-safe-cases* [*elim!*]:

bisim $Vs\ (new\ C)\ e'\ xs$
bisim $Vs\ (newA\ T[e])\ e'\ xs$
bisim $Vs\ (Cast\ T\ e)\ e'\ xs$
bisim $Vs\ (e\ instanceof\ T)\ e'\ xs$
bisim $Vs\ (Val\ v)\ e'\ xs$
bisim $Vs\ (Var\ V)\ e'\ xs$
bisim $Vs\ (V:=e)\ e'\ xs$
bisim $Vs\ (Val\ v[i])\ e'\ xs$
bisim $Vs\ (Val\ v[Val\ v'] := e)\ e'\ xs$
bisim $Vs\ (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e))\ e'\ xs$
bisim $Vs\ (a \cdot length)\ e'\ xs$
bisim $Vs\ (e \cdot F\{D\})\ e'\ xs$
bisim $Vs\ (sync(o')\ e)\ e'\ xs$
bisim $Vs\ (insync(a)\ e)\ e'\ xs$
bisim $Vs\ (e;;e')\ e''\ xs$
bisim $Vs\ (if\ (b)\ e1\ else\ e2)\ e'\ xs$
bisim $Vs\ (while\ (b)\ e)\ e'\ xs$
bisim $Vs\ (throw\ e)\ e'\ xs$
bisim $Vs\ (try\ e\ catch(C\ V)\ e')\ e''\ xs$
bisim $Vs\ e'\ (new\ C)\ xs$
bisim $Vs\ e'\ (newA\ T[e])\ xs$
bisim $Vs\ e'\ (Cast\ T\ e)\ xs$
bisim $Vs\ e'\ (e\ instanceof\ T)\ xs$
bisim $Vs\ e'\ (Val\ v)\ xs$
bisim $Vs\ e'\ (Var\ V)\ xs$
bisim $Vs\ e'\ (V:=e)\ xs$
bisim $Vs\ e'\ (Val\ v[i])\ xs$
bisim $Vs\ e'\ (Val\ v[Val\ v'] := e)\ xs$
bisim $Vs\ e'\ (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e))\ xs$
bisim $Vs\ e'\ (a \cdot length)\ xs$
bisim $Vs\ e'\ (e \cdot F\{D\})\ xs$
bisim $Vs\ e'\ (sync_V(o')\ e)\ xs$
bisim $Vs\ e'\ (insync_V(a)\ e)\ xs$
bisim $Vs\ e''\ (e;;e')\ xs$
bisim $Vs\ e'\ (if\ (b)\ e1\ else\ e2)\ xs$
bisim $Vs\ e'\ (while\ (b)\ e)\ xs$

bisim $Vs\ e'\ (throw\ e)\ xs$
bisim $Vs\ e''\ (try\ e\ catch\ (C\ V)\ e')\ xs$

inductive-cases *bisim-cases* [elim]:

bisim $Vs\ (e1\ \langle\langle bop \rangle\rangle\ e2)\ e'\ xs$
bisim $Vs\ (a[i])\ e'\ xs$
bisim $Vs\ (a[i]:=e)\ e'\ xs$
bisim $Vs\ (e \cdot F\{D\}:=e')\ e''\ xs$
bisim $Vs\ (e \cdot compareAndSwap(D \cdot F, e', e''))\ e''' \ xs$
bisim $Vs\ (e \cdot M(es))\ e'\ xs$
bisim $Vs\ \{V:T=vo; e\}\ e'\ xs$
bisim $Vs\ e'\ (e1\ \langle\langle bop \rangle\rangle\ e2)\ xs$
bisim $Vs\ e'\ (a[i])\ xs$
bisim $Vs\ e'\ (a[i]:=e)\ xs$
bisim $Vs\ e''\ (e \cdot F\{D\}:=e')\ xs$
bisim $Vs\ e'''\ (e \cdot compareAndSwap(D \cdot F, e', e''))\ xs$
bisim $Vs\ e'\ (e \cdot M(es))\ xs$
bisim $Vs\ e'\ \{V:T=vo; e\}\ xs$

inductive-cases *bisims-safe-cases* [elim!]:

bisims $Vs\ []\ es\ xs$
bisims $Vs\ es\ []\ xs$

inductive-cases *bisims-cases* [elim]:

bisims $Vs\ (e\ \# \ es)\ es'\ xs$
bisims $Vs\ es'\ (e\ \# \ es)\ xs$

Delay bisimulation for call stacks

inductive *bisim01* :: 'addr expr \Rightarrow 'addr expr1 \times 'addr locals1 \Rightarrow bool

where

$\llbracket bisim\ []\ e\ e'\ xs; fv\ e = \{\}; \mathcal{D}\ e\ [\{\}]; max\text{-vars}\ e' \leq length\ xs; call\ e = [aMvs]; call1\ e' = [aMvs] \rrbracket$
 $\implies bisim01\ e\ (e',\ xs)$

inductive *bisim-list* :: 'addr expr list \Rightarrow ('addr expr1 \times 'addr locals1) list \Rightarrow bool

where

bisim-listNil: *bisim-list* $[]\ []$
| *bisim-listCons*:
 $\llbracket bisim\text{-list}\ es\ exs'; bisim\ []\ e\ e'\ xs;$
 $fv\ e = \{\}; \mathcal{D}\ e\ [\{\}];$
 $max\text{-vars}\ e' \leq length\ xs;$
 $call\ e = [aMvs]; call1\ e' = [aMvs] \rrbracket$
 $\implies bisim\text{-list}\ (e\ \# \ es)\ ((e',\ xs)\ \# \ exs')$

inductive-cases *bisim-list-cases* [elim!]:

bisim-list $[]\ exs'$
bisim-list $(ex\ \# \ exs)\ exs'$
bisim-list $exs\ (ex'\ \# \ exs')$

fun *bisim-list1* ::

'addr expr \times 'addr expr list \Rightarrow ('addr expr1 \times 'addr locals1) \times ('addr expr1 \times 'addr locals1) list \Rightarrow bool

where

bisim-list1 $(e, es)\ ((e1, xs1), exs1) \longleftrightarrow$
bisim-list $es\ exs1 \wedge bisim\ []\ e\ e1\ xs1 \wedge fv\ e = \{\} \wedge \mathcal{D}\ e\ [\{\}] \wedge max\text{-vars}\ e1 \leq length\ xs1$

definition *bisim-red0-Red1* ::

$((\text{'addr expr} \times \text{'addr expr list}) \times \text{'heap})$
 $\Rightarrow (((\text{'addr expr1} \times \text{'addr locals1}) \times (\text{'addr expr1} \times \text{'addr locals1}) \text{ list}) \times \text{'heap}) \Rightarrow \text{bool}$
where *bisim-red0-Red1* $\equiv (\lambda(es, h) (exs, h'). \text{bisim-list1 } es \ exs \wedge h = h')$

abbreviation *ta-bisim01* ::

$(\text{'addr}, \text{'thread-id}, \text{'heap}) \text{J0-thread-action} \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'heap}) \text{J1-thread-action} \Rightarrow \text{bool}$
where
ta-bisim01 $\equiv \text{ta-bisim } (\lambda t. \text{bisim-red0-Red1})$

definition *bisim-wait01* ::

$(\text{'addr expr} \times \text{'addr expr list}) \Rightarrow (\text{'addr expr1} \times \text{'addr locals1}) \times (\text{'addr expr1} \times \text{'addr locals1}) \text{ list}$
 $\Rightarrow \text{bool}$
where *bisim-wait01* $\equiv \lambda(e0, es0) ((e1, xs1), exs1). \text{call } e0 \neq \text{None} \wedge \text{call1 } e1 \neq \text{None}$

lemma *bisim-list1I*[*intro?*]:

$\llbracket \text{bisim-list } es \ exs1; \text{bisim } [] \ e \ e1 \ xs1; \text{fv } e = \{\};$
 $\mathcal{D} \ e \ [\{\}]; \text{max-vars } e1 \leq \text{length } xs1 \rrbracket$
 $\implies \text{bisim-list1 } (e, es) ((e1, xs1), exs1)$
 $\langle \text{proof} \rangle$

lemma *bisim-list1E*[*elim?*]:

assumes *bisim-list1* $(e, es) ((e1, xs1), exs1)$
obtains *bisim-list* $es \ exs1 \text{bisim } [] \ e \ e1 \ xs1 \text{fv } e = \{\} \mathcal{D} \ e \ [\{\}] \text{max-vars } e1 \leq \text{length } xs1$
 $\langle \text{proof} \rangle$

lemma *bisim-list1-elim*:

assumes *bisim-list1* $es' \ exs$
obtains $e \ es \ e1 \ xs1 \ exs1$
where $es' = (e, es) \ exs = ((e1, xs1), exs1)$
and *bisim-list* $es \ exs1 \text{bisim } [] \ e \ e1 \ xs1 \text{fv } e = \{\} \mathcal{D} \ e \ [\{\}] \text{max-vars } e1 \leq \text{length } xs1$
 $\langle \text{proof} \rangle$

declare *bisim-list1.simps* [*simp del*]

lemma *bisims-map-Val-conv* [*simp*]: *bisims* $Vs \ (\text{map Val } vs) \ es \ xs = (es = \text{map Val } vs)$

$\langle \text{proof} \rangle$

declare *compEs1-conv-map* [*simp del*]

lemma *bisim-contains-insync*: *bisim* $Vs \ e \ e' \ xs \implies \text{contains-insync } e = \text{contains-insync } e'$

and *bisims-contains-insyncs*: *bisims* $Vs \ es \ es' \ xs \implies \text{contains-insyncs } es = \text{contains-insyncs } es'$
 $\langle \text{proof} \rangle$

lemma *bisims-map-Val-Throw*:

bisims $Vs \ (\text{map Val } vs \ @ \ \text{Throw } a \ \# \ es) \ es' \ xs \longleftrightarrow es' = \text{map Val } vs \ @ \ \text{Throw } a \ \# \ \text{compEs1 } Vs \ es$
 $\wedge \neg \text{contains-insyncs } es$
 $\langle \text{proof} \rangle$

lemma *compE1-bisim* [*intro*]: $\llbracket \text{fv } e \subseteq \text{set } Vs; \neg \text{contains-insync } e \rrbracket \implies \text{bisim } Vs \ e \ (\text{compE1 } Vs \ e) \ xs$

and *compEs1-bisims* [*intro*]: $\llbracket \text{fvs } es \subseteq \text{set } Vs; \neg \text{contains-insyncs } es \rrbracket \implies \text{bisims } Vs \ es \ (\text{compEs1 } Vs \ es) \ xs$

<proof>

lemma *bisim-hidden-unmod*: $\llbracket \text{bisim } Vs \ e \ e' \ xs; \text{hidden } Vs \ i \rrbracket \Longrightarrow \text{unmod } e' \ i$
and *bisims-hidden-unmods*: $\llbracket \text{bisims } Vs \ es \ es' \ xs; \text{hidden } Vs \ i \rrbracket \Longrightarrow \text{unmods } es' \ i$
<proof>

lemma *bisim-fv-unmod*: $\llbracket \text{bisim } Vs \ e \ e' \ xs; \ i < \text{length } Vs; \ Vs \ ! \ i \notin \text{fv } e \rrbracket \Longrightarrow \text{unmod } e' \ i$
and *bisims-fvs-unmods*: $\llbracket \text{bisims } Vs \ es \ es' \ xs; \ i < \text{length } Vs; \ Vs \ ! \ i \notin \text{fvs } es \rrbracket \Longrightarrow \text{unmods } es' \ i$
<proof>

lemma *bisim-extRet2J* [intro!]: $\text{bisim } Vs \ e \ e' \ xs \Longrightarrow \text{bisim } Vs \ (\text{extRet2J } e \ va) \ (\text{extRet2J1 } e' \ va) \ xs$
<proof>

lemma *bisims-map-Val-conv2* [simp]: $\text{bisims } Vs \ es \ (\text{map } \text{Val } vs) \ xs = (es = \text{map } \text{Val } vs)$
<proof>

lemma *bisims-map-Val-Throw2*:
 $\text{bisims } Vs \ es' \ (\text{map } \text{Val } vs \ @ \ \text{Throw } a \ \# \ es) \ xs \longleftrightarrow$
 $(\exists es''. es' = \text{map } \text{Val } vs \ @ \ \text{Throw } a \ \# \ es'' \wedge es = \text{compEs1 } Vs \ es'' \wedge \neg \text{contains-insyncs } es'')$
<proof>

lemma *hidden-bisim-unmod*: $\llbracket \text{bisim } Vs \ e \ e' \ xs; \text{hidden } Vs \ i \rrbracket \Longrightarrow \text{unmod } e' \ i$
and *hidden-bisims-unmods*: $\llbracket \text{bisims } Vs \ es \ es' \ xs; \text{hidden } Vs \ i \rrbracket \Longrightarrow \text{unmods } es' \ i$
<proof>

lemma *bisim-list-list-all2-conv*:
 $\text{bisim-list } es \ exs' \longleftrightarrow \text{list-all2 } \text{bisim01 } es \ exs'$
<proof>

lemma *bisim-list-extTA2J0-extTA2J1*:
assumes *wf*: $wf\text{-}J\text{-prog } P$
and *sees*: $P \vdash C \text{ sees } M:[] \rightarrow T = \llbracket \text{meth} \rrbracket \text{ in } D$
shows *bisim-list1* $(\text{extNTA2J0 } P \ (C, M, a)) \ (\text{extNTA2J1 } (\text{compP1 } P) \ (C, M, a))$
<proof>

lemma *bisim-max-vars*: $\text{bisim } Vs \ e \ e' \ xs \Longrightarrow \text{max-vars } e = \text{max-vars } e'$
and *bisims-max-varss*: $\text{bisims } Vs \ es \ es' \ xs \Longrightarrow \text{max-varss } es = \text{max-varss } es'$
<proof>

lemma *bisim-call*: $\text{bisim } Vs \ e \ e' \ xs \Longrightarrow \text{call } e = \text{call } e'$
and *bisims-calls*: $\text{bisims } Vs \ es \ es' \ xs \Longrightarrow \text{calls } es = \text{calls } es'$
<proof>

lemma *bisim-call-None-call1*: $\llbracket \text{bisim } Vs \ e \ e' \ xs; \text{call } e = \text{None} \rrbracket \Longrightarrow \text{call1 } e' = \text{None}$
and *bisims-calls-None-calls1*: $\llbracket \text{bisims } Vs \ es \ es' \ xs; \text{calls } es = \text{None} \rrbracket \Longrightarrow \text{calls1 } es' = \text{None}$
<proof>

lemma *bisim-call1-Some-call*:
 $\llbracket \text{bisim } Vs \ e \ e' \ xs; \text{call1 } e' = \llbracket aMvs \rrbracket \rrbracket \Longrightarrow \text{call } e = \llbracket aMvs \rrbracket$

and *bisims-calls1-Some-calls*:
 $\llbracket \text{bisims } Vs \ es \ es' \ xs; \text{calls1 } es' = \llbracket aMvs \rrbracket \rrbracket \Longrightarrow \text{calls } es = \llbracket aMvs \rrbracket$
<proof>

lemma *blocks-bisim*:

assumes *bisim*: $bisim (Vs @ pns) e e' xs$
and *length*: $length\ vs = length\ pns\ length\ Ts = length\ pns$
and *xs*: $\forall i. i < length\ vs \longrightarrow xs\ !\ (i + length\ Vs) = vs\ !\ i$
shows $bisim\ Vs\ (blocks\ pns\ Ts\ vs\ e)\ (blocks1\ (length\ Vs)\ Ts\ e')\ xs$
 $\langle proof \rangle$

lemma *fixes* $e :: ('a, 'b, 'addr)\ exp$ **and** $es :: ('a, 'b, 'addr)\ exp\ list$

shows *inline-call-max-vars*: $call\ e = [aMvs] \Longrightarrow max\ vars\ (inline\ call\ e'\ e) \leq max\ vars\ e + max\ vars\ e'$
and *inline-calls-max-varss*: $calls\ es = [aMvs] \Longrightarrow max\ varss\ (inline\ calls\ e'\ es) \leq max\ varss\ es + max\ vars\ e'$
 $\langle proof \rangle$

lemma *assumes* $final\ E\ bisim\ VS\ E\ E'\ xs$

shows *inline-call-compE1*: $call\ e = [aMvs] \Longrightarrow inline\ call\ E'\ (compE1\ Vs\ e) = compE1\ Vs\ (inline\ call\ E\ e)$
and *inline-calls-compEs1*: $calls\ es = [aMvs] \Longrightarrow inline\ calls\ E'\ (compEs1\ Vs\ es) = compEs1\ Vs\ (inline\ calls\ E\ es)$
 $\langle proof \rangle$

lemma *assumes* $bisim: bisim\ VS\ E\ E'\ XS$

and *final*: $final\ E$
shows *bisim-inline-call*:
 $\llbracket bisim\ Vs\ e\ e'\ xs; call\ e = [aMvs]; fv\ e \subseteq set\ Vs \rrbracket$
 $\Longrightarrow bisim\ Vs\ (inline\ call\ E\ e)\ (inline\ call\ E'\ e')\ xs$

and *bisims-inline-calls*:
 $\llbracket bisims\ Vs\ es\ es'\ xs; calls\ es = [aMvs]; fvs\ es \subseteq set\ Vs \rrbracket$
 $\Longrightarrow bisims\ Vs\ (inline\ calls\ E\ es)\ (inline\ calls\ E'\ es')\ xs$

$\langle proof \rangle$

declare *hyperUn-ac* [*simp del*]

lemma *sqInt-lem3*: $\llbracket A \sqsubseteq A'; B \sqsubseteq B' \rrbracket \Longrightarrow A \sqcap B \sqsubseteq A' \sqcap B'$
 $\langle proof \rangle$

lemma *sqUn-lem3*: $\llbracket A \sqsubseteq A'; B \sqsubseteq B' \rrbracket \Longrightarrow A \sqcup B \sqsubseteq A' \sqcup B'$
 $\langle proof \rangle$

lemma *A-inline-call*: $call\ e = [aMvs] \Longrightarrow \mathcal{A}\ e \sqsubseteq \mathcal{A}\ (inline\ call\ e'\ e)$
and *As-inline-calls*: $calls\ es = [aMvs] \Longrightarrow \mathcal{A}s\ es \sqsubseteq \mathcal{A}s\ (inline\ calls\ e'\ es)$
 $\langle proof \rangle$

lemma *assumes* $final\ e'$

shows *defass-inline-call*: $\llbracket call\ e = [aMvs]; \mathcal{D}\ e\ A \rrbracket \Longrightarrow \mathcal{D}\ (inline\ call\ e'\ e)\ A$
and *defass-inline-calls*: $\llbracket calls\ es = [aMvs]; \mathcal{D}s\ es\ A \rrbracket \Longrightarrow \mathcal{D}s\ (inline\ calls\ e'\ es)\ A$
 $\langle proof \rangle$

lemma *bisim-B*: $bisim\ Vs\ e\ E\ xs \Longrightarrow \mathcal{B}\ E\ (length\ Vs)$

and *bisims-Bs*: $bisims\ Vs\ es\ Es\ xs \Longrightarrow \mathcal{B}s\ Es\ (length\ Vs)$
 $\langle proof \rangle$

lemma *bisim-expr-locks-eq*: $\text{bisim } Vs e e' xs \implies \text{expr-locks } e = \text{expr-locks } e'$
and *bisims-expr-lockss-eq*: $\text{bisims } Vs es es' xs \implies \text{expr-lockss } es = \text{expr-lockss } es'$
 ⟨proof⟩

lemma *bisim-list-expr-lockss-eq*: $\text{bisim-list } es es' \implies \text{expr-lockss } es = \text{expr-lockss } (\text{map fst } es')$
 ⟨proof⟩

context *J1-heap-base* **begin**

lemma [*simp*]:
fixes $e :: ('a, 'b, 'addr) \text{ exp}$ **and** $es :: ('a, 'b, 'addr) \text{ exp list}$
shows $\tau \text{move1-comp}P: \tau \text{move1 } (\text{comp}P f P) h e = \tau \text{move1 } P h e$
and $\tau \text{moves1-comp}P: \tau \text{moves1 } (\text{comp}P f P) h es = \tau \text{moves1 } P h es$
 ⟨proof⟩

lemma $\tau \text{Move1-comp}P$ [*simp*]: $\tau \text{Move1 } (\text{comp}P f P) = \tau \text{Move1 } P$
 ⟨proof⟩

lemma *red1-preserves-unmod*:
 $\llbracket \text{uf}, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{unmod } e i \rrbracket \implies (\text{lcl } s') ! i = (\text{lcl } s) ! i$
and *reds1-preserves-unmod*:
 $\llbracket \text{uf}, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{unmods } es i \rrbracket \implies (\text{lcl } s') ! i = (\text{lcl } s) ! i$
 ⟨proof⟩

lemma *red1-unmod-preserved*:
 $\llbracket \text{uf}, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{unmod } e i \rrbracket \implies \text{unmod } e' i$
and *reds1-unmods-preserved*:
 $\llbracket \text{uf}, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{unmods } es i \rrbracket \implies \text{unmods } es' i$
 ⟨proof⟩

lemma $\tau \text{red1t-unmod-preserved}$:
 $\llbracket \tau \text{red1gt } \text{uf } P t h (e, xs) (e', xs'); \text{unmod } e i \rrbracket \implies \text{unmod } e' i$
 ⟨proof⟩

lemma $\tau \text{red1r-unmod-preserved}$:
 $\llbracket \tau \text{red1gr } \text{uf } P t h (e, xs) (e', xs'); \text{unmod } e i \rrbracket \implies \text{unmod } e' i$
 ⟨proof⟩

lemma $\tau \text{red1t-preserves-unmod}$:
 $\llbracket \tau \text{red1gt } \text{uf } P t h (e, xs) (e', xs'); \text{unmod } e i; i < \text{length } xs \rrbracket$
 $\implies xs' ! i = xs ! i$
 ⟨proof⟩

lemma $\tau \text{red1'r-preserves-unmod}$:
 $\llbracket \tau \text{red1gr } \text{uf } P t h (e, xs) (e', xs'); \text{unmod } e i; i < \text{length } xs \rrbracket$
 $\implies xs' ! i = xs ! i$
 ⟨proof⟩

end

context *J-heap-base* **begin**

lemma [*simp*]:

fixes $e :: ('a, 'b, 'addr) \text{ exp}$ **and** $es :: ('a, 'b, 'addr) \text{ exp list}$
shows $\tau \text{move0-comp}P: \tau \text{move0} (\text{comp}P f P) h e = \tau \text{move0} P h e$
and $\tau \text{moves0-comp}P: \tau \text{moves0} (\text{comp}P f P) h es = \tau \text{moves0} P h es$
 $\langle \text{proof} \rangle$

lemma $\tau \text{Move0-comp}P [\text{simp}]: \tau \text{Move0} (\text{comp}P f P) = \tau \text{Move0} P$
 $\langle \text{proof} \rangle$

end

end

7.23 Unlocking a sync block never fails

theory *Correctness1Threaded* **imports**

J0J1Bisim

../Framework/FWInitFinLift

begin

definition *lock-oks1* ::

$('addr, 'thread-id) \text{ locks}$

$\Rightarrow ('addr, 'thread-id, (('a, 'b, 'addr) \text{ exp} \times 'c) \times (('a, 'b, 'addr) \text{ exp} \times 'c) \text{ list}) \text{ thread-info} \Rightarrow \text{bool}$

where

$\bigwedge ln. \text{lock-oks1 } ls \ ts \equiv \forall t. (\text{case } (ts \ t) \text{ of None} \Rightarrow (\forall l. \text{has-locks } (ls \ \$ \ l) \ t = 0)$
 $\quad \mid \lfloor [(ex, \text{exs}), \text{ln}] \rfloor \Rightarrow (\forall l. \text{has-locks } (ls \ \$ \ l) \ t + \text{ln} \ \$ \ l = \text{expr-lockss } (\text{map fst } (ex$
 $\# \text{exs})) \ l))$

primrec *el-loc-ok* :: $'addr \text{ expr1} \Rightarrow 'addr \text{ locals1} \Rightarrow \text{bool}$

and *els-loc-ok* :: $'addr \text{ expr1 list} \Rightarrow 'addr \text{ locals1} \Rightarrow \text{bool}$

where

$\text{el-loc-ok } (\text{new } C) \ xs \longleftrightarrow \text{True}$
 $\text{el-loc-ok } (\text{newA } T[e]) \ xs \longleftrightarrow \text{el-loc-ok } e \ xs$
 $\text{el-loc-ok } (\text{Cast } T \ e) \ xs \longleftrightarrow \text{el-loc-ok } e \ xs$
 $\text{el-loc-ok } (e \ \text{instanceof } T) \ xs \longleftrightarrow \text{el-loc-ok } e \ xs$
 $\text{el-loc-ok } (e \ \llbracket \text{bop} \rrbracket e') \ xs \longleftrightarrow \text{el-loc-ok } e \ xs \wedge \text{el-loc-ok } e' \ xs$
 $\text{el-loc-ok } (\text{Var } V) \ xs \longleftrightarrow \text{True}$
 $\text{el-loc-ok } (\text{Val } v) \ xs \longleftrightarrow \text{True}$
 $\text{el-loc-ok } (V := e) \ xs \longleftrightarrow \text{el-loc-ok } e \ xs$
 $\text{el-loc-ok } (a[i]) \ xs \longleftrightarrow \text{el-loc-ok } a \ xs \wedge \text{el-loc-ok } i \ xs$
 $\text{el-loc-ok } (a[i] := e) \ xs \longleftrightarrow \text{el-loc-ok } a \ xs \wedge \text{el-loc-ok } i \ xs \wedge \text{el-loc-ok } e \ xs$
 $\text{el-loc-ok } (a \cdot \text{length}) \ xs \longleftrightarrow \text{el-loc-ok } a \ xs$
 $\text{el-loc-ok } (e \cdot F\{D\}) \ xs \longleftrightarrow \text{el-loc-ok } e \ xs$
 $\text{el-loc-ok } (e \cdot F\{D\} := e') \ xs \longleftrightarrow \text{el-loc-ok } e \ xs \wedge \text{el-loc-ok } e' \ xs$
 $\text{el-loc-ok } (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) \ xs \longleftrightarrow \text{el-loc-ok } e \ xs \wedge \text{el-loc-ok } e' \ xs \wedge \text{el-loc-ok } e'' \ xs$
 $\text{el-loc-ok } (e \cdot M(ps)) \ xs \longleftrightarrow \text{el-loc-ok } e \ xs \wedge \text{els-loc-ok } ps \ xs$
 $\text{el-loc-ok } \{V: T=vo; e\} \ xs \longleftrightarrow (\text{case } vo \text{ of None} \Rightarrow \text{el-loc-ok } e \ xs \mid [v] \Rightarrow \text{el-loc-ok } e \ (xs[V := v]))$
 $\text{el-loc-ok } (\text{sync}_V(e) \ e') \ xs \longleftrightarrow \text{el-loc-ok } e \ xs \wedge \text{el-loc-ok } e' \ xs \wedge \text{unmod } e' \ V$
 $\text{el-loc-ok } (\text{insync}_V(a) \ e) \ xs \longleftrightarrow xs ! V = \text{Addr } a \wedge \text{el-loc-ok } e \ xs \wedge \text{unmod } e \ V$
 $\text{el-loc-ok } (e;; e') \ xs \longleftrightarrow \text{el-loc-ok } e \ xs \wedge \text{el-loc-ok } e' \ xs$
 $\text{el-loc-ok } (\text{if } (b) \ e \ \text{else } e') \ xs \longleftrightarrow \text{el-loc-ok } b \ xs \wedge \text{el-loc-ok } e \ xs \wedge \text{el-loc-ok } e' \ xs$
 $\text{el-loc-ok } (\text{while } (b) \ c) \ xs \longleftrightarrow \text{el-loc-ok } b \ xs \wedge \text{el-loc-ok } c \ xs$
 $\text{el-loc-ok } (\text{throw } e) \ xs \longleftrightarrow \text{el-loc-ok } e \ xs$
 $\text{el-loc-ok } (\text{try } e \ \text{catch}(C \ V) \ e') \ xs \longleftrightarrow \text{el-loc-ok } e \ xs \wedge \text{el-loc-ok } e' \ xs$

| $els\text{-}loc\text{-}ok \ [] \ xs \longleftrightarrow True$
| $els\text{-}loc\text{-}ok \ (e \# \ es) \ xs \longleftrightarrow el\text{-}loc\text{-}ok \ e \ xs \wedge els\text{-}loc\text{-}ok \ es \ xs$

lemma $el\text{-}loc\text{-}okI$: $\llbracket \neg \text{contains-insync } e; \text{syncvars } e; \mathcal{B} \ e \ n \rrbracket \Longrightarrow el\text{-}loc\text{-}ok \ e \ xs$
and $els\text{-}loc\text{-}okI$: $\llbracket \neg \text{contains-insyncs } es; \text{syncvarss } es; \mathcal{B}s \ es \ n \rrbracket \Longrightarrow els\text{-}loc\text{-}ok \ es \ xs$
 $\langle proof \rangle$

lemma $el\text{-}loc\text{-}ok\text{-}compE1$: $\llbracket \neg \text{contains-insync } e; \text{fv } e \subseteq \text{set } Vs \rrbracket \Longrightarrow el\text{-}loc\text{-}ok \ (\text{comp}E1 \ Vs \ e) \ xs$
and $els\text{-}loc\text{-}ok\text{-}compEs1$: $\llbracket \neg \text{contains-insyncs } es; \text{fvs } es \subseteq \text{set } Vs \rrbracket \Longrightarrow els\text{-}loc\text{-}ok \ (\text{comp}Es1 \ Vs \ es) \ xs$
 $\langle proof \rangle$

lemma shows $el\text{-}loc\text{-}ok\text{-}not\text{-}contains\text{-}insync\text{-}local\text{-}change$:
 $\llbracket \neg \text{contains-insync } e; el\text{-}loc\text{-}ok \ e \ xs \rrbracket \Longrightarrow el\text{-}loc\text{-}ok \ e \ xs'$
and $els\text{-}loc\text{-}ok\text{-}not\text{-}contains\text{-}insyncs\text{-}local\text{-}change$:
 $\llbracket \neg \text{contains-insyncs } es; els\text{-}loc\text{-}ok \ es \ xs \rrbracket \Longrightarrow els\text{-}loc\text{-}ok \ es \ xs'$
 $\langle proof \rangle$

lemma $el\text{-}loc\text{-}ok\text{-}update$: $\llbracket \mathcal{B} \ e \ n; V < n \rrbracket \Longrightarrow el\text{-}loc\text{-}ok \ e \ (xs[V := v]) = el\text{-}loc\text{-}ok \ e \ xs$
and $els\text{-}loc\text{-}ok\text{-}update$: $\llbracket \mathcal{B}s \ es \ n; V < n \rrbracket \Longrightarrow els\text{-}loc\text{-}ok \ es \ (xs[V := v]) = els\text{-}loc\text{-}ok \ es \ xs$
 $\langle proof \rangle$

lemma $els\text{-}loc\text{-}ok\text{-}map\text{-}Val$ $[simp]$:
 $els\text{-}loc\text{-}ok \ (\text{map } Val \ vs) \ xs$
 $\langle proof \rangle$

lemma $els\text{-}loc\text{-}ok\text{-}map\text{-}Val\text{-}append$ $[simp]$:
 $els\text{-}loc\text{-}ok \ (\text{map } Val \ vs \ @ \ es) \ xs = els\text{-}loc\text{-}ok \ es \ xs$
 $\langle proof \rangle$

lemma $el\text{-}loc\text{-}ok\text{-}extRet2J$ $[simp]$:
 $el\text{-}loc\text{-}ok \ e \ xs \Longrightarrow el\text{-}loc\text{-}ok \ (\text{extRet}2J \ e \ va) \ xs$
 $\langle proof \rangle$

definition $el\text{-}loc\text{-}ok1$:: $((nat, nat, 'addr) \ exp \times 'addr \ locals1) \times ((nat, nat, 'addr) \ exp \times 'addr \ locals1) \ list \Rightarrow bool$
where $el\text{-}loc\text{-}ok1 = (\lambda((e, xs), es). el\text{-}loc\text{-}ok \ e \ xs \wedge \text{sync-ok } e \wedge (\forall (e, xs) \in \text{set } es. el\text{-}loc\text{-}ok \ e \ xs \wedge \text{sync-ok } e))$

lemma $el\text{-}loc\text{-}ok1\text{-}simps$:
 $el\text{-}loc\text{-}ok1 \ ((e, xs), es) = (el\text{-}loc\text{-}ok \ e \ xs \wedge \text{sync-ok } e \wedge (\forall (e, xs) \in \text{set } es. el\text{-}loc\text{-}ok \ e \ xs \wedge \text{sync-ok } e))$
 $\langle proof \rangle$

lemma $el\text{-}loc\text{-}ok\text{-}blocks1$ $[simp]$:
 $el\text{-}loc\text{-}ok \ (\text{blocks1 } n \ Ts \ body) \ xs = el\text{-}loc\text{-}ok \ body \ xs$
 $\langle proof \rangle$

lemma $sync\text{-}oks\text{-}blocks1$ $[simp]$: $\text{sync-ok } (\text{blocks1 } n \ Ts \ e) = \text{sync-ok } e$
 $\langle proof \rangle$

lemma assumes fn : $final \ e'$
shows $el\text{-}loc\text{-}ok\text{-}inline\text{-}call$: $el\text{-}loc\text{-}ok \ e \ xs \Longrightarrow el\text{-}loc\text{-}ok \ (\text{inline-call } e' \ e) \ xs$
and $els\text{-}loc\text{-}ok\text{-}inline\text{-}calls$: $els\text{-}loc\text{-}ok \ es \ xs \Longrightarrow els\text{-}loc\text{-}ok \ (\text{inline-calls } e' \ es) \ xs$

$\langle \text{proof} \rangle$

lemma assumes *sync-ok e'*

shows *sync-ok-inline-call*: $\text{sync-ok } e \implies \text{sync-ok } (\text{inline-call } e' e)$

and *sync-oks-inline-calls*: $\text{sync-oks } es \implies \text{sync-oks } (\text{inline-calls } e' es)$

$\langle \text{proof} \rangle$

lemma *bisim-sync-ok*:

bisim $\forall s e e' xs \implies \text{sync-ok } e$

bisim $\forall s e e' xs \implies \text{sync-ok } e'$

and *bisims-sync-oks*:

bisims $\forall s es es' xs \implies \text{sync-oks } es$

bisims $\forall s es es' xs \implies \text{sync-oks } es'$

$\langle \text{proof} \rangle$

lemma assumes *final e'*

shows *expr-locks-inline-call-final*:

expr-locks $(\text{inline-call } e' e) = \text{expr-locks } e$

and *expr-lockss-inline-calls-final*:

expr-lockss $(\text{inline-calls } e' es) = \text{expr-lockss } es$

$\langle \text{proof} \rangle$

lemma *lock-oks1I*:

$\llbracket \bigwedge t l. ts \ t = \text{None} \implies \text{has-locks } (ls \ \$ \ l) \ t = 0;$

$\bigwedge t e x \text{exs } ln \ l. ts \ t = \llbracket (((e, x), \text{exs}), ln) \rrbracket \implies \text{has-locks } (ls \ \$ \ l) \ t + ln \ \$ \ l = \text{expr-locks } e \ l +$

$\text{expr-lockss } (\text{map } \text{fst } \text{exs}) \ l \rrbracket$

$\implies \text{lock-oks1 } ls \ ts$

$\langle \text{proof} \rangle$

lemma *lock-oks1E*:

$\llbracket \text{lock-oks1 } ls \ ts;$

$\forall t. ts \ t = \text{None} \longrightarrow (\forall l. \text{has-locks } (ls \ \$ \ l) \ t = 0) \implies Q;$

$\forall t e x \text{exs } ln. ts \ t = \llbracket (((e, x), \text{exs}), ln) \rrbracket \longrightarrow (\forall l. \text{has-locks } (ls \ \$ \ l) \ t + ln \ \$ \ l = \text{expr-locks } e \ l +$

$\text{expr-lockss } (\text{map } \text{fst } \text{exs}) \ l) \implies Q \rrbracket$

$\implies Q$

$\langle \text{proof} \rangle$

lemma *lock-oks1D1*:

$\llbracket \text{lock-oks1 } ls \ ts; ts \ t = \text{None} \rrbracket \implies \forall l. \text{has-locks } (ls \ \$ \ l) \ t = 0$

$\langle \text{proof} \rangle$

lemma *lock-oks1D2*:

$\bigwedge ln. \llbracket \text{lock-oks1 } ls \ ts; ts \ t = \llbracket (((e, x), \text{exs}), ln) \rrbracket \rrbracket$

$\implies \forall l. \text{has-locks } (ls \ \$ \ l) \ t + ln \ \$ \ l = \text{expr-locks } e \ l + \text{expr-lockss } (\text{map } \text{fst } \text{exs}) \ l$

$\langle \text{proof} \rangle$

lemma *lock-oks1-thr-updI*:

$\bigwedge ln. \llbracket \text{lock-oks1 } ls \ ts; ts \ t = \llbracket (((e, xs), \text{exs}), ln) \rrbracket;$

$\forall l. \text{expr-locks } e \ l + \text{expr-lockss } (\text{map } \text{fst } \text{exs}) \ l = \text{expr-locks } e' \ l + \text{expr-lockss } (\text{map } \text{fst } \text{exs}') \ l \rrbracket$

$\implies \text{lock-oks1 } ls \ (ts(t \mapsto (((e', xs'), \text{exs}'), ln)))$

$\langle \text{proof} \rangle$

definition *mbisim-Red1'-Red1* ::

$((\text{'addr}, \text{'thread-id}, (\text{'addr expr1} \times \text{'addr locals1}) \times (\text{'addr expr1} \times \text{'addr locals1}) \text{ list}, \text{'heap}, \text{'addr})$
 $\text{state},$

$(\text{'addr}, \text{'thread-id}, (\text{'addr expr1} \times \text{'addr locals1}) \times (\text{'addr expr1} \times \text{'addr locals1}) \text{ list}, \text{'heap}, \text{'addr})$
 $\text{state}) \text{ bisim}$

where

$\text{mbisim-Red1'-Red1 } s1 \ s2 =$

$(s1 = s2 \wedge \text{lock-oks1 } (\text{locks } s1) (\text{thr } s1) \wedge \text{ts-ok } (\lambda t \text{ exexs } h. \text{el-loc-ok1 } \text{exexs}) (\text{thr } s1) (\text{shr } s1))$

lemma *sync-ok-blocks*:

$\llbracket \text{length } vs = \text{length } pns; \text{length } Ts = \text{length } pns \rrbracket$

$\implies \text{sync-ok } (\text{blocks } pns \ Ts \ vs \ \text{body}) = \text{sync-ok } \text{body}$

$\langle \text{proof} \rangle$

context *J1-heap-base* **begin**

lemma *red1-True-into-red1-False*:

$\llbracket \text{True}, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{el-loc-ok } e \ (lcl \ s) \rrbracket$

$\implies \text{False}, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \vee (\exists l. ta = \{\!\{ \text{UnlockFail} \rightarrow l \}\!\}) \wedge \text{expr-lockss } e \ l > 0)$

and *reds1-True-into-reds1-False*:

$\llbracket \text{True}, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{els-loc-ok } es \ (lcl \ s) \rrbracket$

$\implies \text{False}, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \vee (\exists l. ta = \{\!\{ \text{UnlockFail} \rightarrow l \}\!\}) \wedge \text{expr-lockss } es \ l > 0)$

$\langle \text{proof} \rangle$

lemma *Red1-True-into-Red1-False*:

assumes $\text{True}, P, t \vdash 1 \langle ex/exs, shr \ s \rangle -ta \rightarrow \langle ex'/exs', m \rangle$

and $\text{el-loc-ok1 } (ex, \text{exs})$

shows $\text{False}, P, t \vdash 1 \langle ex/exs, shr \ s \rangle -ta \rightarrow \langle ex'/exs', m \rangle \vee$

$(\exists l. ta = \{\!\{ \text{UnlockFail} \rightarrow l \}\!\}) \wedge \text{expr-lockss } (\text{fst } ex \ \# \ \text{map } \text{fst } \text{exs}) \ l > 0)$

$\langle \text{proof} \rangle$

lemma shows *red1-preserves-el-loc-ok*:

$\llbracket \text{uf}, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e; \text{el-loc-ok } e \ (lcl \ s) \rrbracket \implies \text{el-loc-ok } e' \ (lcl \ s')$

and *reds1-preserves-els-loc-ok*:

$\llbracket \text{uf}, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{sync-oks } es; \text{els-loc-ok } es \ (lcl \ s) \rrbracket \implies \text{els-loc-ok } es' \ (lcl \ s')$

$\langle \text{proof} \rangle$

lemma *red1-preserves-sync-ok*: $\llbracket \text{uf}, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e \rrbracket \implies \text{sync-ok } e'$

and *reds1-preserves-sync-oks*: $\llbracket \text{uf}, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{sync-oks } es \rrbracket \implies \text{sync-oks } es'$

$\langle \text{proof} \rangle$

lemma *Red1-preserves-el-loc-ok1*:

assumes $\text{wf}: \text{wf-J1-prog } P$

shows $\llbracket \text{uf}, P, t \vdash 1 \langle ex/exs, m \rangle -ta \rightarrow \langle ex'/exs', m' \rangle; \text{el-loc-ok1 } (ex, \text{exs}) \rrbracket \implies \text{el-loc-ok1 } (ex', \text{exs}')$

$\langle \text{proof} \rangle$

lemma assumes $\text{wf}: \text{wf-J1-prog } P$

shows *red1-el-loc-ok1-new-thread*:

$\llbracket \text{uf}, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewThread } t' \ (C, M, a) \ h \in \text{set } \{\!\{ ta \}\!\}_t \rrbracket$

$\implies \text{el-loc-ok1 } ((\{0: \text{Class } (\text{fst } (\text{method } P \ C \ M)) = \text{None}; \text{the } (\text{snd } (\text{snd } (\text{snd } (\text{method } P \ C \ M))))\}),$

$xs), \llbracket \rrbracket)$

and *reds1-el-loc-ok1-new-thread*:

$\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; NewThread t' (C, M, a) h \in set \{ta\}_t \rrbracket$
 $\implies el\text{-}loc\text{-}ok1 (\{0:Class (fst (method P C M))=None; the (snd (snd (snd (method P C M))))\},$
 $xs), \llbracket \rrbracket$
 $\langle proof \rangle$

lemma *Red1-el-loc-ok1-new-thread*:

assumes *wf*: *wf-J1-prog P*

shows $\llbracket uf, P, t \vdash 1 \langle ex/exs, m \rangle -ta \rightarrow \langle ex'/exs', m' \rangle; NewThread t' exexs m' \in set \{ta\}_t \rrbracket$
 $\implies el\text{-}loc\text{-}ok1 exexs$

$\langle proof \rangle$

lemma *Red1-el-loc-ok*:

assumes *wf*: *wf-J1-prog P*

shows *lifting-wf final-expr1 (mred1g uf P) ($\lambda t exexs h. el\text{-}loc\text{-}ok1 exexs$)*

$\langle proof \rangle$

lemma *mred1-eq-mred1'*:

assumes *lok*: *lock-oks1 (locks s) (thr s)*

and *elo*: *ts-ok ($\lambda t exexs h. el\text{-}loc\text{-}ok1 exexs$) (thr s) (shr s)*

and *tst*: *thr s t = [(exexs, no-wait-locks)]*

and *aoe*: *Red1-mthr.actions-ok s t ta*

shows *mred1 P t (exexs, shr s) ta = mred1' P t (exexs, shr s) ta*

$\langle proof \rangle$

lemma *Red1-mthr-eq-Red1-mthr'*:

assumes *lok*: *lock-oks1 (locks s) (thr s)*

and *elo*: *ts-ok ($\lambda t exexs h. el\text{-}loc\text{-}ok1 exexs$) (thr s) (shr s)*

shows *Red1-mthr.redT True P s = Red1-mthr.redT False P s*

$\langle proof \rangle$

lemma **assumes** *wf*: *wf-J1-prog P*

shows *expr-locks-new-thread1*:

$\llbracket uf, P, t \vdash 1 \langle e, s \rangle -TA \rightarrow \langle e', s' \rangle; NewThread t' (ex, exs) h \in set (map (convert\text{-}new\text{-}thread\text{-}action$
 $(extNTA2J1 P)) \{TA\}_t) \rrbracket$

$\implies expr\text{-}lockss (map fst (ex \# exs)) = (\lambda ad. 0)$

and *expr-locks-new-thread1*:

$\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-TA \rightarrow] \langle es', s' \rangle; NewThread t' (ex, exs) h \in set (map (convert\text{-}new\text{-}thread\text{-}action$
 $(extNTA2J1 P)) \{TA\}_t) \rrbracket$

$\implies expr\text{-}lockss (map fst (ex \# exs)) = (\lambda ad. 0)$

$\langle proof \rangle$

lemma **assumes** *wf*: *wf-J1-prog P*

shows *red1-update-expr-locks*:

$\llbracket False, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; sync\text{-}ok e; el\text{-}loc\text{-}ok e (lcl s) \rrbracket$

$\implies upd\text{-}expr\text{-}locks (int o expr\text{-}locks e) \{ta\}_l = int o expr\text{-}locks e'$

and *reds1-update-expr-locks*:

$\llbracket False, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; sync\text{-}oks es; els\text{-}loc\text{-}ok es (lcl s) \rrbracket$

$\implies upd\text{-}expr\text{-}locks (int o expr\text{-}lockss es) \{ta\}_l = int o expr\text{-}lockss es'$

$\langle proof \rangle$

lemma *Red1'-preserves-lock-oks*:

assumes *wf*: *wf-J1-prog P*

and *Red*: *Red1-mthr.redT False P s1 ta1 s1'*

and *locks*: *lock-oks1* (*locks s1*) (*thr s1*)
and *sync*: *ts-ok* (λt *exxs h. el-loc-ok1 exxs*) (*thr s1*) (*shr s1*)
shows *lock-oks1* (*locks s1'*) (*thr s1'*)
 <proof>

lemma *Red1'-Red1-bisimulation*:

assumes *wf*: *wf-J1-prog P*
shows *bisimulation* (*Red1-mthr.redT False P*) (*Red1-mthr.redT True P*) *mbisim-Red1'-Red1* (=)
 <proof>

lemma *Red1'-Red1-bisimulation-final*:

wf-J1-prog P
 \implies *bisimulation-final* (*Red1-mthr.redT False P*) (*Red1-mthr.redT True P*)
mbisim-Red1'-Red1 (=) *Red1-mthr.mfinal Red1-mthr.mfinal*
 <proof>

lemma *bisim-J1-J1-start*:

assumes *wf*: *wf-J1-prog P*
and *wf-start*: *wf-start-state P C M vs*
shows *mbisim-Red1'-Red1* (*J1-start-state P C M vs*) (*J1-start-state P C M vs*)
 <proof>

lemma *Red1'-Red1-bisim-into-weak*:

assumes *wf*: *wf-J1-prog P*
shows *bisimulation-into-delay* (*Red1-mthr.redT False P*) (*Red1-mthr.redT True P*) *mbisim-Red1'-Red1*
 (=) (*Red1-mthr.m τ move P*) (*Red1-mthr.m τ move P*)
 <proof>

end

sublocale *J1-heap-base* < *Red1-mthr*:

if- τ multithreaded-wf
final-expr1
mred1g uf P
convert-RA
 τ *MOVE1 P*
for *uf P*
 <proof>

context *J1-heap-base* **begin**

abbreviation *if-lock-oks1* ::

('*addr*, '*thread-id*) *locks*
 \Rightarrow ('*addr*, '*thread-id*, (*status* \times (('a, 'b, '*addr*) *exp* \times '*c*) \times (('a, 'b, '*addr*) *exp* \times '*c*) *list*)) *thread-info*
 \Rightarrow *bool*

where

if-lock-oks1 ls ts \equiv *lock-oks1 ls (init-fin-descend-thr ts)*

definition *if- $mbisim-Red1'-Red1$* ::

(('a, 'b, '*addr*, '*thread-id*, *status* \times (('a, 'b, '*addr*) *exp* \times '*c*) \times (('a, 'b, '*addr*) *exp* \times '*c*) *list*), '*heap*, '*addr*)
state,
 (('a, 'b, '*addr*, '*thread-id*, *status* \times (('a, 'b, '*addr*) *exp* \times '*c*) \times (('a, 'b, '*addr*) *exp* \times '*c*) *list*), '*heap*, '*addr*)
state) *bisim*

where

if-mbisim-Red1'-Red1 $s1\ s2 \longleftrightarrow$
 $s1 = s2 \wedge \text{if-lock-oks1} (\text{locks } s1) (\text{thr } s1) \wedge \text{ts-ok} (\text{init-fin-lift} (\lambda t\ \text{exexs } h. \text{el-loc-ok1 } \text{exexs})) (\text{thr } s1)$
 $(\text{shr } s1)$

lemma *if-mbisim-Red1'-Red1-imp-mbisim-Red1'-Red1*:
if-mbisim-Red1'-Red1 $s1\ s2 \implies \text{mbisim-Red1'-Red1} (\text{init-fin-descend-state } s1) (\text{init-fin-descend-state } s2)$
 ⟨proof⟩

lemma *if-Red1-mthr-imp-if-Red1-mthr'*:
assumes *lok*: *if-lock-oks1* (*locks* s) (*thr* s)
and *elo*: *ts-ok* (*init-fin-lift* ($\lambda t\ \text{exexs } h. \text{el-loc-ok1 } \text{exexs}$)) (*thr* s) (*shr* s)
and *Red*: *Red1-mthr.if.redT* $uf\ P\ s\ \text{tta } s'$
shows *Red1-mthr.if.redT* ($\neg\ uf$) $P\ s\ \text{tta } s'$
 ⟨proof⟩

lemma *if-Red1-mthr-eq-if-Red1-mthr'*:
assumes *lok*: *if-lock-oks1* (*locks* s) (*thr* s)
and *elo*: *ts-ok* (*init-fin-lift* ($\lambda t\ \text{exexs } h. \text{el-loc-ok1 } \text{exexs}$)) (*thr* s) (*shr* s)
shows *Red1-mthr.if.redT* $\text{True } P\ s = \text{Red1-mthr.if.redT } \text{False } P\ s$
 ⟨proof⟩

lemma *if-Red1-el-loc-ok*:
assumes *wf*: *wf-J1-prog* P
shows *lifting-wf* *Red1-mthr.init-fin-final* (*Red1-mthr.init-fin* $uf\ P$) (*init-fin-lift* ($\lambda t\ \text{exexs } h. \text{el-loc-ok1 } \text{exexs}$))
 ⟨proof⟩

lemma *if-Red1'-preserves-if-lock-oks*:
assumes *wf*: *wf-J1-prog* P
and *Red*: *Red1-mthr.if.redT* $\text{False } P\ s1\ \text{ta1 } s1'$
and *loks*: *if-lock-oks1* (*locks* $s1$) (*thr* $s1$)
and *sync*: *ts-ok* (*init-fin-lift* ($\lambda t\ \text{exexs } h. \text{el-loc-ok1 } \text{exexs}$)) (*thr* $s1$) (*shr* $s1$)
shows *if-lock-oks1* (*locks* $s1'$) (*thr* $s1'$)
 ⟨proof⟩

lemma *Red1'-Red1-if-bisimulation*:
assumes *wf*: *wf-J1-prog* P
shows *bisimulation* (*Red1-mthr.if.redT* $\text{False } P$) (*Red1-mthr.if.redT* $\text{True } P$) *if-mbisim-Red1'-Red1*
 (=)
 ⟨proof⟩

lemma *if-bisim-J1-J1-start*:
assumes *wf*: *wf-J1-prog* P
and *wf-start*: *wf-start-state* $P\ C\ M\ vs$
shows *if-mbisim-Red1'-Red1* (*init-fin-lift-state* *status* (*J1-start-state* $P\ C\ M\ vs$)) (*init-fin-lift-state* *status* (*J1-start-state* $P\ C\ M\ vs$))
 ⟨proof⟩

lemma *if-Red1'-Red1-bisim-into-weak*:
assumes *wf*: *wf-J1-prog* P
shows *bisimulation-into-delay* (*Red1-mthr.if.redT* $\text{False } P$) (*Red1-mthr.if.redT* $\text{True } P$) *if-mbisim-Red1'-Red1*
 (=) (*Red1-mthr.if.m τ move* P) (*Red1-mthr.if.m τ move* P)
 ⟨proof⟩

lemma *if-Red1'-Red1-bisimulation-final*:
wf-J1-prog P
 \implies *bisimulation-final (Red1-mthr.if.redT False P) (Red1-mthr.if.redT True P)*
if-mbisim-Red1'-Red1 (=) Red1-mthr.if.mfinal Red1-mthr.if.mfinal
 \langle *proof* \rangle

end

end

7.24 Semantic Correctness of Stage 1

theory *Correctness1* **imports**

J0J1Bisim
 \dots /*J/DefAssPreservation*

begin

lemma *finals-map-Val [simp]: finals (map Val vs)*
 \langle *proof* \rangle

context *J-heap-base* **begin**

lemma *τ red0r-preserves-defass*:
assumes *wf: wf-J-prog P*
shows $\llbracket \tau\text{red0r extTA } P \text{ th } (e, xs) (e', xs'); \mathcal{D} e [dom\ xs] \rrbracket \implies \mathcal{D} e' [dom\ xs']$
 \langle *proof* \rangle

lemma *τ red0t-preserves-defass*:
assumes *wf: wf-J-prog P*
shows $\llbracket \tau\text{red0t extTA } P \text{ th } (e, xs) (e', xs'); \mathcal{D} e [dom\ xs] \rrbracket \implies \mathcal{D} e' [dom\ xs']$
 \langle *proof* \rangle

end

lemma *LAss-lem*:
 $\llbracket x \in set\ xs; size\ xs \leq size\ ys \rrbracket$
 $\implies m1 \subseteq_m m2(xs[\mapsto]ys) \implies m1(x \mapsto y) \subseteq_m m2(xs[\mapsto]ys[index\ xs\ x := y])$
 \langle *proof* \rangle

lemma *Block-lem*:
fixes *l :: 'a \rightarrow 'b*
assumes *0: l \subseteq_m [Vs \mapsto] ls]*
and *1: l' \subseteq_m [Vs \mapsto] ls', V \mapsto v]*
and *hidden: V \in set Vs \implies ls ! index Vs V = ls' ! index Vs V*
and *size: size ls = size ls' size Vs < size ls'*
shows *l'(V := l V) \subseteq_m [Vs \mapsto] ls'*
 \langle *proof* \rangle

7.24.1 Correctness proof

locale *J0-J1-heap-base* =
J?: J-heap-base +
J1?: J1-heap-base +

constrains *addr2thread-id* :: ('addr :: addr) ⇒ 'thread-id
and *thread-id2addr* :: 'thread-id ⇒ 'addr
and *empty-heap* :: 'heap
and *allocate* :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and *typeof-addr* :: 'heap ⇒ 'addr → htype
and *heap-read* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and *heap-write* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
begin

lemma *ta-bisim01-extTA2J0-extTA2J1*:

assumes *wf*: wf-J-prog *P*
and *nt*: $\bigwedge n T C M a h. \llbracket n < \text{length } \{ta\}_t; \{ta\}_t ! n = \text{NewThread } T (C, M, a) h \rrbracket$
 $\implies \text{typeof-addr } h a = \lfloor \text{Class-type } C \rfloor \wedge (\exists T \text{ meth } D. P \vdash C \text{ sees } M:[] \rightarrow T = \lfloor \text{meth} \rfloor \text{ in } D)$
shows *ta-bisim01* (*extTA2J0* *P ta*) (*extTA2J1* (*compP1* *P*) *ta*)
<proof>

lemma *red-external-ta-bisim01*:

$\llbracket \text{wf-J-prog } P; P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext} \langle va, h' \rangle \rrbracket \implies \text{ta-bisim01} (\text{extTA2J0 } P \text{ ta}) (\text{extTA2J1} (\text{compP1 } P) \text{ ta})$
<proof>

lemmas $\tau\text{red1t-expr} =$

NewArray- $\tau\text{red1t-xt}$ *Cast- $\tau\text{red1t-xt}$* *InstanceOf- $\tau\text{red1t-xt}$* *BinOp- $\tau\text{red1t-xt1}$* *BinOp- $\tau\text{red1t-xt2}$* *LAss- τred1t*
AAcc- $\tau\text{red1t-xt1}$ *AAcc- $\tau\text{red1t-xt2}$* *AAss- $\tau\text{red1t-xt1}$* *AAss- $\tau\text{red1t-xt2}$* *AAss- $\tau\text{red1t-xt3}$*
ALength- $\tau\text{red1t-xt}$ *FAcc- $\tau\text{red1t-xt}$* *FAss- $\tau\text{red1t-xt1}$* *FAss- $\tau\text{red1t-xt2}$*
CAS- $\tau\text{red1t-xt1}$ *CAS- $\tau\text{red1t-xt2}$* *CAS- $\tau\text{red1t-xt3}$* *Call- $\tau\text{red1t-obj}$*
Call- $\tau\text{red1t-param}$ *Block-None- $\tau\text{red1t-xt}$* *Block- $\tau\text{red1t-Some}$* *Sync- $\tau\text{red1t-xt}$* *InSync- $\tau\text{red1t-xt}$*
Seq- $\tau\text{red1t-xt}$ *Cond- $\tau\text{red1t-xt}$* *Throw- $\tau\text{red1t-xt}$* *Try- $\tau\text{red1t-xt}$*

lemmas $\tau\text{red1r-expr} =$

NewArray- $\tau\text{red1r-xt}$ *Cast- $\tau\text{red1r-xt}$* *InstanceOf- $\tau\text{red1r-xt}$* *BinOp- $\tau\text{red1r-xt1}$* *BinOp- $\tau\text{red1r-xt2}$* *LAss- τred1r*
AAcc- $\tau\text{red1r-xt1}$ *AAcc- $\tau\text{red1r-xt2}$* *AAss- $\tau\text{red1r-xt1}$* *AAss- $\tau\text{red1r-xt2}$* *AAss- $\tau\text{red1r-xt3}$*
ALength- $\tau\text{red1r-xt}$ *FAcc- $\tau\text{red1r-xt}$* *FAss- $\tau\text{red1r-xt1}$* *FAss- $\tau\text{red1r-xt2}$*
CAS- $\tau\text{red1r-xt1}$ *CAS- $\tau\text{red1r-xt2}$* *CAS- $\tau\text{red1r-xt3}$* *Call- $\tau\text{red1r-obj}$*
Call- $\tau\text{red1r-param}$ *Block-None- $\tau\text{red1r-xt}$* *Block- $\tau\text{red1r-Some}$* *Sync- $\tau\text{red1r-xt}$* *InSync- $\tau\text{red1r-xt}$*
Seq- $\tau\text{red1r-xt}$ *Cond- $\tau\text{red1r-xt}$* *Throw- $\tau\text{red1r-xt}$* *Try- $\tau\text{red1r-xt}$*

definition *sim-move01* ::

'*addr* *J1-prog* ⇒ '*thread-id* ⇒ ('*addr*, '*thread-id*, '*heap*) *J0-thread-action* ⇒ '*addr* *expr* ⇒ '*addr* *expr1*
⇒ '*heap*
⇒ '*addr* *locals1* ⇒ ('*addr*, '*thread-id*, '*heap*) *external-thread-action* ⇒ '*addr* *expr1* ⇒ '*heap* ⇒ '*addr*
locals1 ⇒ bool

where

sim-move01 *P t ta0 e0 e h xs ta e' h' xs'* $\longleftrightarrow \neg \text{final } e0 \wedge$
(if $\tau\text{move0 } P h e0$ then $h' = h \wedge ta0 = \varepsilon \wedge ta = \varepsilon \wedge \tau\text{red1}'t P t h (e, xs) (e', xs')$
else *ta-bisim01* *ta0* (*extTA2J1* *P ta*) \wedge
(if *call* *e0* = *None* \vee *call1* *e* = *None*
then $(\exists e'' xs''. \tau\text{red1}'r P t h (e, xs) (e'', xs'') \wedge \text{False}, P, t \vdash 1 \langle e'', (h, xs'') \rangle -ta \rightarrow \langle e', (h', xs') \rangle)$
 \wedge
 $\neg \tau\text{move1 } P h e''$)
else $\text{False}, P, t \vdash 1 \langle e, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \wedge \neg \tau\text{move1 } P h e$)

definition *sim-moves01* ::

'*addr* *J1-prog* ⇒ '*thread-id* ⇒ ('*addr*, '*thread-id*, '*heap*) *J0-thread-action* ⇒ '*addr* *expr list* ⇒ '*addr*

$expr1\ list \Rightarrow 'heap$
 $\Rightarrow 'addr\ locals1 \Rightarrow ('addr, 'thread-id, 'heap)\ external-thread-action \Rightarrow 'addr\ expr1\ list \Rightarrow 'heap \Rightarrow$
 $'addr\ locals1 \Rightarrow bool$

where

$sim-moves01\ P\ t\ ta0\ es\ h\ xs\ ta\ es'\ h'\ xs' \longleftrightarrow \neg\ finals\ es0 \wedge$
 $(if\ \tau moves0\ P\ h\ es0\ then\ h' = h \wedge ta0 = \varepsilon \wedge ta = \varepsilon \wedge \tau reds1\ 't\ P\ t\ h\ (es, xs)\ (es', xs')$
 $else\ ta-bisim01\ ta0\ (extTA2J1\ P\ ta) \wedge$
 $(if\ calls\ es0 = None \vee calls1\ es = None$
 $then\ (\exists\ es''\ xs''.\ \tau reds1\ 'r\ P\ t\ h\ (es, xs)\ (es'', xs'') \wedge False, P, t \vdash 1\ \langle es'', (h, xs'') \rangle [-ta \rightarrow] \langle es', (h',$
 $xs') \rangle \wedge$
 $\quad \neg\ \tau moves1\ P\ h\ es'')$
 $else\ False, P, t \vdash 1\ \langle es, (h, xs) \rangle [-ta \rightarrow] \langle es', (h', xs') \rangle \wedge \neg\ \tau moves1\ P\ h\ es)$

declare $\tau red1t-expr\ [elim!]\ \tau red1r-expr\ [elim!]$

lemma $sim-move01-expr$:

assumes $sim-move01\ P\ t\ ta0\ e0\ e\ h\ xs\ ta\ e'\ h'\ xs'$

shows

$sim-move01\ P\ t\ ta0\ (newA\ T[e0])\ (newA\ T[e])\ h\ xs\ ta\ (newA\ T[e'])\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (Cast\ T\ e0)\ (Cast\ T\ e)\ h\ xs\ ta\ (Cast\ T\ e')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (e0\ instanceof\ T)\ (e\ instanceof\ T)\ h\ xs\ ta\ (e'\ instanceof\ T)\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (e0\ \langle bop \rangle\ e2)\ (e\ \langle bop \rangle\ e2')\ h\ xs\ ta\ (e'\ \langle bop \rangle\ e2')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (Val\ v\ \langle bop \rangle\ e0)\ (Val\ v\ \langle bop \rangle\ e)\ h\ xs\ ta\ (Val\ v\ \langle bop \rangle\ e')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (V := e0)\ (V := e)\ h\ xs\ ta\ (V := e')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (e0[e2])\ (e[e2'])\ h\ xs\ ta\ (e'[e2'])\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (Val\ v[e0])\ (Val\ v[e])\ h\ xs\ ta\ (Val\ v[e'])\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (e0[e2] := e3)\ (e[e2'] := e3')\ h\ xs\ ta\ (e'[e2'] := e3')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (Val\ v[e0] := e3)\ (Val\ v[e] := e3')\ h\ xs\ ta\ (Val\ v[e'] := e3')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (AAss\ (Val\ v)\ (Val\ v')\ e0)\ (AAss\ (Val\ v)\ (Val\ v')\ e)\ h\ xs\ ta\ (AAss\ (Val\ v)\ (Val$
 $v')\ e')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (e0.length)\ (e.length)\ h\ xs\ ta\ (e'.length)\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (e0.F\{D\})\ (e.F\{D'\})\ h\ xs\ ta\ (e'.F\{D'\})\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (FAss\ e0\ F\ D\ e2)\ (FAss\ e\ F'\ D'\ e2')\ h\ xs\ ta\ (FAss\ e'\ F'\ D'\ e2')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (FAss\ (Val\ v)\ F\ D\ e0)\ (FAss\ (Val\ v)\ F'\ D'\ e)\ h\ xs\ ta\ (FAss\ (Val\ v)\ F'\ D'\ e')$
 $h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (CompareAndSwap\ e0\ D\ F\ e2\ e3)\ (CompareAndSwap\ e\ D\ F\ e2'\ e3')\ h\ xs\ ta$
 $(CompareAndSwap\ e'\ D\ F\ e2'\ e3')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (CompareAndSwap\ (Val\ v)\ D\ F\ e0\ e3)\ (CompareAndSwap\ (Val\ v)\ D\ F\ e\ e3')\ h$
 $xs\ ta\ (CompareAndSwap\ (Val\ v)\ D\ F\ e'\ e3')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (CompareAndSwap\ (Val\ v)\ D\ F\ (Val\ v')\ e0)\ (CompareAndSwap\ (Val\ v)\ D\ F$
 $(Val\ v')\ e)\ h\ xs\ ta\ (CompareAndSwap\ (Val\ v)\ D\ F\ (Val\ v')\ e')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (e0.M(es))\ (e.M(es'))\ h\ xs\ ta\ (e'.M(es'))\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (\{V:T=vo; e0\})\ (\{V':T=None; e\})\ h\ xs\ ta\ (\{V':T=None; e'\})\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (sync(e0)\ e2)\ (sync_{V'}(e)\ e2')\ h\ xs\ ta\ (sync_{V'}(e')\ e2')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (insync(a)\ e0)\ (insync_{V'}(a')\ e)\ h\ xs\ ta\ (insync_{V'}(a')\ e')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (e0;;e2)\ (e;;e2')\ h\ xs\ ta\ (e';;e2')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (if\ (e0)\ e2\ else\ e3)\ (if\ (e)\ e2'\ else\ e3')\ h\ xs\ ta\ (if\ (e')\ e2'\ else\ e3')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (throw\ e0)\ (throw\ e)\ h\ xs\ ta\ (throw\ e')\ h'\ xs'$
 $sim-move01\ P\ t\ ta0\ (try\ e0\ catch(C\ V)\ e2)\ (try\ e\ catch(C'\ V')\ e2')\ h\ xs\ ta\ (try\ e'\ catch(C'\ V')$
 $e2')\ h'\ xs'$
 $\langle proof \rangle$

lemma $sim-moves01-expr$:

$sim-move01\ P\ t\ ta0\ e0\ e\ h\ xs\ ta\ e'\ h'\ xs' \implies sim-moves01\ P\ t\ ta0\ (e0 \# es2)\ (e \# es2')\ h\ xs\ ta$

$(e' \# es2') h' xs'$
 $sim\text{-moves01 } P t ta0 es0 es h xs ta es' h' xs' \implies sim\text{-moves01 } P t ta0 (Val v \# es0) (Val v \# es)$
 $h xs ta (Val v \# es') h' xs'$
 ⟨proof⟩

lemma *sim-move01-CallParams*:

$sim\text{-moves01 } P t ta0 es0 es h xs ta es' h' xs'$
 $\implies sim\text{-move01 } P t ta0 (Val v \cdot M(es0)) (Val v \cdot M(es)) h xs ta (Val v \cdot M(es')) h' xs'$
 ⟨proof⟩

lemma *sim-move01-reds*:

$\llbracket (h', a) \in allocate\ h\ (Class\text{-type}\ C); ta0 = \{\!\{NewHeapElem\ a\ (Class\text{-type}\ C)\!\}; ta = \{\!\{NewHeapElem\ a\ (Class\text{-type}\ C)\!\} \rrbracket$
 $\implies sim\text{-move01 } P t ta0 (new\ C) (new\ C) h xs ta (addr\ a) h' xs$
 $allocate\ h\ (Class\text{-type}\ C) = \{\} \implies sim\text{-move01 } P t \varepsilon (new\ C) (new\ C) h xs \varepsilon (THROW\ OutOfMemory) h xs$
 $\llbracket (h', a) \in allocate\ h\ (Array\text{-type}\ T\ (nat\ (sint\ i))); 0 \leq s\ i;$
 $ta0 = \{\!\{NewHeapElem\ a\ (Array\text{-type}\ T\ (nat\ (sint\ i)))\!\}; ta = \{\!\{NewHeapElem\ a\ (Array\text{-type}\ T\ (nat\ (sint\ i)))\!\} \rrbracket$
 $\implies sim\text{-move01 } P t ta0 (newA\ T[Val\ (Intg\ i)]) (newA\ T[Val\ (Intg\ i)]) h xs ta (addr\ a) h' xs$
 $i < s\ 0 \implies sim\text{-move01 } P t \varepsilon (newA\ T[Val\ (Intg\ i)]) (newA\ T[Val\ (Intg\ i)]) h xs \varepsilon (THROW\ NegativeArraySize) h xs$
 $\llbracket allocate\ h\ (Array\text{-type}\ T\ (nat\ (sint\ i))) = \{\}; 0 \leq s\ i \rrbracket$
 $\implies sim\text{-move01 } P t \varepsilon (newA\ T[Val\ (Intg\ i)]) (newA\ T[Val\ (Intg\ i)]) h xs \varepsilon (THROW\ OutOfMemory) h xs$
 $\llbracket typeof_h\ v = \lfloor U \rfloor; P \vdash U \leq T \rrbracket$
 $\implies sim\text{-move01 } P t \varepsilon (Cast\ T\ (Val\ v)) (Cast\ T\ (Val\ v)) h xs \varepsilon (Val\ v) h xs$
 $\llbracket typeof_h\ v = \lfloor U \rfloor; \neg P \vdash U \leq T \rrbracket$
 $\implies sim\text{-move01 } P t \varepsilon (Cast\ T\ (Val\ v)) (Cast\ T\ (Val\ v)) h xs \varepsilon (THROW\ ClassCast) h xs$
 $\llbracket typeof_h\ v = \lfloor U \rfloor; b \longleftrightarrow v \neq Null \wedge P \vdash U \leq T \rrbracket$
 $\implies sim\text{-move01 } P t \varepsilon ((Val\ v)\ instanceof\ T) ((Val\ v)\ instanceof\ T) h xs \varepsilon (Val\ (Bool\ b)) h xs$
 $binop\ bop\ v1\ v2 = Some\ (Inl\ v) \implies sim\text{-move01 } P t \varepsilon ((Val\ v1) \ll bop \ll (Val\ v2)) (Val\ v1 \ll bop \ll Val\ v2) h xs \varepsilon (Val\ v) h xs$
 $binop\ bop\ v1\ v2 = Some\ (Inr\ a) \implies sim\text{-move01 } P t \varepsilon ((Val\ v1) \ll bop \ll (Val\ v2)) (Val\ v1 \ll bop \ll Val\ v2) h xs \varepsilon (Throw\ a) h xs$
 $\llbracket xs!V = v; V < size\ xs \rrbracket \implies sim\text{-move01 } P t \varepsilon (Var\ V') (Var\ V) h xs \varepsilon (Val\ v) h xs$
 $V < length\ xs \implies sim\text{-move01 } P t \varepsilon (V' := Val\ v) (V := Val\ v) h xs \varepsilon unit\ h\ (xs[V := v])$
 $sim\text{-move01 } P t \varepsilon (null[Val\ v]) (null[Val\ v]) h xs \varepsilon (THROW\ NullPointer) h xs$
 $\llbracket typeof\text{-addr}\ h\ a = \lfloor Array\text{-type}\ T\ n \rfloor; i < s\ 0 \vee sint\ i \geq int\ n \rrbracket$
 $\implies sim\text{-move01 } P t \varepsilon (addr\ a[Val\ (Intg\ i)]) ((addr\ a)[Val\ (Intg\ i)]) h xs \varepsilon (THROW\ ArrayIndexOutOfBounds) h xs$
 $\llbracket typeof\text{-addr}\ h\ a = \lfloor Array\text{-type}\ T\ n \rfloor; 0 \leq s\ i; sint\ i < int\ n;$
 $heap\text{-read}\ h\ a\ (ACell\ (nat\ (sint\ i)))\ v;$
 $ta0 = \{\!\{ReadMem\ a\ (ACell\ (nat\ (sint\ i)))\ v\!\};$
 $ta = \{\!\{ReadMem\ a\ (ACell\ (nat\ (sint\ i)))\ v\!\} \rrbracket$
 $\implies sim\text{-move01 } P t ta0 (addr\ a[Val\ (Intg\ i)]) ((addr\ a)[Val\ (Intg\ i)]) h xs ta (Val\ v) h xs$
 $sim\text{-move01 } P t \varepsilon (null[Val\ v] := Val\ v') (null[Val\ v] := Val\ v') h xs \varepsilon (THROW\ NullPointer) h xs$
 $\llbracket typeof\text{-addr}\ h\ a = \lfloor Array\text{-type}\ T\ n \rfloor; i < s\ 0 \vee sint\ i \geq int\ n \rrbracket$
 $\implies sim\text{-move01 } P t \varepsilon (AAss\ (addr\ a) (Val\ (Intg\ i)) (Val\ v)) (AAss\ (addr\ a) (Val\ (Intg\ i)) (Val\ v))$
 $h xs \varepsilon (THROW\ ArrayIndexOutOfBounds) h xs$
 $\llbracket typeof\text{-addr}\ h\ a = \lfloor Array\text{-type}\ T\ n \rfloor; 0 \leq s\ i; sint\ i < int\ n; typeof_h\ v = \lfloor U \rfloor; \neg (P \vdash U \leq T) \rrbracket$
 $\implies sim\text{-move01 } P t \varepsilon (AAss\ (addr\ a) (Val\ (Intg\ i)) (Val\ v)) (AAss\ (addr\ a) (Val\ (Intg\ i)) (Val\ v))$
 $h xs \varepsilon (THROW\ ArrayStore) h xs$
 $\llbracket typeof\text{-addr}\ h\ a = \lfloor Array\text{-type}\ T\ n \rfloor; 0 \leq s\ i; sint\ i < int\ n; typeof_h\ v = Some\ U; P \vdash U \leq T;$

$heap\text{-}write\ h\ a\ (ACell\ (nat\ (sint\ i)))\ v\ h'$;
 $ta0 = \{\!\!| WriteMem\ a\ (ACell\ (nat\ (sint\ i)))\ v \!\!\}$; $ta = \{\!\!| WriteMem\ a\ (ACell\ (nat\ (sint\ i)))\ v \!\!\}$]
 $\implies sim\text{-}move01\ P\ t\ ta0\ (AAss\ (addr\ a)\ (Val\ (Intg\ i))\ (Val\ v))\ (AAss\ (addr\ a)\ (Val\ (Intg\ i))\ (Val\ v))\ h\ xs\ ta\ unit\ h'\ xs$
 $typeof\text{-}addr\ h\ a = \lfloor Array\text{-}type\ T\ n \rfloor \implies sim\text{-}move01\ P\ t\ \varepsilon\ (addr\ a \cdot length)\ (addr\ a \cdot length)\ h\ xs\ \varepsilon$
 $(Val\ (Intg\ (word\text{-}of\text{-}int\ (int\ n))))\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (null \cdot length)\ (null \cdot length)\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$

$\llbracket heap\text{-}read\ h\ a\ (CField\ D\ F)\ v; ta0 = \{\!\!| ReadMem\ a\ (CField\ D\ F)\ v \!\!\}; ta = \{\!\!| ReadMem\ a\ (CField\ D\ F)\ v \!\!\} \rrbracket$
 $\implies sim\text{-}move01\ P\ t\ ta0\ (addr\ a \cdot F\{D\})\ (addr\ a \cdot F\{D\})\ h\ xs\ ta\ (Val\ v)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (null \cdot F\{D\})\ (null \cdot F\{D\})\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$
 $\llbracket heap\text{-}write\ h\ a\ (CField\ D\ F)\ v\ h'; ta0 = \{\!\!| WriteMem\ a\ (CField\ D\ F)\ v \!\!\}; ta = \{\!\!| WriteMem\ a\ (CField\ D\ F)\ v \!\!\} \rrbracket$
 $\implies sim\text{-}move01\ P\ t\ ta0\ (addr\ a \cdot F\{D\} := Val\ v)\ (addr\ a \cdot F\{D\} := Val\ v)\ h\ xs\ ta\ unit\ h'\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (null \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'))\ (null \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'))\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$
 $\llbracket heap\text{-}read\ h\ a\ (CField\ D\ F)\ v''; heap\text{-}write\ h\ a\ (CField\ D\ F)\ v'\ h'; v'' = v;$
 $ta0 = \{\!\!| ReadMem\ a\ (CField\ D\ F)\ v'', WriteMem\ a\ (CField\ D\ F)\ v' \!\!\}; ta = \{\!\!| ReadMem\ a\ (CField\ D\ F)\ v'', WriteMem\ a\ (CField\ D\ F)\ v' \!\!\} \rrbracket$
 $\implies sim\text{-}move01\ P\ t\ ta0\ (addr\ a \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'))\ (addr\ a \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'))\ h\ xs\ ta\ true\ h'\ xs$
 $\llbracket heap\text{-}read\ h\ a\ (CField\ D\ F)\ v''; v'' \neq v;$
 $ta0 = \{\!\!| ReadMem\ a\ (CField\ D\ F)\ v'' \!\!\}; ta = \{\!\!| ReadMem\ a\ (CField\ D\ F)\ v' \!\!\} \rrbracket$
 $\implies sim\text{-}move01\ P\ t\ ta0\ (addr\ a \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'))\ (addr\ a \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'))\ h\ xs\ ta\ false\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (null \cdot F\{D\} := Val\ v)\ (null \cdot F\{D\} := Val\ v)\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (\{V':T=vo; Val\ u\})\ (\{V':T=None; Val\ u\})\ h\ xs\ \varepsilon\ (Val\ u)\ h\ xs$
 $V < length\ xs \implies sim\text{-}move01\ P\ t\ \varepsilon\ (sync(null)\ e0)\ (sync_V(null)\ e1)\ h\ xs\ \varepsilon\ (THROW\ NullPointer)$
 $h\ (xs[V := Null])$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (Val\ v;;e0)\ (Val\ v;;e1)\ h\ xs\ \varepsilon\ e1\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (if\ (true)\ e0\ else\ e0')\ (if\ (true)\ e1\ else\ e1')\ h\ xs\ \varepsilon\ e1\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (if\ (false)\ e0\ else\ e0')\ (if\ (false)\ e1\ else\ e1')\ h\ xs\ \varepsilon\ e1'\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (throw\ null)\ (throw\ null)\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (try\ (Val\ v)\ catch(C\ V')\ e0)\ (try\ (Val\ v)\ catch(C\ V)\ e1)\ h\ xs\ \varepsilon\ (Val\ v)\ h\ xs$
 $\llbracket typeof\text{-}addr\ h\ a = \lfloor Class\text{-}type\ D \rfloor; P \vdash D \leq^* C; V < length\ xs \rrbracket$
 $\implies sim\text{-}move01\ P\ t\ \varepsilon\ (try\ (Throw\ a)\ catch(C\ V')\ e0)\ (try\ (Throw\ a)\ catch(C\ V)\ e1)\ h\ xs\ \varepsilon$
 $(\{V:Class\ C=None; e1\})\ h\ (xs[V := Addr\ a])$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (newA\ T[Throw\ a])\ (newA\ T[Throw\ a])\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (Cast\ T\ (Throw\ a))\ (Cast\ T\ (Throw\ a))\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ ((Throw\ a)\ instanceof\ T)\ ((Throw\ a)\ instanceof\ T)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ ((Throw\ a)\ \ll bop \gg e0)\ ((Throw\ a)\ \ll bop \gg e1)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (Val\ v\ \ll bop \gg (Throw\ a))\ (Val\ v\ \ll bop \gg (Throw\ a))\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (V' := Throw\ a)\ (V := Throw\ a)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (Throw\ a[e0])\ (Throw\ a[e1])\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (Val\ v[Throw\ a])\ (Val\ v[Throw\ a])\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (Throw\ a[e0] := e0')\ (Throw\ a[e1] := e1')\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (Val\ v[Throw\ a] := e0)\ (Val\ v[Throw\ a] := e1)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (Val\ v[Val\ v'] := Throw\ a)\ (Val\ v[Val\ v'] := Throw\ a)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (Throw\ a \cdot length)\ (Throw\ a \cdot length)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (Throw\ a \cdot F\{D\})\ (Throw\ a \cdot F\{D\})\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (Throw\ a \cdot F\{D\} := e0)\ (Throw\ a \cdot F\{D\} := e1)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (Val\ v \cdot F\{D\} := Throw\ a)\ (Val\ v \cdot F\{D\} := Throw\ a)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim\text{-}move01\ P\ t\ \varepsilon\ (Throw\ a \cdot compareAndSwap(D \cdot F, e2, e3))\ (Throw\ a \cdot compareAndSwap(D \cdot F, e2',$

$e3')$ $h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim-move01\ P\ t\ \varepsilon\ (Val\ v \cdot compareAndSwap(D \cdot F, Throw\ a, e3))\ (Val\ v \cdot compareAndSwap(D \cdot F, Throw\ a, e3'))\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim-move01\ P\ t\ \varepsilon\ (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', Throw\ a))\ (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', Throw\ a))\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim-move01\ P\ t\ \varepsilon\ (Throw\ a \cdot M(es0))\ (Throw\ a \cdot M(es1))\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim-move01\ P\ t\ \varepsilon\ (\{V':T=vo; Throw\ a\})\ (\{V:T=None; Throw\ a\})\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim-move01\ P\ t\ \varepsilon\ (sync(Throw\ a)\ e0)\ (sync_V(Throw\ a)\ e1)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim-move01\ P\ t\ \varepsilon\ (Throw\ a;;e0)\ (Throw\ a;;e1)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim-move01\ P\ t\ \varepsilon\ (if\ (Throw\ a)\ e0\ else\ e0')\ (if\ (Throw\ a)\ e1\ else\ e1')\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $sim-move01\ P\ t\ \varepsilon\ (throw\ (Throw\ a))\ (throw\ (Throw\ a))\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $\langle proof \rangle$

lemma *sim-move01-ThrowParams*:

$sim-move01\ P\ t\ \varepsilon\ (Val\ v \cdot M(map\ Val\ vs\ @\ Throw\ a\ \# es0))\ (Val\ v \cdot M(map\ Val\ vs\ @\ Throw\ a\ \# es1))\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $\langle proof \rangle$

lemma *sim-move01-CallNull*:

$sim-move01\ P\ t\ \varepsilon\ (null \cdot M(map\ Val\ vs))\ (null \cdot M(map\ Val\ vs))\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$
 $\langle proof \rangle$

lemma *sim-move01-SyncLocks*:

$\llbracket V < length\ xs; ta0 = \{\{Lock \rightarrow a, SyncLock\ a\}\}; ta = \{\{Lock \rightarrow a, SyncLock\ a\}\} \rrbracket$
 $\implies sim-move01\ P\ t\ ta0\ (sync(addr\ a)\ e0)\ (sync_V(addr\ a)\ e1)\ h\ xs\ ta\ (insync_V(a)\ e1)\ h\ (xs[V := Addr\ a])$
 $\llbracket xs!V = Addr\ a'; V < length\ xs; ta0 = \{\{Unlock \rightarrow a', SyncUnlock\ a'\}\}; ta = \{\{Unlock \rightarrow a', SyncUnlock\ a'\}\} \rrbracket$
 $\implies sim-move01\ P\ t\ ta0\ (insync(a')\ (Val\ v))\ (insync_V(a)\ (Val\ v))\ h\ xs\ ta\ (Val\ v)\ h\ xs$
 $\llbracket xs!V = Addr\ a'; V < length\ xs; ta0 = \{\{Unlock \rightarrow a', SyncUnlock\ a'\}\}; ta = \{\{Unlock \rightarrow a', SyncUnlock\ a'\}\} \rrbracket$
 $\implies sim-move01\ P\ t\ ta0\ (insync(a')\ (Throw\ a''))\ (insync_V(a)\ (Throw\ a''))\ h\ xs\ ta\ (Throw\ a'')\ h\ xs$
 $\langle proof \rangle$

lemma *sim-move01-TryFail*:

$\llbracket typeof-addr\ h\ a = \lfloor Class-type\ D \rfloor; \neg P \vdash D \preceq^* C \rrbracket$
 $\implies sim-move01\ P\ t\ \varepsilon\ (try\ (Throw\ a)\ catch(C\ V')\ e0)\ (try\ (Throw\ a)\ catch(C\ V)\ e1)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $\langle proof \rangle$

lemma *sim-move01-BlockSome*:

$\llbracket sim-move01\ P\ t\ ta0\ e0\ e\ h\ (xs[V := v])\ ta\ e'\ h'\ xs'; V < length\ xs \rrbracket$
 $\implies sim-move01\ P\ t\ ta0\ (\{V':T=[v]; e0\})\ (\{V:T=[v]; e\})\ h\ xs\ ta\ (\{V:T=None; e'\})\ h'\ xs'$
 $V < length\ xs \implies sim-move01\ P\ t\ \varepsilon\ (\{V':T=[v]; Val\ u\})\ (\{V:T=[v]; Val\ u\})\ h\ xs\ \varepsilon\ (Val\ u)\ h\ (xs[V := v])$
 $V < length\ xs \implies sim-move01\ P\ t\ \varepsilon\ (\{V':T=[v]; Throw\ a\})\ (\{V:T=[v]; Throw\ a\})\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ (xs[V := v])$
 $\langle proof \rangle$

lemmas *sim-move01-intros* =

$sim-move01-expr\ sim-move01-reds\ sim-move01-ThrowParams\ sim-move01-CallNull\ sim-move01-TryFail$
 $sim-move01-BlockSome\ sim-move01-CallParams$

declare *sim-move01-intros*[intro]

lemma *sim-move01-preserves-len*: $\text{sim-move01 } P \ t \ ta0 \ e0 \ e \ h \ xs \ ta \ e' \ h' \ xs' \implies \text{length } xs' = \text{length } xs$
 ⟨proof⟩

lemma *sim-move01-preserves-unmod*:

$\llbracket \text{sim-move01 } P \ t \ ta0 \ e0 \ e \ h \ xs \ ta \ e' \ h' \ xs'; \text{unmod } e \ i; \ i < \text{length } xs \rrbracket \implies xs' ! i = xs ! i$
 ⟨proof⟩

lemma *assumes wf: wf-J-prog P*

shows *red1-simulates-red-aux*:

$\llbracket \text{extTA2J0 } P, P, t \vdash \langle e1, S \rangle \text{-TA} \rightarrow \langle e1', S' \rangle; \text{bisim } vs \ e1 \ e2 \ XS; \text{fv } e1 \subseteq \text{set } vs;$
 $\text{lcl } S \subseteq_m [vs \mapsto] XS; \text{length } vs + \text{max-vars } e1 \leq \text{length } XS;$
 $\forall aMvs. \text{call } e1 = [aMvs] \rightarrow \text{synthesized-call } P \ (hp \ S) \ aMvs \rrbracket$
 $\implies \exists ta \ e2' \ XS'. \text{sim-move01 } (compP1 \ P) \ t \ TA \ e1 \ e2 \ (hp \ S) \ XS \ ta \ e2' \ (hp \ S') \ XS' \wedge \text{bisim } vs \ e1' \ e2' \ XS' \wedge \text{lcl } S' \subseteq_m [vs \mapsto] XS'$
 (is $\llbracket -; -; -; -; ?synth \ e1 \ S \rrbracket \implies ?concl \ e1 \ e2 \ S \ XS \ e1' \ S' \ TA \ vs$)

and *reds1-simulates-reds-aux*:

$\llbracket \text{extTA2J0 } P, P, t \vdash \langle es1, S \rangle \text{-TA} \rightarrow \langle es1', S' \rangle; \text{bisims } vs \ es1 \ es2 \ XS; \text{fvs } es1 \subseteq \text{set } vs;$
 $\text{lcl } S \subseteq_m [vs \mapsto] XS; \text{length } vs + \text{max-varss } es1 \leq \text{length } XS;$
 $\forall aMvs. \text{calls } es1 = [aMvs] \rightarrow \text{synthesized-call } P \ (hp \ S) \ aMvs \rrbracket$
 $\implies \exists ta \ es2' \ xs'. \text{sim-moves01 } (compP1 \ P) \ t \ TA \ es1 \ es2 \ (hp \ S) \ XS \ ta \ es2' \ (hp \ S') \ xs' \wedge \text{bisims } vs \ es1' \ es2' \ xs' \wedge \text{lcl } S' \subseteq_m [vs \mapsto] xs'$
 (is $\llbracket -; -; -; -; ?synths \ es1 \ S \rrbracket \implies ?concls \ es1 \ es2 \ S \ XS \ es1' \ S' \ TA \ vs$)
 ⟨proof⟩

end

declare *max-dest* [iff del]

declare *split-paired-Ex* [simp del]

primrec *countInitBlock* :: ('a, 'b, 'addr) exp \Rightarrow nat

and *countInitBlocks* :: ('a, 'b, 'addr) exp list \Rightarrow nat

where

$\text{countInitBlock } (\text{new } C) = 0$
 $\text{countInitBlock } (\text{newA } T \ |e|) = \text{countInitBlock } e$
 $\text{countInitBlock } (\text{Cast } T \ e) = \text{countInitBlock } e$
 $\text{countInitBlock } (e \ \text{instanceof } T) = \text{countInitBlock } e$
 $\text{countInitBlock } (\text{Val } v) = 0$
 $\text{countInitBlock } (\text{Var } V) = 0$
 $\text{countInitBlock } (V := e) = \text{countInitBlock } e$
 $\text{countInitBlock } (e \ \ll \text{bop} \ e') = \text{countInitBlock } e + \text{countInitBlock } e'$
 $\text{countInitBlock } (a \ |i|) = \text{countInitBlock } a + \text{countInitBlock } i$
 $\text{countInitBlock } (AAss \ a \ i \ e) = \text{countInitBlock } a + \text{countInitBlock } i + \text{countInitBlock } e$
 $\text{countInitBlock } (a \cdot \text{length}) = \text{countInitBlock } a$
 $\text{countInitBlock } (e \cdot F \{D\}) = \text{countInitBlock } e$
 $\text{countInitBlock } (FAss \ e \ F \ D \ e') = \text{countInitBlock } e + \text{countInitBlock } e'$
 $\text{countInitBlock } (e \cdot \text{compareAndSwap}(D \cdot F, \ e', \ e'')) =$
 $\text{countInitBlock } e + \text{countInitBlock } e' + \text{countInitBlock } e''$
 $\text{countInitBlock } (e \cdot M(es)) = \text{countInitBlock } e + \text{countInitBlocks } es$
 $\text{countInitBlock } (\{V:T=vo; e\}) = (\text{case } vo \ \text{of } \text{None} \Rightarrow 0 \ | \ \text{Some } v \Rightarrow 1) + \text{countInitBlock } e$
 $\text{countInitBlock } (\text{sync}_{V'}(e) \ e') = \text{countInitBlock } e + \text{countInitBlock } e'$
 $\text{countInitBlock } (\text{insync}_{V'}(ad) \ e) = \text{countInitBlock } e$

$| \text{countInitBlock } (e; e') = \text{countInitBlock } e + \text{countInitBlock } e'$
 $| \text{countInitBlock } (\text{if } (e) e1 \text{ else } e2) = \text{countInitBlock } e + \text{countInitBlock } e1 + \text{countInitBlock } e2$
 $| \text{countInitBlock } (\text{while}(b) e) = \text{countInitBlock } b + \text{countInitBlock } e$
 $| \text{countInitBlock } (\text{throw } e) = \text{countInitBlock } e$
 $| \text{countInitBlock } (\text{try } e \text{ catch}(C V) e') = \text{countInitBlock } e + \text{countInitBlock } e'$

$| \text{countInitBlocks } [] = 0$
 $| \text{countInitBlocks } (e \# es) = \text{countInitBlock } e + \text{countInitBlocks } es$

context *J0-J1-heap-base* **begin**

lemmas $\tau\text{red0r-expr} =$

$\text{NewArray-}\tau\text{red0r-xt}$ $\text{Cast-}\tau\text{red0r-xt}$ $\text{InstanceOf-}\tau\text{red0r-xt}$ $\text{BinOp-}\tau\text{red0r-xt1}$ $\text{BinOp-}\tau\text{red0r-xt2}$ $\text{LAss-}\tau\text{red0r-xt}$
 $\text{AAss-}\tau\text{red0r-xt1}$ $\text{AAss-}\tau\text{red0r-xt2}$ $\text{AAss-}\tau\text{red0r-xt3}$ $\text{AAss-}\tau\text{red0r-xt2}$ $\text{AAss-}\tau\text{red0r-xt3}$
 $\text{ALength-}\tau\text{red0r-xt}$ $\text{FAcc-}\tau\text{red0r-xt}$ $\text{FAss-}\tau\text{red0r-xt1}$ $\text{FAss-}\tau\text{red0r-xt2}$
 $\text{CAS-}\tau\text{red0r-xt1}$ $\text{CAS-}\tau\text{red0r-xt2}$ $\text{CAS-}\tau\text{red0r-xt3}$ $\text{Call-}\tau\text{red0r-obj}$
 $\text{Call-}\tau\text{red0r-param}$ $\text{Block-}\tau\text{red0r-xt}$ $\text{Sync-}\tau\text{red0r-xt}$ $\text{InSync-}\tau\text{red0r-xt}$
 $\text{Seq-}\tau\text{red0r-xt}$ $\text{Cond-}\tau\text{red0r-xt}$ $\text{Throw-}\tau\text{red0r-xt}$ $\text{Try-}\tau\text{red0r-xt}$

lemmas $\tau\text{red0t-expr} =$

$\text{NewArray-}\tau\text{red0t-xt}$ $\text{Cast-}\tau\text{red0t-xt}$ $\text{InstanceOf-}\tau\text{red0t-xt}$ $\text{BinOp-}\tau\text{red0t-xt1}$ $\text{BinOp-}\tau\text{red0t-xt2}$ $\text{LAss-}\tau\text{red0t-xt}$
 $\text{AAss-}\tau\text{red0t-xt1}$ $\text{AAss-}\tau\text{red0t-xt2}$ $\text{AAss-}\tau\text{red0t-xt1}$ $\text{AAss-}\tau\text{red0t-xt2}$ $\text{AAss-}\tau\text{red0t-xt3}$
 $\text{ALength-}\tau\text{red0t-xt}$ $\text{FAcc-}\tau\text{red0t-xt}$ $\text{FAss-}\tau\text{red0t-xt1}$ $\text{FAss-}\tau\text{red0t-xt2}$
 $\text{CAS-}\tau\text{red0t-xt1}$ $\text{CAS-}\tau\text{red0t-xt2}$ $\text{CAS-}\tau\text{red0t-xt3}$ $\text{Call-}\tau\text{red0t-obj}$
 $\text{Call-}\tau\text{red0t-param}$ $\text{Block-}\tau\text{red0t-xt}$ $\text{Sync-}\tau\text{red0t-xt}$ $\text{InSync-}\tau\text{red0t-xt}$
 $\text{Seq-}\tau\text{red0t-xt}$ $\text{Cond-}\tau\text{red0t-xt}$ $\text{Throw-}\tau\text{red0t-xt}$ $\text{Try-}\tau\text{red0t-xt}$

declare $\tau\text{red0r-expr}$ [elim!]

declare $\tau\text{red0t-expr}$ [elim!]

definition *sim-move10* ::

$'\text{addr } J\text{-prog} \Rightarrow '\text{thread-id} \Rightarrow (' \text{addr}, '\text{thread-id}, '\text{heap}) \text{ external-thread-action} \Rightarrow '\text{addr } \text{expr1} \Rightarrow '\text{addr } \text{expr1} \Rightarrow '\text{addr } \text{expr}$
 $\Rightarrow '\text{heap} \Rightarrow '\text{addr locals} \Rightarrow (' \text{addr}, '\text{thread-id}, '\text{heap}) J0\text{-thread-action} \Rightarrow '\text{addr } \text{expr} \Rightarrow '\text{heap} \Rightarrow '\text{addr } \text{locals} \Rightarrow \text{bool}$

where

$\text{sim-move10 } P t \text{ ta1 } e1 e1' e h \text{ xs } \text{ ta } e' h' \text{ xs}' \longleftrightarrow \neg \text{final } e1 \wedge$
 $(\text{if } \tau\text{move1 } P h e1 \text{ then } (\tau\text{red0t } (\text{extTA2J0 } P) P t h (e, \text{xs}) (e', \text{xs}')) \vee \text{countInitBlock } e1' < \text{countInitBlock } e1 \wedge e' = e \wedge \text{xs}' = \text{xs}) \wedge h' = h \wedge \text{ta1} = \varepsilon \wedge \text{ta} = \varepsilon$
 $\text{else } \text{ta-bisim01 } \text{ta} (\text{extTA2J1 } (\text{compP1 } P) \text{ ta1}) \wedge$
 $(\text{if } \text{call } e = \text{None} \vee \text{call1 } e1 = \text{None}$
 $\text{then } (\exists e'' \text{ xs}''. \tau\text{red0r } (\text{extTA2J0 } P) P t h (e, \text{xs}) (e'', \text{xs}'') \wedge \text{extTA2J0 } P, P, t \vdash \langle e'', (h, \text{xs}'') \rangle$
 $\text{-ta} \rightarrow \langle e', (h', \text{xs}') \rangle \wedge \text{no-call } P h e'' \wedge \neg \tau\text{move0 } P h e'')$
 $\text{else } \text{extTA2J0 } P, P, t \vdash \langle e, (h, \text{xs}) \rangle \text{-ta} \rightarrow \langle e', (h', \text{xs}') \rangle \wedge \text{no-call } P h e \wedge \neg \tau\text{move0 } P h e))$

definition *sim-moves10* ::

$'\text{addr } J\text{-prog} \Rightarrow '\text{thread-id} \Rightarrow (' \text{addr}, '\text{thread-id}, '\text{heap}) \text{ external-thread-action} \Rightarrow '\text{addr } \text{expr1 list} \Rightarrow '\text{addr } \text{expr1 list}$
 $\Rightarrow '\text{addr } \text{expr list} \Rightarrow '\text{heap} \Rightarrow '\text{addr locals} \Rightarrow (' \text{addr}, '\text{thread-id}, '\text{heap}) J0\text{-thread-action} \Rightarrow '\text{addr } \text{expr list} \Rightarrow '\text{heap}$
 $\Rightarrow '\text{addr locals} \Rightarrow \text{bool}$

where

$\text{sim-moves10 } P t \text{ ta1 } es1 es1' es h \text{ xs } \text{ ta } es' h' \text{ xs}' \longleftrightarrow \neg \text{finals } es1 \wedge$
 $(\text{if } \tau\text{moves1 } P h es1 \text{ then } (\tau\text{reds0t } (\text{extTA2J0 } P) P t h (es, \text{xs}) (es', \text{xs}')) \vee \text{countInitBlocks } es1' <$

$\text{countInitBlocks } es1 \wedge es' = es \wedge xs' = xs) \wedge h' = h \wedge ta1 = \varepsilon \wedge ta = \varepsilon$
 $\text{else } ta\text{-bisim01 } ta (\text{extTA2J1 } (\text{compP1 } P) ta1) \wedge$
 $(\text{if calls } es = \text{None} \vee \text{calls1 } es1 = \text{None}$
 $\text{then } (\exists es'' xs''. \tau\text{reds0r } (\text{extTA2J0 } P) P t h (es, xs) (es'', xs'') \wedge \text{extTA2J0 } P, P, t \vdash \langle es'', (h,$
 $xs'') \rangle [-ta \rightarrow] \langle es', (h', xs') \rangle \wedge \text{no-calls } P h es'' \wedge \neg \tau\text{moves0 } P h es'')$
 $\text{else } \text{extTA2J0 } P, P, t \vdash \langle es, (h, xs) \rangle [-ta \rightarrow] \langle es', (h', xs') \rangle \wedge \text{no-calls } P h es \wedge \neg \tau\text{moves0 } P h$
 $es))$

lemma *sim-move10-expr*:

assumes *sim-move10* $P t ta1 e1 e1' e h xs ta e' h' xs'$

shows

$\text{sim-move10 } P t ta1 (\text{newA } T[e1]) (\text{newA } T[e1']) (\text{newA } T[e]) h xs ta (\text{newA } T[e']) h' xs'$
 $\text{sim-move10 } P t ta1 (\text{Cast } T e1) (\text{Cast } T e1') (\text{Cast } T e) h xs ta (\text{Cast } T e') h' xs'$
 $\text{sim-move10 } P t ta1 (e1 \text{ instanceof } T) (e1' \text{ instanceof } T) (e \text{ instanceof } T) h xs ta (e' \text{ instanceof } T)$
 $h' xs'$
 $\text{sim-move10 } P t ta1 (e1 \ll\text{bop}\gg e2) (e1' \ll\text{bop}\gg e2) (e \ll\text{bop}\gg e2') h xs ta (e' \ll\text{bop}\gg e2') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{Val } v \ll\text{bop}\gg e1) (\text{Val } v \ll\text{bop}\gg e1') (\text{Val } v \ll\text{bop}\gg e) h xs ta (\text{Val } v \ll\text{bop}\gg e') h'$
 xs'
 $\text{sim-move10 } P t ta1 (V := e1) (V := e1') (V' := e) h xs ta (V' := e') h' xs'$
 $\text{sim-move10 } P t ta1 (e1[e2]) (e1'[e2']) (e[e2]) h xs ta (e'[e2']) h' xs'$
 $\text{sim-move10 } P t ta1 (\text{Val } v[e1]) (\text{Val } v[e1']) (\text{Val } v[e]) h xs ta (\text{Val } v[e']) h' xs'$
 $\text{sim-move10 } P t ta1 (e1[e2] := e3) (e1'[e2'] := e3) (e[e2] := e3') h xs ta (e'[e2'] := e3') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{Val } v[e1] := e3) (\text{Val } v[e1'] := e3) (\text{Val } v[e] := e3') h xs ta (\text{Val } v[e'] :=$
 $e3') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{AAss } (\text{Val } v) (\text{Val } v') e1) (\text{AAss } (\text{Val } v) (\text{Val } v') e1') (\text{AAss } (\text{Val } v) (\text{Val } v')$
 $e) h xs ta (\text{AAss } (\text{Val } v) (\text{Val } v') e') h' xs'$
 $\text{sim-move10 } P t ta1 (e1 \cdot \text{length}) (e1' \cdot \text{length}) (e \cdot \text{length}) h xs ta (e' \cdot \text{length}) h' xs'$
 $\text{sim-move10 } P t ta1 (e1 \cdot F\{D\}) (e1' \cdot F\{D\}) (e \cdot F\{D\}) h xs ta (e' \cdot F\{D\}) h' xs'$
 $\text{sim-move10 } P t ta1 (\text{FAss } e1 F D e2) (\text{FAss } e1' F D e2) (\text{FAss } e F' D' e2') h xs ta (\text{FAss } e' F' D'$
 $e2') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{FAss } (\text{Val } v) F D e1) (\text{FAss } (\text{Val } v) F D e1') (\text{FAss } (\text{Val } v) F' D' e) h xs ta$
 $(\text{FAss } (\text{Val } v) F' D' e') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{CompareAndSwap } e1 F D e2 e3) (\text{CompareAndSwap } e1' F D e2 e3) (\text{CompareAndSwap}$
 $e F' D' e2' e3') h xs ta (\text{CompareAndSwap } e' F' D' e2' e3') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{CompareAndSwap } (\text{Val } v) F D e1 e3) (\text{CompareAndSwap } (\text{Val } v) F D e1' e3)$
 $(\text{CompareAndSwap } (\text{Val } v) F' D' e e3') h xs ta (\text{CompareAndSwap } (\text{Val } v) F' D' e' e3') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{CompareAndSwap } (\text{Val } v) F D (\text{Val } v') e1) (\text{CompareAndSwap } (\text{Val } v) F D$
 $(\text{Val } v') e1') (\text{CompareAndSwap } (\text{Val } v) F' D' (\text{Val } v') e) h xs ta (\text{CompareAndSwap } (\text{Val } v) F' D'$
 $(\text{Val } v') e') h' xs'$
 $\text{sim-move10 } P t ta1 (e1 \cdot M(es)) (e1' \cdot M(es)) (e \cdot M(es')) h xs ta (e' \cdot M(es')) h' xs'$
 $\text{sim-move10 } P t ta1 (\text{sync}_V(e1) e2) (\text{sync}_V(e1') e2) (\text{sync}(e) e2') h xs ta (\text{sync}(e') e2') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{insync}_V(a) e1) (\text{insync}_V(a) e1') (\text{insync}(a') e) h xs ta (\text{insync}(a') e') h' xs'$
 $\text{sim-move10 } P t ta1 (e1;;e2) (e1';;e2) (e;;e2') h xs ta (e';;e2') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{if } (e1) e2 \text{ else } e3) (\text{if } (e1') e2 \text{ else } e3) (\text{if } (e) e2' \text{ else } e3') h xs ta (\text{if } (e') e2'$
 $\text{else } e3') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{throw } e1) (\text{throw } e1') (\text{throw } e) h xs ta (\text{throw } e') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{try } e1 \text{ catch}(C V) e2) (\text{try } e1' \text{ catch}(C V) e2) (\text{try } e \text{ catch}(C' V') e2') h xs$
 $ta (\text{try } e' \text{ catch}(C' V') e2') h' xs'$
 $\langle \text{proof} \rangle$

lemma *sim-moves10-expr*:

$\text{sim-move10 } P t ta1 e1 e1' e h xs ta e' h' xs' \implies \text{sim-moves10 } P t ta1 (e1 \# es2) (e1' \# es2) (e$
 $\# es2') h xs ta (e' \# es2') h' xs'$
 $\text{sim-moves10 } P t ta1 es1 es1' es h xs ta es' h' xs' \implies \text{sim-moves10 } P t ta1 (\text{Val } v \# es1) (\text{Val } v \#$

$es1')$ (Val $v \# es$) $h xs ta$ (Val $v \# es'$) $h' xs'$
 ⟨proof⟩

lemma *sim-move10-CallParams*:

$sim-moves10 P t ta1 es1 es1' es h xs ta es' h' xs'$
 $\implies sim-move10 P t ta1 (Val v \cdot M(es1)) (Val v \cdot M(es1')) (Val v \cdot M(es)) h xs ta (Val v \cdot M(es')) h' xs'$
 ⟨proof⟩

lemma *sim-move10-Block*:

$sim-move10 P t ta1 e1 e1' e h (xs(V' := vo)) ta e' h' xs'$
 $\implies sim-move10 P t ta1 (\{V:T=None; e1\}) (\{V:T=None; e1'\}) (\{V':T=vo; e\}) h xs ta (\{V':T=xs' V'; e'\}) h' (xs'(V' := xs V'))$
 ⟨proof⟩

lemma *sim-move10-reds*:

$\llbracket (h', a) \in allocate\ h\ (Class\text{-}type\ C); ta1 = \{\!\! \{NewHeapElem\ a\ (Class\text{-}type\ C)\!\!\}; ta = \{\!\! \{NewHeapElem\ a\ (Class\text{-}type\ C)\!\!\} \rrbracket$
 $\implies sim-move10 P t ta1 (new\ C) e1' (new\ C) h xs ta (addr\ a) h' xs$
 $allocate\ h\ (Class\text{-}type\ C) = \{\!\! \{ \implies sim-move10 P t \varepsilon (new\ C) e1' (new\ C) h xs \varepsilon (THROW\ OutOfMemory) h xs$
 $\llbracket (h', a) \in allocate\ h\ (Array\text{-}type\ T\ (nat\ (sint\ i))); 0 \leq i;$
 $ta1 = \{\!\! \{NewHeapElem\ a\ (Array\text{-}type\ T\ (nat\ (sint\ i))\!\!\}; ta = \{\!\! \{NewHeapElem\ a\ (Array\text{-}type\ T\ (nat\ (sint\ i))\!\!\} \rrbracket$
 $\implies sim-move10 P t ta1 (newA\ T[Val\ (Intg\ i)]) e1' (newA\ T[Val\ (Intg\ i)]) h xs ta (addr\ a) h' xs$
 $i < s\ 0 \implies sim-move10 P t \varepsilon (newA\ T[Val\ (Intg\ i)]) e1' (newA\ T[Val\ (Intg\ i)]) h xs \varepsilon (THROW\ NegativeArraySize) h xs$
 $\llbracket allocate\ h\ (Array\text{-}type\ T\ (nat\ (sint\ i))) = \{\!\! \{; 0 \leq i$
 $\implies sim-move10 P t \varepsilon (newA\ T[Val\ (Intg\ i)]) e1' (newA\ T[Val\ (Intg\ i)]) h xs \varepsilon (THROW\ OutOfMemory) h xs$
 $\llbracket typeof_h\ v = \lfloor U \rfloor; P \vdash U \leq T \rrbracket$
 $\implies sim-move10 P t \varepsilon (Cast\ T\ (Val\ v)) e1' (Cast\ T\ (Val\ v)) h xs \varepsilon (Val\ v) h xs$
 $\llbracket typeof_h\ v = \lfloor U \rfloor; \neg P \vdash U \leq T \rrbracket$
 $\implies sim-move10 P t \varepsilon (Cast\ T\ (Val\ v)) e1' (Cast\ T\ (Val\ v)) h xs \varepsilon (THROW\ ClassCast) h xs$
 $\llbracket typeof_h\ v = \lfloor U \rfloor; b \longleftrightarrow v \neq Null \wedge P \vdash U \leq T \rrbracket$
 $\implies sim-move10 P t \varepsilon ((Val\ v)\ instanceof\ T) e1' ((Val\ v)\ instanceof\ T) h xs \varepsilon (Val\ (Bool\ b)) h xs$
 $binop\ bop\ v1\ v2 = Some\ (Inl\ v) \implies sim-move10 P t \varepsilon ((Val\ v1) \llbracket bop \rrbracket (Val\ v2)) e1' (Val\ v1 \llbracket bop \rrbracket Val\ v2) h xs \varepsilon (Val\ v) h xs$
 $binop\ bop\ v1\ v2 = Some\ (Inr\ a) \implies sim-move10 P t \varepsilon ((Val\ v1) \llbracket bop \rrbracket (Val\ v2)) e1' (Val\ v1 \llbracket bop \rrbracket Val\ v2) h xs \varepsilon (Throw\ a) h xs$
 $xs\ V = \lfloor v \rfloor \implies sim-move10 P t \varepsilon (Var\ V') e1' (Var\ V) h xs \varepsilon (Val\ v) h xs$
 $sim-move10 P t \varepsilon (V' := Val\ v) e1' (V := Val\ v) h xs \varepsilon unit\ h\ (xs(V \mapsto v))$
 $sim-move10 P t \varepsilon (null[Val\ v]) e1' (null[Val\ v]) h xs \varepsilon (THROW\ NullPointer) h xs$
 $\llbracket typeof\text{-}addr\ h\ a = \lfloor Array\text{-}type\ T\ n \rfloor; i < s\ 0 \vee sint\ i \geq int\ n \rrbracket$
 $\implies sim-move10 P t \varepsilon (addr\ a[Val\ (Intg\ i)]) e1' ((addr\ a)[Val\ (Intg\ i)]) h xs \varepsilon (THROW\ ArrayIndexOutOfBounds) h xs$
 $\llbracket typeof\text{-}addr\ h\ a = \lfloor Array\text{-}type\ T\ n \rfloor; 0 \leq i; sint\ i < int\ n;$
 $heap\text{-}read\ h\ a\ (ACell\ (nat\ (sint\ i)))\ v;$
 $ta1 = \{\!\! \{ReadMem\ a\ (ACell\ (nat\ (sint\ i)))\ v\!\!\}; ta = \{\!\! \{ReadMem\ a\ (ACell\ (nat\ (sint\ i)))\ v\!\!\} \rrbracket$
 $\implies sim-move10 P t ta1 (addr\ a[Val\ (Intg\ i)]) e1' ((addr\ a)[Val\ (Intg\ i)]) h xs ta (Val\ v) h xs$
 $sim-move10 P t \varepsilon (null[Val\ v] := Val\ v') e1' (null[Val\ v] := Val\ v') h xs \varepsilon (THROW\ NullPointer) h xs$
 $\llbracket typeof\text{-}addr\ h\ a = \lfloor Array\text{-}type\ T\ n \rfloor; i < s\ 0 \vee sint\ i \geq int\ n \rrbracket$
 $\implies sim-move10 P t \varepsilon (AAss\ (addr\ a)\ (Val\ (Intg\ i))\ (Val\ v)) e1' (AAss\ (addr\ a)\ (Val\ (Intg\ i))\ (Val\ v)) h xs \varepsilon (THROW\ ArrayIndexOutOfBounds) h xs$

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor; 0 \leq i; \text{sint } i < \text{int } n; \text{typeof}_h \ v = \lfloor U \rfloor; \neg (P \vdash U \leq T) \rrbracket$
 $\implies \text{sim-move10 } P \ t \ \varepsilon \ (AAss \ (\text{addr } a) \ (\text{Val } (\text{Intg } i)) \ (\text{Val } v)) \ e1' \ (AAss \ (\text{addr } a) \ (\text{Val } (\text{Intg } i)) \ (\text{Val } v)) \ h \ xs \ \varepsilon \ (\text{THROW } \text{ArrayStore}) \ h \ xs$

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor; 0 \leq i; \text{sint } i < \text{int } n; \text{typeof}_h \ v = \text{Some } U; P \vdash U \leq T; \text{heap-write } h \ a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ v \ h';$
 $\text{ta1} = \{\!\!| \text{WriteMem } a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ v \!\!\}; \text{ta} = \{\!\!| \text{WriteMem } a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ v \!\!\}$
 $\implies \text{sim-move10 } P \ t \ \text{ta1} \ (AAss \ (\text{addr } a) \ (\text{Val } (\text{Intg } i)) \ (\text{Val } v)) \ e1' \ (AAss \ (\text{addr } a) \ (\text{Val } (\text{Intg } i)) \ (\text{Val } v)) \ h \ xs \ \text{ta} \ \text{unit} \ h' \ xs$

$\text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor \implies \text{sim-move10 } P \ t \ \varepsilon \ (\text{addr } a \cdot \text{length}) \ e1' \ (\text{addr } a \cdot \text{length}) \ h \ xs \ \varepsilon \ (\text{Val } (\text{Intg } (\text{word-of-nat } n))) \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{null} \cdot \text{length}) \ e1' \ (\text{null} \cdot \text{length}) \ h \ xs \ \varepsilon \ (\text{THROW } \text{NullPointer}) \ h \ xs$

$\llbracket \text{heap-read } h \ a \ (\text{CField } D \ F) \ v; \text{ta1} = \{\!\!| \text{ReadMem } a \ (\text{CField } D \ F) \ v \!\!\}; \text{ta} = \{\!\!| \text{ReadMem } a \ (\text{CField } D \ F) \ v \!\!\} \rrbracket$
 $\implies \text{sim-move10 } P \ t \ \text{ta1} \ (\text{addr } a \cdot F\{D\}) \ e1' \ (\text{addr } a \cdot F\{D\}) \ h \ xs \ \text{ta} \ (\text{Val } v) \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{null} \cdot F\{D\}) \ e1' \ (\text{null} \cdot F\{D\}) \ h \ xs \ \varepsilon \ (\text{THROW } \text{NullPointer}) \ h \ xs$

$\llbracket \text{heap-write } h \ a \ (\text{CField } D \ F) \ v \ h'; \text{ta1} = \{\!\!| \text{WriteMem } a \ (\text{CField } D \ F) \ v \!\!\}; \text{ta} = \{\!\!| \text{WriteMem } a \ (\text{CField } D \ F) \ v \!\!\} \rrbracket$
 $\implies \text{sim-move10 } P \ t \ \text{ta1} \ (\text{addr } a \cdot F\{D\} := \text{Val } v) \ e1' \ (\text{addr } a \cdot F\{D\} := \text{Val } v) \ h \ xs \ \text{ta} \ \text{unit} \ h' \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{null} \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v')) \ e1' \ (\text{null} \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v')) \ h \ xs \ \varepsilon \ (\text{THROW } \text{NullPointer}) \ h \ xs$

$\llbracket \text{heap-read } h \ a \ (\text{CField } D \ F) \ v''; \text{heap-write } h \ a \ (\text{CField } D \ F) \ v' \ h'; v'' = v;$
 $\text{ta1} = \{\!\!| \text{ReadMem } a \ (\text{CField } D \ F) \ v'', \text{WriteMem } a \ (\text{CField } D \ F) \ v' \!\!\}; \text{ta} = \{\!\!| \text{ReadMem } a \ (\text{CField } D \ F) \ v'', \text{WriteMem } a \ (\text{CField } D \ F) \ v' \!\!\} \rrbracket$
 $\implies \text{sim-move10 } P \ t \ \text{ta1} \ (\text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v')) \ e1' \ (\text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v')) \ h \ xs \ \text{ta} \ \text{true} \ h' \ xs$

$\llbracket \text{heap-read } h \ a \ (\text{CField } D \ F) \ v''; v'' \neq v;$
 $\text{ta1} = \{\!\!| \text{ReadMem } a \ (\text{CField } D \ F) \ v'' \!\!\}; \text{ta} = \{\!\!| \text{ReadMem } a \ (\text{CField } D \ F) \ v'' \!\!\} \rrbracket$
 $\implies \text{sim-move10 } P \ t \ \text{ta1} \ (\text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v')) \ e1' \ (\text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v')) \ h \ xs \ \text{ta} \ \text{false} \ h \ xs$

$\text{sim-move10 } P \ t \ \varepsilon \ (\text{null} \cdot F\{D\} := \text{Val } v) \ e1' \ (\text{null} \cdot F\{D\} := \text{Val } v) \ h \ xs \ \varepsilon \ (\text{THROW } \text{NullPointer}) \ h \ xs$

$\text{sim-move10 } P \ t \ \varepsilon \ (\{V':T=\text{None}; \text{Val } u\}) \ e1' \ (\{V':T=\text{vo}; \text{Val } u\}) \ h \ xs \ \varepsilon \ (\text{Val } u) \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (\{V':T=\lfloor v \rfloor; e\}) \ (\{V':T=\text{None}; e\}) \ (\{V':T=\text{vo}; e'\}) \ h \ xs \ \varepsilon \ (\{V':T=\text{vo}; e'\}) \ h \ xs$

$\text{sim-move10 } P \ t \ \varepsilon \ (\text{sync}_{V'}(\text{null}) \ e0) \ e1' \ (\text{sync}(\text{null}) \ e1) \ h \ xs \ \varepsilon \ (\text{THROW } \text{NullPointer}) \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{Val } v;; e0) \ e1' \ (\text{Val } v;; e1) \ h \ xs \ \varepsilon \ e1 \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{if } (\text{true}) \ e0 \ \text{else } e0') \ e1' \ (\text{if } (\text{true}) \ e1 \ \text{else } e2) \ h \ xs \ \varepsilon \ e1 \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{if } (\text{false}) \ e0 \ \text{else } e0') \ e1' \ (\text{if } (\text{false}) \ e1 \ \text{else } e2) \ h \ xs \ \varepsilon \ e2 \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{throw } \text{null}) \ e1' \ (\text{throw } \text{null}) \ h \ xs \ \varepsilon \ (\text{THROW } \text{NullPointer}) \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{try } (\text{Val } v) \ \text{catch}(C \ V') \ e0) \ e1' \ (\text{try } (\text{Val } v) \ \text{catch}(C \ V) \ e1) \ h \ xs \ \varepsilon \ (\text{Val } v) \ h \ xs$

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } D \rfloor; P \vdash D \leq^* C \rrbracket$
 $\implies \text{sim-move10 } P \ t \ \varepsilon \ (\text{try } (\text{Throw } a) \ \text{catch}(C \ V') \ e0) \ e1' \ (\text{try } (\text{Throw } a) \ \text{catch}(C \ V) \ e1) \ h \ xs \ \varepsilon \ (\{V:\text{Class } C=\lfloor \text{Addr } a \rfloor; e1\}) \ h \ xs$

$\text{sim-move10 } P \ t \ \varepsilon \ (\text{newA } T \lfloor \text{Throw } a \rfloor) \ e1' \ (\text{newA } T \lfloor \text{Throw } a \rfloor) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{Cast } T \ (\text{Throw } a)) \ e1' \ (\text{Cast } T \ (\text{Throw } a)) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ ((\text{Throw } a) \ \text{instanceof } T) \ e1' \ ((\text{Throw } a) \ \text{instanceof } T) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ ((\text{Throw } a) \ \llbracket \text{bop} \rrbracket \ e0) \ e1' \ ((\text{Throw } a) \ \llbracket \text{bop} \rrbracket \ e1) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{Val } v \ \llbracket \text{bop} \rrbracket \ (\text{Throw } a)) \ e1' \ (\text{Val } v \ \llbracket \text{bop} \rrbracket \ (\text{Throw } a)) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (V' := \text{Throw } a) \ e1' \ (V := \text{Throw } a) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{Throw } a \lfloor e0 \rfloor) \ e1' \ (\text{Throw } a \lfloor e1 \rfloor) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{Val } v \lfloor \text{Throw } a \rfloor) \ e1' \ (\text{Val } v \lfloor \text{Throw } a \rfloor) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{Throw } a \lfloor e0 \rfloor := e0') \ e1' \ (\text{Throw } a \lfloor e1 \rfloor := e2) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{Val } v \lfloor \text{Throw } a \rfloor := e0) \ e1' \ (\text{Val } v \lfloor \text{Throw } a \rfloor := e1) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$

$sim-move10 P t \varepsilon (Val v \lfloor Val v' \rfloor := Throw a) e1' (Val v \lfloor Val v' \rfloor := Throw a) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (Throw a \cdot length) e1' (Throw a \cdot length) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (Throw a \cdot F\{D\}) e1' (Throw a \cdot F\{D\}) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (Throw a \cdot F\{D\} := e0) e1' (Throw a \cdot F\{D\} := e1) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (Val v \cdot F\{D\} := Throw a) e1' (Val v \cdot F\{D\} := Throw a) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (CompareAndSwap (Throw a) D F e0 e0') e1' (Throw a \cdot compareAndSwap(D \cdot F, e1'', e1''')) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (CompareAndSwap (Val v) D F (Throw a) e0') e1' (Val v \cdot compareAndSwap(D \cdot F, Throw a, e1''')) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (CompareAndSwap (Val v) D F (Val v') (Throw a)) e1' (Val v \cdot compareAndSwap(D \cdot F, Val v', Throw a)) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (Throw a \cdot M(es0)) e1' (Throw a \cdot M(es1)) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (Val v \cdot M(map Val vs @ Throw a \# es0)) e1' (Val v \cdot M(map Val vs @ Throw a \# es1)) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (\{V':T=None; Throw a\}) e1' (\{V:T=vo; Throw a\}) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (sync_{V'}(Throw a) e0) e1' (sync(Throw a) e1) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (Throw a;;e0) e1' (Throw a;;e1) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (if (Throw a) e0 else e0') e1' (if (Throw a) e1 else e2) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (throw (Throw a)) e1' (throw (Throw a)) h xs \varepsilon (Throw a) h xs$
 <proof>

lemma *sim-move10-CallNull*:

$sim-move10 P t \varepsilon (null \cdot M(map Val vs)) e1' (null \cdot M(map Val vs)) h xs \varepsilon (THROW NullPointer) h xs$
 <proof>

lemma *sim-move10-SyncLocks*:

$\llbracket ta1 = \{\!| Lock \rightarrow a, SyncLock a \!| \}; ta = \{\!| Lock \rightarrow a, SyncLock a \!| \} \rrbracket$
 $\implies sim-move10 P t ta1 (sync_{V'}(addr a) e0) e1' (sync(addr a) e1) h xs ta (insync(a) e1) h xs$
 $\llbracket ta1 = \{\!| Unlock \rightarrow a, SyncUnlock a \!| \}; ta = \{\!| Unlock \rightarrow a, SyncUnlock a \!| \} \rrbracket$
 $\implies sim-move10 P t ta1 (insync_{V'}(a') (Val v)) e1' (insync(a) (Val v)) h xs ta (Val v) h xs$
 $\llbracket ta1 = \{\!| Unlock \rightarrow a, SyncUnlock a \!| \}; ta = \{\!| Unlock \rightarrow a, SyncUnlock a \!| \} \rrbracket$
 $\implies sim-move10 P t ta1 (insync_{V'}(a') (Throw a'')) e1' (insync(a) (Throw a'')) h xs ta (Throw a'')$
 $h xs$
 <proof>

lemma *sim-move10-TryFail*:

$\llbracket typeof-addr h a = \lfloor Class-type D \rfloor; \neg P \vdash D \preceq^* C \rrbracket$
 $\implies sim-move10 P t \varepsilon (try (Throw a) catch(C V') e0) e1' (try (Throw a) catch(C V) e1) h xs \varepsilon (Throw a) h xs$
 <proof>

lemmas *sim-move10-intros =*

sim-move10-expr sim-move10-reds sim-move10-CallNull sim-move10-TryFail sim-move10-Block sim-move10-CallParams

lemma *sim-move10-preserves-defass*:

assumes *wf: wf-J-prog P*
shows $\llbracket sim-move10 P t ta1 e1 e1' e h xs ta e' h' xs'; D e \lfloor dom xs \rfloor \rrbracket \implies D e' \lfloor dom xs' \rfloor$
 <proof>

declare *sim-move10-intros[intro]*

lemma **assumes** *wf: wf-J-prog P*

shows *red-simulates-red1-aux*:

$\llbracket \text{False}, \text{compP1 } P, t \vdash 1 \langle e1, S \rangle - \text{TA} \rightarrow \langle e1', S' \rangle; \text{bisim } vs \ e2 \ e1 \ (\text{lcl } S); \text{fv } e2 \subseteq \text{set } vs;$
 $x \subseteq_m [vs \mapsto] \text{lcl } S]; \text{length } vs + \text{max-vars } e1 \leq \text{length } (\text{lcl } S);$
 $\mathcal{D} \ e2 \ [\text{dom } x] \rrbracket$
 $\implies \exists ta \ e2' \ x'. \text{sim-move10 } P \ t \ \text{TA} \ e1 \ e1' \ e2 \ (\text{hp } S) \ x \ ta \ e2' \ (\text{hp } S') \ x' \wedge \text{bisim } vs \ e2' \ e1' \ (\text{lcl } S')$
 $\wedge x' \subseteq_m [vs \mapsto] \text{lcl } S'$
 $(\text{is } \llbracket -; -; -; -; - \rrbracket \implies ?\text{concl } e1 \ e1' \ e2 \ S \ x \ \text{TA} \ S' \ e1' \ vs)$

and *reds-simulates-reds1-aux*:

$\llbracket \text{False}, \text{compP1 } P, t \vdash 1 \langle es1, S \rangle [-\text{TA} \rightarrow] \langle es1', S' \rangle; \text{bisims } vs \ es2 \ es1 \ (\text{lcl } S); \text{fvs } es2 \subseteq \text{set } vs;$
 $x \subseteq_m [vs \mapsto] \text{lcl } S]; \text{length } vs + \text{max-varss } es1 \leq \text{length } (\text{lcl } S);$
 $\mathcal{D} \ s \ es2 \ [\text{dom } x] \rrbracket$
 $\implies \exists ta \ es2' \ x'. \text{sim-moves10 } P \ t \ \text{TA} \ es1 \ es1' \ es2 \ (\text{hp } S) \ x \ ta \ es2' \ (\text{hp } S') \ x' \wedge \text{bisims } vs \ es2' \ es1'$
 $(\text{lcl } S') \wedge x' \subseteq_m [vs \mapsto] \text{lcl } S'$
 $(\text{is } \llbracket -; -; -; -; - \rrbracket \implies ?\text{concls } es1 \ es1' \ es2 \ S \ x \ \text{TA} \ S' \ es1' \ vs)$
 $\langle \text{proof} \rangle$

lemma *bisim-call-Some-call1*:

$\llbracket \text{bisim } Vs \ e \ e' \ xs; \text{call } e = \lfloor aMvs \rfloor; \text{length } Vs + \text{max-vars } e' \leq \text{length } xs \rrbracket$
 $\implies \exists e'' \ xs'. \tau \text{red1}'r \ P \ t \ h \ (e', xs) \ (e'', xs') \wedge \text{call1 } e'' = \lfloor aMvs \rfloor \wedge$
 $\text{bisim } Vs \ e \ e'' \ xs' \wedge \text{take } (\text{length } Vs) \ xs = \text{take } (\text{length } Vs) \ xs'$

and *bisims-calls-Some-calls1*:

$\llbracket \text{bisims } Vs \ es \ es' \ xs; \text{calls } es = \lfloor aMvs \rfloor; \text{length } Vs + \text{max-varss } es' \leq \text{length } xs \rrbracket$
 $\implies \exists es'' \ xs'. \tau \text{reds1}'r \ P \ t \ h \ (es', xs) \ (es'', xs') \wedge \text{calls1 } es'' = \lfloor aMvs \rfloor \wedge$
 $\text{bisims } Vs \ es \ es'' \ xs' \wedge \text{take } (\text{length } Vs) \ xs = \text{take } (\text{length } Vs) \ xs'$

$\langle \text{proof} \rangle$

lemma *sim-move01-into-Red1*:

$\text{sim-move01 } P \ t \ ta \ e \ E' \ h \ xs \ ta' \ e2' \ h' \ xs'$
 $\implies \text{if } \tau \text{Move0 } P \ h \ (e, es1)$
 $\text{then } \tau \text{Red1}'t \ P \ t \ h \ ((E', xs), \text{exs2}) \ ((e2', xs'), \text{exs2}) \wedge ta = \varepsilon \wedge h = h'$
 $\text{else } \exists \text{ex2}' \ \text{exs2}' \ ta'. \tau \text{Red1}'r \ P \ t \ h \ ((E', xs), \text{exs2}) \ (\text{ex2}', \text{exs2}') \wedge$
 $(\text{call } e = \text{None} \vee \text{call1 } E' = \text{None} \vee \text{ex2}' = (E', xs) \wedge \text{exs2}' = \text{exs2}) \wedge$
 $\text{False}, P, t \vdash 1 \langle \text{ex2}'/\text{exs2}', h \rangle - \text{ta}' \rightarrow \langle (e2', xs')/\text{exs2}', h \rangle \wedge$
 $\neg \tau \text{Move1 } P \ h \ (\text{ex2}', \text{exs2}') \wedge \text{ta-bisim01 } ta \ ta'$

$\langle \text{proof} \rangle$

lemma *sim-move01-max-vars-decr*:

$\text{sim-move01 } P \ t \ ta \ e0 \ e \ h \ xs \ ta' \ e' \ h' \ xs' \implies \text{max-vars } e' \leq \text{max-vars } e$

$\langle \text{proof} \rangle$

lemma *Red1-simulates-red0*:

assumes *wf*: *wf-J-prog* *P*

and *red*: $P, t \vdash 0 \langle e1/es1, h \rangle - \text{ta} \rightarrow \langle e1'/es1', h' \rangle$

and *bisiml*: *bisim-list1* (*e1*, *es1*) (*ex2*, *exs2*)

shows $\exists \text{ex2}'' \ \text{exs2}''. \text{bisim-list1} \ (e1', es1') \ (\text{ex2}'', \text{exs2}'') \wedge$

(*if* $\tau \text{Move0 } P \ h \ (e1, es1)$

then $\tau \text{Red1}'t \ (\text{compP1 } P) \ t \ h \ (\text{ex2}, \text{exs2}) \ (\text{ex2}'', \text{exs2}'') \wedge \text{ta} = \varepsilon \wedge h = h'$

else $\exists \text{ex2}' \ \text{exs2}' \ ta'. \tau \text{Red1}'r \ (\text{compP1 } P) \ t \ h \ (\text{ex2}, \text{exs2}) \ (\text{ex2}', \text{exs2}') \wedge$

$(\text{call } e1 = \text{None} \vee \text{call1} \ (\text{fst } \text{ex2}) = \text{None} \vee \text{ex2}' = \text{ex2} \wedge \text{exs2}' = \text{exs2}) \wedge$

$\text{False}, \text{compP1 } P, t \vdash 1 \langle \text{ex2}'/\text{exs2}', h \rangle - \text{ta}' \rightarrow \langle \text{ex2}''/\text{exs2}'', h' \rangle \wedge$

$\neg \tau \text{Move1 } P \ h \ (\text{ex2}', \text{exs2}') \wedge \text{ta-bisim01 } ta \ ta'$)

(**is** $\exists \text{ex2}'' \ \text{exs2}'' . - \wedge ?\text{red } \text{ex2}'' \ \text{exs2}''$)

$\langle \text{proof} \rangle$

lemma *sim-move10-into-red0*:

assumes *wf*: *wf-J-prog P*

and *sim*: *sim-move10 P t ta e2 e2' e h Map.empty ta' e' h' x'*

and *fv*: *fv e = {}*

shows *if* $\tau\text{move1 } P \ h \ e2$

then $(\tau\text{Red0t } P \ t \ h \ (e, es) \ (e', es) \vee \text{countInitBlock } e2' < \text{countInitBlock } e2 \wedge e' = e \wedge x' = \text{Map.empty}) \wedge ta = \varepsilon \wedge h = h'$

else $\exists e'' \ ta'. \tau\text{Red0r } P \ t \ h \ (e, es) \ (e'', es) \wedge$

$(\text{call1 } e2 = \text{None} \vee \text{call } e = \text{None} \vee e'' = e) \wedge$

$P, t \vdash 0 \langle e''/es, h \rangle \text{---} ta' \rightarrow \langle e'/es, h' \rangle \wedge$

$\neg \tau\text{Move0 } P \ h \ (e'', es) \wedge ta\text{-bisim01 } ta' \ (\text{extTA2J1 } (\text{compP1 } P) \ ta)$

<proof>

lemma *red0-simulates-Red1*:

assumes *wf*: *wf-J-prog P*

and *red*: *False, compP1 P, t \vdash 1 \langle ex2/exs2, h \rangle \text{---} ta \rightarrow \langle ex2'/exs2', h' \rangle*

and *bisiml*: *bisim-list1 (e, es) (ex2, exs2)*

shows $\exists e' \ es'. \text{bisim-list1 } (e', es') \ (ex2', exs2') \wedge$

(if $\tau\text{Move1 } P \ h \ (ex2, exs2)$

then $(\tau\text{Red0t } P \ t \ h \ (e, es) \ (e', es') \vee \text{countInitBlock } (\text{fst } ex2') < \text{countInitBlock } (\text{fst } ex2) \wedge exs2' = exs2 \wedge e' = e \wedge es' = es) \wedge$

$ta = \varepsilon \wedge h = h'$

else $\exists e'' \ es'' \ ta'. \tau\text{Red0r } P \ t \ h \ (e, es) \ (e'', es'') \wedge$

$(\text{call1 } (\text{fst } ex2) = \text{None} \vee \text{call } e = \text{None} \vee e'' = e \wedge es'' = es) \wedge$

$P, t \vdash 0 \langle e''/es'', h \rangle \text{---} ta' \rightarrow \langle e'/es', h' \rangle \wedge$

$\neg \tau\text{Move0 } P \ h \ (e'', es'') \wedge ta\text{-bisim01 } ta' \ ta)$

(is $\exists e' \ es' . - \wedge ?\text{red } e' \ es')$

<proof>

end

sublocale *J0-J1-heap-base < red0-Red1'*: *FWdelay-bisimulation-base*

final-expr0

mred0 P

final-expr1

mred1' (compP1 P)

convert-RA

$\lambda t. \text{bisim-red0-Red1}$

bisim-wait01

$\tau\text{MOVE0 } P$

$\tau\text{MOVE1 } (\text{compP1 } P)$

for *P*

<proof>

context *J0-J1-heap-base begin*

lemma *delay-bisimulation-red0-Red1*:

assumes *wf*: *wf-J-prog P*

shows *delay-bisimulation-measure (mred0 P t) (mred1' (compP1 P) t) bisim-red0-Red1 (ta-bisim*
($\lambda t. \text{bisim-red0-Red1}$)) ($\tau\text{MOVE0 } P$) ($\tau\text{MOVE1 } (\text{compP1 } P)$) ($\lambda es \ es'. \text{False}$) ($\lambda(((e', xs'), exs'), h')$)
(($(e, xs), exs$), h). countInitBlock e' < countInitBlock e)

(is *delay-bisimulation-measure - - - - - ? $\mu1$? $\mu2$)*

<proof>

lemma *delay-bisimulation-diverge-red0-Red1*:

assumes *wf-J-prog P*

shows *delay-bisimulation-diverge (mred0 P t) (mred1' (compP1 P) t) bisim-red0-Red1 (ta-bisim (λt. bisim-red0-Red1)) (τMOVE0 P) (τMOVE1 (compP1 P))*

<proof>

lemma *red0-Red1'-FWweak-bisim*:

assumes *wf: wf-J-prog P*

shows *FWdelay-bisimulation-diverge final-expr0 (mred0 P) final-expr1 (mred1' (compP1 P)) (λt. bisim-red0-Red1) bisim-wait01 (τMOVE0 P) (τMOVE1 (compP1 P))*

<proof>

lemma *bisim-J0-J1-start*:

assumes *wf: wf-J-prog P*

and *start: wf-start-state P C M vs*

shows *red0-Red1'.mbisim (J0-start-state P C M vs) (J1-start-state (compP1 P) C M vs)*

<proof>

end

end

7.25 Preservation of well-formedness from source code to intermediate language

theory *JJ1WellForm imports*

../J/JWellForm

J1WellForm

Compiler1

begin

The compiler preserves well-formedness. Is less trivial than it may appear. We start with two simple properties: preservation of well-typedness

lemma **assumes** *wf: wf-prog wfmd P*

shows *compE1-pres-wt: [P, [Vs[\mapsto]Ts] \vdash e :: U; size Ts = size Vs] \implies compP f P, Ts \vdash 1 compE1 Vs e :: U*

and *compEs1-pres-wt: [P, [Vs[\mapsto]Ts] \vdash es [::] Us; size Ts = size Vs] \implies compP f P, Ts \vdash 1 compEs1 Vs es [::] Us*

<proof>

and the correct block numbering:

The main complication is preservation of definite assignment \mathcal{D} .

lemma **fixes** *e :: 'addr expr and es :: 'addr expr list*

shows *A-compE1-None[simp]: $\mathcal{A} e = \text{None} \implies \mathcal{A} (\text{compE1 } Vs e) = \text{None}$*

and *As-compEs1-None: $\mathcal{A}s es = \text{None} \implies \mathcal{A}s (\text{compEs1 } Vs es) = \text{None}$*

<proof>

lemma **fixes** *e :: 'addr expr and es :: 'addr expr list*

shows *A-compE1: [$\mathcal{A} e = \lfloor A \rfloor$; $fv e \subseteq set Vs$] $\implies \mathcal{A} (\text{compE1 } Vs e) = \lfloor index Vs ' A \rfloor$*

and *As-compEs1: [$\mathcal{A}s es = \lfloor A \rfloor$; $fvs es \subseteq set Vs$] $\implies \mathcal{A}s (\text{compEs1 } Vs es) = \lfloor index Vs ' A \rfloor$*

<proof>

lemma *fixes* $e :: ('a, 'b, 'addr) \text{exp}$ **and** $es :: ('a, 'b, 'addr) \text{exp list}$

shows $D\text{-None}$ [iff]: $\mathcal{D} e \text{ None}$

and $Ds\text{-None}$ [iff]: $\mathcal{D}s es \text{ None}$

$\langle \text{proof} \rangle$

declare $Un\text{-ac}$ [simp]

lemma *fixes* $e :: 'addr \text{expr}$ **and** $es :: 'addr \text{expr list}$

shows $D\text{-index-compE1}$: $\llbracket A \subseteq \text{set } Vs; fv e \subseteq \text{set } Vs \rrbracket \implies \mathcal{D} e [A] \implies \mathcal{D} (\text{compE1 } Vs e) [\text{index } Vs 'A]$

and $Ds\text{-index-compEs1}$: $\llbracket A \subseteq \text{set } Vs; fvs es \subseteq \text{set } Vs \rrbracket \implies \mathcal{D}s es [A] \implies \mathcal{D}s (\text{compEs1 } Vs es) [\text{index } Vs 'A]$

$\langle \text{proof} \rangle$

declare $Un\text{-ac}$ [simp del]

lemma *index-image-set*: $\text{distinct } xs \implies \text{index } xs ' \text{set } xs = \{..<\text{size } xs\}$

$\langle \text{proof} \rangle$

lemma $D\text{-compE1}$:

$\llbracket \mathcal{D} e [\text{set } Vs]; fv e \subseteq \text{set } Vs; \text{distinct } Vs \rrbracket \implies \mathcal{D} (\text{compE1 } Vs e) [\{..<\text{length } Vs\}]$

$\langle \text{proof} \rangle$

lemma $D\text{-compE1}'$:

assumes $\mathcal{D} e [\text{set}(V\#Vs)]$ **and** $fv e \subseteq \text{set}(V\#Vs)$ **and** $\text{distinct}(V\#Vs)$

shows $\mathcal{D} (\text{compE1 } (V\#Vs) e) [\{..\text{length } Vs\}]$

$\langle \text{proof} \rangle$

lemma compP1-pres-wf : $\text{wf-J-prog } P \implies \text{wf-J1-prog } (\text{compP1 } P)$

$\langle \text{proof} \rangle$

end

theory *Compiler* **imports** *Compiler1 Compiler2* **begin**

definition $J2JVM :: 'addr \text{J-prog} \Rightarrow 'addr \text{jvm-prog}$

where [code del]: $J2JVM \equiv \text{compP2} \circ \text{compP1}$

lemma $J2JVM\text{-code}$ [code]:

$J2JVM = \text{compP } (\lambda C M Ts T (pns, body). \text{compMb2 } (\text{compE1 } (\text{this}\#pns) body))$

$\langle \text{proof} \rangle$

end

7.26 Correctness of both stages

theory *Correctness*

imports

J0Bisim

J1Deadlock

../Framework/FWBisimDeadlock

Correctness2

Correctness1Threaded

```

    Correctness1
    J1WellForm
    Compiler
begin

locale J-JVM-heap-conf-base =
  J0-J1-heap-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
+
  J1-JVM-heap-conf-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf compP1 P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr J-prog
begin

definition bisimJ2JVM ::
  (('addr, 'thread-id, 'addr expr × 'addr locals, 'heap, 'addr) state,
  ('addr, 'thread-id, 'addr option × 'addr frame list, 'heap, 'addr) state) bisim
where bisimJ2JVM = red-red0.mbisim ◦B red0-Red1'.mbisim ◦B mbisim-Red1'-Red1 ◦B Red1-execd.mbisim

definition tlsimJ2JVM ::
  ('thread-id × ('addr, 'thread-id, 'heap) J-thread-action,
  'thread-id × ('addr, 'thread-id, 'heap) jvm-thread-action) bisim
where tlsimJ2JVM = red-red0.mta-bisim ◦B red0-Red1'.mta-bisim ◦B (=) ◦B Red1-execd.mta-bisim

end

lemma compP2-has-method [simp]: compP2 P ⊢ C has M ⟷ P ⊢ C has M
⟨proof⟩

locale J-JVM-conf-read =
  J1-JVM-conf-read
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf compP1 P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set

```

```

and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'addr J-prog
begin

sublocale J-JVM-heap-conf-base <proof>

theorem bisimJ2JVM-weak-bisim:
  assumes wf: wf-J-prog P
  shows delay-bisimulation-diverge-final (mredT P) (execd-mthr.redT (J2JVM P)) bisimJ2JVM tl-
simJ2JVM
  (red-mthr.m $\tau$ move P) (execd-mthr.m $\tau$ move (J2JVM P)) red-mthr.mfinal exec-mthr.mfinal
  <proof>

lemma bisimJ2JVM-start:
  assumes wf: wf-J-prog P
  and start: wf-start-state P C M vs
  shows bisimJ2JVM (J-start-state P C M vs) (JVM-start-state (J2JVM P) C M vs)
  <proof>

end

fun exception :: 'addr expr  $\times$  'addr locals  $\Rightarrow$  'addr option  $\times$  'addr frame list
where exception (Throw a, xs) = ([a], [])
| exception - = (None, [])

definition mexception ::
  ('addr,'thread-id,'addr expr  $\times$  'addr locals,'heap,'addr) state  $\Rightarrow$ 
  ('addr,'thread-id,'addr option  $\times$  'addr frame list,'heap,'addr) state
where
   $\bigwedge$  ln. mexception s  $\equiv$ 
  (locks s, ( $\lambda$ t. case thr s t of [(e, ln)]  $\Rightarrow$  [(exception e, ln] | None  $\Rightarrow$  None, shr s), wset s, interrupts
  s)

declare compP1-def [simp del]

context J-JVM-heap-conf-base begin

lemma bisimJ2JVM-mfinal-mexception:
  assumes bisim: bisimJ2JVM s s'
  and fin: exec-mthr.mfinal s'
  and fin': red-mthr.mfinal s
  and tsNotEmpty: thr s t  $\neq$  None
  shows s' = mexception s
  <proof>

end

context J-JVM-conf-read begin

theorem J2JVM-correct1:

```

```

fixes  $C M vs$ 
defines  $s: s \equiv J\text{-start-state } P C M vs$ 
and comps:  $cs \equiv JVM\text{-start-state } (J2JVM P) C M vs$ 
assumes  $wf: wf\text{-}J\text{-prog } P$ 
and  $wf\text{-start}: wf\text{-start-state } P C M vs$ 
and  $red: red\text{-mthr.mthr.}\tau\text{Runs } P s \xi$ 
obtains  $\xi'$ 
where  $execd\text{-mthr.mthr.}\tau\text{Runs } (J2JVM P) cs \xi' tlist\text{-all2 } t\text{lsim}J2JVM (rel\text{-option } bisimJ2JVM) \xi$ 
 $\xi'$ 
and  $\bigwedge s'. \llbracket tfinite \xi; terminal \xi = \lfloor s' \rfloor; red\text{-mthr.mfinal } s' \rrbracket$ 
 $\implies tfinite \xi' \wedge terminal \xi' = \lfloor mexception s' \rfloor$ 
and  $\bigwedge s'. \llbracket tfinite \xi; terminal \xi = \lfloor s' \rfloor; red\text{-mthr.deadlock } P s' \rrbracket$ 
 $\implies \exists cs'. tfinite \xi' \wedge terminal \xi' = \lfloor cs' \rfloor \wedge execd\text{-mthr.deadlock } (J2JVM P) cs' \wedge bisimJ2JVM$ 
 $s' cs'$ 
and  $\llbracket tfinite \xi; terminal \xi = None \rrbracket \implies tfinite \xi' \wedge terminal \xi' = None$ 
and  $\neg tfinite \xi \implies \neg tfinite \xi'$ 
 $\langle proof \rangle$ 

```

theorem *J2JVM-correct2:*

```

fixes  $C M vs$ 
defines  $s: s \equiv J\text{-start-state } P C M vs$ 
and comps:  $cs \equiv JVM\text{-start-state } (J2JVM P) C M vs$ 
assumes  $wf: wf\text{-}J\text{-prog } P$ 
and  $wf\text{-start}: wf\text{-start-state } P C M vs$ 
and  $exec: execd\text{-mthr.mthr.}\tau\text{Runs } (J2JVM P) cs \xi'$ 
obtains  $\xi$ 
where  $red\text{-mthr.mthr.}\tau\text{Runs } P s \xi tlist\text{-all2 } t\text{lsim}J2JVM (rel\text{-option } bisimJ2JVM) \xi \xi'$ 
and  $\bigwedge cs'. \llbracket tfinite \xi'; terminal \xi' = \lfloor cs' \rfloor; exec\text{-mthr.mfinal } cs' \rrbracket$ 
 $\implies \exists s'. tfinite \xi \wedge terminal \xi = \lfloor s' \rfloor \wedge cs' = mexception s' \wedge bisimJ2JVM s' cs'$ 
and  $\bigwedge cs'. \llbracket tfinite \xi'; terminal \xi' = \lfloor cs' \rfloor; execd\text{-mthr.deadlock } (J2JVM P) cs' \rrbracket$ 
 $\implies \exists s'. tfinite \xi \wedge terminal \xi = \lfloor s' \rfloor \wedge red\text{-mthr.deadlock } P s' \wedge bisimJ2JVM s' cs'$ 
and  $\llbracket tfinite \xi'; terminal \xi' = None \rrbracket \implies tfinite \xi \wedge terminal \xi = None$ 
and  $\neg tfinite \xi' \implies \neg tfinite \xi$ 
 $\langle proof \rangle$ 

```

end

declare *compP1-def* [*simp*]

theorem *wt-J2JVM:* $wf\text{-}J\text{-prog } P \implies wf\text{-jvm-prog } (J2JVM P)$

$\langle proof \rangle$

end

theory *Preprocessor*

imports

PCompiler

../J/Annotate

../J/JWellForm

begin

primrec *annotate-Mb* ::

$'addr J\text{-prog} \Rightarrow cname \Rightarrow mname \Rightarrow ty list \Rightarrow ty \Rightarrow (vname list \times 'addr expr) \Rightarrow (vname list \times 'addr expr)$

where *annotate-Mb* $P C M Ts T (pns, e) = (pns, annotate P [this \# pns \mapsto] Class C \# Ts) e$

```

declare annotate-Mb.simps [simp del]

primrec annotate-Mb-code ::
  'addr J-prog  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  ty list  $\Rightarrow$  ty  $\Rightarrow$  (vname list  $\times$  'addr expr)  $\Rightarrow$  (vname list  $\times$ 
  'addr expr)
where annotate-Mb-code P C M Ts T (pns, e) = (pns, annotate-code P [this # pns [ $\mapsto$ ] Class C #
  Ts] e)
declare annotate-Mb-code.simps [simp del]

definition annotate-prog :: 'addr J-prog  $\Rightarrow$  'addr J-prog
where annotate-prog P = compP (annotate-Mb P) P

definition annotate-prog-code :: 'addr J-prog  $\Rightarrow$  'addr J-prog
where annotate-prog-code P = compP (annotate-Mb-code P) P

lemma fixes is-lub
  shows WT-compP: is-lub,P,E  $\vdash$  e :: T  $\Longrightarrow$  is-lub,compP f P,E  $\vdash$  e :: T
  and WTs-compP: is-lub,P,E  $\vdash$  es [::] Ts  $\Longrightarrow$  is-lub,compP f P,E  $\vdash$  es [::] Ts
   $\langle$ proof $\rangle$ 

lemma fixes is-lub
  shows Anno-compP: is-lub,P,E  $\vdash$  e  $\rightsquigarrow$  e'  $\Longrightarrow$  is-lub,compP f P,E  $\vdash$  e  $\rightsquigarrow$  e'
  and Annos-compP: is-lub,P,E  $\vdash$  es [ $\rightsquigarrow$ ] es'  $\Longrightarrow$  is-lub,compP f P,E  $\vdash$  es [ $\rightsquigarrow$ ] es'
   $\langle$ proof $\rangle$ 

lemma annotate-prog-code-eq-annotate-prog:
  assumes wf: wf-J-prog (annotate-prog-code P)
  shows annotate-prog-code P = annotate-prog P
   $\langle$ proof $\rangle$ 

end
theory Compiler-Main
imports
  J0
  Correctness
  Preprocessor
begin

end

```


Chapter 8

Memory Models

```
theory MM
imports
  ../Common/Heap
begin

type-synonym addr = nat
type-synonym thread-id = addr

abbreviation (input)
  addr2thread-id :: addr  $\Rightarrow$  thread-id
where addr2thread-id  $\equiv \lambda x. x$ 

abbreviation (input)
  thread-id2addr :: thread-id  $\Rightarrow$  addr
where thread-id2addr  $\equiv \lambda x. x$ 

instantiation nat :: addr begin
definition hash-addr  $\equiv$  int
definition monitor-funfun-to-list  $\equiv$  (funfun-to-list :: nat  $\Rightarrow$  f nat  $\Rightarrow$  nat list)
instance
  <proof>
end

definition new-Addr :: (addr  $\rightarrow$  'b)  $\Rightarrow$  addr option
where new-Addr h  $\equiv$  if  $\exists a. h a = \text{None}$  then Some(LEAST a. h a = None) else None

lemma new-Addr-SomeD:
  new-Addr h = Some a  $\implies$  h a = None
  <proof>

lemma new-Addr-SomeI:
  finite (dom h)  $\implies \exists a. \text{new-Addr } h = \text{Some } a$ 
  <proof>

8.0.1 Code generation

definition gen-new-Addr :: (addr  $\rightarrow$  'b)  $\Rightarrow$  addr  $\Rightarrow$  addr option
where gen-new-Addr h n  $\equiv$  if  $\exists a. a \geq n \wedge h a = \text{None}$  then Some(LEAST a. a  $\geq$  n  $\wedge$  h a = None)
  else None
```

lemma *new-Addr-code-code* [*code*]:
new-Addr h = gen-new-Addr h 0
 ⟨*proof*⟩

lemma *gen-new-Addr-code* [*code*]:
gen-new-Addr h n = (if h n = None then Some n else gen-new-Addr h (Suc n))
 ⟨*proof*⟩

end

8.1 Sequential consistency

theory *SC*

imports

../Common/Conform

MM

begin

8.1.1 Objects and Arrays

type-synonym

fields = vname × cname → addr val — field name, defining class, value

type-synonym

cells = addr val list

datatype *heapobj*

= Obj cname fields

— class instance with class name and fields

| *Arr ty fields cells*

— element type, fields (from object), and list of each cell's content

lemma *rec-heapobj* [*simp*]: *rec-heapobj = case-heapobj*
 ⟨*proof*⟩

primrec *obj-ty :: heapobj ⇒ htype*

where

obj-ty (Obj C f) = Class-type C

| *obj-ty (Arr T fs cs) = Array-type T (length cs)*

fun *is-Arr :: heapobj ⇒ bool* **where**

is-Arr (Obj C fs) = False

| *is-Arr (Arr T f el) = True*

lemma *is-Arr-conv*:

is-Arr arobj = (∃ T f el. arobj = Arr T f el)

⟨*proof*⟩

lemma *is-ArrE*:

$\llbracket is-Arr arobj; \bigwedge T f el. arobj = Arr T f el \implies thesis \rrbracket \implies thesis$

$\llbracket \neg is-Arr arobj; \bigwedge C fs. arobj = Obj C fs \implies thesis \rrbracket \implies thesis$

⟨*proof*⟩

definition $init\text{-}fields :: ('field\text{-}name \times (ty \times fmod)) list \Rightarrow 'field\text{-}name \rightarrow addr\ val$
where $init\text{-}fields \equiv map\text{-}of \circ map (\lambda(FD,(T, fm)). (FD, default\text{-}val T))$

primrec

— a new, blank object with default values in all fields:

$blank :: 'm\ prog \Rightarrow htype \Rightarrow heapobj$

where

$blank\ P\ (Class\text{-}type\ C) = Obj\ C\ (init\text{-}fields\ (fields\ P\ C))$

| $blank\ P\ (Array\text{-}type\ T\ n) = Arr\ T\ (init\text{-}fields\ (fields\ P\ Object))\ (replicate\ n\ (default\text{-}val\ T))$

lemma $obj\text{-}ty\text{-}blank$ [iff]:

$obj\text{-}ty\ (blank\ P\ hT) = hT$

$\langle proof \rangle$

8.1.2 Heap

type-synonym $heap = addr \rightarrow heapobj$

translations

$(type)\ heap \leq (type)\ nat \Rightarrow heapobj\ option$

abbreviation $sc\text{-}empty :: heap$

where $sc\text{-}empty \equiv Map.empty$

fun $the\text{-}obj :: heapobj \Rightarrow cname \times fields$ **where**

$the\text{-}obj\ (Obj\ C\ fs) = (C, fs)$

fun $the\text{-}arr :: heapobj \Rightarrow ty \times fields \times cells$ **where**

$the\text{-}arr\ (Arr\ T\ f\ el) = (T, f, el)$

abbreviation

$cname\text{-}of :: heap \Rightarrow addr \Rightarrow cname$ **where**

$cname\text{-}of\ hp\ a == fst\ (the\text{-}obj\ (the\ (hp\ a)))$

definition $sc\text{-}allocate :: 'm\ prog \Rightarrow heap \Rightarrow htype \Rightarrow (heap \times addr)\ set$

where

$sc\text{-}allocate\ P\ h\ hT =$

$(case\ new\text{-}Addr\ h\ of\ None \Rightarrow \{\})$

| $Some\ a \Rightarrow \{(h(a \mapsto blank\ P\ hT), a)\}$

definition $sc\text{-}typeof\text{-}addr :: heap \Rightarrow addr \Rightarrow htype\ option$

where $sc\text{-}typeof\text{-}addr\ h\ a = map\text{-}option\ obj\text{-}ty\ (h\ a)$

inductive $sc\text{-}heap\text{-}read :: heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow addr\ val \Rightarrow bool$

for $h :: heap$ **and** $a :: addr$

where

$Obj: \llbracket h\ a = \llbracket Obj\ C\ fs \rrbracket; fs\ (F, D) = \llbracket v \rrbracket \rrbracket \Longrightarrow sc\text{-}heap\text{-}read\ h\ a\ (CField\ D\ F)\ v$

| $Arr: \llbracket h\ a = \llbracket Arr\ T\ f\ el \rrbracket; n < length\ el \rrbracket \Longrightarrow sc\text{-}heap\text{-}read\ h\ a\ (ACell\ n)\ (el\ !\ n)$

| $ArrObj: \llbracket h\ a = \llbracket Arr\ T\ f\ el \rrbracket; f\ (F, Object) = \llbracket v \rrbracket \rrbracket \Longrightarrow sc\text{-}heap\text{-}read\ h\ a\ (CField\ Object\ F)\ v$

hide-fact (open) $Obj\ Arr\ ArrObj$

inductive-cases $sc\text{-}heap\text{-}read\text{-}cases$ [elim!]:

sc-heap-read $h\ a\ (CField\ C\ F)\ v$
sc-heap-read $h\ a\ (ACell\ n)\ v$

inductive *sc-heap-write* $::\ heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow addr\ val \Rightarrow heap \Rightarrow bool$
for $h :: heap$ **and** $a :: addr$

where

Obj: $\llbracket h\ a = [Obj\ C\ fs];\ h' = h(a \mapsto Obj\ C\ (fs((F, D) \mapsto v))) \rrbracket \Longrightarrow sc\text{-}heap\text{-}write\ h\ a\ (CField\ D\ F)\ v\ h'$

| *Arr*: $\llbracket h\ a = [Arr\ T\ f\ el];\ h' = h(a \mapsto Arr\ T\ f\ (el[n := v])) \rrbracket \Longrightarrow sc\text{-}heap\text{-}write\ h\ a\ (ACell\ n)\ v\ h'$

| *ArrObj*: $\llbracket h\ a = [Arr\ T\ f\ el];\ h' = h(a \mapsto Arr\ T\ (f((F, Object) \mapsto v))\ el) \rrbracket \Longrightarrow sc\text{-}heap\text{-}write\ h\ a\ (CField\ Object\ F)\ v\ h'$

hide-fact (**open**) *Obj Arr ArrObj*

inductive-cases *sc-heap-write-cases* [*elim!*]:

sc-heap-write $h\ a\ (CField\ C\ F)\ v\ h'$

sc-heap-write $h\ a\ (ACell\ n)\ v\ h'$

consts *sc-spurious-wakeups* $::\ bool$

interpretation *sc*:

heap-base

addr2thread-id

thread-id2addr

sc-spurious-wakeups

sc-empty

sc-allocate P

sc-typeof-addr

sc-heap-read

sc-heap-write

for P $\langle proof \rangle$

Translate notation from *heap-base*

abbreviation *sc-preallocated* $::\ 'm\ prog \Rightarrow heap \Rightarrow bool$

where *sc-preallocated* $\equiv sc.preallocated\ TYPE('m)$

abbreviation *sc-start-tid* $::\ 'md\ prog \Rightarrow thread\text{-}id$

where *sc-start-tid* $\equiv sc.start\text{-}tid\ TYPE('md)$

abbreviation *sc-start-heap-ok* $::\ 'm\ prog \Rightarrow bool$

where *sc-start-heap-ok* $\equiv sc.start\text{-}heap\text{-}ok\ TYPE('m)$

abbreviation *sc-start-heap* $::\ 'm\ prog \Rightarrow heap$

where *sc-start-heap* $\equiv sc.start\text{-}heap\ TYPE('m)$

abbreviation *sc-start-state* $::$

$(cname \Rightarrow mname \Rightarrow ty\ list \Rightarrow ty \Rightarrow 'm \Rightarrow addr\ val\ list \Rightarrow 'x)$

$\Rightarrow 'm\ prog \Rightarrow cname \Rightarrow mname \Rightarrow addr\ val\ list \Rightarrow (addr, thread\text{-}id, 'x, heap, addr)\ state$

where

sc-start-state $f\ P \equiv sc.start\text{-}state\ TYPE('m)\ P\ f\ P$

abbreviation *sc-wf-start-state* $::\ 'm\ prog \Rightarrow cname \Rightarrow mname \Rightarrow addr\ val\ list \Rightarrow bool$

where *sc-wf-start-state* $P \equiv sc.wf\text{-}start\text{-}state\ TYPE('m)\ P\ P$

notation $sc.conf$ ($\langle -, - \vdash_{sc} - : \leq \rightarrow [51, 51, 51, 51] 50 \rangle$)

notation $sc.confs$ ($\langle -, - \vdash_{sc} - [:\leq] \rightarrow [51, 51, 51, 51] 50 \rangle$)

notation $sc.hevt$ ($\langle - \leq_{sc} \rightarrow [51, 51] 50 \rangle$)

lemma $sc\text{-start-heap-ok}$: $sc\text{-start-heap-ok } P$

$\langle proof \rangle$

lemma $sc\text{-wf-start-state-iff}$:

$sc\text{-wf-start-state } P C M vs \longleftrightarrow (\exists Ts T meth D. P \vdash C \text{ sees } M: Ts \rightarrow T = [meth] \text{ in } D \wedge P, sc\text{-start-heap } P \vdash_{sc} vs [:\leq] Ts)$

$\langle proof \rangle$

lemma $sc\text{-heap}$:

$heap \text{ addr2thread-id thread-id2addr } (sc\text{-allocate } P) \text{ sc-typeof-addr } sc\text{-heap-write } P$

$\langle proof \rangle$

interpretation sc :

$heap$

$addr2thread-id$

$thread-id2addr$

$sc\text{-spurious-wakeups}$

$sc\text{-empty}$

$sc\text{-allocate } P$

$sc\text{-typeof-addr}$

$sc\text{-heap-read}$

$sc\text{-heap-write}$

for $P \langle proof \rangle$

lemma $sc\text{-hevt-new}$:

$h a = None \implies h \leq_{sc} h(a \mapsto arrobj)$

$\langle proof \rangle$

lemma $sc\text{-hevt-upd-obj}$: $h a = Some (Obj C fs) \implies h \leq_{sc} h(a \mapsto (Obj C fs'))$

$\langle proof \rangle$

lemma $sc\text{-hevt-upd-arr}$: $\llbracket h a = Some (Arr T f e); length e = length e' \rrbracket \implies h \leq_{sc} h(a \mapsto (Arr T f' e'))$

$\langle proof \rangle$

8.1.3 Conformance

definition $sc\text{-fconf}$:: $'m \text{ prog} \Rightarrow cname \Rightarrow heap \Rightarrow fields \Rightarrow bool$ ($\langle -, - \vdash_{sc} - \surd \rangle [51, 51, 51, 51] 50$)

where $P, C, h \vdash_{sc} fs \surd = (\forall F D T fm. P \vdash C \text{ has } F: T (fm) \text{ in } D \longrightarrow (\exists v. fs(F, D) = Some v \wedge P, h \vdash_{sc} v : \leq T))$

primrec $sc\text{-oconf}$:: $'m \text{ prog} \Rightarrow heap \Rightarrow heapobj \Rightarrow bool$ ($\langle -, - \vdash_{sc} - \surd \rangle [51, 51, 51] 50$)

where

$P, h \vdash_{sc} Obj C fs \surd \longleftrightarrow is\text{-class } P C \wedge P, C, h \vdash_{sc} fs \surd$

$| P, h \vdash_{sc} Arr T fs el \surd \longleftrightarrow is\text{-type } P (T[]) \wedge P, Object, h \vdash_{sc} fs \surd \wedge (\forall v \in set\ el. P, h \vdash_{sc} v : \leq T)$

definition $sc\text{-hconf}$:: $'m \text{ prog} \Rightarrow heap \Rightarrow bool$ ($\langle - \vdash_{sc} - \surd \rangle [51, 51] 50$)

where $P \vdash_{sc} h \surd \longleftrightarrow (\forall a\ obj. h a = Some obj \longrightarrow P, h \vdash_{sc} obj \surd)$

interpretation sc : $heap\text{-conf-base}$

addr2thread-id
thread-id2addr
sc-spurious-wakeups
sc-empty
sc-allocate P
sc-typeof-addr
sc-heap-read
sc-heap-write
sc-hconf P
P

for P \langle proof \rangle

declare *sc.typeof-addr-thread-id2-addr-addr2thread-id* [*simp del*]

lemma *sc-conf-upd-obj*: $h\ a = \text{Some}(\text{Obj } C\ fs) \implies (P, h(a \mapsto (\text{Obj } C\ fs'))) \vdash_{sc} x : \leq T) = (P, h \vdash_{sc} x : \leq T)$
 \langle proof \rangle

lemma *sc-conf-upd-arr*: $h\ a = \text{Some}(\text{Arr } T\ f\ el) \implies (P, h(a \mapsto (\text{Arr } T\ f'\ el'))) \vdash_{sc} x : \leq T') = (P, h \vdash_{sc} x : \leq T')$
 \langle proof \rangle

lemma *sc-oconf-hext*: $P, h \vdash_{sc} \text{obj } \checkmark \implies h \trianglelefteq_{sc} h' \implies P, h' \vdash_{sc} \text{obj } \checkmark$
 \langle proof \rangle

lemma *sc-oconf-init-fields*:
assumes $P \vdash C$ *has-fields FDTs*
shows $P, h \vdash_{sc} (\text{Obj } C\ (\text{init-fields } FDTs)) \checkmark$
 \langle proof \rangle

lemma *sc-oconf-init*:
is-htype P hT $\implies P, h \vdash_{sc} \text{blank } P\ hT \checkmark$
 \langle proof \rangle

lemma *sc-oconf-fupd* [*intro?*]:
 $\llbracket P \vdash C$ *has F:T (fm) in D*; $P, h \vdash_{sc} v : \leq T$; $P, h \vdash_{sc} (\text{Obj } C\ fs) \checkmark \rrbracket$
 $\implies P, h \vdash_{sc} (\text{Obj } C\ (fs((F, D) \mapsto v))) \checkmark$
 \langle proof \rangle

lemma *sc-oconf-fupd-arr* [*intro?*]:
 $\llbracket P, h \vdash_{sc} v : \leq T$; $P, h \vdash_{sc} (\text{Arr } T\ f\ el) \checkmark \rrbracket$
 $\implies P, h \vdash_{sc} (\text{Arr } T\ f\ (el[i := v])) \checkmark$
 \langle proof \rangle

lemma *sc-oconf-fupd-arr-fields*:
 $\llbracket P \vdash \text{Object}$ *has F:T (fm) in Object*; $P, h \vdash_{sc} v : \leq T$; $P, h \vdash_{sc} (\text{Arr } T'\ f\ el) \checkmark \rrbracket$
 $\implies P, h \vdash_{sc} (\text{Arr } T'\ (f((F, \text{Object}) \mapsto v))\ el) \checkmark$
 \langle proof \rangle

lemma *sc-oconf-new*: $\llbracket P, h \vdash_{sc} \text{obj } \checkmark$; $h\ a = \text{None} \rrbracket \implies P, h(a \mapsto \text{arrobj}) \vdash_{sc} \text{obj } \checkmark$
 \langle proof \rangle

lemmas *sc-oconf-upd-obj = sc-oconf-hext* [*OF - sc-hext-upd-obj*]

lemma *sc-oconf-upd-arr*:

assumes $P, h \vdash_{sc} \text{obj} \checkmark$

and $ha: h a = \lfloor \text{Arr } T f \text{ el} \rfloor$

shows $P, h(a \mapsto \text{Arr } T f' \text{ el}') \vdash_{sc} \text{obj} \checkmark$

$\langle \text{proof} \rangle$

lemma *sc-hconfD*: $\llbracket P \vdash_{sc} h \checkmark; h a = \text{Some } \text{obj} \rrbracket \Longrightarrow P, h \vdash_{sc} \text{obj} \checkmark$

$\langle \text{proof} \rangle$

lemmas *sc-preallocated-new* = *sc.preallocated-hext* [*OF* - *sc-hext-new*]

lemmas *sc-preallocated-upd-obj* = *sc.preallocated-hext* [*OF* - *sc-hext-upd-obj*]

lemmas *sc-preallocated-upd-arr* = *sc.preallocated-hext* [*OF* - *sc-hext-upd-arr*]

lemma *sc-hconf-new*: $\llbracket P \vdash_{sc} h \checkmark; h a = \text{None}; P, h \vdash_{sc} \text{obj} \checkmark \rrbracket \Longrightarrow P \vdash_{sc} h(a \mapsto \text{obj}) \checkmark$

$\langle \text{proof} \rangle$

lemma *sc-hconf-upd-obj*: $\llbracket P \vdash_{sc} h \checkmark; h a = \text{Some } (\text{Obj } C fs); P, h \vdash_{sc} (\text{Obj } C fs') \checkmark \rrbracket \Longrightarrow P \vdash_{sc} h(a \mapsto (\text{Obj } C fs')) \checkmark$

$\langle \text{proof} \rangle$

lemma *sc-hconf-upd-arr*: $\llbracket P \vdash_{sc} h \checkmark; h a = \text{Some } (\text{Arr } T f \text{ el}); P, h \vdash_{sc} (\text{Arr } T f' \text{ el}') \checkmark \rrbracket \Longrightarrow P \vdash_{sc} h(a \mapsto (\text{Arr } T f' \text{ el}')) \checkmark$

$\langle \text{proof} \rangle$

lemma *sc-heap-conf*:

heap-conf addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-write (sc-hconf P) P

$\langle \text{proof} \rangle$

interpretation *sc: heap-conf*

addr2thread-id

thread-id2addr

sc-spurious-wakeups

sc-empty

sc-allocate P

sc-typeof-addr

sc-heap-read

sc-heap-write

sc-hconf P

P

for *P*

$\langle \text{proof} \rangle$

lemma *sc-heap-progress*:

heap-progress addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-read sc-heap-write (sc-hconf P) P

$\langle \text{proof} \rangle$

interpretation *sc: heap-progress*

addr2thread-id

thread-id2addr

sc-spurious-wakeups

sc-empty

sc-allocate P

sc-typeof-addr
sc-heap-read
sc-heap-write
sc-hconf P
P

for *P*
 ⟨*proof*⟩

lemma *sc-heap-conf-read*:

heap-conf-read addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-read
sc-heap-write (sc-hconf P) P

⟨*proof*⟩

interpretation *sc: heap-conf-read*

addr2thread-id
thread-id2addr
sc-spurious-wakeups
sc-empty
sc-allocate P
sc-typeof-addr
sc-heap-read
sc-heap-write
sc-hconf P
P

for *P*

⟨*proof*⟩

abbreviation *sc-deterministic-heap-ops* :: '*m prog* ⇒ *bool*

where *sc-deterministic-heap-ops* ≡ *sc.deterministic-heap-ops TYPE('m)*

lemma *sc-deterministic-heap-ops*: \neg *sc-spurious-wakeups* ⇒ *sc-deterministic-heap-ops P*

⟨*proof*⟩

8.1.4 Code generation

code-pred

(*modes: i ⇒ i ⇒ i ⇒ i ⇒ bool, i ⇒ i ⇒ i ⇒ o ⇒ bool*)
sc-heap-read ⟨*proof*⟩

code-pred

(*modes: i ⇒ i ⇒ i ⇒ i ⇒ i ⇒ bool, i ⇒ i ⇒ i ⇒ i ⇒ o ⇒ bool*)
sc-heap-write ⟨*proof*⟩

lemma *eval-sc-heap-read-i-i-i-o*:

Predicate.eval (sc-heap-read-i-i-i-o h ad al) = sc-heap-read h ad al
 ⟨*proof*⟩

lemma *eval-sc-heap-write-i-i-i-i-o*:

Predicate.eval (sc-heap-write-i-i-i-i-o h ad al v) = sc-heap-write h ad al v
 ⟨*proof*⟩

end

```

theory SC-Interp
imports
  SC
  ../Compiler/Correctness
  ../J/Deadlocked
  ../BV/JVMDeadlocked
begin

```

Do not interpret these locales, it just takes too long to generate all definitions and theorems.

lemma *sc-J-typesafe*:

```

J-typesafe addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-read sc-heap-write
(sc-hconf P) P
⟨proof⟩

```

lemma *sc-JVM-typesafe*:

```

JVM-typesafe addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-read
sc-heap-write (sc-hconf P) P
⟨proof⟩

```

lemma *compP2-compP1-cons*:

```

is-type (compP2 (compP1 P)) = is-type P
is-class (compP2 (compP1 P)) = is-class P
sc.addr-loc-type (compP2 (compP1 P)) = sc.addr-loc-type P
sc.conf (compP2 (compP1 P)) = sc.conf P
⟨proof⟩

```

lemma *sc-J-JVM-conf-read*:

```

J-JVM-conf-read addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-read
sc-heap-write (sc-hconf P) P
⟨proof⟩

```

end

8.2 Sequential consistency with efficient data structures

theory *SC-Collections*

```

imports
  ../Common/Conform

  ../Basic/JT-ICF
  MM
begin

```

hide-const (**open**) *new-Addr*

hide-fact (**open**) *new-Addr-SomeD new-Addr-SomeI*

8.2.1 Objects and Arrays

type-synonym *fields* = (*char*, (*cname*, *addr val*) *lm*) *tm*

type-synonym *array-cells* = (*nat*, *addr val*) *rbt*

type-synonym *array-fields* = (*vname*, *addr val*) *lm*

datatype *heapobj*

= *Obj cname fields* — class instance with class name and fields

| *Arr ty nat array-fields array-cells* — element type, size, fields and cell contents

lemma *rec-heapobj* [*simp*]: *rec-heapobj* = *case-heapobj*
 ⟨*proof*⟩

primrec *obj-ty* :: *heapobj* ⇒ *htype*

where

obj-ty (*Obj c f*) = *Class-type c*
 | *obj-ty* (*Arr t si f e*) = *Array-type t si*

fun *is-Arr* :: *heapobj* ⇒ *bool* **where**

is-Arr (*Obj C fs*) = *False*
 | *is-Arr* (*Arr T f si el*) = *True*

lemma *is-Arr-conv*:

is-Arr arrobj = (∃ *T si f el. arrobj* = *Arr T si f el*)
 ⟨*proof*⟩

lemma *is-ArrE*:

[[*is-Arr arrobj*; ∧ *T si f el. arrobj* = *Arr T si f el* ⇒ *thesis*]] ⇒ *thesis*
 [[¬ *is-Arr arrobj*; ∧ *C fs. arrobj* = *Obj C fs* ⇒ *thesis*]] ⇒ *thesis*
 ⟨*proof*⟩

definition *init-fields* :: ((*vname* × *cname*) × *ty*) *list* ⇒ *fields*

where

init-fields *FDTs* ≡
foldr (λ(*F*, *D*), *T*) *fields*.
 let *F'* = *String.explode F*
 in *tm-update F' (lm-update D (default-val T)*
 (case *tm-lookup F' fields* of *None* ⇒ *lm-empty* () | *Some lm* ⇒ *lm*)
fields)
FDTs (*tm-empty* ())

definition *init-fields-array* :: (*vname* × *ty*) *list* ⇒ *array-fields*

where

init-fields-array ≡ *lm.to-map* ∘ *map* (λ(*F*, *T*). (*F*, *default-val T*))

definition *init-cells* :: *ty* ⇒ *nat* ⇒ *array-cells*

where *init-cells T n* = *foldl* (λ*cells i. rm-update i (default-val T) cells*) (*rm-empty* ()) [*0..<n*]

primrec — a new, blank object with default values in all fields:

blank :: '*m prog* ⇒ *htype* ⇒ *heapobj*

where

blank P (*Class-type C*) = *Obj C (init-fields (map (λ(*FD*, (*T*, *fm*)). (*FD*, *T*)) (TypeRel.fields P C)))*
 | *blank P* (*Array-type T n*) =
*Arr T n (init-fields-array (map (λ((*F*, *D*), (*T*, *fm*)). (*F*, *T*)) (TypeRel.fields P Object))) (init-cells T n)*

lemma *obj-ty-blank* [*iff*]: *obj-ty (blank P hT)* = *hT*

⟨*proof*⟩

8.2.2 Heap

type-synonym *heap* = (*addr*, *heapobj*) *rbt*

translations

(*type*) *heap* <= (*type*) (*nat*, *heapobj*) *rbt*

abbreviation *sc-empty* :: *heap*

where *sc-empty* ≡ *rm-empty* ()

fun *the-obj* :: *heapobj* ⇒ *cname* × *fields* **where**

the-obj (*Obj C fs*) = (*C*, *fs*)

fun *the-arr* :: *heapobj* ⇒ *ty* × *nat* × *array-fields* × *array-cells* **where**

the-arr (*Arr T si f el*) = (*T*, *si*, *f*, *el*)

abbreviation

cname-of :: *heap* ⇒ *addr* ⇒ *cname* **where**

cname-of *hp a* == *fst* (*the-obj* (*the* (*rm-lookup a hp*)))

definition *new-Addr* :: *heap* ⇒ *addr* *option*

where *new-Addr h* = *Some* (*case rm-max h* ($\lambda\cdot$ *True*) *of None* ⇒ 0 | *Some* (*a*, -) ⇒ *a* + 1)

definition *sc-allocate* :: *'m prog* ⇒ *heap* ⇒ *hType* ⇒ (*heap* × *addr*) *set*

where

sc-allocate P h hT =

(*case new-Addr h of None* ⇒ {}
| *Some a* ⇒ {(*rm-update a* (*blank P hT*) *h*, *a*)}

definition *sc-typeof-addr* :: *heap* ⇒ *addr* ⇒ *hType* *option*

where *sc-typeof-addr h a* = *map-option obj-ty* (*rm-lookup a h*)

inductive *sc-heap-read* :: *heap* ⇒ *addr* ⇒ *addr-loc* ⇒ *addr val* ⇒ *bool*

for *h* :: *heap* **and** *a* :: *addr*

where

Obj: [*rm-lookup a h* = [*Obj C fs*]; *tm-lookup* (*String.explode F*) *fs* = [*fs'*]; *lm-lookup D fs'* = [*v*]]
⇒ *sc-heap-read h a* (*CField D F*) *v*

| *Arr*: [*rm-lookup a h* = [*Arr T si f el*]; *n* < *si*] ⇒ *sc-heap-read h a* (*ACell n*) (*the* (*rm-lookup n el*))

| *ArrObj*: [*rm-lookup a h* = [*Arr T si f el*]; *lm-lookup F f* = [*v*]] ⇒ *sc-heap-read h a* (*CField Object F*) *v*

hide-fact (**open**) *Obj Arr ArrObj*

inductive-cases *sc-heap-read-cases* [*elim!*]:

sc-heap-read h a (*CField C F*) *v*

sc-heap-read h a (*ACell n*) *v*

inductive *sc-heap-write* :: *heap* ⇒ *addr* ⇒ *addr-loc* ⇒ *addr val* ⇒ *heap* ⇒ *bool*

for *h* :: *heap* **and** *a* :: *addr*

where

Obj:

[*rm-lookup a h* = [*Obj C fs*]; *F'* = *String.explode F*;

h' = *rm-update a* (*Obj C* (*tm-update F'* (*lm-update D v* (*case tm-lookup* (*String.explode F*) *fs of None* ⇒ *lm-empty* () | *Some fs'* ⇒ *fs'*)) *fs*)) *h*]

⇒ *sc-heap-write h a* (*CField D F*) *v h'*

| *Arr*:
 $\llbracket \text{rm-lookup } a \ h = \lfloor \text{Arr } T \ \text{si } f \ \text{el} \rfloor; h' = \text{rm-update } a \ (\text{Arr } T \ \text{si } f \ (\text{rm-update } n \ v \ \text{el})) \ h \rrbracket$
 $\implies \text{sc-heap-write } h \ a \ (\text{ACell } n) \ v \ h'$

| *ArrObj*:
 $\llbracket \text{rm-lookup } a \ h = \lfloor \text{Arr } T \ \text{si } f \ \text{el} \rfloor; h' = \text{rm-update } a \ (\text{Arr } T \ \text{si } (\text{lm-update } F \ v \ f) \ \text{el}) \ h \rrbracket$
 $\implies \text{sc-heap-write } h \ a \ (\text{CField } \text{Object } F) \ v \ h'$

hide-fact (**open**) *Obj Arr ArrObj*

inductive-cases *sc-heap-write-cases* [*elim!*]:

sc-heap-write *h a (CField C F) v h'*

sc-heap-write *h a (ACell n) v h'*

consts *sc-spurious-wakeups* :: *bool*

lemma *new-Addr-SomeD*: *new-Addr h = [a] \implies rm-lookup a h = None*
 $\langle \text{proof} \rangle$

interpretation *sc*:

heap-base

addr2thread-id

thread-id2addr

sc-spurious-wakeups

sc-empty

sc-allocate P

sc-typeof-addr

sc-heap-read

sc-heap-write

for *P* $\langle \text{proof} \rangle$

Translate notation from *heap-base*

abbreviation *sc-preallocated* :: *'m prog \Rightarrow heap \Rightarrow bool*

where *sc-preallocated* \equiv *sc.preallocated TYPE('m)*

abbreviation *sc-start-tid* :: *'md prog \Rightarrow thread-id*

where *sc-start-tid* \equiv *sc.start-tid TYPE('md)*

abbreviation *sc-start-heap-ok* :: *'m prog \Rightarrow bool*

where *sc-start-heap-ok* \equiv *sc.start-heap-ok TYPE('m)*

abbreviation *sc-start-heap* :: *'m prog \Rightarrow heap*

where *sc-start-heap* \equiv *sc.start-heap TYPE('m)*

abbreviation *sc-start-state* ::

(cname \Rightarrow mname \Rightarrow ty list \Rightarrow ty \Rightarrow 'm \Rightarrow addr val list \Rightarrow 'x)

\Rightarrow 'm prog \Rightarrow cname \Rightarrow mname \Rightarrow addr val list \Rightarrow (*addr, thread-id, 'x, heap, addr*) *state*

where

sc-start-state f P \equiv *sc.start-state TYPE('m) P f P*

abbreviation *sc-wf-start-state* :: *'m prog \Rightarrow cname \Rightarrow mname \Rightarrow addr val list \Rightarrow bool*

where *sc-wf-start-state P* \equiv *sc.wf-start-state TYPE('m) P P*

notation *sc.conf* ($\langle -, \vdash_{sc} - : \leq - \rangle$ [*51,51,51,51*] *50*)

notation *sc.conf*s ($\langle -, - \vdash_{sc} - \[:\leq] \rightarrow [51,51,51,51] 50 \rangle$)

notation *sc.hext* ($\langle -, \leq_{sc} \rightarrow [51,51] 50 \rangle$)

lemma *new-Addr-SomeI*: $\exists a. \text{new-Addr } h = \text{Some } a$
 $\langle \text{proof} \rangle$

lemma *sc-start-heap-ok*: *sc-start-heap-ok* *P*
 $\langle \text{proof} \rangle$

lemma *sc-wf-start-state-iff*:
sc-wf-start-state *P C M vs* $\longleftrightarrow (\exists Ts T \text{meth } D. P \vdash C \text{ sees } M: Ts \rightarrow T = [meth] \text{ in } D \wedge P, \text{sc-start-heap}$
 $P \vdash_{sc} vs \[:\leq] Ts)$
 $\langle \text{proof} \rangle$

lemma *sc-heap*:
heap addr2thread-id thread-id2addr (sc-allocate P) sc-typeof-addr sc-heap-write P
 $\langle \text{proof} \rangle$

interpretation *sc*:

heap
addr2thread-id
thread-id2addr
sc-spurious-wakeups
sc-empty
sc-allocate P
sc-typeof-addr
sc-heap-read
sc-heap-write
P
for *P* $\langle \text{proof} \rangle$

declare *sc.typeof-addr-thread-id2-addr-addr2thread-id* [*simp del*]

lemma *sc-hext-new*:
 $\text{rm-lookup } a \ h = \text{None} \implies h \leq_{sc} \text{rm-update } a \ \text{arobj } h$
 $\langle \text{proof} \rangle$

lemma *sc-hext-upd-obj*: $\text{rm-lookup } a \ h = \text{Some } (\text{Obj } C \ fs) \implies h \leq_{sc} \text{rm-update } a \ (\text{Obj } C \ fs') \ h$
 $\langle \text{proof} \rangle$

lemma *sc-hext-upd-arr*: $\llbracket \text{rm-lookup } a \ h = \text{Some } (\text{Arr } T \ si \ f \ e) \rrbracket \implies h \leq_{sc} \text{rm-update } a \ (\text{Arr } T \ si \ f' \ e') \ h$
 $\langle \text{proof} \rangle$

8.2.3 Conformance

definition *sc-oconf* $:: 'm \ \text{prog} \Rightarrow \text{heap} \Rightarrow \text{heapobj} \Rightarrow \text{bool} \ \langle -, - \vdash_{sc} - \surd \rangle [51,51,51] 50$

where

$P, h \vdash_{sc} \text{obj } \surd \equiv$
 $(\text{case } \text{obj} \text{ of}$
 $\text{Obj } C \ fs \Rightarrow$
 $\text{is-class } P \ C \wedge$
 $(\forall F \ D \ T \ fm. P \vdash C \ \text{has } F:T \ (fm) \ \text{in } D \longrightarrow$
 $(\exists fs' \ v. \text{tm-}\alpha \ fs \ (\text{String.explode } F) = \text{Some } fs' \wedge \text{lm-}\alpha \ fs' \ D = \text{Some } v \wedge P, h \vdash_{sc} v \[:\leq] T))$

| $Arr\ T\ si\ f\ el \Rightarrow$
 $is\text{-}type\ P\ (T[\])\ \wedge\ (\forall n. n < si \longrightarrow (\exists v. rm\text{-}\alpha\ el\ n = Some\ v \wedge P, h \vdash_{sc}\ v : \leq T)) \wedge$
 $(\forall F\ T\ fm. P \vdash Object\ has\ F:T\ (fm)\ in\ Object \longrightarrow (\exists v. lm\text{-}lookup\ F\ f = Some\ v \wedge P, h \vdash_{sc}\ v : \leq T)))$

definition $sc\text{-}hconf :: 'm\ prog \Rightarrow heap \Rightarrow bool$ ($\langle \vdash_{sc} - \checkmark \rangle$ [51,51] 50)
where $P \vdash_{sc}\ h\ \checkmark \iff (\forall a\ obj. rm\text{-}\alpha\ h\ a = Some\ obj \longrightarrow P, h \vdash_{sc}\ obj\ \checkmark)$

interpretation sc :

$heap\text{-}conf\text{-}base$
 $addr2thread\text{-}id$
 $thread\text{-}id2addr$
 $sc\text{-}spurious\text{-}wakeups$
 $sc\text{-}empty$
 $sc\text{-}allocate\ P$
 $sc\text{-}typeof\text{-}addr$
 $sc\text{-}heap\text{-}read$
 $sc\text{-}heap\text{-}write$
 $sc\text{-}hconf\ P$
 P

for P

$\langle proof \rangle$

lemma $sc\text{-}conf\text{-}upd\text{-}obj$: $rm\text{-}lookup\ a\ h = Some\ (Obj\ C\ fs) \implies (P, rm\text{-}update\ a\ (Obj\ C\ fs')\ h \vdash_{sc}\ x : \leq T) = (P, h \vdash_{sc}\ x : \leq T)$

$\langle proof \rangle$

lemma $sc\text{-}conf\text{-}upd\text{-}arr$:

$rm\text{-}lookup\ a\ h = Some\ (Arr\ T\ si\ f\ el) \implies (P, rm\text{-}update\ a\ (Arr\ T\ si\ f'\ el')\ h \vdash_{sc}\ x : \leq T') = (P, h \vdash_{sc}\ x : \leq T')$

$\langle proof \rangle$

lemma $sc\text{-}oconf\text{-}hext$: $P, h \vdash_{sc}\ obj\ \checkmark \implies h \sqsubseteq_{sc}\ h' \implies P, h' \vdash_{sc}\ obj\ \checkmark$

$\langle proof \rangle$

lemma $map\text{-}of\text{-}fields\text{-}init\text{-}fields$:

assumes $map\text{-}of\ FDTs\ (F, D) = [(T, fm)]$

shows $\exists fs' v. tm\text{-}\alpha\ (init\text{-}fields\ (map\ (\lambda(FD, (T, fm)). (FD, T))\ FDTs))\ (String.\ explode\ F) = [fs']$
 $\wedge\ lm\text{-}\alpha\ fs'\ D = [v] \wedge sc.\ conf\ P\ h\ v\ T$

$\langle proof \rangle$

lemma $sc\text{-}oconf\text{-}init\text{-}fields$:

assumes $P \vdash C\ has\text{-}fields\ FDTs$

shows $P, h \vdash_{sc}\ (Obj\ C\ (init\text{-}fields\ (map\ (\lambda(FD, (T, fm)). (FD, T))\ FDTs)))\ \checkmark$

$\langle proof \rangle$

lemma $sc\text{-}oconf\text{-}init\text{-}arr$:

assumes $type$: $is\text{-}type\ P\ (T[\])$

shows $P, h \vdash_{sc}\ Arr\ T\ n\ (init\text{-}fields\text{-}array\ (map\ (\lambda((F, D), (T, fm)). (F, T))\ (TypeRel.\ fields\ P\ Object)))\ (init\text{-}cells\ T\ n)\ \checkmark$

$\langle proof \rangle$

lemma $sc\text{-}oconf\text{-}fupd$ [intro?]:

$\llbracket P \vdash C\ has\ F:T\ (fm)\ in\ D; P, h \vdash_{sc}\ v : \leq T; P, h \vdash_{sc}\ (Obj\ C\ fs)\ \checkmark; \rrbracket$

$$fs' = (\text{case } tm\text{-lookup } (String.\text{explode } F) \text{ fs of } None \Rightarrow lm\text{-empty } () \mid Some \text{ fs}' \Rightarrow fs')$$

$$\Longrightarrow P, h \vdash_{sc} (Obj \ C \ (tm\text{-update } (String.\text{explode } F) \ (lm\text{-update } D \ v \ fs') \ fs)) \ \checkmark$$
 <proof>

lemma *sc-oconf-fupd-arr* [intro?]:

$$\llbracket P, h \vdash_{sc} v : \leq T; P, h \vdash_{sc} (Arr \ T \ si \ f \ el) \ \checkmark \rrbracket$$

$$\Longrightarrow P, h \vdash_{sc} (Arr \ T \ si \ f \ (rm\text{-update } i \ v \ el)) \ \checkmark$$
 <proof>

lemma *sc-oconf-fupd-arr-fields*:

$$\llbracket P \vdash \text{Object has } F:T \ (fm) \ \text{in } \text{Object}; P, h \vdash_{sc} v : \leq T; P, h \vdash_{sc} (Arr \ T' \ si \ f \ el) \ \checkmark \rrbracket$$

$$\Longrightarrow P, h \vdash_{sc} (Arr \ T' \ si \ (lm\text{-update } F \ v \ f) \ el) \ \checkmark$$
 <proof>

lemma *sc-oconf-new*: $\llbracket P, h \vdash_{sc} obj \ \checkmark; rm\text{-lookup } a \ h = None \rrbracket \Longrightarrow P, rm\text{-update } a \ arrobj \ h \vdash_{sc} obj$

$$\checkmark$$
 <proof>

lemmas *sc-oconf-upd-obj* = *sc-oconf-hext* [OF - *sc-hext-upd-obj*]

lemma *sc-oconf-upd-arr*:
assumes $P, h \vdash_{sc} obj \ \checkmark$
and $ha: rm\text{-lookup } a \ h = \lfloor Arr \ T \ si \ f \ el \rfloor$
shows $P, rm\text{-update } a \ (Arr \ T \ si \ f' \ el') \ h \vdash_{sc} obj \ \checkmark$
 <proof>

lemma *sc-oconf-blank*: $is\text{-hType } P \ hT \Longrightarrow P, h \vdash_{sc} blank \ P \ hT \ \checkmark$
 <proof>

lemma *sc-hconfD*: $\llbracket P \vdash_{sc} h \ \checkmark; rm\text{-lookup } a \ h = Some \ obj \rrbracket \Longrightarrow P, h \vdash_{sc} obj \ \checkmark$
 <proof>

lemmas *sc-preallocated-new* = *sc.preallocated-hext*[OF - *sc-hext-new*]

lemmas *sc-preallocated-upd-obj* = *sc.preallocated-hext* [OF - *sc-hext-upd-obj*]

lemmas *sc-preallocated-upd-arr* = *sc.preallocated-hext* [OF - *sc-hext-upd-arr*]

lemma *sc-hconf-new*: $\llbracket P \vdash_{sc} h \ \checkmark; rm\text{-lookup } a \ h = None; P, h \vdash_{sc} obj \ \checkmark \rrbracket \Longrightarrow P \vdash_{sc} rm\text{-update } a \ obj \ h \ \checkmark$
 <proof>

lemma *sc-hconf-upd-obj*: $\llbracket P \vdash_{sc} h \ \checkmark; rm\text{-lookup } a \ h = Some \ (Obj \ C \ fs); P, h \vdash_{sc} (Obj \ C \ fs') \ \checkmark \rrbracket$

$$\Longrightarrow P \vdash_{sc} rm\text{-update } a \ (Obj \ C \ fs') \ h \ \checkmark$$
 <proof>

lemma *sc-hconf-upd-arr*: $\llbracket P \vdash_{sc} h \ \checkmark; rm\text{-lookup } a \ h = Some(Arr \ T \ si \ f \ el); P, h \vdash_{sc} (Arr \ T \ si \ f' \ el') \ \checkmark \rrbracket$

$$\Longrightarrow P \vdash_{sc} rm\text{-update } a \ (Arr \ T \ si \ f' \ el') \ h \ \checkmark$$
 <proof>

lemma *sc-heap-conf*:
 $heap\text{-conf } addr2thread\text{-id } thread\text{-id}2addr \ sc\text{-empty } (sc\text{-allocate } P) \ sc\text{-typeof-addr } sc\text{-heap-write } (sc\text{-hconf } P) \ P$
 <proof>

interpretation *sc*:

heap-conf
addr2thread-id
thread-id2addr
sc-spurious-wakeups
sc-empty
sc-allocate P
sc-typeof-addr
sc-heap-read
sc-heap-write
sc-hconf P
P
for P
 ⟨*proof*⟩

lemma *sc-heap-progress*:

heap-progress addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-read
sc-heap-write (sc-hconf P) P
 ⟨*proof*⟩

interpretation *sc*:

heap-progress
addr2thread-id
thread-id2addr
sc-spurious-wakeups
sc-empty
sc-allocate P
sc-typeof-addr
sc-heap-read
sc-heap-write
sc-hconf P
P
for P
 ⟨*proof*⟩

lemma *sc-heap-conf-read*:

heap-conf-read addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-read
sc-heap-write (sc-hconf P) P
 ⟨*proof*⟩

interpretation *sc*:

heap-conf-read
addr2thread-id
thread-id2addr
sc-spurious-wakeups
sc-empty
sc-allocate P
sc-typeof-addr
sc-heap-read
sc-heap-write
sc-hconf P
P
for P
 ⟨*proof*⟩

abbreviation *sc-deterministic-heap-ops* :: 'm prog ⇒ bool
where *sc-deterministic-heap-ops* ≡ *sc.deterministic-heap-ops* TYPE('m)

lemma *sc-deterministic-heap-ops*: ¬ *sc-spurious-wakeups* ⇒ *sc-deterministic-heap-ops* P
 ⟨proof⟩

8.2.4 Code generation

code-pred
 (modes: *i* ⇒ *i* ⇒ *i* ⇒ *i* ⇒ bool, *i* ⇒ *i* ⇒ *i* ⇒ *o* ⇒ bool)
sc-heap-read ⟨proof⟩

code-pred
 (modes: *i* ⇒ *i* ⇒ *i* ⇒ *i* ⇒ *i* ⇒ bool, *i* ⇒ *i* ⇒ *i* ⇒ *i* ⇒ *o* ⇒ bool)
sc-heap-write ⟨proof⟩

lemma *eval-sc-heap-read-i-i-i-o*:
 Predicate.eval (*sc-heap-read-i-i-i-o* h ad al) = *sc-heap-read* h ad al
 ⟨proof⟩

lemma *eval-sc-heap-write-i-i-i-o*:
 Predicate.eval (*sc-heap-write-i-i-i-o* h ad al v) = *sc-heap-write* h ad al v
 ⟨proof⟩

end

8.3 Orders as predicates

theory *Orders*

imports

Main

begin

8.3.1 Preliminaries

transfer *refl-on* et al. from *HOL.Relation* to predicates

abbreviation *refl-onP* :: 'a set ⇒ ('a ⇒ 'a ⇒ bool) ⇒ bool
where *refl-onP* A r ≡ *refl-on* A {(x, y). r x y}

abbreviation *reflP* :: ('a ⇒ 'a ⇒ bool) ⇒ bool
where *reflP* == *refl-onP* UNIV

abbreviation *symP* :: ('a ⇒ 'a ⇒ bool) ⇒ bool
where *symP* r ≡ *sym* {(x, y). r x y}

abbreviation *total-onP* :: 'a set ⇒ ('a ⇒ 'a ⇒ bool) ⇒ bool
where *total-onP* A r ≡ *total-on* A {(x, y). r x y}

abbreviation *irreflP* :: ('a ⇒ 'a ⇒ bool) ⇒ bool
where *irreflP* r == *irrefl* {(x, y). r x y}

definition *irreflclp* :: ('a ⇒ 'a ⇒ bool) ⇒ 'a ⇒ 'a ⇒ bool (↪[≠] [1000] 1000)
where *r[≠]* a b = (r a b ∧ a ≠ b)

definition *porder-on* :: 'a set \Rightarrow ('a \Rightarrow 'a \Rightarrow bool) \Rightarrow bool
where *porder-on* A r \longleftrightarrow *refl-onP* A r \wedge *transp* r \wedge *antisymp* r

definition *torder-on* :: 'a set \Rightarrow ('a \Rightarrow 'a \Rightarrow bool) \Rightarrow bool
where *torder-on* A r \longleftrightarrow *porder-on* A r \wedge *total-onP* A r

definition *order-consistent* :: ('a \Rightarrow 'a \Rightarrow bool) \Rightarrow ('a \Rightarrow 'a \Rightarrow bool) \Rightarrow bool
where *order-consistent* r s \longleftrightarrow (\forall a a'. r a a' \wedge s a' a \longrightarrow a = a')

definition *restrictP* :: ('a \Rightarrow 'a \Rightarrow bool) \Rightarrow 'a set \Rightarrow 'a \Rightarrow 'a \Rightarrow bool (**infixl** <|' 110)
where (r |' A) a b \longleftrightarrow r a b \wedge a \in A \wedge b \in A

definition *inv-imageP* :: ('a \Rightarrow 'a \Rightarrow bool) \Rightarrow ('b \Rightarrow 'a) \Rightarrow 'b \Rightarrow 'b \Rightarrow bool
where [*iff*]: *inv-imageP* r f a b \longleftrightarrow r (f a) (f b)

lemma *refl-onPI*: (\bigwedge a a'. r a a' \Longrightarrow a \in A \wedge a' \in A) \Longrightarrow (\bigwedge a. a : A \Longrightarrow r a a) \Longrightarrow *refl-onP* A r
 <proof>

lemma *refl-onPD*: *refl-onP* A r \Longrightarrow a : A \Longrightarrow r a a
 <proof>

lemma *refl-onPD1*: *refl-onP* A r \Longrightarrow r a b \Longrightarrow a : A
 <proof>

lemma *refl-onPD2*: *refl-onP* A r \Longrightarrow r a b \Longrightarrow b : A
 <proof>

lemma *refl-onP-Int*: *refl-onP* A r \Longrightarrow *refl-onP* B s \Longrightarrow *refl-onP* (A \cap B) (λ a a'. r a a' \wedge s a a')
 <proof>

lemma *refl-onP-Un*: *refl-onP* A r \Longrightarrow *refl-onP* B s \Longrightarrow *refl-onP* (A \cup B) (λ a a'. r a a' \vee s a a')
 <proof>

lemma *refl-onP-empty[simp]*: *refl-onP* {} (λ a a'. False)
 <proof>

lemma *refl-onP-tranclp*:
assumes *refl-onP* A r
shows *refl-onP* A r⁺⁺
 <proof>

lemma *irreflPI*: (\bigwedge a. \neg r a a) \Longrightarrow *irreflP* r
 <proof>

lemma *irreflPE*:
assumes *irreflP* r
obtains \forall a. \neg r a a
 <proof>

lemma *irreflPD*: [*irreflP* r; r a a] \Longrightarrow False
 <proof>

lemma *irreflclpD*:

$r^{\neq} a b \implies r a b \wedge a \neq b$
 ⟨proof⟩

lemma *irreflclpI* [*intro!*]:
 $\llbracket r a b; a \neq b \rrbracket \implies r^{\neq} a b$
 ⟨proof⟩

lemma *irreflclpE* [*elim!*]:
assumes $r^{\neq} a b$
obtains $r a b a \neq b$
 ⟨proof⟩

lemma *transPI*: $(\bigwedge x y z. \llbracket r x y; r y z \rrbracket \implies r x z) \implies \text{transp } r$
 ⟨proof⟩

lemma *transPD*: $\llbracket \text{transp } r; r x y; r y z \rrbracket \implies r x z$
 ⟨proof⟩

lemma *transP-tranclp*: $\text{transp } r^{\hat{++}}$
 ⟨proof⟩

lemma *antisymPI*: $(\bigwedge x y. \llbracket r x y; r y x \rrbracket \implies x = y) \implies \text{antisymp } r$
 ⟨proof⟩

lemma *antisymPD*: $\llbracket \text{antisymp } r; r a b; r b a \rrbracket \implies a = b$
 ⟨proof⟩

lemma *antisym-subset*:
 $\llbracket \text{antisymp } r; \bigwedge a a'. s a a' \implies r a a' \rrbracket \implies \text{antisymp } s$
 ⟨proof⟩

lemma *symPI*: $(\bigwedge x y. r x y \implies r y x) \implies \text{symP } r$
 ⟨proof⟩

lemma *symPD*: $\llbracket \text{symP } r; r x y \rrbracket \implies r y x$
 ⟨proof⟩

8.3.2 Easy properties

lemma *porder-onI*:
 $\llbracket \text{refl-onP } A r; \text{antisymp } r; \text{transp } r \rrbracket \implies \text{porder-on } A r$
 ⟨proof⟩

lemma *porder-onE*:
assumes $\text{porder-on } A r$
obtains $\text{refl-onP } A r \text{ antisymp } r \text{ transp } r$
 ⟨proof⟩

lemma *torder-onI*:
 $\llbracket \text{porder-on } A r; \text{total-onP } A r \rrbracket \implies \text{torder-on } A r$
 ⟨proof⟩

lemma *torder-onE*:
assumes $\text{torder-on } A r$

obtains *porder-on A r total-onP A r*
 ⟨proof⟩

lemma *total-onI*:
 $(\bigwedge x y. \llbracket x \in A; y \in A \rrbracket \implies (x, y) \in r \vee x = y \vee (y, x) \in r) \implies \text{total-on } A \ r$
 ⟨proof⟩

lemma *total-onPI*:
 $(\bigwedge x y. \llbracket x \in A; y \in A \rrbracket \implies r \ x \ y \vee x = y \vee r \ y \ x) \implies \text{total-onP } A \ r$
 ⟨proof⟩

lemma *total-onD*:
 $\llbracket \text{total-on } A \ r; x \in A; y \in A \rrbracket \implies (x, y) \in r \vee x = y \vee (y, x) \in r$
 ⟨proof⟩

lemma *total-onPD*:
 $\llbracket \text{total-onP } A \ r; x \in A; y \in A \rrbracket \implies r \ x \ y \vee x = y \vee r \ y \ x$
 ⟨proof⟩

8.3.3 Order consistency

lemma *order-consistentI*:
 $(\bigwedge a a'. \llbracket r \ a \ a'; s \ a' \ a \rrbracket \implies a = a') \implies \text{order-consistent } r \ s$
 ⟨proof⟩

lemma *order-consistentD*:
 $\llbracket \text{order-consistent } r \ s; r \ a \ a'; s \ a' \ a \rrbracket \implies a = a'$
 ⟨proof⟩

lemma *order-consistent-subset*:
 $\llbracket \text{order-consistent } r \ s; \bigwedge a a'. r' \ a \ a' \implies r \ a \ a'; \bigwedge a a'. s' \ a \ a' \implies s \ a \ a' \rrbracket \implies \text{order-consistent } r' \ s'$
 ⟨proof⟩

lemma *order-consistent-sym*:
 $\text{order-consistent } r \ s \implies \text{order-consistent } s \ r$
 ⟨proof⟩

lemma *antisym-order-consistent-self*:
 $\text{antisym } r \implies \text{order-consistent } r \ r$
 ⟨proof⟩

lemma *total-on-refl-on-consistent-into*:
assumes $r: \text{total-onP } A \ r \ \text{refl-onP } A \ r$
and $\text{consist: order-consistent } r \ s$
and $x: x \in A$ **and** $y: y \in A$ **and** $s: s \ x \ y$
shows $r \ x \ y$
 ⟨proof⟩

lemma *porder-torder-tranclpE* [consumes 5, case-names base step]:
assumes $r: \text{porder-on } A \ r$
and $s: \text{torder-on } B \ s$
and $\text{consist: order-consistent } r \ s$
and $B\text{-subset-}A: B \subseteq A$
and $\text{trancl: } (\lambda a b. r \ a \ b \vee s \ a \ b) \hat{+} \ x \ y$

obtains $r x y$
 | $u v$ **where** $r x u s u v r v y$
 $\langle proof \rangle$

lemma *torder-on-porder-on-consistent-tranclp-antisym:*

assumes r : *porder-on* $A r$
and s : *torder-on* $B s$
and *consist*: *order-consistent* $r s$
and *B-subset-A*: $B \subseteq A$
shows *antisymp* $(\lambda x y. r x y \vee s x y)^{++}$
 $\langle proof \rangle$

lemma *porder-on-torder-on-tranclp-porder-onI:*

assumes r : *porder-on* $A r$
and s : *torder-on* $B s$
and *consist*: *order-consistent* $r s$
and *subset*: $B \subseteq A$
shows *porder-on* $A (\lambda a b. r a b \vee s a b)^{++}$
 $\langle proof \rangle$

lemma *porder-on-sub-torder-on-tranclp-porder-onI:*

assumes r : *porder-on* $A r$
and s : *torder-on* $B s$
and *consist*: *order-consistent* $r s$
and t : $\bigwedge x y. t x y \implies s x y$
and *subset*: $B \subseteq A$
shows *porder-on* $A (\lambda x y. r x y \vee t x y)^{++}$
 $\langle proof \rangle$

8.3.4 Order restrictions

lemma *restrictPI* [*intro!*, *simp*]:

$\llbracket r a b; a \in A; b \in A \rrbracket \implies (r \upharpoonright A) a b$
 $\langle proof \rangle$

lemma *restrictPE* [*elim!*]:

assumes $(r \upharpoonright A) a b$
obtains $r a b a \in A b \in A$
 $\langle proof \rangle$

lemma *restrictP-empty* [*simp*]: $R \upharpoonright \{\} = (\lambda - . False)$

$\langle proof \rangle$

lemma *refl-on-restrictPI:*

refl-onP $A r \implies \text{refl-onP } (A \cap B) (r \upharpoonright B)$
 $\langle proof \rangle$

lemma *refl-on-restrictPI':*

$\llbracket \text{refl-onP } A r; B = A \cap C \rrbracket \implies \text{refl-onP } B (r \upharpoonright C)$
 $\langle proof \rangle$

lemma *antisym-restrictPI:*

antisymp $r \implies \text{antisymp } (r \upharpoonright A)$
 $\langle proof \rangle$

lemma *trans-restrictPI*:

$\text{transp } r \implies \text{transp } (r \mid' A)$
 $\langle \text{proof} \rangle$

lemma *porder-on-restrictPI*:

$\text{porder-on } A \ r \implies \text{porder-on } (A \cap B) \ (r \mid' B)$
 $\langle \text{proof} \rangle$

lemma *porder-on-restrictPI'*:

$\llbracket \text{porder-on } A \ r; B = A \cap C \rrbracket \implies \text{porder-on } B \ (r \mid' C)$
 $\langle \text{proof} \rangle$

lemma *total-on-restrictPI*:

$\text{total-onP } A \ r \implies \text{total-onP } (A \cap B) \ (r \mid' B)$
 $\langle \text{proof} \rangle$

lemma *total-on-restrictPI'*:

$\llbracket \text{total-onP } A \ r; B = A \cap C \rrbracket \implies \text{total-onP } B \ (r \mid' C)$
 $\langle \text{proof} \rangle$

lemma *torder-on-restrictPI*:

$\text{torder-on } A \ r \implies \text{torder-on } (A \cap B) \ (r \mid' B)$
 $\langle \text{proof} \rangle$

lemma *torder-on-restrictPI'*:

$\llbracket \text{torder-on } A \ r; B = A \cap C \rrbracket \implies \text{torder-on } B \ (r \mid' C)$
 $\langle \text{proof} \rangle$

lemma *restrictP-commute*:

fixes $r :: 'a \Rightarrow 'a \Rightarrow \text{bool}$
shows $r \mid' A \mid' B = r \mid' B \mid' A$
 $\langle \text{proof} \rangle$

lemma *restrictP-subsume1*:

fixes $r :: 'a \Rightarrow 'a \Rightarrow \text{bool}$
assumes $A \subseteq B$
shows $r \mid' A \mid' B = r \mid' A$
 $\langle \text{proof} \rangle$

lemma *restrictP-subsume2*:

fixes $r :: 'a \Rightarrow 'a \Rightarrow \text{bool}$
assumes $B \subseteq A$
shows $r \mid' A \mid' B = r \mid' B$
 $\langle \text{proof} \rangle$

lemma *restrictP-idem [simp]*:

fixes $r :: 'a \Rightarrow 'a \Rightarrow \text{bool}$
shows $r \mid' A \mid' A = r \mid' A$
 $\langle \text{proof} \rangle$

8.3.5 Maximal elements w.r.t. a total order

definition *max-torder* :: $('a \Rightarrow 'a \Rightarrow \text{bool}) \Rightarrow 'a \Rightarrow 'a \Rightarrow 'a$

where $\text{max-torder } r \ a \ b = (\text{if } \text{Domainp } r \ a \ \wedge \ \text{Domainp } r \ b \ \text{then } \text{if } r \ a \ b \ \text{then } b \ \text{else } a \ \text{else if } a = b \ \text{then } a \ \text{else } \text{SOME } a. \neg \text{Domainp } r \ a)$

lemma *refl-on-DomainD*: $\text{refl-on } A \ r \implies A = \text{Domain } r$
 ⟨proof⟩

lemma *refl-onP-DomainPD*: $\text{refl-onP } A \ r \implies A = \{a. \text{Domainp } r \ a\}$
 ⟨proof⟩

lemma *semilattice-max-torder*:
assumes *tot*: *torder-on* $A \ r$
shows *semilattice* ($\text{max-torder } r$)
 ⟨proof⟩

lemma *max-torder-ge-conv-disj*:
assumes *tot*: *torder-on* $A \ r$ **and** $x: x \in A$ **and** $y: y \in A$
shows $r \ z \ (\text{max-torder } r \ x \ y) \longleftrightarrow r \ z \ x \ \vee \ r \ z \ y$
 ⟨proof⟩

definition *Max-torder* :: $('a \Rightarrow 'a \Rightarrow \text{bool}) \Rightarrow 'a \ \text{set} \Rightarrow 'a$
where
 $\text{Max-torder } r = \text{semilattice-set.F } (\text{max-torder } r)$

context
fixes $A \ r$
assumes *tot*: *torder-on* $A \ r$
begin

lemma *semilattice-set*:
semilattice-set ($\text{max-torder } r$)
 ⟨proof⟩

lemma *domain*:
 $\text{Domainp } r \ a \longleftrightarrow a \in A$
 ⟨proof⟩

lemma *Max-torder-in-Domain*:
assumes $B: \text{finite } B \ B \neq \{\}$ $B \subseteq A$
shows $\text{Max-torder } r \ B \in A$
 ⟨proof⟩

lemma *Max-torder-in-set*:
assumes $B: \text{finite } B \ B \neq \{\}$ $B \subseteq A$
shows $\text{Max-torder } r \ B \in B$
 ⟨proof⟩

lemma *Max-torder-above-iff*:
assumes $B: \text{finite } B \ B \neq \{\}$ $B \subseteq A$
shows $r \ x \ (\text{Max-torder } r \ B) \longleftrightarrow (\exists a \in B. r \ x \ a)$
 ⟨proof⟩

end

lemma *Max-torder-above*:

assumes *tot*: *torder-on* *A* *r*
and *finite* *B* *a* $\in B$ $B \subseteq A$
shows *r* *a* (*Max-torder* *r* *B*)
 ⟨*proof*⟩

lemma *inv-imageP-id* [*simp*]: *inv-imageP* *R* *id* = *R*
 ⟨*proof*⟩

lemma *inv-into-id* [*simp*]: $a \in A \implies \text{inv-into } A \text{ id } a = a$
 ⟨*proof*⟩

end

8.4 Axiomatic specification of the JMM

theory *JMM-Spec*
imports
 Orders
 ../Common/Observable-Events
 Coinductive.Coinductive-List
begin

8.4.1 Definitions

type-synonym *JMM-action* = *nat*
type-synonym ('*addr*, '*thread-id*) *execution* = ('*thread-id* \times ('*addr*, '*thread-id*) *obs-event* *action*) *llist*

definition *actions* :: ('*addr*, '*thread-id*) *execution* \Rightarrow *JMM-action* *set*
where *actions* *E* = {*n*. *enat* *n* < *llength* *E*}

definition *action-tid* :: ('*addr*, '*thread-id*) *execution* \Rightarrow *JMM-action* \Rightarrow '*thread-id*
where *action-tid* *E* *a* = *fst* (*lnth* *E* *a*)

definition *action-obs* :: ('*addr*, '*thread-id*) *execution* \Rightarrow *JMM-action* \Rightarrow ('*addr*, '*thread-id*) *obs-event* *action*
where *action-obs* *E* *a* = *snd* (*lnth* *E* *a*)

definition *tactions* :: ('*addr*, '*thread-id*) *execution* \Rightarrow '*thread-id* \Rightarrow *JMM-action* *set*
where *tactions* *E* *t* = {*a*. *a* \in *actions* *E* \wedge *action-tid* *E* *a* = *t*}

inductive *is-new-action* :: ('*addr*, '*thread-id*) *obs-event* *action* \Rightarrow *bool*
where
 NewHeapElem: *is-new-action* (*NormalAction* (*NewHeapElem* *a* *hT*))

inductive *is-write-action* :: ('*addr*, '*thread-id*) *obs-event* *action* \Rightarrow *bool*
where
 NewHeapElem: *is-write-action* (*NormalAction* (*NewHeapElem* *ad* *hT*))
 | *WriteMem*: *is-write-action* (*NormalAction* (*WriteMem* *ad* *al* *v*))

Initialisation actions are synchronisation actions iff they initialize volatile fields – cf. JMM mailing list, message no. 62 (5 Nov. 2006). However, intuitively correct programs might not be correctly synchronized:

x = 0

 $r1 = x \mid r2 = x$

Here, if x is not volatile, the initial write can belong to at most one thread. Hence, it happens-before to either $r1 = x$ or $r2 = x$, but not both. In any sequentially consistent execution, both reads must read from the initialisation action $x = 0$, but it is not happens-before ordered to one of them.

Moreover, if only volatile initialisations synchronize-with all thread-start actions, this breaks the proof of the DRF guarantee since it assumes that the happens-before relation $\leq_{hb} a$ for an action a in a topologically sorted action sequence depends only on the actions before it. Counter example: (y is volatile)

$[(t1, \text{start}), (t1, \text{init } x), (t2, \text{start}), (t1, \text{init } y), \dots]$

Here, $(t1, \text{init } x) \leq_{hb} (t2, \text{start})$ via: $(t1, \text{init } x) \leq_{po} (t1, \text{init } y) \leq_{sw} (t2, \text{start})$, but in $[(t1, \text{start}), (t1, \text{init } x), (t2, \text{start})]$, not $(t1, \text{init } x) \leq_{hb} (t2, \text{start})$.

Sevcik speculated that one might add an initialisation thread which performs all initialisation actions. All normal threads' start action would then synchronize on the final action of the initialisation thread. However, this contradicts the memory chain condition in the final field extension to the JMM (threads must read addresses of objects that they have not created themselves before they can access the fields of the object at that address) – not modelled here.

Instead, we leave every initialisation action in the thread it belongs to, but order it explicitly before the thread's start action and add synchronizes-with edges from *all* initialisation actions to *all* thread start actions.

inductive $saction :: 'm prog \Rightarrow ('addr, 'thread-id) \text{ obs-event action} \Rightarrow bool$

for $P :: 'm prog$

where

$NewHeapElem: saction P (NormalAction (NewHeapElem a hT))$
 $| Read: is-volatile P al \Longrightarrow saction P (NormalAction (ReadMem a al v))$
 $| Write: is-volatile P al \Longrightarrow saction P (NormalAction (WriteMem a al v))$
 $| ThreadStart: saction P (NormalAction (ThreadStart t))$
 $| ThreadJoin: saction P (NormalAction (ThreadJoin t))$
 $| SyncLock: saction P (NormalAction (SyncLock a))$
 $| SyncUnlock: saction P (NormalAction (SyncUnlock a))$
 $| ObsInterrupt: saction P (NormalAction (ObsInterrupt t))$
 $| ObsInterrupted: saction P (NormalAction (ObsInterrupted t))$
 $| InitialThreadAction: saction P InitialThreadAction$
 $| ThreadFinishAction: saction P ThreadFinishAction$

definition $sactions :: 'm prog \Rightarrow ('addr, 'thread-id) \text{ execution} \Rightarrow JMM\text{-action set}$

where $sactions P E = \{a. a \in actions E \wedge saction P (action\text{-obs } E a)\}$

inductive-set $write\text{-actions} :: ('addr, 'thread-id) \text{ execution} \Rightarrow JMM\text{-action set}$

for $E :: ('addr, 'thread-id) \text{ execution}$

where

$write\text{-actions}I: \llbracket a \in actions E; is\text{-write-action } (action\text{-obs } E a) \rrbracket \Longrightarrow a \in write\text{-actions } E$

$NewObj$ and $NewArr$ actions only initialize those fields and array cells that are in fact in the object or array. Hence, they are not a write for - reads from addresses for which no object/array is created during the whole execution - reads from fields/cells that are not part of the object/array at the specified address.

primrec $addr\text{-locs} :: 'm prog \Rightarrow htype \Rightarrow addr\text{-loc set}$

where

$$\begin{aligned} \text{addr-locs } P \text{ (Class-type } C) &= \{CField\ D\ F \mid D\ F\ T\ fm.\ P \vdash C \text{ has } F:T\ (fm) \text{ in } D\} \\ \text{addr-locs } P \text{ (Array-type } T\ n) &= (\{ACell\ n' \mid n'.\ n' < n\} \cup \{CField\ Object\ F \mid F.\ \exists fm\ T.\ P \vdash Object \\ &\text{ has } F:T\ (fm) \text{ in } Object\}) \end{aligned}$$

action-loc-aux would naturally be an inductive set, but *inductive_set* does not allow to pattern match on parameters. Hence, specify it using function and derive the setup manually.

fun *action-loc-aux* :: 'm prog \Rightarrow ('addr, 'thread-id) obs-event action \Rightarrow ('addr \times addr-loc) set
where

$$\begin{aligned} \text{action-loc-aux } P \text{ (NormalAction (NewHeapElem ad (Class-type } C)) &= \\ &\{(ad, CField\ D\ F) \mid D\ F\ T\ fm.\ P \vdash C \text{ has } F:T\ (fm) \text{ in } D\} \\ \text{action-loc-aux } P \text{ (NormalAction (NewHeapElem ad (Array-type } T\ n')) &= \\ &\{(ad, ACell\ n) \mid n.\ n < n'\} \cup \{(ad, CField\ D\ F) \mid D\ F\ T\ fm.\ P \vdash Object \text{ has } F:T\ (fm) \text{ in } D\} \\ \text{action-loc-aux } P \text{ (NormalAction (WriteMem ad al v))} &= \{(ad, al)\} \\ \text{action-loc-aux } P \text{ (NormalAction (ReadMem ad al v))} &= \{(ad, al)\} \\ \text{action-loc-aux } - - &= \{\} \end{aligned}$$

lemma *action-loc-aux-intros* [*intro?*]:

$$\begin{aligned} P \vdash \text{class-type-of } hT \text{ has } F:T\ (fm) \text{ in } D &\Longrightarrow (ad, CField\ D\ F) \in \text{action-loc-aux } P \text{ (NormalAction} \\ &\text{(NewHeapElem ad } hT)) \\ n < n' &\Longrightarrow (ad, ACell\ n) \in \text{action-loc-aux } P \text{ (NormalAction (NewHeapElem ad (Array-type } T\ n')) \\ (ad, al) &\in \text{action-loc-aux } P \text{ (NormalAction (WriteMem ad al v))} \\ (ad, al) &\in \text{action-loc-aux } P \text{ (NormalAction (ReadMem ad al v))} \\ \langle \text{proof} \rangle & \end{aligned}$$

lemma *action-loc-aux-cases* [*elim?*, *cases set: action-loc-aux*]:

$$\begin{aligned} \text{assumes } adal \in \text{action-loc-aux } P \text{ obs} \\ \text{obtains (NewHeapElem) } hT\ F\ T\ fm\ D\ ad \text{ where } obs = \text{NormalAction (NewHeapElem ad } hT) \text{ } adal &= \\ = (ad, CField\ D\ F) \ P \vdash \text{class-type-of } hT \text{ has } F:T\ (fm) \text{ in } D \\ \mid \text{(NewArr) } n\ n' \text{ ad } T \text{ where } obs = \text{NormalAction (NewHeapElem ad (Array-type } T\ n')) \text{ } adal = & \\ = (ad, ACell\ n) \ n < n' \\ \mid \text{(WriteMem) } ad\ al\ v \text{ where } obs = \text{NormalAction (WriteMem ad al v)} \text{ } adal = (ad, al) & \\ \mid \text{(ReadMem) } ad\ al\ v \text{ where } obs = \text{NormalAction (ReadMem ad al v)} \text{ } adal = (ad, al) & \\ \langle \text{proof} \rangle & \end{aligned}$$

lemma *action-loc-aux-simps* [*simp*]:

$$\begin{aligned} (ad', al') \in \text{action-loc-aux } P \text{ (NormalAction (NewHeapElem ad } hT)) &\longleftrightarrow \\ (\exists D\ F\ T\ fm.\ ad = ad' \wedge al' = CField\ D\ F \wedge P \vdash \text{class-type-of } hT \text{ has } F:T\ (fm) \text{ in } D) \vee & \\ (\exists n\ T\ n'.\ ad = ad' \wedge al' = ACell\ n \wedge hT = \text{Array-type } T\ n' \wedge n < n') & \\ (ad', al') \in \text{action-loc-aux } P \text{ (NormalAction (WriteMem ad al v))} &\longleftrightarrow ad = ad' \wedge al = al' \\ (ad', al') \in \text{action-loc-aux } P \text{ (NormalAction (ReadMem ad al v))} &\longleftrightarrow ad = ad' \wedge al = al' \\ (ad', al') \notin \text{action-loc-aux } P \text{ InitialThreadAction} & \\ (ad', al') \notin \text{action-loc-aux } P \text{ ThreadFinishAction} & \\ (ad', al') \notin \text{action-loc-aux } P \text{ (NormalAction (ExternalCall a m vs v))} & \\ (ad', al') \notin \text{action-loc-aux } P \text{ (NormalAction (ThreadStart t))} & \\ (ad', al') \notin \text{action-loc-aux } P \text{ (NormalAction (ThreadJoin t))} & \\ (ad', al') \notin \text{action-loc-aux } P \text{ (NormalAction (SyncLock a))} & \\ (ad', al') \notin \text{action-loc-aux } P \text{ (NormalAction (SyncUnlock a))} & \\ (ad', al') \notin \text{action-loc-aux } P \text{ (NormalAction (ObsInterrupt t))} & \\ (ad', al') \notin \text{action-loc-aux } P \text{ (NormalAction (ObsInterrupted t))} & \\ \langle \text{proof} \rangle & \end{aligned}$$

declare *action-loc-aux.simps* [*simp del*]

abbreviation $action\text{-}loc :: 'm\ prog \Rightarrow ('addr, 'thread\text{-}id)\ execution \Rightarrow JMM\text{-}action \Rightarrow ('addr \times addr\text{-}loc)\ set$

where $action\text{-}loc\ P\ E\ a \equiv action\text{-}loc\text{-}aux\ P\ (action\text{-}obs\ E\ a)$

inductive-set $read\text{-}actions :: ('addr, 'thread\text{-}id)\ execution \Rightarrow JMM\text{-}action\ set$

for $E :: ('addr, 'thread\text{-}id)\ execution$

where

$ReadMem: \llbracket a \in actions\ E; action\text{-}obs\ E\ a = NormalAction\ (ReadMem\ ad\ al\ v) \rrbracket \Longrightarrow a \in read\text{-}actions\ E$

fun $addr\text{-}loc\text{-}default :: 'm\ prog \Rightarrow htype \Rightarrow addr\text{-}loc \Rightarrow 'addr\ val$

where

$addr\text{-}loc\text{-}default\ P\ (Class\text{-}type\ C)\ (CField\ D\ F) = default\text{-}val\ (fst\ (the\ (map\text{-}of\ (fields\ P\ C)\ (F, D))))$

$| addr\text{-}loc\text{-}default\ P\ (Array\text{-}type\ T\ n)\ (ACell\ n') = default\text{-}val\ T$

$| addr\text{-}loc\text{-}default\text{-}Array\text{-}CField:$

$addr\text{-}loc\text{-}default\ P\ (Array\text{-}type\ T\ n)\ (CField\ D\ F) = default\text{-}val\ (fst\ (the\ (map\text{-}of\ (fields\ P\ Object)\ (F, Object))))$

$| addr\text{-}loc\text{-}default\ P\ _ = undefined$

definition $new\text{-}actions\text{-}for :: 'm\ prog \Rightarrow ('addr, 'thread\text{-}id)\ execution \Rightarrow ('addr \times addr\text{-}loc) \Rightarrow JMM\text{-}action\ set$

where

$new\text{-}actions\text{-}for\ P\ E\ adal =$

$\{a. a \in actions\ E \wedge adal \in action\text{-}loc\ P\ E\ a \wedge is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a)\}$

inductive-set $external\text{-}actions :: ('addr, 'thread\text{-}id)\ execution \Rightarrow JMM\text{-}action\ set$

for $E :: ('addr, 'thread\text{-}id)\ execution$

where

$\llbracket a \in actions\ E; action\text{-}obs\ E\ a = NormalAction\ (ExternalCall\ ad\ M\ vs\ v) \rrbracket$

$\Longrightarrow a \in external\text{-}actions\ E$

fun $value\text{-}written\text{-}aux :: 'm\ prog \Rightarrow ('addr, 'thread\text{-}id)\ obs\text{-}event\ action \Rightarrow addr\text{-}loc \Rightarrow 'addr\ val$

where

$value\text{-}written\text{-}aux\ P\ (NormalAction\ (NewHeapElem\ ad'\ hT))\ al = addr\text{-}loc\text{-}default\ P\ hT\ al$

$| value\text{-}written\text{-}aux\text{-}WriteMem':$

$value\text{-}written\text{-}aux\ P\ (NormalAction\ (WriteMem\ ad\ al'\ v))\ al = (if\ al = al'\ then\ v\ else\ undefined)$

$| value\text{-}written\text{-}aux\text{-}undefined:$

$value\text{-}written\text{-}aux\ P\ _ = undefined$

primrec $value\text{-}written :: 'm\ prog \Rightarrow ('addr, 'thread\text{-}id)\ execution \Rightarrow JMM\text{-}action \Rightarrow ('addr \times addr\text{-}loc) \Rightarrow 'addr\ val$

where $value\text{-}written\ P\ E\ a\ (ad, al) = value\text{-}written\text{-}aux\ P\ (action\text{-}obs\ E\ a)\ al$

definition $value\text{-}read :: ('addr, 'thread\text{-}id)\ execution \Rightarrow JMM\text{-}action \Rightarrow 'addr\ val$

where

$value\text{-}read\ E\ a =$

$(case\ action\text{-}obs\ E\ a\ of$

$NormalAction\ obs \Rightarrow$

$(case\ obs\ of$

$ReadMem\ ad\ al\ v \Rightarrow v$

$| _ \Rightarrow undefined)$

$| _ \Rightarrow undefined)$

definition $action\text{-}order :: ('addr, 'thread\text{-}id)\ execution \Rightarrow JMM\text{-}action \Rightarrow JMM\text{-}action \Rightarrow bool\ (\leftarrow \vdash)$

$\vdash a \rightarrow [51, 0, 50] 50$

where

$E \vdash a \leq a' \iff$
 $a \in \text{actions } E \wedge a' \in \text{actions } E \wedge$
(if is-new-action (action-obs E a)
then is-new-action (action-obs E a') $\implies a \leq a'$
else \neg is-new-action (action-obs E a') $\wedge a \leq a'$)

definition *program-order* :: ('addr, 'thread-id) execution \Rightarrow JMM-action \Rightarrow JMM-action \Rightarrow bool (\vdash
 $\vdash \vdash a \rightarrow [51, 0, 50] 50$)

where

$E \vdash a \leq_{po} a' \iff E \vdash a \leq a' \wedge \text{action-tid } E a = \text{action-tid } E a'$

inductive *synchronizes-with* ::

'm prog
 \Rightarrow ('thread-id \times ('addr, 'thread-id) obs-event action) \Rightarrow ('thread-id \times ('addr, 'thread-id) obs-event
action) \Rightarrow bool
($\vdash \vdash \vdash a \rightarrow [51, 51, 51] 50$)

for $P ::$ 'm prog

where

ThreadStart: $P \vdash (t, \text{NormalAction } (\text{ThreadStart } t')) \rightsquigarrow_{sw} (t', \text{InitialThreadAction})$
| *ThreadFinish*: $P \vdash (t, \text{ThreadFinishAction}) \rightsquigarrow_{sw} (t', \text{NormalAction } (\text{ThreadJoin } t))$
| *UnlockLock*: $P \vdash (t, \text{NormalAction } (\text{SyncUnlock } a)) \rightsquigarrow_{sw} (t', \text{NormalAction } (\text{SyncLock } a))$
| — Only volatile writes synchronize with volatile reads. We could check volatility of al here, but this
is checked by *sactions* in *sync-with* anyway.

Volatile: $P \vdash (t, \text{NormalAction } (\text{WriteMem } a al v)) \rightsquigarrow_{sw} (t', \text{NormalAction } (\text{ReadMem } a al v'))$

| *VolatileNew*:

$al \in \text{addr-locs } P hT$

$\implies P \vdash (t, \text{NormalAction } (\text{NewHeapElem } a hT)) \rightsquigarrow_{sw} (t', \text{NormalAction } (\text{ReadMem } a al v))$

| *NewHeapElem*: $P \vdash (t, \text{NormalAction } (\text{NewHeapElem } a hT)) \rightsquigarrow_{sw} (t', \text{InitialThreadAction})$

| *Interrupt*: $P \vdash (t, \text{NormalAction } (\text{ObsInterrupt } t')) \rightsquigarrow_{sw} (t'', \text{NormalAction } (\text{ObsInterrupted } t'))$

definition *sync-order* ::

'm prog \Rightarrow ('addr, 'thread-id) execution \Rightarrow JMM-action \Rightarrow JMM-action \Rightarrow bool
($\vdash \vdash \vdash a \rightarrow [51, 0, 0, 50] 50$)

where

$P, E \vdash a \leq_{so} a' \iff a \in \text{sactions } P E \wedge a' \in \text{sactions } P E \wedge E \vdash a \leq a'$

definition *sync-with* ::

'm prog \Rightarrow ('addr, 'thread-id) execution \Rightarrow JMM-action \Rightarrow JMM-action \Rightarrow bool
($\vdash \vdash \vdash a \rightarrow [51, 0, 0, 50] 50$)

where

$P, E \vdash a \leq_{sw} a' \iff$

$P, E \vdash a \leq_{so} a' \wedge P \vdash (\text{action-tid } E a, \text{action-obs } E a) \rightsquigarrow_{sw} (\text{action-tid } E a', \text{action-obs } E a')$

definition *po-sw* :: 'm prog \Rightarrow ('addr, 'thread-id) execution \Rightarrow JMM-action \Rightarrow JMM-action \Rightarrow bool

where *po-sw* $P E a a' \iff E \vdash a \leq_{po} a' \vee P, E \vdash a \leq_{sw} a'$

abbreviation *happens-before* ::

'm prog \Rightarrow ('addr, 'thread-id) execution \Rightarrow JMM-action \Rightarrow JMM-action \Rightarrow bool
($\vdash \vdash \vdash a \rightarrow [51, 0, 0, 50] 50$)

where *happens-before* $P E \equiv (\text{po-sw } P E)^{++}$

type-synonym *write-seen* = JMM-action \Rightarrow JMM-action

definition *is-write-seen* :: 'm prog \Rightarrow ('addr, 'thread-id) execution \Rightarrow write-seen \Rightarrow bool **where**
is-write-seen P E ws \longleftrightarrow
 $(\forall a \in \text{read-actions } E. \forall ad \ al \ v. \text{action-obs } E \ a = \text{NormalAction } (\text{ReadMem } ad \ al \ v) \longrightarrow$
 $ws \ a \in \text{write-actions } E \wedge (ad, al) \in \text{action-loc } P \ E \ (ws \ a) \wedge$
 $\text{value-written } P \ E \ (ws \ a) \ (ad, al) = v \wedge \neg P, E \vdash a \leq_{hb} ws \ a \wedge$
 $(\text{is-volatile } P \ al \longrightarrow \neg P, E \vdash a \leq_{so} ws \ a) \wedge$
 $(\forall w' \in \text{write-actions } E. (ad, al) \in \text{action-loc } P \ E \ w' \longrightarrow$
 $(P, E \vdash ws \ a \leq_{hb} w' \wedge P, E \vdash w' \leq_{hb} a \vee \text{is-volatile } P \ al \wedge P, E \vdash ws \ a \leq_{so} w' \wedge P, E \vdash w'$
 $\leq_{so} a) \longrightarrow$
 $w' = ws \ a))$

definition *thread-start-actions-ok* :: ('addr, 'thread-id) execution \Rightarrow bool
where
thread-start-actions-ok E \longleftrightarrow

$(\forall a \in \text{actions } E. \neg \text{is-new-action } (\text{action-obs } E \ a) \longrightarrow$
 $(\exists i. i \leq a \wedge \text{action-obs } E \ i = \text{InitialThreadAction} \wedge \text{action-tid } E \ i = \text{action-tid } E \ a))$

primrec *wf-exec* :: 'm prog \Rightarrow ('addr, 'thread-id) execution \times write-seen \Rightarrow bool ($\langle \cdot \vdash \cdot \sqrt{\cdot} \rangle$ [51, 50]
51)

where $P \vdash (E, ws) \sqrt{\cdot} \longleftrightarrow \text{is-write-seen } P \ E \ ws \wedge \text{thread-start-actions-ok } E$

inductive *most-recent-write-for* :: 'm prog \Rightarrow ('addr, 'thread-id) execution \Rightarrow JMM-action \Rightarrow JMM-action
 \Rightarrow bool

$(\langle \cdot, \cdot \vdash \cdot \rightsquigarrow mrw \rightarrow [50, 0, 51] \ 51)$

for P :: 'm prog **and** E :: ('addr, 'thread-id) execution **and** ra :: JMM-action **and** wa :: JMM-action

where

$\llbracket ra \in \text{read-actions } E; adal \in \text{action-loc } P \ E \ ra; E \vdash wa \leq_a ra;$
 $wa \in \text{write-actions } E; adal \in \text{action-loc } P \ E \ wa;$
 $\bigwedge wa'. \llbracket wa' \in \text{write-actions } E; adal \in \text{action-loc } P \ E \ wa' \rrbracket$
 $\implies E \vdash wa' \leq_a wa \vee E \vdash ra \leq_a wa'$
 $\implies P, E \vdash ra \rightsquigarrow mrw \ wa$

primrec *sequentially-consistent* :: 'm prog \Rightarrow (('addr, 'thread-id) execution \times write-seen) \Rightarrow bool

where

sequentially-consistent P (E, ws) $\longleftrightarrow (\forall r \in \text{read-actions } E. P, E \vdash r \rightsquigarrow mrw \ ws \ r)$

8.4.2 Actions

inductive-cases *is-new-action-cases* [elim!]:

is-new-action (NormalAction (ExternalCall a M vs v))
is-new-action (NormalAction (ReadMem a al v))
is-new-action (NormalAction (WriteMem a al v))
is-new-action (NormalAction (NewHeapElem a hT))
is-new-action (NormalAction (ThreadStart t))
is-new-action (NormalAction (ThreadJoin t))
is-new-action (NormalAction (SyncLock a))
is-new-action (NormalAction (SyncUnlock a))
is-new-action (NormalAction (ObsInterrupt t))
is-new-action (NormalAction (ObsInterrupted t))
is-new-action InitialThreadAction
is-new-action ThreadFinishAction

inductive-simps *is-new-action-simps* [simp]:

```

is-new-action (NormalAction (NewHeapElem a hT))
is-new-action (NormalAction (ExternalCall a M vs v))
is-new-action (NormalAction (ReadMem a al v))
is-new-action (NormalAction (WriteMem a al v))
is-new-action (NormalAction (ThreadStart t))
is-new-action (NormalAction (ThreadJoin t))
is-new-action (NormalAction (SyncLock a))
is-new-action (NormalAction (SyncUnlock a))
is-new-action (NormalAction (ObsInterrupt t))
is-new-action (NormalAction (ObsInterrupted t))
is-new-action InitialThreadAction
is-new-action ThreadFinishAction

```

lemmas *is-new-action-iff* = *is-new-action.simps*

inductive-simps *is-write-action-simps* [*simp*]:

```

is-write-action InitialThreadAction
is-write-action ThreadFinishAction
is-write-action (NormalAction (ExternalCall a m vs v))
is-write-action (NormalAction (ReadMem a al v))
is-write-action (NormalAction (WriteMem a al v))
is-write-action (NormalAction (NewHeapElem a hT))
is-write-action (NormalAction (ThreadStart t))
is-write-action (NormalAction (ThreadJoin t))
is-write-action (NormalAction (SyncLock a))
is-write-action (NormalAction (SyncUnlock a))
is-write-action (NormalAction (ObsInterrupt t))
is-write-action (NormalAction (ObsInterrupted t))

```

declare *saction.intros* [*intro!*]

inductive-cases *saction-cases* [*elim!*]:

```

saction P (NormalAction (ExternalCall a M vs v))
saction P (NormalAction (ReadMem a al v))
saction P (NormalAction (WriteMem a al v))
saction P (NormalAction (NewHeapElem a hT))
saction P (NormalAction (ThreadStart t))
saction P (NormalAction (ThreadJoin t))
saction P (NormalAction (SyncLock a))
saction P (NormalAction (SyncUnlock a))
saction P (NormalAction (ObsInterrupt t))
saction P (NormalAction (ObsInterrupted t))
saction P InitialThreadAction
saction P ThreadFinishAction

```

inductive-simps *saction-simps* [*simp*]:

```

saction P (NormalAction (ExternalCall a M vs v))
saction P (NormalAction (ReadMem a al v))
saction P (NormalAction (WriteMem a al v))
saction P (NormalAction (NewHeapElem a hT))
saction P (NormalAction (ThreadStart t))
saction P (NormalAction (ThreadJoin t))
saction P (NormalAction (SyncLock a))
saction P (NormalAction (SyncUnlock a))

```

saction P (NormalAction (ObsInterrupt t))
saction P (NormalAction (ObsInterrupted t))
saction P InitialThreadAction
saction P ThreadFinishAction

lemma *new-action-saction* [*simp*, *intro*]: *is-new-action a* \implies *saction P a*
 ⟨*proof*⟩

lemmas *saction-iff* = *saction.simps*

lemma *actionsD*: *a* \in *actions E* \implies *enat a* < *llength E*
 ⟨*proof*⟩

lemma *actionsE*:
assumes *a* \in *actions E*
obtains *enat a* < *llength E*
 ⟨*proof*⟩

lemma *actions-lappend*:
llength xs = *enat n* \implies *actions (lappend xs ys)* = *actions xs* \cup ((+) *n*) ‘ *actions ys*
 ⟨*proof*⟩

lemma *tactionsE*:
assumes *a* \in *tactions E t*
obtains *obs* **where** *a* \in *actions E* *action-tid E a* = *t* *action-obs E a* = *obs*
 ⟨*proof*⟩

lemma *sactionsI*:
 $\llbracket a \in \text{actions } E; \text{saction } P (\text{action-obs } E a) \rrbracket \implies a \in \text{sactions } P E$
 ⟨*proof*⟩

lemma *sactionsE*:
assumes *a* \in *sactions P E*
obtains *a* \in *actions E* *saction P (action-obs E a)*
 ⟨*proof*⟩

lemma *sactions-actions* [*simp*]:
a \in *sactions P E* \implies *a* \in *actions E*
 ⟨*proof*⟩

lemma *value-written-aux-WriteMem* [*simp*]:
value-written-aux P (NormalAction (WriteMem ad al v)) al = *v*
 ⟨*proof*⟩

declare *value-written-aux-undefined* [*simp del*]
declare *value-written-aux-WriteMem'* [*simp del*]

inductive-simps *is-write-action-iff*:
is-write-action a

inductive-simps *write-actions-iff*:
a \in *write-actions E*

lemma *write-actions-actions* [*simp*]:

$a \in \text{write-actions } E \implies a \in \text{actions } E$
 ⟨proof⟩

inductive-simps *read-actions-iff*:
 $a \in \text{read-actions } E$

lemma *read-actions-actions* [simp]:
 $a \in \text{read-actions } E \implies a \in \text{actions } E$
 ⟨proof⟩

lemma *read-action-action-locE*:
assumes $r \in \text{read-actions } E$
obtains $ad\ al$ **where** $(ad, al) \in \text{action-loc } P\ E\ r$
 ⟨proof⟩

lemma *read-actions-not-write-actions*:
 $\llbracket a \in \text{read-actions } E; a \in \text{write-actions } E \rrbracket \implies \text{False}$
 ⟨proof⟩

lemma *read-actions-Int-write-actions* [simp]:
 $\text{read-actions } E \cap \text{write-actions } E = \{\}$
 $\text{write-actions } E \cap \text{read-actions } E = \{\}$
 ⟨proof⟩

lemma *action-loc-addr-fun*:
 $\llbracket (ad, al) \in \text{action-loc } P\ E\ a; (ad', al') \in \text{action-loc } P\ E\ a \rrbracket \implies ad = ad'$
 ⟨proof⟩

lemma *value-written-cong* [cong]:
 $\llbracket P = P'; a = a'; \text{action-obs } E\ a' = \text{action-obs } E'\ a' \rrbracket$
 $\implies \text{value-written } P\ E\ a = \text{value-written } P'\ E'\ a'$
 ⟨proof⟩

declare *value-written.simps* [simp del]

lemma *new-actionsI*:
 $\llbracket a \in \text{actions } E; adal \in \text{action-loc } P\ E\ a; \text{is-new-action } (\text{action-obs } E\ a) \rrbracket$
 $\implies a \in \text{new-actions-for } P\ E\ adal$
 ⟨proof⟩

lemma *new-actionsE*:
assumes $a \in \text{new-actions-for } P\ E\ adal$
obtains $a \in \text{actions } E\ adal \in \text{action-loc } P\ E\ a\ \text{is-new-action } (\text{action-obs } E\ a)$
 ⟨proof⟩

lemma *action-loc-read-action-singleton*:
 $\llbracket r \in \text{read-actions } E; adal \in \text{action-loc } P\ E\ r; adal' \in \text{action-loc } P\ E\ r \rrbracket \implies adal = adal'$
 ⟨proof⟩

lemma *addr-locsI*:
 $P \vdash \text{class-type-of } hT \text{ has } F:T \text{ (fm) in } D \implies CField\ D\ F \in \text{addr-locs } P\ hT$
 $\llbracket hT = \text{Array-type } T\ n; n' < n \rrbracket \implies ACell\ n' \in \text{addr-locs } P\ hT$
 ⟨proof⟩

8.4.3 Orders

8.4.4 Action order

lemma *action-orderI*:

assumes $a \in \text{actions } E$ $a' \in \text{actions } E$

and $\llbracket \text{is-new-action } (\text{action-obs } E a); \text{is-new-action } (\text{action-obs } E a') \rrbracket \implies a \leq a'$

and $\neg \text{is-new-action } (\text{action-obs } E a) \implies \neg \text{is-new-action } (\text{action-obs } E a') \wedge a \leq a'$

shows $E \vdash a \leq a'$

<proof>

lemma *action-orderE*:

assumes $E \vdash a \leq a'$

obtains $a \in \text{actions } E$ $a' \in \text{actions } E$

$\text{is-new-action } (\text{action-obs } E a) \text{ is-new-action } (\text{action-obs } E a') \longrightarrow a \leq a'$

$\mid a \in \text{actions } E$ $a' \in \text{actions } E$

$\neg \text{is-new-action } (\text{action-obs } E a) \neg \text{is-new-action } (\text{action-obs } E a') a \leq a'$

<proof>

lemma *refl-action-order*:

$\text{refl-onP } (\text{actions } E) (\text{action-order } E)$

<proof>

lemma *antisym-action-order*:

$\text{antisymp } (\text{action-order } E)$

<proof>

lemma *trans-action-order*:

$\text{transp } (\text{action-order } E)$

<proof>

lemma *porder-action-order*:

$\text{porder-on } (\text{actions } E) (\text{action-order } E)$

<proof>

lemma *total-action-order*:

$\text{total-onP } (\text{actions } E) (\text{action-order } E)$

<proof>

lemma *torder-action-order*:

$\text{torder-on } (\text{actions } E) (\text{action-order } E)$

<proof>

lemma *wf-action-order*: $\text{wfP } (\text{action-order } E) \neq \neq$

<proof>

lemma *action-order-is-new-actionD*:

$\llbracket E \vdash a \leq a'; \text{is-new-action } (\text{action-obs } E a') \rrbracket \implies \text{is-new-action } (\text{action-obs } E a)$

<proof>

8.4.5 Program order

lemma *program-orderI*:

assumes $E \vdash a \leq a'$ **and** $\text{action-tid } E a = \text{action-tid } E a'$

shows $E \vdash a \leq_{po} a'$

<proof>

lemma *program-orderE*:

assumes $E \vdash a \leq_{po} a'$

obtains $t \text{ obs } obs'$

where $E \vdash a \leq_a a'$

and $\text{action-tid } E \ a = t \ \text{action-obs } E \ a = \text{obs}$

and $\text{action-tid } E \ a' = t \ \text{action-obs } E \ a' = \text{obs}'$

<proof>

lemma *refl-on-program-order*:

$\text{refl-onP } (\text{actions } E) \ (\text{program-order } E)$

<proof>

lemma *antisym-program-order*:

$\text{antisymp } (\text{program-order } E)$

<proof>

lemma *trans-program-order*:

$\text{transp } (\text{program-order } E)$

<proof>

lemma *porder-program-order*:

$\text{porder-on } (\text{actions } E) \ (\text{program-order } E)$

<proof>

lemma *total-program-order-on-tactions*:

$\text{total-onP } (\text{tactions } E \ t) \ (\text{program-order } E)$

<proof>

8.4.6 Synchronization order

lemma *sync-orderI*:

$\llbracket E \vdash a \leq_a a'; a \in \text{sactions } P \ E; a' \in \text{sactions } P \ E \rrbracket \implies P, E \vdash a \leq_{so} a'$

<proof>

lemma *sync-orderE*:

assumes $P, E \vdash a \leq_{so} a'$

obtains $a \in \text{sactions } P \ E \ a' \in \text{sactions } P \ E \ E \vdash a \leq_a a'$

<proof>

lemma *refl-on-sync-order*:

$\text{refl-onP } (\text{sactions } P \ E) \ (\text{sync-order } P \ E)$

<proof>

lemma *antisym-sync-order*:

$\text{antisymp } (\text{sync-order } P \ E)$

<proof>

lemma *trans-sync-order*:

$\text{transp } (\text{sync-order } P \ E)$

<proof>

lemma *porder-sync-order*:

porder-on (*sactions* P E) (*sync-order* P E)
 ⟨proof⟩

lemma *total-sync-order*:
total-on P (*sactions* P E) (*sync-order* P E)
 ⟨proof⟩

lemma *torder-sync-order*:
torder-on (*sactions* P E) (*sync-order* P E)
 ⟨proof⟩

8.4.7 Synchronizes with

lemma *sync-withI*:
 $\llbracket P, E \vdash a \leq_{so} a'; P \vdash (\text{action-tid } E \ a, \text{action-obs } E \ a) \rightsquigarrow_{sw} (\text{action-tid } E \ a', \text{action-obs } E \ a') \rrbracket$
 $\implies P, E \vdash a \leq_{sw} a'$
 ⟨proof⟩

lemma *sync-withE*:
assumes $P, E \vdash a \leq_{sw} a'$
obtains $P, E \vdash a \leq_{so} a' P \vdash (\text{action-tid } E \ a, \text{action-obs } E \ a) \rightsquigarrow_{sw} (\text{action-tid } E \ a', \text{action-obs } E \ a')$
 ⟨proof⟩

lemma *irrefl-synchronizes-with*:
irrefl P (*synchronizes-with* P)
 ⟨proof⟩

lemma *irrefl-sync-with*:
irrefl P (*sync-with* P E)
 ⟨proof⟩

lemma *antisym-sync-with*:
antisymp (*sync-with* P E)
 ⟨proof⟩

lemma *consistent-program-order-sync-order*:
order-consistent (*program-order* E) (*sync-order* P E)
 ⟨proof⟩

lemma *consistent-program-order-sync-with*:
order-consistent (*program-order* E) (*sync-with* P E)
 ⟨proof⟩

8.4.8 Happens before

lemma *porder-happens-before*:
porder-on (*actions* E) (*happens-before* P E)
 ⟨proof⟩

lemma *porder-tranclp-po-so*:
porder-on (*actions* E) $(\lambda a \ a'. \text{program-order } E \ a \ a' \vee \text{sync-order } P \ E \ a \ a')^{\wedge++}$
 ⟨proof⟩

lemma *happens-before-refl*:

assumes $a \in \text{actions } E$
shows $P, E \vdash a \leq_{hb} a$
 ⟨proof⟩

lemma *happens-before-into-po-so-tranclp*:
assumes $P, E \vdash a \leq_{hb} a'$
shows $(\lambda a a'. E \vdash a \leq_{po} a' \vee P, E \vdash a \leq_{so} a')^{\wedge++} a a'$
 ⟨proof⟩

lemma *po-sw-into-action-order*:
 $po-sw P E a a' \implies E \vdash a \leq a'$
 ⟨proof⟩

lemma *happens-before-into-action-order*:
assumes $P, E \vdash a \leq_{hb} a'$
shows $E \vdash a \leq a'$
 ⟨proof⟩

lemma *action-order-consistent-with-happens-before*:
 $order-consistent (action-order E) (happens-before P E)$
 ⟨proof⟩

lemma *happens-before-new-actionD*:
assumes $hb: P, E \vdash a \leq_{hb} a'$
and $new: is-new-action (action-obs E a')$
shows $is-new-action (action-obs E a) \wedge action-tid E a = action-tid E a' \implies a \leq a'$
 ⟨proof⟩

lemma *external-actions-not-new*:
 $\llbracket a \in \text{external-actions } E; is-new-action (action-obs E a) \rrbracket \implies False$
 ⟨proof⟩

8.4.9 Most recent writes and sequential consistency

lemma *most-recent-write-for-fun*:
 $\llbracket P, E \vdash ra \rightsquigarrow_{mrw} wa; P, E \vdash ra \rightsquigarrow_{mrw} wa' \rrbracket \implies wa = wa'$
 ⟨proof⟩

lemma *THE-most-recent-writeI*: $P, E \vdash r \rightsquigarrow_{mrw} w \implies (THE w. P, E \vdash r \rightsquigarrow_{mrw} w) = w$
 ⟨proof⟩

lemma *most-recent-write-for-write-actionsD*:
assumes $P, E \vdash ra \rightsquigarrow_{mrw} wa$
shows $wa \in \text{write-actions } E$
 ⟨proof⟩

lemma *most-recent-write-recent*:
 $\llbracket P, E \vdash r \rightsquigarrow_{mrw} w; adal \in \text{action-loc } P E r; w' \in \text{write-actions } E; adal \in \text{action-loc } P E w' \rrbracket$
 $\implies E \vdash w' \leq a w \vee E \vdash r \leq a w'$
 ⟨proof⟩

lemma *is-write-seenI*:
 $\llbracket \bigwedge a ad al v. \llbracket a \in \text{read-actions } E; action-obs E a = \text{NormalAction } (\text{ReadMem } ad al v) \rrbracket$
 $\implies ws a \in \text{write-actions } E;$

$$\begin{aligned}
& \llbracket \bigwedge a \text{ ad al } v. \llbracket a \in \text{read-actions } E; \text{action-obs } E a = \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } v) \rrbracket \\
& \implies (ad, al) \in \text{action-loc } P E (ws a); \\
& \llbracket \bigwedge a \text{ ad al } v. \llbracket a \in \text{read-actions } E; \text{action-obs } E a = \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } v) \rrbracket \\
& \implies \text{value-written } P E (ws a) (ad, al) = v; \\
& \llbracket \bigwedge a \text{ ad al } v. \llbracket a \in \text{read-actions } E; \text{action-obs } E a = \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } v) \rrbracket \\
& \implies \neg P, E \vdash a \leq_{hb} ws a; \\
& \llbracket \bigwedge a \text{ ad al } v. \llbracket a \in \text{read-actions } E; \text{action-obs } E a = \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } v); \text{is-volatile } \\
& P \text{ al} \rrbracket \\
& \implies \neg P, E \vdash a \leq_{so} ws a; \\
& \llbracket \bigwedge a \text{ ad al } v \text{ a}'. \llbracket a \in \text{read-actions } E; \text{action-obs } E a = \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } v); \\
& \quad \text{a}' \in \text{write-actions } E; (ad, al) \in \text{action-loc } P E \text{ a}'; P, E \vdash ws a \leq_{hb} \text{a}'; \\
& \quad P, E \vdash \text{a}' \leq_{hb} a \rrbracket \implies \text{a}' = ws a; \\
& \llbracket \bigwedge a \text{ ad al } v \text{ a}'. \llbracket a \in \text{read-actions } E; \text{action-obs } E a = \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } v); \\
& \quad \text{a}' \in \text{write-actions } E; (ad, al) \in \text{action-loc } P E \text{ a}'; \text{is-volatile } P \text{ al}; P, E \vdash ws a \leq_{so} \text{a}'; \\
& \quad P, E \vdash \text{a}' \leq_{so} a \rrbracket \implies \text{a}' = ws a \rrbracket \\
& \implies \text{is-write-seen } P E ws \\
& \langle \text{proof} \rangle
\end{aligned}$$

lemma *is-write-seenD*:

$$\begin{aligned}
& \llbracket \text{is-write-seen } P E ws; a \in \text{read-actions } E; \text{action-obs } E a = \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } v) \rrbracket \\
& \implies ws a \in \text{write-actions } E \wedge (ad, al) \in \text{action-loc } P E (ws a) \wedge \text{value-written } P E (ws a) (ad, al) \\
& = v \wedge \neg P, E \vdash a \leq_{hb} ws a \wedge (\text{is-volatile } P \text{ al} \longrightarrow \neg P, E \vdash a \leq_{so} ws a) \wedge \\
& (\forall \text{a}' \in \text{write-actions } E. (ad, al) \in \text{action-loc } P E \text{ a}' \wedge (P, E \vdash ws a \leq_{hb} \text{a}' \wedge P, E \vdash \text{a}' \leq_{hb} a \vee \\
& \text{is-volatile } P \text{ al} \wedge P, E \vdash ws a \leq_{so} \text{a}' \wedge P, E \vdash \text{a}' \leq_{so} a) \longrightarrow \text{a}' = ws a) \\
& \langle \text{proof} \rangle
\end{aligned}$$

lemma *thread-start-actions-okI*:

$$\begin{aligned}
& (\bigwedge a. \llbracket a \in \text{actions } E; \neg \text{is-new-action } (\text{action-obs } E a) \rrbracket \\
& \implies \exists i. i \leq a \wedge \text{action-obs } E i = \text{InitialThreadAction} \wedge \text{action-tid } E i = \text{action-tid } E a) \\
& \implies \text{thread-start-actions-ok } E \\
& \langle \text{proof} \rangle
\end{aligned}$$

lemma *thread-start-actions-okD*:

$$\begin{aligned}
& \llbracket \text{thread-start-actions-ok } E; a \in \text{actions } E; \neg \text{is-new-action } (\text{action-obs } E a) \rrbracket \\
& \implies \exists i. i \leq a \wedge \text{action-obs } E i = \text{InitialThreadAction} \wedge \text{action-tid } E i = \text{action-tid } E a \\
& \langle \text{proof} \rangle
\end{aligned}$$

lemma *thread-start-actions-ok-prefix*:

$$\llbracket \text{thread-start-actions-ok } E'; \text{lprefix } E E' \rrbracket \implies \text{thread-start-actions-ok } E$$

<proof>

lemma *wf-execI* [*intro?*]:

$$\begin{aligned}
& \llbracket \text{is-write-seen } P E ws; \\
& \quad \text{thread-start-actions-ok } E \rrbracket \\
& \implies P \vdash (E, ws) \checkmark \\
& \langle \text{proof} \rangle
\end{aligned}$$

lemma *wf-exec-is-write-seenD*:

$$P \vdash (E, ws) \checkmark \implies \text{is-write-seen } P E ws$$

<proof>

lemma *wf-exec-thread-start-actions-okD*:

$$P \vdash (E, ws) \checkmark \implies \text{thread-start-actions-ok } E$$

<proof>

lemma *sequentially-consistentI*:

$(\bigwedge r. r \in \text{read-actions } E \implies P, E \vdash r \rightsquigarrow_{\text{mrw}} ws \ r)$
 $\implies \text{sequentially-consistent } P \ (E, ws)$

$\langle \text{proof} \rangle$

lemma *sequentially-consistentE*:

assumes *sequentially-consistent* $P \ (E, ws)$ $a \in \text{read-actions } E$
obtains $P, E \vdash a \rightsquigarrow_{\text{mrw}} ws \ a$

$\langle \text{proof} \rangle$

declare *sequentially-consistent.simps* [*simp del*]

8.4.10 Similar actions

Similar actions differ only in the values written/read.

inductive *sim-action* ::

$(\text{'addr}, \text{'thread-id}) \text{ obs-event action} \Rightarrow (\text{'addr}, \text{'thread-id}) \text{ obs-event action} \Rightarrow \text{bool}$
 $(\leftarrow \approx \rightarrow [50, 50] \ 51)$

where

InitialThreadAction: $\text{InitialThreadAction} \approx \text{InitialThreadAction}$
| *ThreadFinishAction*: $\text{ThreadFinishAction} \approx \text{ThreadFinishAction}$
| *NewHeapElem*: $\text{NormalAction} \ (\text{NewHeapElem } a \ hT) \approx \text{NormalAction} \ (\text{NewHeapElem } a \ hT)$
| *ReadMem*: $\text{NormalAction} \ (\text{ReadMem } ad \ al \ v) \approx \text{NormalAction} \ (\text{ReadMem } ad \ al \ v')$
| *WriteMem*: $\text{NormalAction} \ (\text{WriteMem } ad \ al \ v) \approx \text{NormalAction} \ (\text{WriteMem } ad \ al \ v')$
| *ThreadStart*: $\text{NormalAction} \ (\text{ThreadStart } t) \approx \text{NormalAction} \ (\text{ThreadStart } t)$
| *ThreadJoin*: $\text{NormalAction} \ (\text{ThreadJoin } t) \approx \text{NormalAction} \ (\text{ThreadJoin } t)$
| *SyncLock*: $\text{NormalAction} \ (\text{SyncLock } a) \approx \text{NormalAction} \ (\text{SyncLock } a)$
| *SyncUnlock*: $\text{NormalAction} \ (\text{SyncUnlock } a) \approx \text{NormalAction} \ (\text{SyncUnlock } a)$
| *ExternalCall*: $\text{NormalAction} \ (\text{ExternalCall } a \ M \ vs \ v) \approx \text{NormalAction} \ (\text{ExternalCall } a \ M \ vs \ v)$
| *ObsInterrupt*: $\text{NormalAction} \ (\text{ObsInterrupt } t) \approx \text{NormalAction} \ (\text{ObsInterrupt } t)$
| *ObsInterrupted*: $\text{NormalAction} \ (\text{ObsInterrupted } t) \approx \text{NormalAction} \ (\text{ObsInterrupted } t)$

definition *sim-actions* :: $(\text{'addr}, \text{'thread-id}) \text{ execution} \Rightarrow (\text{'addr}, \text{'thread-id}) \text{ execution} \Rightarrow \text{bool}$ $(\leftarrow [\approx]$
 $\rightarrow [51, 50] \ 51)$

where *sim-actions* = *llist-all2* $(\lambda(t, a) \ (t', a'). \ t = t' \wedge a \approx a')$

lemma *sim-action-refl* [*intro!*, *simp*]:

$obs \approx obs$

$\langle \text{proof} \rangle$

inductive-cases *sim-action-cases* [*elim!*]:

InitialThreadAction $\approx obs$
ThreadFinishAction $\approx obs$
NormalAction $(\text{NewHeapElem } a \ hT) \approx obs$
NormalAction $(\text{ReadMem } ad \ al \ v) \approx obs$
NormalAction $(\text{WriteMem } ad \ al \ v) \approx obs$
NormalAction $(\text{ThreadStart } t) \approx obs$
NormalAction $(\text{ThreadJoin } t) \approx obs$
NormalAction $(\text{SyncLock } a) \approx obs$
NormalAction $(\text{SyncUnlock } a) \approx obs$
NormalAction $(\text{ObsInterrupt } t) \approx obs$
NormalAction $(\text{ObsInterrupted } t) \approx obs$
NormalAction $(\text{ExternalCall } a \ M \ vs \ v) \approx obs$

$obs \approx InitialThreadAction$
 $obs \approx ThreadFinishAction$
 $obs \approx NormalAction (NewHeapElem a hT)$
 $obs \approx NormalAction (ReadMem ad al v')$
 $obs \approx NormalAction (WriteMem ad al v')$
 $obs \approx NormalAction (ThreadStart t)$
 $obs \approx NormalAction (ThreadJoin t)$
 $obs \approx NormalAction (SyncLock a)$
 $obs \approx NormalAction (SyncUnlock a)$
 $obs \approx NormalAction (ObsInterrupt t)$
 $obs \approx NormalAction (ObsInterrupted t)$
 $obs \approx NormalAction (ExternalCall a M vs v)$

inductive-simps *sim-action-simps* [*simp*]:

$InitialThreadAction \approx obs$
 $ThreadFinishAction \approx obs$
 $NormalAction (NewHeapElem a hT) \approx obs$
 $NormalAction (ReadMem ad al v) \approx obs$
 $NormalAction (WriteMem ad al v) \approx obs$
 $NormalAction (ThreadStart t) \approx obs$
 $NormalAction (ThreadJoin t) \approx obs$
 $NormalAction (SyncLock a) \approx obs$
 $NormalAction (SyncUnlock a) \approx obs$
 $NormalAction (ObsInterrupt t) \approx obs$
 $NormalAction (ObsInterrupted t) \approx obs$
 $NormalAction (ExternalCall a M vs v) \approx obs$

$obs \approx InitialThreadAction$
 $obs \approx ThreadFinishAction$
 $obs \approx NormalAction (NewHeapElem a hT)$
 $obs \approx NormalAction (ReadMem ad al v')$
 $obs \approx NormalAction (WriteMem ad al v')$
 $obs \approx NormalAction (ThreadStart t)$
 $obs \approx NormalAction (ThreadJoin t)$
 $obs \approx NormalAction (SyncLock a)$
 $obs \approx NormalAction (SyncUnlock a)$
 $obs \approx NormalAction (ObsInterrupt t)$
 $obs \approx NormalAction (ObsInterrupted t)$
 $obs \approx NormalAction (ExternalCall a M vs v)$

lemma *sim-action-trans* [*trans*]:

$\llbracket obs \approx obs'; obs' \approx obs'' \rrbracket \implies obs \approx obs''$
 $\langle proof \rangle$

lemma *sim-action-sym* [*sym*]:

assumes $obs \approx obs'$
shows $obs' \approx obs$
 $\langle proof \rangle$

lemma *sim-actions-sym* [*sym*]:

$E [\approx] E' \implies E' [\approx] E$
 $\langle proof \rangle$

lemma *sim-actions-action-obsD*:

$E [\approx] E' \implies \text{action-obs } E \ a \approx \text{action-obs } E' \ a$
 ⟨proof⟩

lemma *sim-actions-action-tidD*:

$E [\approx] E' \implies \text{action-tid } E \ a = \text{action-tid } E' \ a$
 ⟨proof⟩

lemma *action-loc-aux-sim-action*:

$a \approx a' \implies \text{action-loc-aux } P \ a = \text{action-loc-aux } P \ a'$
 ⟨proof⟩

lemma *eq-into-sim-actions*:

assumes $E = E'$
shows $E [\approx] E'$
 ⟨proof⟩

8.4.11 Well-formedness conditions for execution sets

locale *executions-base* =

fixes $\mathcal{E} :: ('addr, 'thread-id) \text{ execution set}$
and $P :: 'm \text{ prog}$

locale *drf* =

executions-base $\mathcal{E} \ P$
for $\mathcal{E} :: ('addr, 'thread-id) \text{ execution set}$
and $P :: 'm \text{ prog} +$
assumes *\mathcal{E} -new-actions-for-fun*:
 $\llbracket E \in \mathcal{E}; a \in \text{new-actions-for } P \ E \ \text{adal}; a' \in \text{new-actions-for } P \ E \ \text{adal} \rrbracket \implies a = a'$
and *\mathcal{E} -sequential-completion*:
 $\llbracket E \in \mathcal{E}; P \vdash (E, ws) \checkmark; \bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws \ a \rrbracket$
 $\implies \exists E' \in \mathcal{E}. \exists ws'. P \vdash (E', ws') \checkmark \wedge \text{ltake } (enat \ r) \ E = \text{ltake } (enat \ r) \ E' \wedge \text{sequentially-consistent}$
 $P \ (E', ws') \wedge$
 $\text{action-tid } E \ r = \text{action-tid } E' \ r \wedge \text{action-obs } E \ r \approx \text{action-obs } E' \ r \wedge$
 $(r \in \text{actions } E \longrightarrow r \in \text{actions } E')$

locale *executions-aux* =

executions-base $\mathcal{E} \ P$
for $\mathcal{E} :: ('addr, 'thread-id) \text{ execution set}$
and $P :: 'm \text{ prog} +$
assumes *init-before-read*:
 $\llbracket E \in \mathcal{E}; P \vdash (E, ws) \checkmark; r \in \text{read-actions } E; \text{adal} \in \text{action-loc } P \ E \ r;$
 $\bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws \ a \rrbracket$
 $\implies \exists i < r. i \in \text{new-actions-for } P \ E \ \text{adal}$
and *\mathcal{E} -new-actions-for-fun*:
 $\llbracket E \in \mathcal{E}; a \in \text{new-actions-for } P \ E \ \text{adal}; a' \in \text{new-actions-for } P \ E \ \text{adal} \rrbracket \implies a = a'$

locale *sc-legal* =

executions-aux $\mathcal{E} \ P$
for $\mathcal{E} :: ('addr, 'thread-id) \text{ execution set}$
and $P :: 'm \text{ prog} +$
assumes *\mathcal{E} -hb-completion*:
 $\llbracket E \in \mathcal{E}; P \vdash (E, ws) \checkmark; \bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws \ a \rrbracket$
 $\implies \exists E' \in \mathcal{E}. \exists ws'. P \vdash (E', ws') \checkmark \wedge \text{ltake } (enat \ r) \ E = \text{ltake } (enat \ r) \ E' \wedge$

$$\begin{aligned}
& (\forall a \in \text{read-actions } E'. \text{ if } a < r \text{ then } ws' a = ws a \text{ else } P, E' \vdash ws' a \leq_{hb} a) \wedge \\
& \text{action-tid } E' r = \text{action-tid } E r \wedge \\
& (\text{if } r \in \text{read-actions } E \text{ then sim-action else } (=)) (\text{action-obs } E' r) (\text{action-obs } E r) \wedge \\
& (r \in \text{actions } E \longrightarrow r \in \text{actions } E')
\end{aligned}$$

locale *jmm-consistent* =
drf?: *drf* \mathcal{E} *P* +
sc-legal \mathcal{E} *P*
for \mathcal{E} :: ('addr, 'thread-id) execution set
and *P* :: 'm prog

8.4.12 Legal executions

record ('addr, 'thread-id) *pre-justifying-execution* =
committed :: JMM-action set
justifying-exec :: ('addr, 'thread-id) execution
justifying-ws :: write-seen

record ('addr, 'thread-id) *justifying-execution* =
('addr, 'thread-id) *pre-justifying-execution* +
action-translation :: JMM-action \Rightarrow JMM-action

type-synonym ('addr, 'thread-id) *justification* = nat \Rightarrow ('addr, 'thread-id) *justifying-execution*

definition *wf-action-translation-on* ::
('addr, 'thread-id) execution \Rightarrow ('addr, 'thread-id) execution \Rightarrow JMM-action set \Rightarrow (JMM-action \Rightarrow JMM-action) \Rightarrow bool

where

$$\begin{aligned}
& \text{wf-action-translation-on } E E' A f \longleftrightarrow \\
& \text{inj-on } f (\text{actions } E) \wedge \\
& (\forall a \in A. \text{action-tid } E a = \text{action-tid } E' (f a) \wedge \text{action-obs } E a \approx \text{action-obs } E' (f a))
\end{aligned}$$

abbreviation *wf-action-translation* :: ('addr, 'thread-id) execution \Rightarrow ('addr, 'thread-id) *justifying-execution* \Rightarrow bool

where

$$\begin{aligned}
& \text{wf-action-translation } E J \equiv \\
& \text{wf-action-translation-on } (\text{justifying-exec } J) E (\text{committed } J) (\text{action-translation } J)
\end{aligned}$$

context

fixes *P* :: 'm prog
and *E* :: ('addr, 'thread-id) execution
and *ws* :: write-seen
and *J* :: ('addr, 'thread-id) justification
begin

This context defines the causality constraints for the JMM. The weak versions are for the fixed JMM as presented by Sevcik and Aspinal at ECOOP 2008.

Committed actions are an ascending chain with all actions of *E* as a limit

definition *is-commit-sequence* :: bool **where**

$$\begin{aligned}
& \text{is-commit-sequence} \longleftrightarrow \\
& \text{committed } (J 0) = \{\} \wedge \\
& (\forall n. \text{action-translation } (J n) \text{ ' committed } (J n) \subseteq \text{action-translation } (J (\text{Suc } n)) \text{ ' committed } (J \\
& (\text{Suc } n))) \wedge \\
& \text{actions } E = (\bigcup n. \text{action-translation } (J n) \text{ ' committed } (J n))
\end{aligned}$$

definition *justification-well-formed* :: *bool where*

$$\text{justification-well-formed} \longleftrightarrow (\forall n. P \vdash (\text{justifying-exec } (J n), \text{justifying-ws } (J n)) \checkmark)$$

definition *committed-subset-actions* :: *bool where* — JMM constraint 1

$$\text{committed-subset-actions} \longleftrightarrow (\forall n. \text{committed } (J n) \subseteq \text{actions } (\text{justifying-exec } (J n)))$$

definition *happens-before-committed* :: *bool where* — JMM constraint 2

$$\begin{aligned} \text{happens-before-committed} &\longleftrightarrow \\ (\forall n. \text{happens-before } P (\text{justifying-exec } (J n)) \mid \text{committed } (J n) = \\ &\text{inv-image } P (\text{happens-before } P E) (\text{action-translation } (J n)) \mid \text{committed } (J n)) \end{aligned}$$

definition *happens-before-committed-weak* :: *bool where* — relaxed JMM constraint

$$\begin{aligned} \text{happens-before-committed-weak} &\longleftrightarrow \\ (\forall n. \forall r \in \text{read-actions } (\text{justifying-exec } (J n)) \cap \text{committed } (J n). \\ &\text{let } r' = \text{action-translation } (J n) r; \\ &\quad w' = \text{ws } r'; \\ &\quad w = \text{inv-into } (\text{actions } (\text{justifying-exec } (J n))) (\text{action-translation } (J n)) w' \text{ in} \\ & (P, E \vdash w' \leq_{hb} r' \longleftrightarrow P, \text{justifying-exec } (J n) \vdash w \leq_{hb} r) \wedge \\ & \neg P, \text{justifying-exec } (J n) \vdash r \leq_{hb} w) \end{aligned}$$

definition *sync-order-committed* :: *bool where* — JMM constraint 3

$$\begin{aligned} \text{sync-order-committed} &\longleftrightarrow \\ (\forall n. \text{sync-order } P (\text{justifying-exec } (J n)) \mid \text{committed } (J n) = \\ &\text{inv-image } P (\text{sync-order } P E) (\text{action-translation } (J n)) \mid \text{committed } (J n)) \end{aligned}$$

definition *value-written-committed* :: *bool where* — JMM constraint 4

$$\begin{aligned} \text{value-written-committed} &\longleftrightarrow \\ (\forall n. \forall w \in \text{write-actions } (\text{justifying-exec } (J n)) \cap \text{committed } (J n). \\ &\text{let } w' = \text{action-translation } (J n) w \\ &\text{in } (\forall \text{adal} \in \text{action-loc } P E w'. \text{value-written } P (\text{justifying-exec } (J n)) w \text{ adal} = \text{value-written } P \\ & E w' \text{ adal})) \end{aligned}$$

definition *write-seen-committed* :: *bool where* — JMM constraint 5

$$\begin{aligned} \text{write-seen-committed} &\longleftrightarrow \\ (\forall n. \forall r' \in \text{read-actions } (\text{justifying-exec } (J n)) \cap \text{committed } (J n). \\ &\text{let } r = \text{action-translation } (J n) r'; \\ &\quad r'' = \text{inv-into } (\text{actions } (\text{justifying-exec } (J (\text{Suc } n)))) (\text{action-translation } (J (\text{Suc } n))) r \\ &\text{in } \text{action-translation } (J (\text{Suc } n)) (\text{justifying-ws } (J (\text{Suc } n)) r'') = \text{ws } r) \end{aligned}$$

uncommitted reads see writes that happen before them – JMM constraint 6

definition *uncommitted-reads-see-hb* :: *bool where*

$$\begin{aligned} \text{uncommitted-reads-see-hb} &\longleftrightarrow \\ (\forall n. \forall r' \in \text{read-actions } (\text{justifying-exec } (J (\text{Suc } n))). \\ &\text{action-translation } (J (\text{Suc } n)) r' \in \text{action-translation } (J n) \text{ committed } (J n) \vee \\ & P, \text{justifying-exec } (J (\text{Suc } n)) \vdash \text{justifying-ws } (J (\text{Suc } n)) r' \leq_{hb} r') \end{aligned}$$

newly committed reads see already committed writes and write-seen relationship must not change any more – JMM constraint 7

definition *committed-reads-see-committed-writes* :: *bool where*

$$\begin{aligned} \text{committed-reads-see-committed-writes} &\longleftrightarrow \\ (\forall n. \forall r' \in \text{read-actions } (\text{justifying-exec } (J (\text{Suc } n))) \cap \text{committed } (J (\text{Suc } n)). \\ &\text{let } r = \text{action-translation } (J (\text{Suc } n)) r'; \\ &\quad \text{committed-}n = \text{action-translation } (J n) \text{ committed } (J n) \end{aligned}$$

$in\ r \in\ committed\text{-}n \vee$
 $(action\text{-}translation\ (J\ (Suc\ n))\ (justifying\text{-}ws\ (J\ (Suc\ n))\ r') \in\ committed\text{-}n \wedge ws\ r \in\ committed\text{-}n)$

definition *committed-reads-see-committed-writes-weak* :: *bool* **where**

committed-reads-see-committed-writes-weak \longleftrightarrow
 $(\forall n. \forall r' \in\ read\text{-}actions\ (justifying\text{-}exec\ (J\ (Suc\ n))) \cap\ committed\ (J\ (Suc\ n))).$
 $let\ r =\ action\text{-}translation\ (J\ (Suc\ n))\ r';$
 $committed\text{-}n =\ action\text{-}translation\ (J\ n)\ 'committed\ (J\ n)$
 $in\ r \in\ committed\text{-}n \vee ws\ r \in\ committed\text{-}n)$

external actions must be committed as soon as hb-subsequent actions are committed – JMM constraint 9

definition *external-actions-committed* :: *bool* **where**

external-actions-committed \longleftrightarrow
 $(\forall n. \forall a \in\ external\text{-}actions\ (justifying\text{-}exec\ (J\ n)). \forall a' \in\ committed\ (J\ n).$
 $P.\ justifying\text{-}exec\ (J\ n) \vdash a \leq_{hb} a' \longrightarrow a \in\ committed\ (J\ n))$

well-formedness conditions for action translations

definition *wf-action-translations* :: *bool* **where**

wf-action-translations \longleftrightarrow
 $(\forall n. wf\text{-}action\text{-}translation\text{-}on\ (justifying\text{-}exec\ (J\ n))\ E\ (committed\ (J\ n))\ (action\text{-}translation\ (J\ n)))$

end

Rule 8 of the justification for the JMM is incorrect because there might be no transitive reduction of the happens-before relation for an infinite execution, if infinitely many initialisation actions have to be ordered before the start action of every thread. Hence, *is-justified-by* omits this constraint.

primrec *is-justified-by* ::

$'m\ prog \Rightarrow ('addr, 'thread\text{-}id)\ execution \times write\text{-}seen \Rightarrow ('addr, 'thread\text{-}id)\ justification \Rightarrow bool$
 $(\langle - \vdash -\ justifies'\text{-}by \rightarrow [51, 50, 50]\ 50)$

where

$P \vdash (E, ws)\ justifies\text{-}by\ J \longleftrightarrow$
 $is\text{-}commit\text{-}sequence\ E\ J \wedge$
 $justification\text{-}well\text{-}formed\ P\ J \wedge$
 $committed\text{-}subset\text{-}actions\ J \wedge$
 $happens\text{-}before\text{-}committed\ P\ E\ J \wedge$
 $sync\text{-}order\text{-}committed\ P\ E\ J \wedge$
 $value\text{-}written\text{-}committed\ P\ E\ J \wedge$
 $write\text{-}seen\text{-}committed\ ws\ J \wedge$
 $uncommitted\text{-}reads\text{-}see\text{-}hb\ P\ J \wedge$
 $committed\text{-}reads\text{-}see\text{-}committed\text{-}writes\ ws\ J \wedge$
 $external\text{-}actions\text{-}committed\ P\ J \wedge$
 $wf\text{-}action\text{-}translations\ E\ J$

Sevcik requires in the fixed JMM that external actions may only be committed when everything that happens before has already been committed. On the level of legality, this constraint is vacuous because it is always possible to delay committing external actions, so we omit it here.

primrec *is-weakly-justified-by* ::

$'m\ prog \Rightarrow ('addr, 'thread\text{-}id)\ execution \times write\text{-}seen \Rightarrow ('addr, 'thread\text{-}id)\ justification \Rightarrow bool$
 $(\langle - \vdash -\ weakly'\text{-}justifies'\text{-}by \rightarrow [51, 50, 50]\ 50)$

where

$$\begin{aligned}
& P \vdash (E, ws) \text{ weakly-justified-by } J \iff \\
& \text{is-commit-sequence } E J \wedge \\
& \text{justification-well-formed } P J \wedge \\
& \text{committed-subset-actions } J \wedge \\
& \text{happens-before-committed-weak } P E ws J \wedge \\
& \text{— no sync-order constraint} \\
& \text{value-written-committed } P E J \wedge \\
& \text{write-seen-committed } ws J \wedge \\
& \text{uncommitted-reads-see-hb } P J \wedge \\
& \text{committed-reads-see-committed-writes-weak } ws J \wedge \\
& \text{wf-action-translations } E J
\end{aligned}$$

Notion of conflict is strengthened to explicitly exclude volatile locations. Otherwise, the following program is not correctly synchronised:

```

volatile x = 0;
-----
r = x; | x = 1;

```

because in the SC execution [Init x 0, (t1, Read x 0), (t2, Write x 1)], the read and write are unrelated in hb, because synchronises-with is asymmetric for volatiles.

The JLS considers conflicting volatiles for data races, but this is only a remark on the DRF guarantee. See JMM mailing list posts #2477 to 2488.

definition *non-volatile-conflict* ::

$$\begin{aligned}
& 'm \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution} \Rightarrow \text{JMM-action} \Rightarrow \text{JMM-action} \Rightarrow \text{bool} \\
& (\langle -, - \vdash (-) \dagger (-) \rangle [51, 50, 50, 50] 51)
\end{aligned}$$

where

$$\begin{aligned}
& P, E \vdash a \dagger a' \iff \\
& (a \in \text{read-actions } E \wedge a' \in \text{write-actions } E \vee \\
& a \in \text{write-actions } E \wedge a' \in \text{read-actions } E \vee \\
& a \in \text{write-actions } E \wedge a' \in \text{write-actions } E) \wedge \\
& (\exists ad \ al. (ad, al) \in \text{action-loc } P E a \cap \text{action-loc } P E a' \wedge \neg \text{is-volatile } P al)
\end{aligned}$$

definition *correctly-synchronized* :: $'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution set} \Rightarrow \text{bool}$

where

$$\begin{aligned}
& \text{correctly-synchronized } P \mathcal{E} \iff \\
& (\forall E \in \mathcal{E}. \forall ws. P \vdash (E, ws) \checkmark \longrightarrow \text{sequentially-consistent } P (E, ws) \\
& \longrightarrow (\forall a \in \text{actions } E. \forall a' \in \text{actions } E. P, E \vdash a \dagger a' \\
& \longrightarrow P, E \vdash a \leq_{hb} a' \vee P, E \vdash a' \leq_{hb} a))
\end{aligned}$$

primrec *gen-legal-execution* ::

$$\begin{aligned}
& ('m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution} \times \text{write-seen} \Rightarrow ('addr, 'thread-id) \text{ justification} \Rightarrow \text{bool}) \\
& \Rightarrow 'm \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution set} \Rightarrow ('addr, 'thread-id) \text{ execution} \times \text{write-seen} \Rightarrow \text{bool}
\end{aligned}$$

where

$$\begin{aligned}
& \text{gen-legal-execution is-justification } P \mathcal{E} (E, ws) \iff \\
& E \in \mathcal{E} \wedge P \vdash (E, ws) \checkmark \wedge \\
& (\exists J. \text{is-justification } P (E, ws) J \wedge \text{range } (justifying-exec \circ J) \subseteq \mathcal{E})
\end{aligned}$$

abbreviation *legal-execution* ::

$$'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution set} \Rightarrow ('addr, 'thread-id) \text{ execution} \times \text{write-seen} \Rightarrow \text{bool}$$

where

$$\text{legal-execution} \equiv \text{gen-legal-execution is-justified-by}$$

abbreviation *weakly-legal-execution* ::

'm prog \Rightarrow ('addr, 'thread-id) execution set \Rightarrow ('addr, 'thread-id) execution \times write-seen \Rightarrow bool

where

weakly-legal-execution \equiv *gen-legal-execution is-weakly-justified-by*

declare *gen-legal-execution.simps* [simp del]

lemma *sym-non-volatile-conflict*:

symP (*non-volatile-conflict* *P E*)

\langle proof \rangle

lemma *legal-executionI*:

$\llbracket E \in \mathcal{E}; P \vdash (E, ws) \checkmark; \text{is-justification } P (E, ws) J; \text{range } (justifying-exec \circ J) \subseteq \mathcal{E} \rrbracket$
 $\implies \text{gen-legal-execution is-justification } P \mathcal{E} (E, ws)$

\langle proof \rangle

lemma *legal-executionE*:

assumes *gen-legal-execution is-justification* *P E (E, ws)*

obtains *J* **where** *E* \in \mathcal{E} *P* \vdash (*E, ws*) \checkmark *is-justification* *P (E, ws) J* *range* (*justifying-exec* \circ *J*) \subseteq \mathcal{E}

\langle proof \rangle

lemma *legal-ED*: *gen-legal-execution is-justification* *P E (E, ws)* $\implies E \in \mathcal{E}$

\langle proof \rangle

lemma *legal-wf-execD*:

gen-legal-execution is-justification *P E Ews* $\implies P \vdash Ews \checkmark$

\langle proof \rangle

lemma *correctly-synchronizedD*:

$\llbracket \text{correctly-synchronized } P \mathcal{E}; E \in \mathcal{E}; P \vdash (E, ws) \checkmark; \text{sequentially-consistent } P (E, ws) \rrbracket$
 $\implies \forall a a'. a \in \text{actions } E \longrightarrow a' \in \text{actions } E \longrightarrow P, E \vdash a \dagger a' \longrightarrow P, E \vdash a \leq_{hb} a' \vee P, E \vdash a' \leq_{hb}$

a

\langle proof \rangle

lemma *wf-action-translation-on-actionD*:

$\llbracket \text{wf-action-translation-on } E E' A f; a \in A \rrbracket$

$\implies \text{action-tid } E a = \text{action-tid } E' (f a) \wedge \text{action-obs } E a \approx \text{action-obs } E' (f a)$

\langle proof \rangle

lemma *wf-action-translation-on-inj-onD*:

wf-action-translation-on *E E' A f* $\implies \text{inj-on } f (\text{actions } E)$

\langle proof \rangle

lemma *wf-action-translation-on-action-locD*:

$\llbracket \text{wf-action-translation-on } E E' A f; a \in A \rrbracket$

$\implies \text{action-loc } P E a = \text{action-loc } P E' (f a)$

\langle proof \rangle

lemma *weakly-justified-write-seen-hb-read-committed*:

assumes *J*: *P* \vdash (*E, ws*) *weakly-justified-by* *J*

and *r*: *r* \in *read-actions* (*justifying-exec* (*J n*)) *r* \in *committed* (*J n*)

shows *ws* (*action-translation* (*J n*) *r*) \in *action-translation* (*J n*) ' *committed* (*J n*)

\langle proof \rangle

lemma *justified-write-seen-hb-read-committed*:

assumes $J: P \vdash (E, ws) \text{ justified-by } J$

and $r: r \in \text{read-actions } (justifying-exec (J n)) \ r \in \text{committed } (J n)$

shows $justifying-ws (J n) \ r \in \text{committed } (J n)$ (**is** *?thesis1*)

and $ws \ (action-translation (J n) \ r) \in \text{action-translation } (J n) \ \text{'committed } (J n)$ (**is** *?thesis2*)

$\langle \text{proof} \rangle$

lemma *is-justified-by-imp-is-weakly-justified-by*:

assumes $justified: P \vdash (E, ws) \text{ justified-by } J$

and $wf: P \vdash (E, ws) \ \checkmark$

shows $P \vdash (E, ws) \text{ weakly-justified-by } J$

$\langle \text{proof} \rangle$

corollary *legal-imp-weakly-legal-execution*:

$\text{legal-execution } P \ \mathcal{E} \ Ews \implies \text{weakly-legal-execution } P \ \mathcal{E} \ Ews$

$\langle \text{proof} \rangle$

lemma *drop-0th-justifying-exec*:

assumes $P \vdash (E, ws) \text{ justified-by } J$

and $wf: P \vdash (E', ws') \ \checkmark$

shows $P \vdash (E, ws) \text{ justified-by } (J(0 := \{\}, justifying-exec = E', justifying-ws = ws', \text{action-translation} = id))$

(**is** $- \vdash - \text{justified-by } ?J$)

$\langle \text{proof} \rangle$

lemma *drop-0th-weakly-justifying-exec*:

assumes $P \vdash (E, ws) \text{ weakly-justified-by } J$

and $wf: P \vdash (E', ws') \ \checkmark$

shows $P \vdash (E, ws) \text{ weakly-justified-by } (J(0 := \{\}, justifying-exec = E', justifying-ws = ws', \text{action-translation} = id))$

(**is** $- \vdash - \text{weakly-justified-by } ?J$)

$\langle \text{proof} \rangle$

8.4.13 Executions with common prefix

lemma *actions-change-prefix*:

assumes $\text{read}: a \in \text{actions } E$

and $\text{prefix}: \text{ltake } n \ E \ [\approx] \ \text{ltake } n \ E'$

and $\text{rn}: \text{enat } a < n$

shows $a \in \text{actions } E'$

$\langle \text{proof} \rangle$

lemma *action-obs-change-prefix*:

assumes $\text{prefix}: \text{ltake } n \ E \ [\approx] \ \text{ltake } n \ E'$

and $\text{rn}: \text{enat } a < n$

shows $\text{action-obs } E \ a \ \approx \ \text{action-obs } E' \ a$

$\langle \text{proof} \rangle$

lemma *action-obs-change-prefix-eq*:

assumes $\text{prefix}: \text{ltake } n \ E = \text{ltake } n \ E'$

and $\text{rn}: \text{enat } a < n$

shows $\text{action-obs } E \ a = \text{action-obs } E' \ a$

$\langle \text{proof} \rangle$

lemma *read-actions-change-prefix*:

assumes *read*: $r \in \text{read-actions } E$
and *prefix*: $\text{ltake } n \ E \ [\approx] \ \text{ltake } n \ E' \ \text{enat } r < n$
shows $r \in \text{read-actions } E'$

<proof>

lemma *sim-action-is-write-action-eq*:

assumes $\text{obs} \approx \text{obs}'$
shows $\text{is-write-action } \text{obs} \longleftrightarrow \text{is-write-action } \text{obs}'$

<proof>

lemma *write-actions-change-prefix*:

assumes *write*: $w \in \text{write-actions } E$
and *prefix*: $\text{ltake } n \ E \ [\approx] \ \text{ltake } n \ E' \ \text{enat } w < n$
shows $w \in \text{write-actions } E'$

<proof>

lemma *action-loc-change-prefix*:

assumes $\text{ltake } n \ E \ [\approx] \ \text{ltake } n \ E' \ \text{enat } a < n$
shows $\text{action-loc } P \ E \ a = \text{action-loc } P \ E' \ a$

<proof>

lemma *sim-action-is-new-action-eq*:

assumes $\text{obs} \approx \text{obs}'$
shows $\text{is-new-action } \text{obs} = \text{is-new-action } \text{obs}'$

<proof>

lemma *action-order-change-prefix*:

assumes *ao*: $E \vdash a \leq a'$
and *prefix*: $\text{ltake } n \ E \ [\approx] \ \text{ltake } n \ E'$
and *an*: $\text{enat } a < n$
and *a'n*: $\text{enat } a' < n$
shows $E' \vdash a \leq a'$

<proof>

lemma *value-written-change-prefix*:

assumes *eq*: $\text{ltake } n \ E = \text{ltake } n \ E'$
and *an*: $\text{enat } a < n$
shows $\text{value-written } P \ E \ a = \text{value-written } P \ E' \ a$

<proof>

lemma *action-tid-change-prefix*:

assumes *prefix*: $\text{ltake } n \ E \ [\approx] \ \text{ltake } n \ E'$
and *an*: $\text{enat } a < n$
shows $\text{action-tid } E \ a = \text{action-tid } E' \ a$

<proof>

lemma *program-order-change-prefix*:

assumes *po*: $E \vdash a \leq_{po} a'$
and *prefix*: $\text{ltake } n \ E \ [\approx] \ \text{ltake } n \ E'$
and *an*: $\text{enat } a < n$
and *a'n*: $\text{enat } a' < n$
shows $E' \vdash a \leq_{po} a'$

<proof>

lemma *sim-action-sactionD*:

assumes $obs \approx obs'$

shows $saction\ P\ obs \longleftrightarrow saction\ P\ obs'$

<proof>

lemma *sactions-change-prefix*:

assumes $sync: a \in sactions\ P\ E$

and $prefix: ltake\ n\ E \approx ltake\ n\ E'$

and $rn: enat\ a < n$

shows $a \in sactions\ P\ E'$

<proof>

lemma *sync-order-change-prefix*:

assumes $so: P, E \vdash a \leq_{so} a'$

and $prefix: ltake\ n\ E \approx ltake\ n\ E'$

and $an: enat\ a < n$

and $a'n: enat\ a' < n$

shows $P, E' \vdash a \leq_{so} a'$

<proof>

lemma *sim-action-synchronizes-withD*:

assumes $obs \approx obs'\ obs'' \approx obs'''$

shows $P \vdash (t, obs) \rightsquigarrow_{sw} (t', obs'') \longleftrightarrow P \vdash (t, obs') \rightsquigarrow_{sw} (t', obs''')$

<proof>

lemma *sync-with-change-prefix*:

assumes $sw: P, E \vdash a \leq_{sw} a'$

and $prefix: ltake\ n\ E \approx ltake\ n\ E'$

and $an: enat\ a < n$

and $a'n: enat\ a' < n$

shows $P, E' \vdash a \leq_{sw} a'$

<proof>

lemma *po-sw-change-prefix*:

assumes $posw: po-sw\ P\ E\ a\ a'$

and $prefix: ltake\ n\ E \approx ltake\ n\ E'$

and $an: enat\ a < n$

and $a'n: enat\ a' < n$

shows $po-sw\ P\ E'\ a\ a'$

<proof>

lemma *happens-before-new-not-new*:

assumes $tsa-ok: thread-start-actions-ok\ E$

and $a: a \in actions\ E$

and $a': a' \in actions\ E$

and $new-a: is-new-action\ (action-obs\ E\ a)$

and $new-a': \neg is-new-action\ (action-obs\ E\ a')$

shows $P, E \vdash a \leq_{hb} a'$

<proof>

lemma *happens-before-change-prefix*:

assumes $hb: P, E \vdash a \leq_{hb} a'$
and $t\text{-}ok: \text{thread-start-actions-ok } E'$
and $prefix: ltake\ n\ E \ [\approx] ltake\ n\ E'$
and $an: \text{enat } a < n$
and $a'n: \text{enat } a' < n$
shows $P, E' \vdash a \leq_{hb} a'$

$\langle proof \rangle$

lemma *thread-start-actions-ok-change*:

assumes $t\text{-}ok: \text{thread-start-actions-ok } E$
and $sim: E \ [\approx] E'$
shows $\text{thread-start-actions-ok } E'$

$\langle proof \rangle$

context *executions-aux* **begin**

lemma *\mathcal{E} -new-same-addr-singleton*:

assumes $E: E \in \mathcal{E}$
shows $\exists a. \text{new-actions-for } P\ E\ \text{adal} \subseteq \{a\}$

$\langle proof \rangle$

lemma *new-action-before-read*:

assumes $E: E \in \mathcal{E}$
and $wf: P \vdash (E, ws) \checkmark$
and $ra: ra \in \text{read-actions } E$
and $adal: \text{adal} \in \text{action-loc } P\ E\ ra$
and $new: wa \in \text{new-actions-for } P\ E\ \text{adal}$
and $sc: \bigwedge a. \llbracket a < ra; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws\ a$
shows $wa < ra$

$\langle proof \rangle$

lemma *mrw-before*:

assumes $E: E \in \mathcal{E}$
and $wf: P \vdash (E, ws) \checkmark$
and $mrw: P, E \vdash r \rightsquigarrow_{mrw} w$
and $sc: \bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws\ a$
shows $w < r$

$\langle proof \rangle$

lemma *mrw-change-prefix*:

assumes $E': E' \in \mathcal{E}$
and $mrw: P, E \vdash r \rightsquigarrow_{mrw} w$
and $t\text{-}ok: \text{thread-start-actions-ok } E'$
and $prefix: ltake\ n\ E \ [\approx] ltake\ n\ E'$
and $an: \text{enat } r < n$
and $a'n: \text{enat } w < n$
shows $P, E' \vdash r \rightsquigarrow_{mrw} w$

$\langle proof \rangle$

lemma *action-order-read-before-write*:

assumes $E: E \in \mathcal{E}\ P \vdash (E, ws) \checkmark$
and $ao: E \vdash w \leq a\ r$
and $r: r \in \text{read-actions } E$

and w : $w \in \text{write-actions } E$
and $adal$: $adal \in \text{action-loc } P \ E \ r \ adal \in \text{action-loc } P \ E \ w$
and sc : $\bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws \ a$
shows $w < r$
 $\langle \text{proof} \rangle$
end
end

8.5 The data race free guarantee of the JMM

theory *JMM-DRF*
imports
 JMM-Spec
begin

context *drf* **begin**

lemma *drf-lemma*:
 assumes wf : $P \vdash (E, ws) \checkmark$
 and E : $E \in \mathcal{E}$
 and $sync$: *correctly-synchronized* $P \ \mathcal{E}$
 and $read\text{-before}$: $\bigwedge r. r \in \text{read-actions } E \implies P, E \vdash ws \ r \leq_{hb} r$
 shows *sequentially-consistent* $P \ (E, ws)$
 $\langle \text{proof} \rangle$

lemma *justified-action-committedD*:
 assumes $justified$: $P \vdash (E, ws) \text{ weakly-justified-by } J$
 and a : $a \in \text{actions } E$
 obtains $n \ a'$ **where** $a = \text{action-translation } (J \ n) \ a' \ a' \in \text{committed } (J \ n)$
 $\langle \text{proof} \rangle$

theorem *drf-weak*:
 assumes $sync$: *correctly-synchronized* $P \ \mathcal{E}$
 and $legal$: *weakly-legal-execution* $P \ \mathcal{E} \ (E, ws)$
 shows *sequentially-consistent* $P \ (E, ws)$
 $\langle \text{proof} \rangle$

corollary *drf*:
 $\llbracket \text{correctly-synchronized } P \ \mathcal{E}; \text{ legal-execution } P \ \mathcal{E} \ (E, ws) \rrbracket$
 $\implies \text{sequentially-consistent } P \ (E, ws)$
 $\langle \text{proof} \rangle$

end
end

8.6 Sequentially consistent executions are legal

theory *SC-Legal* **imports**
 JMM-Spec
begin

context *executions-base* **begin**

primrec *commit-for-sc* :: 'm prog \Rightarrow ('addr, 'thread-id) execution \times write-seen \Rightarrow ('addr, 'thread-id) justification

where

commit-for-sc P (E , ws) n =
 (if *enat* $n \leq$ *llength* E then
 let (E' , ws') = *SOME* (E' , ws'). $E' \in \mathcal{E} \wedge P \vdash (E', ws') \checkmark \wedge$ *enat* $n \leq$ *llength* $E' \wedge$
 ltake (*enat* ($n - 1$)) $E =$ *ltake* (*enat* ($n - 1$)) $E' \wedge$
 ($n > 0 \longrightarrow$ *action-tid* $E' (n - 1) =$ *action-tid* $E (n - 1) \wedge$
 (if $n - 1 \in$ *read-actions* E then *sim-action* else (=))
 (*action-obs* $E' (n - 1)$) (*action-obs* $E (n - 1)$) \wedge
 ($\forall i < n - 1. i \in$ *read-actions* $E \longrightarrow ws' i = ws i)$) \wedge
 ($\forall r \in$ *read-actions* $E'. n - 1 \leq r \longrightarrow P, E' \vdash ws' r \leq_{hb} r$)
 in (*committed* = $\{..<n\}$, *justifying-exec* = E' , *justifying-ws* = ws' , *action-translation* = *id*)
 else (*committed* = *actions* E , *justifying-exec* = E , *justifying-ws* = ws , *action-translation* = *id*))

end

context *sc-legal* **begin**

lemma *commit-for-sc-correct*:

assumes $E: E \in \mathcal{E}$

and *wf*: $P \vdash (E, ws) \checkmark$

and *sc*: *sequentially-consistent* P (E, ws)

shows *wf-action-translation-commit-for-sc*:

$\bigwedge n. \text{wf-action-translation } E \text{ (commit-for-sc } P (E, ws) n) \text{ (is } \bigwedge n. \text{?thesis1 } n)$

and *commit-for-sc-in- \mathcal{E}* :

$\bigwedge n. \text{justifying-exec (commit-for-sc } P (E, ws) n) \in \mathcal{E} \text{ (is } \bigwedge n. \text{?thesis2 } n)$

and *commit-for-sc-wf*:

$\bigwedge n. P \vdash (\text{justifying-exec (commit-for-sc } P (E, ws) n), \text{justifying-ws (commit-for-sc } P (E, ws) n))$

\checkmark

(is $\bigwedge n. \text{?thesis3 } n$)

and *commit-for-sc-justification*:

$P \vdash (E, ws) \text{justified-by commit-for-sc } P (E, ws) \text{ (is ?thesis4)}$

<proof>

theorem *SC-is-legal*:

assumes $E: E \in \mathcal{E}$

and *wf*: $P \vdash (E, ws) \checkmark$

and *sc*: *sequentially-consistent* P (E, ws)

shows *legal-execution* $P \mathcal{E} (E, ws)$

<proof>

end

context *jmm-consistent* **begin**

theorem *consistent*:

assumes $E \in \mathcal{E} P \vdash (E, ws) \checkmark$

shows $\exists E \in \mathcal{E}. \exists ws. \text{legal-execution } P \mathcal{E} (E, ws)$

<proof>

end

end

8.7 Non-speculative prefixes of executions

theory *Non-Speculative* imports

JMM-Spec

../Framework/FWLTS

begin

declare *addr-locsI* [*simp*]

8.7.1 Previously written values

fun *w-value* ::

'm prog \Rightarrow (*'addr* \times *addr-loc*) \Rightarrow *'addr val set* \Rightarrow (*'addr*, *'thread-id*) *obs-event action*
 \Rightarrow (*'addr* \times *addr-loc*) \Rightarrow *'addr val set*)

where

w-value P vs (*NormalAction* (*WriteMem ad al v*)) = *vs*((*ad*, *al*) := *insert v (vs (ad, al))*)

| *w-value P vs* (*NormalAction* (*NewHeapElem ad hT*)) =

(λ (*ad'*, *al*). *if ad = ad' \wedge al \in addr-locs P hT*
then insert (addr-loc-default P hT al) (vs (ad, al))
else vs (ad', al))

| *w-value P vs - = vs*

lemma *w-value-cases*:

obtains *ad al v* **where** *x = NormalAction (WriteMem ad al v)*

| *ad hT* **where** *x = NormalAction (NewHeapElem ad hT)*

| *ad M vs v* **where** *x = NormalAction (ExternalCall ad M vs v)*

| *ad al v* **where** *x = NormalAction (ReadMem ad al v)*

| *t* **where** *x = NormalAction (ThreadStart t)*

| *t* **where** *x = NormalAction (ThreadJoin t)*

| *ad* **where** *x = NormalAction (SyncLock ad)*

| *ad* **where** *x = NormalAction (SyncUnlock ad)*

| *t* **where** *x = NormalAction (ObsInterrupt t)*

| *t* **where** *x = NormalAction (ObsInterrupted t)*

| *x = InitialThreadAction*

| *x = ThreadFinishAction*

<proof>

abbreviation *w-values* ::

'm prog \Rightarrow (*'addr* \times *addr-loc*) \Rightarrow *'addr val set* \Rightarrow (*'addr*, *'thread-id*) *obs-event action list*
 \Rightarrow (*'addr* \times *addr-loc*) \Rightarrow *'addr val set*)

where *w-values P* \equiv *foldl (w-value P)*

lemma *in-w-valuesD*:

assumes *w*: *v \in w-values P vs0 obs (ad, al)*

and *v*: *v \notin vs0 (ad, al)*

shows \exists *obs' wa obs''*. *obs = obs' @ wa # obs'' \wedge is-write-action wa \wedge (ad, al) \in action-loc-aux P*
wa \wedge

value-written-aux P wa al = v

(**is** *?concl obs*)

<proof>

lemma *w-values-WriteMemD*:

assumes *NormalAction* (*WriteMem ad al v*) \in *set obs*

shows $v \in w\text{-values } P \text{ vs0 obs } (ad, al)$

\langle *proof* \rangle

lemma *w-values-new-actionD*:

assumes *NormalAction* (*NewHeapElem ad hT*) \in *set obs* (*ad, al*) \in *action-loc-aux P* (*NormalAction* (*NewHeapElem ad hT*))

shows *addr-loc-default P hT al* \in *w-values P vs0 obs* (*ad, al*)

\langle *proof* \rangle

lemma *w-value-mono: vs0 adal* \subseteq *w-value P vs0 ob adal*

\langle *proof* \rangle

lemma *w-values-mono: vs0 adal* \subseteq *w-values P vs0 obs adal*

\langle *proof* \rangle

lemma *w-value-greater: vs0* \leq *w-value P vs0 ob*

\langle *proof* \rangle

lemma *w-values-greater: vs0* \leq *w-values P vs0 obs*

\langle *proof* \rangle

lemma *w-values-eq-emptyD*:

assumes *w-values P vs0 obs adal* = $\{\}$

and $w \in$ *set obs* **and** *is-write-action w* **and** *adal* \in *action-loc-aux P w*

shows *False*

\langle *proof* \rangle

8.7.2 Coinductive version of non-speculative prefixes

coinductive *non-speculative* ::

'm prog \Rightarrow (*'addr* \times *addr-loc* \Rightarrow *'addr val set*) \Rightarrow (*'addr, 'thread-id*) *obs-event action llist* \Rightarrow *bool*

for *P* :: *'m prog*

where

LNil: *non-speculative P vs LNil*

| *LCons*:

\llbracket *case ob of NormalAction (ReadMem ad al v)* \Rightarrow $v \in$ *vs* (*ad, al*) | - \Rightarrow *True*;

non-speculative P (w-value P vs ob) obs \rrbracket

\Longrightarrow *non-speculative P vs (LCons ob obs)*

inductive-simps *non-speculative-simps* [*simp*]:

non-speculative P vs LNil

non-speculative P vs (LCons ob obs)

lemma *non-speculative-lappend*:

assumes *lfinite obs*

shows *non-speculative P vs (lappend obs obs')* \longleftrightarrow

non-speculative P vs obs \wedge *non-speculative P (w-values P vs (list-of obs)) obs'*

(**is** *?concl vs obs*)

\langle *proof* \rangle

lemma**assumes** *non-speculative P vs obs***shows** *non-speculative-ltake: non-speculative P vs (ltake n obs) (is ?thesis1)***and** *non-speculative-ldrop: non-speculative P (w-values P vs (list-of (ltake n obs))) (ldrop n obs) (is ?thesis2)**<proof>***lemma** *non-speculative-coinduct-append [consumes 1, case-names non-speculative, case-conclusion non-speculative LNil lappend]:***assumes** *major: X vs obs***and** *step: \bigwedge vs obs. X vs obs* $\implies obs = LNil \vee$ $(\exists obs' obs''. obs = lappend obs' obs'' \wedge obs' \neq LNil \wedge non-speculative P vs obs' \wedge$
 $(lfinite obs' \longrightarrow (X (w-values P vs (list-of obs')) obs'' \vee$
 $non-speculative P (w-values P vs (list-of obs')) obs''))$ **(is \bigwedge vs obs. $- \implies - \vee ?step vs obs$)****shows** *non-speculative P vs obs**<proof>***lemma** *non-speculative-coinduct-append-wf**[consumes 2, case-names non-speculative, case-conclusion non-speculative LNil lappend]:***assumes** *major: X vs obs a***and** *wf: wf R***and** *step: \bigwedge vs obs a. X vs obs a* $\implies obs = LNil \vee$ $(\exists obs' obs'' a'. obs = lappend obs' obs'' \wedge non-speculative P vs obs' \wedge (obs' = LNil \longrightarrow (a', a)$
 $\in R) \wedge$ $(lfinite obs' \longrightarrow X (w-values P vs (list-of obs')) obs'' a' \vee$
 $non-speculative P (w-values P vs (list-of obs')) obs''))$ **(is \bigwedge vs obs a. $- \implies - \vee ?step vs obs a$)****shows** *non-speculative P vs obs**<proof>***lemma** *non-speculative-nthI:* $(\bigwedge i ad al v.$ $\llbracket enat i < llength obs; lnth obs i = NormalAction (ReadMem ad al v);$
 $non-speculative P vs (ltake (enat i) obs) \rrbracket$ $\implies v \in w-values P vs (list-of (ltake (enat i) obs)) (ad, al)$ $\implies non-speculative P vs obs$ *<proof>***locale** *executions-sc-hb =**executions-base \mathcal{E} P***for** $\mathcal{E} :: ('addr, 'thread-id)$ *execution set***and** $P :: 'm$ *prog +***assumes** *\mathcal{E} -new-actions-for-fun:* $\llbracket E \in \mathcal{E}; a \in new-actions-for P E adal; a' \in new-actions-for P E adal \rrbracket \implies a = a'$ **and** *\mathcal{E} -ex-new-action:* $\llbracket E \in \mathcal{E}; ra \in read-actions E; adal \in action-loc P E ra; non-speculative P (\lambda-. \{\}) (ltake (enat ra)$
 $(lmap snd E)) \rrbracket$ $\implies \exists wa. wa \in new-actions-for P E adal \wedge wa < ra$ **begin****lemma** *\mathcal{E} -new-same-addr-singleton:*

assumes $E: E \in \mathcal{E}$
shows $\exists a. \text{new-actions-for } P \ E \ \text{adal} \subseteq \{a\}$
 $\langle \text{proof} \rangle$

lemma *new-action-before-read*:

assumes $E: E \in \mathcal{E}$
and $ra: ra \in \text{read-actions } E$
and $adal: adal \in \text{action-loc } P \ E \ ra$
and $new: wa \in \text{new-actions-for } P \ E \ adal$
and $sc: \text{non-speculative } P \ (\lambda-. \{\}) \ (\text{ltake } (\text{enat } ra) \ (\text{lmap } \text{snd } E))$
shows $wa < ra$
 $\langle \text{proof} \rangle$

lemma *most-recent-write-exists*:

assumes $E: E \in \mathcal{E}$
and $ra: ra \in \text{read-actions } E$
and $sc: \text{non-speculative } P \ (\lambda-. \{\}) \ (\text{ltake } (\text{enat } ra) \ (\text{lmap } \text{snd } E))$
shows $\exists wa. P, E \vdash ra \rightsquigarrow mrw \ wa$
 $\langle \text{proof} \rangle$

lemma *mrw-before*:

assumes $E: E \in \mathcal{E}$
and $mrw: P, E \vdash r \rightsquigarrow mrw \ w$
and $sc: \text{non-speculative } P \ (\lambda-. \{\}) \ (\text{ltake } (\text{enat } r) \ (\text{lmap } \text{snd } E))$
shows $w < r$
 $\langle \text{proof} \rangle$

lemma *sequentially-consistent-most-recent-write-for*:

assumes $E: E \in \mathcal{E}$
and $sc: \text{non-speculative } P \ (\lambda-. \{\}) \ (\text{lmap } \text{snd } E)$
shows *sequentially-consistent* $P \ (E, \lambda r. \text{THE } w. P, E \vdash r \rightsquigarrow mrw \ w)$
 $\langle \text{proof} \rangle$

end

locale *jmm-multithreaded* = *multithreaded-base* +

constrains $\text{final} :: 'x \Rightarrow \text{bool}$
and $r :: ('l, 'thread-id, 'x, 'm, 'w, ('addr, 'thread-id) \text{ obs-event action}) \text{ semantics}$
and $\text{convert-RA} :: 'l \text{ released-locks} \Rightarrow ('addr, 'thread-id) \text{ obs-event action list}$
fixes $P :: 'md \ \text{prog}$

end

8.8 Sequentially consistent completion of executions in the JMM

theory *SC-Completion*

imports

Non-Speculative

begin

8.8.1 Most recently written values

fun *mrw-value* ::

'm prog \Rightarrow (('addr \times addr-loc) \rightarrow ('addr val \times bool)) \Rightarrow ('addr, 'thread-id) obs-event action
 \Rightarrow (('addr \times addr-loc) \rightarrow ('addr val \times bool))

where

mrw-value *P* *vs* (NormalAction (WriteMem *ad* *al* *v*)) = *vs*((*ad*, *al*) \mapsto (*v*, True))
| *mrw-value* *P* *vs* (NormalAction (NewHeapElem *ad* *hT*)) =
 $(\lambda(ad', al). \text{if } ad = ad' \wedge al \in \text{addr-locs } P \text{ hT} \wedge (\text{case } vs (ad, al) \text{ of None} \Rightarrow \text{True} \mid \text{Some } (v, b) \Rightarrow \neg b)$
 $\text{then Some } (\text{addr-loc-default } P \text{ hT } al, \text{False})$
 $\text{else } vs (ad', al))$
| *mrw-value* *P* *vs* - = *vs*

lemma *mrw-value-cases*:

obtains *ad* *al* *v* **where** $x = \text{NormalAction } (\text{WriteMem } ad \ al \ v)$
| *ad* *hT* **where** $x = \text{NormalAction } (\text{NewHeapElem } ad \ hT)$
| *ad* *M* *vs* *v* **where** $x = \text{NormalAction } (\text{ExternalCall } ad \ M \ vs \ v)$
| *ad* *al* *v* **where** $x = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$
| *t* **where** $x = \text{NormalAction } (\text{ThreadStart } t)$
| *t* **where** $x = \text{NormalAction } (\text{ThreadJoin } t)$
| *ad* **where** $x = \text{NormalAction } (\text{SyncLock } ad)$
| *ad* **where** $x = \text{NormalAction } (\text{SyncUnlock } ad)$
| *t* **where** $x = \text{NormalAction } (\text{ObsInterrupt } t)$
| *t* **where** $x = \text{NormalAction } (\text{ObsInterrupted } t)$
| $x = \text{InitialThreadAction}$
| $x = \text{ThreadFinishAction}$

$\langle \text{proof} \rangle$

abbreviation *mrw-values* ::

'm prog \Rightarrow (('addr \times addr-loc) \rightarrow ('addr val \times bool)) \Rightarrow ('addr, 'thread-id) obs-event action list
 \Rightarrow (('addr \times addr-loc) \rightarrow ('addr val \times bool))

where *mrw-values* *P* $\equiv \text{foldl } (\text{mrw-value } P)$

lemma *mrw-values-eq-SomeD*:

assumes *mrw*: *mrw-values* *P* *vs0* *obs* (*ad*, *al*) = [(*v*, *b*)]
and *vs0* (*ad*, *al*) = [(*v*, *b*)] $\implies \exists wa. wa \in \text{set } obs \wedge \text{is-write-action } wa \wedge (ad, al) \in \text{action-loc-aux } P \ wa \wedge (b \longrightarrow \neg \text{is-new-action } wa)$
shows $\exists obs' \ wa \ obs''. obs = obs' \ @ \ wa \ \# \ obs'' \wedge \text{is-write-action } wa \wedge (ad, al) \in \text{action-loc-aux } P \ wa \wedge$
 $\text{value-written-aux } P \ wa \ al = v \wedge (\text{is-new-action } wa \longleftrightarrow \neg b) \wedge$
 $(\forall ob \in \text{set } obs''. \text{is-write-action } ob \longrightarrow (ad, al) \in \text{action-loc-aux } P \ ob \longrightarrow \text{is-new-action } ob \wedge$
b)
(is ?concl *obs*)
 $\langle \text{proof} \rangle$

lemma *mrw-values-WriteMemD*:

assumes NormalAction (WriteMem *ad* *al* *v'*) $\in \text{set } obs$
shows $\exists v. \text{mrw-values } P \ vs0 \ obs (ad, al) = \text{Some } (v, \text{True})$
 $\langle \text{proof} \rangle$

lemma *mrw-values-new-actionD*:

assumes $w \in \text{set } obs \ \text{is-new-action } w \ \text{ad}al \in \text{action-loc-aux } P \ w$
shows $\exists v \ b. \text{mrw-values } P \ vs0 \ obs \ \text{ad}al = \text{Some } (v, b)$

<proof>

lemma *mrw-value-dom-mono*:

$dom\ vs \subseteq dom\ (mrw\text{-value}\ P\ vs\ ob)$

<proof>

lemma *mrw-values-dom-mono*:

$dom\ vs \subseteq dom\ (mrw\text{-values}\ P\ vs\ obs)$

<proof>

lemma *mrw-values-eq-NoneD*:

assumes $mrw\text{-values}\ P\ vs\ 0\ obs\ adal = None$

and $w \in set\ obs$ **and** $is\text{-write}\text{-action}\ w$ **and** $adal \in action\text{-loc}\text{-aux}\ P\ w$

shows $False$

<proof>

lemma *mrw-values-mrw*:

assumes $mrw: mrw\text{-values}\ P\ vs\ 0\ (map\ snd\ obs)\ (ad,\ al) = [(v,\ b)]$

and *initial*: $vs\ 0\ (ad,\ al) = [(v,\ b)] \implies \exists wa. wa \in set\ (map\ snd\ obs) \wedge is\text{-write}\text{-action}\ wa \wedge (ad,\ al) \in action\text{-loc}\text{-aux}\ P\ wa \wedge (b \longrightarrow \neg is\text{-new}\text{-action}\ wa)$

shows $\exists i. i < length\ obs \wedge P,\ llist\text{-of}\ (obs\ @\ [(t,\ NormalAction\ (ReadMem\ ad\ al\ v))]) \vdash length\ obs \rightsquigarrow mrw\ i \wedge value\text{-written}\ P\ (llist\text{-of}\ obs)\ i\ (ad,\ al) = v$

<proof>

lemma *mrw-values-no-write-unchanged*:

assumes $no\text{-write}: \bigwedge w. \llbracket w \in set\ obs; is\text{-write}\text{-action}\ w; adal \in action\text{-loc}\text{-aux}\ P\ w \rrbracket$

$\implies case\ vs\ adal\ of\ None \Rightarrow False \mid Some\ (v,\ b) \Rightarrow b \wedge is\text{-new}\text{-action}\ w$

shows $mrw\text{-values}\ P\ vs\ obs\ adal = vs\ adal$

<proof>

8.8.2 Coinductive version of sequentially consistent prefixes

coinductive *ta-seq-consist* ::

$'m\ prog \Rightarrow ('addr \times addr\text{-loc} \rightarrow 'addr\ val \times bool) \Rightarrow ('addr,\ 'thread\text{-id})\ obs\text{-event}\ action\ llist \Rightarrow bool$

for $P :: 'm\ prog$

where

$LNil: ta\text{-seq}\text{-consist}\ P\ vs\ LNil$

$| LCons:$

$\llbracket case\ ob\ of\ NormalAction\ (ReadMem\ ad\ al\ v) \Rightarrow \exists b. vs\ (ad,\ al) = [(v,\ b)] \mid - \Rightarrow True; \rrbracket$

$ta\text{-seq}\text{-consist}\ P\ (mrw\text{-value}\ P\ vs\ ob)\ obs \llbracket$

$\implies ta\text{-seq}\text{-consist}\ P\ vs\ (LCons\ ob\ obs)$

inductive-simps *ta-seq-consist-simps* [*simp*]:

$ta\text{-seq}\text{-consist}\ P\ vs\ LNil$

$ta\text{-seq}\text{-consist}\ P\ vs\ (LCons\ ob\ obs)$

lemma *ta-seq-consist-lappend*:

assumes $lfinite\ obs$

shows $ta\text{-seq}\text{-consist}\ P\ vs\ (lappend\ obs\ obs') \longleftrightarrow$

$ta\text{-seq}\text{-consist}\ P\ vs\ obs \wedge ta\text{-seq}\text{-consist}\ P\ (mrw\text{-values}\ P\ vs\ (list\text{-of}\ obs))\ obs'$

(**is** *?concl vs obs*)

<proof>

lemma

assumes *ta-seq-consist P vs obs*
shows *ta-seq-consist-ltake: ta-seq-consist P vs (ltake n obs) (is ?thesis1)*
and *ta-seq-consist-ldrop: ta-seq-consist P (mrw-values P vs (list-of (ltake n obs))) (ldrop n obs) (is ?thesis2)*
 ⟨proof⟩

lemma *ta-seq-consist-coinduct-append* [consumes 1, case-names *ta-seq-consist*, case-conclusion *ta-seq-consist LNil lappend*]:

assumes *major: X vs obs*
and *step: $\bigwedge vs obs. X vs obs$*
 $\implies obs = LNil \vee$
 $(\exists obs' obs''. obs = lappend obs' obs'' \wedge obs' \neq LNil \wedge ta-seq-consist P vs obs' \wedge$
 $(lfinite obs' \longrightarrow (X (mrw-values P vs (list-of obs')) obs'' \vee$
 $ta-seq-consist P (mrw-values P vs (list-of obs')) obs''))$
(is $\bigwedge vs obs. - \implies - \vee ?step vs obs$)
shows *ta-seq-consist P vs obs*
 ⟨proof⟩

lemma *ta-seq-consist-coinduct-append-wf*

[consumes 2, case-names *ta-seq-consist*, case-conclusion *ta-seq-consist LNil lappend*]:

assumes *major: X vs obs a*
and *wf: wf R*
and *step: $\bigwedge vs obs a. X vs obs a$*
 $\implies obs = LNil \vee$
 $(\exists obs' obs'' a'. obs = lappend obs' obs'' \wedge ta-seq-consist P vs obs' \wedge (obs' = LNil \longrightarrow (a', a) \in$
 $R) \wedge$
 $(lfinite obs' \longrightarrow X (mrw-values P vs (list-of obs')) obs'' a' \vee$
 $ta-seq-consist P (mrw-values P vs (list-of obs')) obs''))$
(is $\bigwedge vs obs a. - \implies - \vee ?step vs obs a$)
shows *ta-seq-consist P vs obs*
 ⟨proof⟩

lemma *ta-seq-consist-nthI*:

$(\bigwedge i ad al v. \llbracket enat i < llength obs; lnth obs i = NormalAction (ReadMem ad al v);$
 $ta-seq-consist P vs (ltake (enat i) obs) \rrbracket$
 $\implies \exists b. mrw-values P vs (list-of (ltake (enat i) obs)) (ad, al) = \llbracket (v, b) \rrbracket$
 $\implies ta-seq-consist P vs obs$
 ⟨proof⟩

lemma *ta-seq-consist-into-non-speculative*:

$\llbracket ta-seq-consist P vs obs; \forall adal. set-option (vs adal) \subseteq vs' adal \times UNIV \rrbracket$
 $\implies non-speculative P vs' obs$
 ⟨proof⟩

lemma *llist-of-list-of-append*:

$lfinite xs \implies llist-of (list-of xs @ ys) = lappend xs (llist-of ys)$
 ⟨proof⟩

lemma *ta-seq-consist-most-recent-write-for*:

assumes *sc: ta-seq-consist P Map.empty (lmap snd E)*
and *read: $r \in read-actions E$*
and *new-actions-for-fun: $\bigwedge adal a a'. \llbracket a \in new-actions-for P E adal; a' \in new-actions-for P E adal$*
 $\rrbracket \implies a = a'$
shows $\exists i. P, E \vdash r \rightsquigarrow mrw i \wedge i < r$

<proof>

lemma *ta-seq-consist-mrw-before*:

assumes *sc*: *ta-seq-consist P Map.empty (lmap snd E)*

and *new-actions-for-fun*: $\bigwedge \text{adal } a \ a'. \llbracket a \in \text{new-actions-for } P \ E \ \text{adal}; a' \in \text{new-actions-for } P \ E \ \text{adal} \rrbracket \implies a = a'$

and *mrw*: $P, E \vdash r \rightsquigarrow_{\text{mrw}} w$

shows $w < r$

<proof>

lemma *ta-seq-consist-imp-sequentially-consistent*:

assumes *tsc-ok*: *thread-start-actions-ok E*

and *new-actions-for-fun*: $\bigwedge \text{adal } a \ a'. \llbracket a \in \text{new-actions-for } P \ E \ \text{adal}; a' \in \text{new-actions-for } P \ E \ \text{adal} \rrbracket \implies a = a'$

and *seq*: *ta-seq-consist P Map.empty (lmap snd E)*

shows $\exists \text{ws. sequentially-consistent } P \ (E, \text{ws}) \wedge P \vdash (E, \text{ws}) \checkmark$

<proof>

8.8.3 Cut-and-update and sequentially consistent completion

inductive *foldl-list-all2* ::

$(b \Rightarrow c \Rightarrow a \Rightarrow a) \Rightarrow (b \Rightarrow c \Rightarrow a \Rightarrow \text{bool}) \Rightarrow (b \Rightarrow c \Rightarrow a \Rightarrow \text{bool}) \Rightarrow b \ \text{list} \Rightarrow c \ \text{list} \Rightarrow a \Rightarrow \text{bool}$

for *f* **and** *P* **and** *Q*

where

foldl-list-all2 f P Q [] [] s

$\llbracket Q \ x \ y \ s; P \ x \ y \ s \implies \text{foldl-list-all2 } f \ P \ Q \ xs \ ys \ (f \ x \ y \ s) \rrbracket \implies \text{foldl-list-all2 } f \ P \ Q \ (x \ \# \ xs) \ (y \ \# \ ys)$

s

inductive-simps *foldl-list-all2-simps* [*simp*]:

foldl-list-all2 f P Q [] ys s

foldl-list-all2 f P Q xs [] s

foldl-list-all2 f P Q (x # xs) (y # ys) s

inductive-simps *foldl-list-all2-Cons1*:

foldl-list-all2 f P Q (x # xs) ys s

inductive-simps *foldl-list-all2-Cons2*:

foldl-list-all2 f P Q xs (y # ys) s

definition *eq-upto-seq-inconsist* ::

$'m \ \text{prog} \Rightarrow ('addr, 'thread-id) \ \text{obs-event action list} \Rightarrow ('addr, 'thread-id) \ \text{obs-event action list}$

$\Rightarrow ('addr \times \text{addr-loc} \rightarrow 'addr \ \text{val} \times \text{bool}) \Rightarrow \text{bool}$

where

eq-upto-seq-inconsist P =

foldl-list-all2 $(\lambda ob \ ob' \ \text{vs. mrw-value } P \ \text{vs } ob)$

$(\lambda ob \ ob' \ \text{vs. case } ob \ \text{of } \text{NormalAction } (\text{ReadMem } ad \ al \ v) \Rightarrow \exists b. \ \text{vs } (ad, al) = \text{Some } (v, b) \mid - \Rightarrow \text{True})$

$(\lambda ob \ ob' \ \text{vs. if } (\text{case } ob \ \text{of } \text{NormalAction } (\text{ReadMem } ad \ al \ v) \Rightarrow \exists b. \ \text{vs } (ad, al) = \text{Some } (v, b) \mid - \Rightarrow \text{True}) \ \text{then } ob = ob' \ \text{else } ob \approx ob')$

lemma *eq-upto-seq-inconsist-simps*:

eq-upto-seq-inconsist P [] obs' vs \longleftrightarrow obs' = []

eq-upto-seq-inconsist P obs [] vs \longleftrightarrow obs = []

$eq\text{-upto}\text{-seq}\text{-inconsistent } P (ob \# obs) (ob' \# obs') vs \longleftrightarrow$
 (case ob of $NormalAction (ReadMem ad al v) \Rightarrow$
 if $(\exists b. vs (ad, al) = [(v, b)])$
 then $ob = ob' \wedge eq\text{-upto}\text{-seq}\text{-inconsistent } P obs obs' (mrw\text{-value } P vs ob)$
 else $ob \approx ob'$
 | $- \Rightarrow ob = ob' \wedge eq\text{-upto}\text{-seq}\text{-inconsistent } P obs obs' (mrw\text{-value } P vs ob)$)
 <proof>

lemma $eq\text{-upto}\text{-seq}\text{-inconsistent}\text{-Cons1}$:

$eq\text{-upto}\text{-seq}\text{-inconsistent } P (ob \# obs) obs' vs \longleftrightarrow$
 $(\exists ob' obs''. obs' = ob' \# obs'' \wedge$
 (case ob of $NormalAction (ReadMem ad al v) \Rightarrow$
 if $(\exists b. vs (ad, al) = [(v, b)])$
 then $ob' = ob \wedge eq\text{-upto}\text{-seq}\text{-inconsistent } P obs obs'' (mrw\text{-value } P vs ob)$
 else $ob \approx ob'$
 | $- \Rightarrow ob' = ob \wedge eq\text{-upto}\text{-seq}\text{-inconsistent } P obs obs'' (mrw\text{-value } P vs ob)))$
 <proof>

lemma $eq\text{-upto}\text{-seq}\text{-inconsistent}\text{-appendD}$:

assumes $eq\text{-upto}\text{-seq}\text{-inconsistent } P (obs @ obs') obs'' vs$
and $ta\text{-seq}\text{-consistent } P vs (l\text{list-of } obs)$
shows $length\ obs \leq length\ obs''$ (**is** ?thesis1)
and $take (length\ obs) obs'' = obs$ (**is** ?thesis2)
and $eq\text{-upto}\text{-seq}\text{-inconsistent } P obs' (drop (length\ obs) obs'') (mrw\text{-values } P vs obs)$ (**is** ?thesis3)
 <proof>

lemma $ta\text{-seq}\text{-consistent}\text{-imp}\text{-eq}\text{-upto}\text{-seq}\text{-inconsistent}\text{-refl}$:

$ta\text{-seq}\text{-consistent } P vs (l\text{list-of } obs) \Longrightarrow eq\text{-upto}\text{-seq}\text{-inconsistent } P obs obs vs$
 <proof>

context notes $split\text{-paired}\text{-Ex} [simp\ del] eq\text{-upto}\text{-seq}\text{-inconsistent}\text{-simps} [simp] \text{ begin}$

lemma $eq\text{-upto}\text{-seq}\text{-inconsistent}\text{-appendI}$:

$\llbracket eq\text{-upto}\text{-seq}\text{-inconsistent } P obs OBS vs;$
 $\llbracket ta\text{-seq}\text{-consistent } P vs (l\text{list-of } obs) \rrbracket \Longrightarrow eq\text{-upto}\text{-seq}\text{-inconsistent } P obs' OBS' (mrw\text{-values } P vs OBS)$
 \rrbracket
 $\Longrightarrow eq\text{-upto}\text{-seq}\text{-inconsistent } P (obs @ obs') (OBS @ OBS') vs$
 <proof>

lemma $eq\text{-upto}\text{-seq}\text{-inconsistent}\text{-trans}$:

$\llbracket eq\text{-upto}\text{-seq}\text{-inconsistent } P obs obs' vs; eq\text{-upto}\text{-seq}\text{-inconsistent } P obs' obs'' vs \rrbracket$
 $\Longrightarrow eq\text{-upto}\text{-seq}\text{-inconsistent } P obs obs'' vs$
 <proof>

lemma $eq\text{-upto}\text{-seq}\text{-inconsistent}\text{-append2}$:

$\llbracket eq\text{-upto}\text{-seq}\text{-inconsistent } P obs obs' vs; \neg ta\text{-seq}\text{-consistent } P vs (l\text{list-of } obs) \rrbracket$
 $\Longrightarrow eq\text{-upto}\text{-seq}\text{-inconsistent } P obs (obs' @ obs'') vs$
 <proof>

end

context $executions\text{-sc}\text{-hb} \text{ begin}$

lemma *ta-seq-consist-mrwI*:

assumes $E: E \in \mathcal{E}$

and $wf: P \vdash (E, ws) \checkmark$

and $mrw: \bigwedge a. \llbracket \text{enat } a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws a$

shows $ta\text{-seq-consist } P \text{ Map.empty (lmap snd (ltake } r \ E))$

<proof>

end

context *jmm-multithreaded* **begin**

definition *complete-sc* :: $(l, 'thread\text{-id}, 'x, 'm, 'w)$ state \Rightarrow $(\text{'addr} \times \text{addr-loc} \rightarrow \text{'addr val} \times \text{bool}) \Rightarrow$
 $(\text{'thread-id} \times (l, 'thread\text{-id}, 'x, 'm, 'w, (\text{'addr}, \text{'thread-id}) \text{ obs-event action}) \text{ thread-action}) \text{ llist}$

where

complete-sc s $vs = \text{unfold-llist}$

$(\lambda(s, vs). \forall t \ ta \ s'. \neg s \text{-t} \triangleright ta \rightarrow s')$

$(\lambda(s, vs). \text{fst } (SOME ((t, ta), s'). s \text{-t} \triangleright ta \rightarrow s' \wedge ta\text{-seq-consist } P \ vs \ (\text{llist-of } \{\{ta\}_o\})))$

$(\lambda(s, vs). \text{let } ((t, ta), s') = SOME ((t, ta), s'). s \text{-t} \triangleright ta \rightarrow s' \wedge ta\text{-seq-consist } P \ vs \ (\text{llist-of } \{\{ta\}_o\})$
 $\text{in } (s', \text{mrw-values } P \ vs \ \{\{ta\}_o\}))$

(s, vs)

definition *sc-completion* :: $(l, 'thread\text{-id}, 'x, 'm, 'w)$ state \Rightarrow $(\text{'addr} \times \text{addr-loc} \rightarrow \text{'addr val} \times \text{bool})$
 $\Rightarrow \text{bool}$

where

sc-completion s $vs \longleftrightarrow$

$(\forall \text{ttas } s' \ t \ x \ ta \ x' \ m'.$

$s \text{-}\triangleright\text{ttas}\text{-}\rightarrow^* s' \longrightarrow ta\text{-seq-consist } P \ vs \ (\text{llist-of } (\text{concat } (\text{map } (\lambda(t, ta). \{\{ta\}_o\} \ \text{ttas}))) \longrightarrow$

$\text{thr } s' \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket \longrightarrow t \vdash (x, \text{shr } s') \text{-ta} \rightarrow (x', m') \longrightarrow \text{actions-ok } s' \ t \ ta \longrightarrow$

$(\exists ta' \ x'' \ m''. t \vdash (x, \text{shr } s') \text{-ta}' \rightarrow (x'', m'') \wedge \text{actions-ok } s' \ t \ ta' \wedge$

$ta\text{-seq-consist } P \ (\text{mrw-values } P \ vs \ (\text{concat } (\text{map } (\lambda(t, ta). \{\{ta\}_o\} \ \text{ttas}))) \ (\text{llist-of } \{\{ta'\}_o\}))$

$\{\{ta'\}_o\}))$

lemma *sc-completionD*:

$\llbracket \text{sc-completion } s \ vs; s \text{-}\triangleright\text{ttas}\text{-}\rightarrow^* s'; ta\text{-seq-consist } P \ vs \ (\text{llist-of } (\text{concat } (\text{map } (\lambda(t, ta). \{\{ta\}_o\} \ \text{ttas}))) \rrbracket;$

$\text{thr } s' \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket; t \vdash (x, \text{shr } s') \text{-ta} \rightarrow (x', m'); \text{actions-ok } s' \ t \ ta \rrbracket$

$\implies \exists ta' \ x'' \ m''. t \vdash (x, \text{shr } s') \text{-ta}' \rightarrow (x'', m'') \wedge \text{actions-ok } s' \ t \ ta' \wedge$

$ta\text{-seq-consist } P \ (\text{mrw-values } P \ vs \ (\text{concat } (\text{map } (\lambda(t, ta). \{\{ta\}_o\} \ \text{ttas}))) \ (\text{llist-of } \{\{ta'\}_o\})$

<proof>

lemma *sc-completionI*:

$(\bigwedge \text{ttas } s' \ t \ x \ ta \ x' \ m'.$

$\llbracket s \text{-}\triangleright\text{ttas}\text{-}\rightarrow^* s'; ta\text{-seq-consist } P \ vs \ (\text{llist-of } (\text{concat } (\text{map } (\lambda(t, ta). \{\{ta\}_o\} \ \text{ttas}))) \rrbracket;$

$\text{thr } s' \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket; t \vdash (x, \text{shr } s') \text{-ta} \rightarrow (x', m'); \text{actions-ok } s' \ t \ ta \rrbracket$

$\implies \exists ta' \ x'' \ m''. t \vdash (x, \text{shr } s') \text{-ta}' \rightarrow (x'', m'') \wedge \text{actions-ok } s' \ t \ ta' \wedge$

$ta\text{-seq-consist } P \ (\text{mrw-values } P \ vs \ (\text{concat } (\text{map } (\lambda(t, ta). \{\{ta\}_o\} \ \text{ttas}))) \ (\text{llist-of } \{\{ta'\}_o\})$

$\implies \text{sc-completion } s \ vs$

<proof>

lemma *sc-completion-shift*:

assumes *sc-c*: $\text{sc-completion } s \ vs$

and τRed : $s \text{-}\triangleright\text{ttas}\text{-}\rightarrow^* s'$

and *sc*: $ta\text{-seq-consist } P \ vs \ (\text{lconcat } (\text{lmap } (\lambda(t, ta). \text{llist-of } \{\{ta\}_o\} \ (\text{llist-of } \text{ttas}))))$

shows $\text{sc-completion } s' \ (\text{mrw-values } P \ vs \ (\text{concat } (\text{map } (\lambda(t, ta). \{\{ta\}_o\} \ \text{ttas}))))$

<proof>

lemma *complete-sc-in-Runs*:

assumes *cau*: *sc-completion s vs*

and *ta-seq-consist-convert-RA*: $\bigwedge vs\ ln. ta-seq-consist\ P\ vs\ (l\text{list-of}\ (convert-RA\ ln))$

shows *mthr.Runs s (complete-sc s vs)*

<proof>

lemma *complete-sc-ta-seq-consist*:

assumes *cau*: *sc-completion s vs*

and *ta-seq-consist-convert-RA*: $\bigwedge vs\ ln. ta-seq-consist\ P\ vs\ (l\text{list-of}\ (convert-RA\ ln))$

shows *ta-seq-consist P vs (lconcat (lmap ($\lambda(t, ta). l\text{list-of}\ \{\!\{ta\}\!\}_o$) (complete-sc s vs)))*

<proof>

lemma *sequential-completion-Runs*:

assumes *sc-completion s vs*

and $\bigwedge vs\ ln. ta-seq-consist\ P\ vs\ (l\text{list-of}\ (convert-RA\ ln))$

shows $\exists\ ttas. mthr.Runs\ s\ ttas \wedge ta-seq-consist\ P\ vs\ (lconcat\ (lmap\ (\lambda(t, ta). l\text{list-of}\ \{\!\{ta\}\!\}_o)\ ttas))$

<proof>

definition *cut-and-update* :: $(l, \text{'thread-id}, \text{'x}, \text{'m}, \text{'w})\ state \Rightarrow (\text{'addr} \times \text{addr-loc} \rightarrow \text{'addr val} \times \text{bool}) \Rightarrow \text{bool}$

where

cut-and-update s vs \longleftrightarrow

$(\forall\ ttas\ s'\ t\ x\ ta\ x'\ m'.$

$s \rightarrow ttas \rightarrow^* s' \longrightarrow ta-seq-consist\ P\ vs\ (l\text{list-of}\ (concat\ (map\ (\lambda(t, ta). \{\!\{ta\}\!\}_o)\ ttas))) \longrightarrow$

$thr\ s'\ t = \llbracket (x, no-wait-locks) \rrbracket \longrightarrow t \vdash (x, shr\ s') -ta \rightarrow (x', m') \longrightarrow actions-ok\ s'\ t\ ta \longrightarrow$

$(\exists\ ta'\ x''\ m''. t \vdash (x, shr\ s') -ta' \rightarrow (x'', m'') \wedge actions-ok\ s'\ t\ ta' \wedge$

$ta-seq-consist\ P\ (mrw-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{\!\{ta\}\!\}_o)\ ttas)))\ (l\text{list-of}\ \{\!\{ta'\}\!\}_o)$

\wedge

$eq\text{-upto-seq-inconsist}\ P\ \{\!\{ta\}\!\}_o\ \{\!\{ta'\}\!\}_o\ (mrw-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{\!\{ta\}\!\}_o)\ ttas))))))$

lemma *cut-and-updateI[intro?]*:

$(\bigwedge\ ttas\ s'\ t\ x\ ta\ x'\ m'.$

$\llbracket s \rightarrow ttas \rightarrow^* s'; ta-seq-consist\ P\ vs\ (l\text{list-of}\ (concat\ (map\ (\lambda(t, ta). \{\!\{ta\}\!\}_o)\ ttas))) \rrbracket;$

$thr\ s'\ t = \llbracket (x, no-wait-locks) \rrbracket; t \vdash (x, shr\ s') -ta \rightarrow (x', m'); actions-ok\ s'\ t\ ta \rrbracket$

$\implies \exists\ ta'\ x''\ m''. t \vdash (x, shr\ s') -ta' \rightarrow (x'', m'') \wedge actions-ok\ s'\ t\ ta' \wedge$

$ta-seq-consist\ P\ (mrw-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{\!\{ta\}\!\}_o)\ ttas)))\ (l\text{list-of}\ \{\!\{ta'\}\!\}_o) \wedge$

$\{\!\{ta'\}\!\}_o \wedge$

$eq\text{-upto-seq-inconsist}\ P\ \{\!\{ta\}\!\}_o\ \{\!\{ta'\}\!\}_o\ (mrw-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{\!\{ta\}\!\}_o)\ ttas))))))$

$\implies cut-and-update\ s\ vs$

<proof>

lemma *cut-and-updateD*:

$\llbracket cut-and-update\ s\ vs; s \rightarrow ttas \rightarrow^* s'; ta-seq-consist\ P\ vs\ (l\text{list-of}\ (concat\ (map\ (\lambda(t, ta). \{\!\{ta\}\!\}_o)\ ttas))) \rrbracket;$

$thr\ s'\ t = \llbracket (x, no-wait-locks) \rrbracket; t \vdash (x, shr\ s') -ta \rightarrow (x', m'); actions-ok\ s'\ t\ ta \rrbracket$

$\implies \exists\ ta'\ x''\ m''. t \vdash (x, shr\ s') -ta' \rightarrow (x'', m'') \wedge actions-ok\ s'\ t\ ta' \wedge$

$ta-seq-consist\ P\ (mrw-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{\!\{ta\}\!\}_o)\ ttas)))\ (l\text{list-of}\ \{\!\{ta'\}\!\}_o)$

\wedge

$eq\text{-upto-seq-inconsist}\ P\ \{\!\{ta\}\!\}_o\ \{\!\{ta'\}\!\}_o\ (mrw-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{\!\{ta\}\!\}_o)\ ttas))))))$

ttas)))
 ⟨proof⟩

lemma *cut-and-update-imp-sc-completion*:
cut-and-update s vs \implies *sc-completion s vs*
 ⟨proof⟩

lemma *sequential-completion*:

assumes *cut-and-update*: *cut-and-update s vs*
and *ta-seq-consist-convert-RA*: $\bigwedge vs \ln. ta-seq-consist P vs (l\text{list-of } (convert-RA \ ln))$
and *Red*: $s \multimap ttas \rightarrow^* s'$
and *sc*: $ta-seq-consist P vs (l\text{list-of } (concat (map (\lambda(t, ta). \{ta\}_o) ttas)))$
and *red*: $s' \multimap ta \rightarrow s''$
shows
 $\exists ta' ttas'. mthr.Runs s' (LCons (t, ta') ttas') \wedge$
 $ta-seq-consist P vs (lconcat (lmap (\lambda(t, ta). l\text{list-of } \{ta\}_o) (lappend (l\text{list-of } ttas) (LCons (t, ta')$
 $ttas')))) \wedge$
 $eq\text{-upto-seq-inconsist } P \ \{ta\}_o \ \{ta'\}_o (mrw\text{-values } P \ vs \ (concat (map (\lambda(t, ta). \{ta\}_o) ttas)))$
 ⟨proof⟩

end

end

8.9 Happens-before consistent completion of executions in the JMM

theory *HB-Completion* **imports**

Non-Speculative

begin

coinductive *ta-hb-consistent* :: $'m \text{ prog} \Rightarrow ('thread-id \times ('addr, 'thread-id) \text{ obs-event action}) \text{ list} \Rightarrow$
 $('thread-id \times ('addr, 'thread-id) \text{ obs-event action}) \text{ llist} \Rightarrow \text{bool}$

for $P :: 'm \text{ prog}$

where

$LNil$: $ta-hb-consistent P \text{ obs } LNil$
 | $LCons$:
 $\llbracket ta-hb-consistent P (obs @ [ob]) obs';$
 $case \ ob \ of \ (t, NormalAction (ReadMem \ ad \ al \ v))$
 $\Rightarrow (\exists w. w \in write\text{-actions } (l\text{list-of } (obs @ [ob])) \wedge (ad, al) \in action\text{-loc } P (l\text{list-of } (obs @ [ob])))$
 $w \wedge$
 $value\text{-written } P (l\text{list-of } (obs @ [ob])) w (ad, al) = v \wedge$
 $P, l\text{list-of } (obs @ [ob]) \vdash w \leq_{hb} length \ obs \wedge$
 $(\forall w' \in write\text{-actions } (l\text{list-of } (obs @ [ob])). (ad, al) \in action\text{-loc } P (l\text{list-of } (obs @ [ob])) w'$
 \longrightarrow
 $(P, l\text{list-of } (obs @ [ob]) \vdash w \leq_{hb} w' \wedge P, l\text{list-of } (obs @ [ob]) \vdash w' \leq_{hb} length \ obs \vee$
 $is\text{-volatile } P \ al \wedge P, l\text{list-of } (obs @ [ob]) \vdash w \leq_{so} w' \wedge P, l\text{list-of } (obs @ [ob]) \vdash w' \leq_{so}$
 $length \ obs) \longrightarrow$
 $w' = w)$
 $\llbracket - \Rightarrow True \rrbracket$
 $\implies ta-hb-consistent P \text{ obs } (LCons \ ob \ obs')$

inductive-simps *ta-hb-consistent-LNil* [*simp*]:

ta-hb-consistent P obs LNil

inductive-simps *ta-hb-consistent-LCons*:

ta-hb-consistent P obs (LCons ob obs')

lemma *ta-hb-consistent-into-non-speculative*:

ta-hb-consistent P obs0 obs

\implies *non-speculative P (w-values P (λ -. {})) (map snd obs0)) (lmap snd obs)*

<proof>

lemma *ta-hb-consistent-lappendI*:

assumes *hb1: ta-hb-consistent P E E'*

and *hb2: ta-hb-consistent P (E @ list-of E') E''*

and *fin: lfinite E'*

shows *ta-hb-consistent P E (lappend E' E'')*

<proof>

lemma *ta-hb-consistent-coinduct-append*

[*consumes 1, case-names ta-hb-consistent, case-conclusion ta-hb-consistent LNil lappend*]:

assumes *major: X E tobs*

and *step: $\bigwedge E$ tobs. X E tobs*

\implies *tobs = LNil \vee*

(\exists tobs' tobs''. tobs = lappend tobs' tobs'' \wedge tobs' \neq LNil \wedge ta-hb-consistent P E tobs' \wedge

(lfinite tobs' \longrightarrow (X (E @ list-of tobs') tobs'' \wedge

ta-hb-consistent P (E @ list-of tobs') tobs''))

(is $\bigwedge E$ tobs. - \implies - \vee ?step E tobs)

shows *ta-hb-consistent P E tobs*

<proof>

lemma *ta-hb-consistent-coinduct-append-wf*

[*consumes 2, case-names ta-hb-consistent, case-conclusion ta-hb-consistent LNil lappend*]:

assumes *major: X E obs a*

and *wf: wf R*

and *step: $\bigwedge E$ obs a. X E obs a*

\implies *obs = LNil \vee*

(\exists obs' obs'' a'. obs = lappend obs' obs'' \wedge ta-hb-consistent P E obs' \wedge (obs' = LNil \longrightarrow (a', a)

$\in R$) \wedge

(lfinite obs' \longrightarrow X (E @ list-of obs') obs'' a' \vee

ta-hb-consistent P (E @ list-of obs') obs''))

(is $\bigwedge E$ obs a. - \implies - \vee ?step E obs a)

shows *ta-hb-consistent P E obs*

<proof>

lemma *ta-hb-consistent-lappendD2*:

assumes *hb: ta-hb-consistent P E (lappend E' E'')*

and *fin: lfinite E'*

shows *ta-hb-consistent P (E @ list-of E') E''*

<proof>

lemma *ta-hb-consistent-Read-hb*:

fixes *E E' defines E'' \equiv lappend (llist-of E') E*

assumes *hb: ta-hb-consistent P E' E*

and *tsa: thread-start-actions-ok E''*

and *E'': is-write-seen P (llist-of E') ws'*

and *new-actions-for-fun*:

$\wedge w w' \text{ adal. } \llbracket w \in \text{new-actions-for } P E'' \text{ adal};$
 $w' \in \text{new-actions-for } P E'' \text{ adal} \rrbracket \implies w = w'$

shows $\exists ws. P \vdash (E'', ws) \surd \wedge (\forall n. n \in \text{read-actions } E'' \longrightarrow \text{length } E' \leq n \longrightarrow P, E'' \vdash ws \ n \leq_{hb} n) \wedge$

$(\forall n. n < \text{length } E' \longrightarrow ws \ n = ws' \ n)$

$\langle \text{proof} \rangle$

lemma *ta-hb-consistent-not-ReadI*:

$(\wedge t \text{ ad al } v. (t, \text{NormalAction } (\text{ReadMem ad al } v)) \notin \text{lset } E) \implies \text{ta-hb-consistent } P E' E$

$\langle \text{proof} \rangle$

context *jmm-multithreaded begin*

definition *complete-hb* :: $('l, 'thread-id, 'x, 'm, 'w) \text{ state} \Rightarrow ('thread-id \times ('addr, 'thread-id) \text{ obs-event action}) \text{ list}$

$\Rightarrow ('thread-id \times ('l, 'thread-id, 'x, 'm, 'w, ('addr, 'thread-id) \text{ obs-event action}) \text{ thread-action}) \text{ llist}$

where

complete-hb $s E = \text{unfold-llist}$

$(\lambda(s, E). \forall t \text{ ta } s'. \neg s -t \triangleright \text{ta} \rightarrow s')$

$(\lambda(s, E). \text{fst } (\text{SOME } ((t, \text{ta}), s'). s -t \triangleright \text{ta} \rightarrow s' \wedge \text{ta-hb-consistent } P E \text{ (llist-of (map (Pair } t \text{ } \{\!\!\{ta\}\!\!\}_o))))))$

$(\lambda(s, E). \text{let } ((t, \text{ta}), s') = \text{SOME } ((t, \text{ta}), s'). s -t \triangleright \text{ta} \rightarrow s' \wedge \text{ta-hb-consistent } P E \text{ (llist-of (map (Pair } t \text{ } \{\!\!\{ta\}\!\!\}_o))))$

$\text{in } (s', E @ \text{map } (\text{Pair } t) \{\!\!\{ta\}\!\!\}_o)$

(s, E)

definition *hb-completion* ::

$('l, 'thread-id, 'x, 'm, 'w) \text{ state} \Rightarrow ('thread-id \times ('addr, 'thread-id) \text{ obs-event action}) \text{ list} \Rightarrow \text{bool}$

where

hb-completion $s E \longleftrightarrow$

$(\forall t \text{ ta } s' t' x' m' i.$

$s -\triangleright t \text{ ta} \rightarrow s' \longrightarrow$

$\text{non-speculative } P \text{ (w-values } P \text{ (}\lambda\text{. } \{\!\!\{ \}\!\!\}) \text{ (map snd } E)) \text{ (llist-of (concat (map } (\lambda(t, \text{ta}). \{\!\!\{ta\}\!\!\}_o) \text{ ttas)))} \longrightarrow$

$\text{thr } s' t = \llbracket (x, \text{no-wait-locks}) \rrbracket \longrightarrow t \vdash (x, \text{shr } s') -\text{ta} \rightarrow (x', m') \longrightarrow \text{actions-ok } s' t \text{ ta} \longrightarrow$

$\text{non-speculative } P \text{ (w-values } P \text{ (}\lambda\text{. } \{\!\!\{ \}\!\!\}) \text{ (map snd } E)) \text{ (concat (map } (\lambda(t, \text{ta}). \{\!\!\{ta\}\!\!\}_o) \text{ ttas)))} \text{ (llist-of (take } i \text{ } \{\!\!\{ta\}\!\!\}_o)) \longrightarrow$

$(\exists t' x'' m''. t \vdash (x, \text{shr } s') -\text{ta}' \rightarrow (x'', m'') \wedge \text{actions-ok } s' t \text{ ta}' \wedge$

$\text{take } i \text{ } \{\!\!\{ta'\}\!\!\}_o = \text{take } i \text{ } \{\!\!\{ta\}\!\!\}_o \wedge$

$\text{ta-hb-consistent } P$

$(E @ \text{concat (map } (\lambda(t, \text{ta}). \text{map } (\text{Pair } t) \{\!\!\{ta\}\!\!\}_o) \text{ ttas}) @ \text{map } (\text{Pair } t) \text{ (take } i$

$\{\!\!\{ta\}\!\!\}_o))$

$(\text{llist-of (map } (\text{Pair } t) \text{ (drop } i \text{ } \{\!\!\{ta'\}\!\!\}_o))) \wedge$

$(i < \text{length } \{\!\!\{ta\}\!\!\}_o \longrightarrow i < \text{length } \{\!\!\{ta'\}\!\!\}_o) \wedge$

$(\text{if } \exists \text{ ad al } v. \{\!\!\{ta\}\!\!\}_o ! i = \text{NormalAction } (\text{ReadMem ad al } v) \text{ then sim-action else}$

$(=)) (\{\!\!\{ta\}\!\!\}_o ! i) (\{\!\!\{ta'\}\!\!\}_o ! i))$

lemma *hb-completionD*:

$\llbracket \text{hb-completion } s E; s -\triangleright t \text{ ta} \rightarrow s';$

$\text{non-speculative } P \text{ (w-values } P \text{ (}\lambda\text{. } \{\!\!\{ \}\!\!\}) \text{ (map snd } E)) \text{ (llist-of (concat (map } (\lambda(t, \text{ta}). \{\!\!\{ta\}\!\!\}_o) \text{ ttas)))};$

$\text{thr } s' t = \llbracket (x, \text{no-wait-locks}) \rrbracket; t \vdash (x, \text{shr } s') -\text{ta} \rightarrow (x', m'); \text{actions-ok } s' t \text{ ta};$

$\text{non-speculative } P \text{ (w-values } P \text{ (}\lambda\text{. } \{\!\!\{ \}\!\!\}) \text{ (map snd } E)) \text{ (concat (map } (\lambda(t, \text{ta}). \{\!\!\{ta\}\!\!\}_o)$

$ttas)))$ (l list-of ($take\ i\ \{\!\{ta\}\!\}_o$)) \llbracket
 $\implies \exists ta' x'' m''. t \vdash (x, shr\ s') -ta' \rightarrow (x'', m'') \wedge actions-ok\ s'\ t\ ta' \wedge$
 $take\ i\ \{\!\{ta'\}\!\}_o = take\ i\ \{\!\{ta\}\!\}_o \wedge$
 $ta-hb-consistent\ P\ (E\ @\ concat\ (map\ (\lambda(t, ta). map\ (Pair\ t)\ \{\!\{ta\}\!\}_o)\ ttas)\ @\ map\ (Pair$
 $t)\ (take\ i\ \{\!\{ta\}\!\}_o))$
 $(l$ list-of ($map\ (Pair\ t)\ (drop\ i\ \{\!\{ta'\}\!\}_o))) \wedge$
 $(i < length\ \{\!\{ta\}\!\}_o \rightarrow i < length\ \{\!\{ta'\}\!\}_o) \wedge$
 $(if\ \exists\ ad\ al\ v. \{\!\{ta\}\!\}_o ! i = NormalAction\ (ReadMem\ ad\ al\ v)\ then\ sim-action\ else\ (=))$
 $(\{\!\{ta\}\!\}_o ! i)\ (\{\!\{ta'\}\!\}_o ! i)$
 $\langle proof \rangle$

lemma *hb-completionI* [*intro?*]:

$(\wedge ttas\ s'\ t\ x\ ta\ x'\ m'\ i.$
 $\llbracket s \rightarrow ttas \rightarrow * s'; non-speculative\ P\ (w-values\ P\ (\lambda-. \{\!\{ \}\!\})\ (map\ snd\ E))\ (l$ list-of ($concat\ (map\ (\lambda(t,$
 $ta). \{\!\{ta\}\!\}_o)\ ttas)))$;
 $thr\ s'\ t = \lfloor (x, no-wait-locks) \rfloor; t \vdash (x, shr\ s') -ta \rightarrow (x', m'); actions-ok\ s'\ t\ ta;$
 $non-speculative\ P\ (w-values\ P\ (w-values\ P\ (\lambda-. \{\!\{ \}\!\})\ (map\ snd\ E))\ (concat\ (map\ (\lambda(t, ta). \{\!\{ta\}\!\}_o)$
 $ttas)))$ (l list-of ($take\ i\ \{\!\{ta\}\!\}_o$)) \llbracket
 $\implies \exists ta' x'' m''. t \vdash (x, shr\ s') -ta' \rightarrow (x'', m'') \wedge actions-ok\ s'\ t\ ta' \wedge take\ i\ \{\!\{ta'\}\!\}_o = take\ i$
 $\{\!\{ta\}\!\}_o \wedge$
 $ta-hb-consistent\ P\ (E\ @\ concat\ (map\ (\lambda(t, ta). map\ (Pair\ t)\ \{\!\{ta\}\!\}_o)\ ttas)\ @\ map\ (Pair$
 $t)\ (take\ i\ \{\!\{ta\}\!\}_o))$ (l list-of ($map\ (Pair\ t)\ (drop\ i\ \{\!\{ta'\}\!\}_o))) \wedge$
 $(i < length\ \{\!\{ta\}\!\}_o \rightarrow i < length\ \{\!\{ta'\}\!\}_o) \wedge$
 $(if\ \exists\ ad\ al\ v. \{\!\{ta\}\!\}_o ! i = NormalAction\ (ReadMem\ ad\ al\ v)\ then\ sim-action\ else\ (=))$
 $(\{\!\{ta\}\!\}_o ! i)\ (\{\!\{ta'\}\!\}_o ! i)$
 $\implies hb-completion\ s\ E$
 $\langle proof \rangle$

lemma *hb-completion-shift*:

assumes *hb-c*: *hb-completion* $s\ E$
and τRed : $s \rightarrow ttas \rightarrow * s'$
and sc : *non-speculative* $P\ (w-values\ P\ (\lambda-. \{\!\{ \}\!\})\ (map\ snd\ E))\ (l$ list-of ($concat\ (map\ (\lambda(t, ta). \{\!\{ta\}\!\}_o)$
 $ttas)))$
 $(is\ non-speculative - ?vs -)$
shows *hb-completion* $s'\ (E\ @\ (concat\ (map\ (\lambda(t, ta). map\ (Pair\ t)\ \{\!\{ta\}\!\}_o)\ ttas))$
 $(is\ hb-completion - ?E)$
 $\langle proof \rangle$

lemma *hb-completion-shift1*:

assumes *hb-c*: *hb-completion* $s\ E$
and Red : $s -t>ta \rightarrow s'$
and sc : *non-speculative* $P\ (w-values\ P\ (\lambda-. \{\!\{ \}\!\})\ (map\ snd\ E))\ (l$ list-of ($\{\!\{ta\}\!\}_o$)
shows *hb-completion* $s'\ (E\ @\ map\ (Pair\ t)\ \{\!\{ta\}\!\}_o)$
 $\langle proof \rangle$

lemma *complete-hb-in-Runs*:

assumes *hb-c*: *hb-completion* $s\ E$
and *ta-hb-consistent-convert-RA*: $\wedge t\ E\ ln. ta-hb-consistent\ P\ E\ (l$ list-of ($map\ (Pair\ t)\ (convert-RA$
 $ln)))$
shows *mthr.Runs* $s\ (complete-hb\ s\ E)$
 $\langle proof \rangle$

lemma *complete-hb-ta-hb-consistent*:

assumes *hb-completion* $s\ E$

and *ta-hb-consistent-convert-RA*: $\bigwedge E t ln. ta-hb-consistent P E (l\text{list-of } (map (Pair t) (convert-RA ln)))$
shows *ta-hb-consistent P E (lconcat (lmap ($\lambda(t, ta). l\text{list-of } (map (Pair t) \{\{ta\}_o\}) (complete-hb s E)))$)*
 (**is** *ta-hb-consistent - - (?obs (complete-hb s E))*)
<proof>

lemma *hb-completion-Runs*:

assumes *hb-completion s E*
and $\bigwedge E t ln. ta-hb-consistent P E (l\text{list-of } (map (Pair t) (convert-RA ln)))$
shows $\exists ttas. mthr.Runs s ttas \wedge ta-hb-consistent P E (l\text{concat } (lmap (\lambda(t, ta). l\text{list-of } (map (Pair t) \{\{ta\}_o\}) ttas))$
<proof>

end

end

8.10 Locales for heap operations with set of allocated addresses

theory *JMM-Heap*

imports

../Common/WellForm

SC-Completion

HB-Completion

begin

definition *w-addr*s :: $('addr \times addr-loc \Rightarrow 'addr \text{ val set}) \Rightarrow 'addr \text{ set}$

where *w-addr*s *vs* = $\{a. \exists adal. Addr a \in vs adal\}$

lemma *w-addr*s-*empty [simp]*: *w-addr*s ($\lambda-. \{\}$) = $\{\}$

<proof>

locale *allocated-heap-base* = *heap-base* +

constrains *addr2thread-id* :: $('addr :: addr) \Rightarrow 'thread-id$

and *thread-id2addr* :: $'thread-id \Rightarrow 'addr$

and *spurious-wakeups* :: *bool*

and *empty-heap* :: *'heap*

and *allocate* :: $'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) \text{ set}$

and *typeof-addr* :: $'heap \Rightarrow 'addr \rightarrow htype$

and *heap-read* :: $'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr \text{ val} \Rightarrow bool$

and *heap-write* :: $'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr \text{ val} \Rightarrow 'heap \Rightarrow bool$

fixes *allocated* :: $'heap \Rightarrow 'addr \text{ set}$

locale *allocated-heap* =

allocated-heap-base +

heap +

constrains *addr2thread-id* :: $('addr :: addr) \Rightarrow 'thread-id$

and *thread-id2addr* :: $'thread-id \Rightarrow 'addr$

and *spurious-wakeups* :: *bool*

and *empty-heap* :: *'heap*

and *allocate* :: $'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) \text{ set}$

and *typeof-addr* :: $'heap \Rightarrow 'addr \rightarrow htype$

and *heap-read* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow bool
and *heap-write* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow 'heap \Rightarrow bool
and *allocated* :: 'heap \Rightarrow 'addr set
and *P* :: 'm prog

assumes *allocated-empty*: allocated empty-heap = {}
and *allocate-allocatedD*:
 $(h', a) \in \text{allocate } h \ hT \implies \text{allocated } h' = \text{insert } a \ (\text{allocated } h) \wedge a \notin \text{allocated } h$
and *heap-write-allocated-same*:
 $\text{heap-write } h \ a \ al \ v \ h' \implies \text{allocated } h' = \text{allocated } h$
begin

lemma *allocate-allocated-mono*: $(h', a) \in \text{allocate } h \ C \implies \text{allocated } h \subseteq \text{allocated } h'$
 $\langle \text{proof} \rangle$

lemma
shows *start-addr-allocated*: allocated start-heap = set start-addr
and *distinct-start-addr'*: distinct start-addr
 $\langle \text{proof} \rangle$

lemma *w-addr-start-heap-obs*: $w\text{-addr-obs } (w\text{-values } P \text{ vs } (\text{map } \text{NormalAction } \text{start-heap-obs})) \subseteq w\text{-addr-obs } vs$
 $\langle \text{proof} \rangle$

end

context *heap-base* **begin**

lemma *addr-loc-default-conf*:
 $P \vdash \text{class-type-of } CTn \text{ has } F:T \ (fm) \text{ in } C$
 $\implies P, h \vdash \text{addr-loc-default } P \ CTn \ (CField \ C \ F) : \leq T$
 $\langle \text{proof} \rangle$

definition *vs-conf* :: 'm prog \Rightarrow 'heap \Rightarrow ('addr \times addr-loc \Rightarrow 'addr val set) \Rightarrow bool
where $vs\text{-conf } P \ h \ vs \iff (\forall ad \ al \ v. v \in vs \ (ad, al) \longrightarrow (\exists T. P, h \vdash ad@al : T \wedge P, h \vdash v : \leq T))$

lemma *vs-confI*:
 $(\bigwedge ad \ al \ v. v \in vs \ (ad, al) \implies \exists T. P, h \vdash ad@al : T \wedge P, h \vdash v : \leq T) \implies vs\text{-conf } P \ h \ vs$
 $\langle \text{proof} \rangle$

lemma *vs-confD*:
 $\llbracket vs\text{-conf } P \ h \ vs; v \in vs \ (ad, al) \rrbracket \implies \exists T. P, h \vdash ad@al : T \wedge P, h \vdash v : \leq T$
 $\langle \text{proof} \rangle$

lemma *vs-conf-insert-iff*:
 $vs\text{-conf } P \ h \ (vs((ad, al) := \text{insert } v \ (vs \ (ad, al))))$
 $\iff vs\text{-conf } P \ h \ vs \wedge (\exists T. P, h \vdash ad@al : T \wedge P, h \vdash v : \leq T)$
 $\langle \text{proof} \rangle$

end

context *heap* **begin**

lemma *vs-conf-hext*: $\llbracket vs\text{-conf } P \ h \ vs; h \leq h' \rrbracket \implies vs\text{-conf } P \ h' \ vs$

$\langle proof \rangle$

lemma *vs-conf-allocate*:

$\llbracket vs\text{-conf } P \ h \ vs; (h', a) \in \text{allocate } h \ hT; \text{is-htype } P \ hT \rrbracket$
 $\implies vs\text{-conf } P \ h' \ (w\text{-value } P \ vs \ (\text{NormalAction } (\text{NewHeapElem } a \ hT)))$

$\langle proof \rangle$

end

heap-read-typeable must not be defined in *heap-conf-base* (where it should be) because this would lead to duplicate definitions of *heap-read-typeable* in contexts where *heap-conf-base* is imported twice with different parameters, e.g., P and $J2JVM \ P$ in *J-JVM-heap-conf-read*.

context *heap-base* **begin**

definition *heap-read-typeable* :: ('heap \Rightarrow bool) \Rightarrow 'm prog \Rightarrow bool

where *heap-read-typeable* $hconf \ P \longleftrightarrow (\forall h \ ad \ al \ v \ T. \ hconf \ h \longrightarrow P, h \vdash ad@al : T \longrightarrow P, h \vdash v : \leq T \longrightarrow \text{heap-read } h \ ad \ al \ v)$

lemma *heap-read-typeableI*:

$(\bigwedge h \ ad \ al \ v \ T. \llbracket P, h \vdash ad@al : T; P, h \vdash v : \leq T; hconf \ h \rrbracket \implies \text{heap-read } h \ ad \ al \ v) \implies \text{heap-read-typeable } hconf \ P$

$\langle proof \rangle$

lemma *heap-read-typeableD*:

$\llbracket \text{heap-read-typeable } hconf \ P; P, h \vdash ad@al : T; P, h \vdash v : \leq T; hconf \ h \rrbracket \implies \text{heap-read } h \ ad \ al \ v$

$\langle proof \rangle$

end

context *heap-base* **begin**

definition *heap-read-typed* :: 'm prog \Rightarrow 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow bool

where *heap-read-typed* $P \ h \ ad \ al \ v \longleftrightarrow \text{heap-read } h \ ad \ al \ v \wedge (\forall T. P, h \vdash ad@al : T \longrightarrow P, h \vdash v : \leq T)$

lemma *heap-read-typedI*:

$\llbracket \text{heap-read } h \ ad \ al \ v; \bigwedge T. P, h \vdash ad@al : T \implies P, h \vdash v : \leq T \rrbracket \implies \text{heap-read-typed } P \ h \ ad \ al \ v$

$\langle proof \rangle$

lemma *heap-read-typed-into-heap-read*:

$\text{heap-read-typed } P \ h \ ad \ al \ v \implies \text{heap-read } h \ ad \ al \ v$

$\langle proof \rangle$

lemma *heap-read-typed-typed*:

$\llbracket \text{heap-read-typed } P \ h \ ad \ al \ v; P, h \vdash ad@al : T \rrbracket \implies P, h \vdash v : \leq T$

$\langle proof \rangle$

end

context *heap-conf* **begin**

lemma *heap-conf-read-heap-read-typed*:

$\text{heap-conf-read } \text{addr2thread-id } \text{thread-id2addr } \text{empty-heap } \text{allocate } \text{typeof-addr } (\text{heap-read-typed } P)$
 $\text{heap-write } hconf \ P$

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<proof>

end

context *heap* **begin**

lemma *start-addr-dom-w-values*:

assumes *wf*: *wf-syscls P*

and *a*: $a \in \text{set } \text{start-addr}$

and *adal*: $P, \text{start-heap} \vdash a@al : T$

shows *w-values P* $(\lambda-. \{\})$ $(\text{map } \text{NormalAction } \text{start-heap-obs}) (a, al) \neq \{\}$

<proof>

end

end

8.11 Combination of locales for heap operations and interleaving

theory *JMM-Framework*

imports

JMM-Heap

../Framework/FWInitFinLift

../Common/WellForm

begin

lemma *enat-plus-eq-enat-conv*: — Move to Extended_Nat

$\text{enat } m + n = \text{enat } k \longleftrightarrow k \geq m \wedge n = \text{enat } (k - m)$

<proof>

declare *convert-new-thread-action-id* [*simp*]

context *heap* **begin**

lemma *init-fin-lift-state-start-state*:

$\text{init-fin-lift-state } s (\text{start-state } f P C M vs) = \text{start-state } (\lambda C M Ts T \text{meth } vs. (s, f C M Ts T \text{meth } vs)) P C M vs$

<proof>

lemma *non-speculative-start-heap-obs*:

$\text{non-speculative } P vs (\text{llist-of } (\text{map } \text{snd } (\text{lift-start-obs } \text{start-tid } \text{start-heap-obs})))$

<proof>

lemma *ta-seq-consist-start-heap-obs*:

$\text{ta-seq-consist } P \text{Map.empty } (\text{llist-of } (\text{map } \text{snd } (\text{lift-start-obs } \text{start-tid } \text{start-heap-obs})))$

<proof>

end

context *allocated-heap* **begin**

lemma *w-addr-lift-start-heap-obs*:

$w\text{-addrs } (w\text{-values } P \text{ vs } (\text{map snd } (\text{lift-start-obs } \text{start-tid } \text{start-heap-obs}))) \subseteq w\text{-addrs vs}$
 ⟨proof⟩

end

context heap begin

lemma w-values-start-heap-obs-typeable:

assumes wf: wf-syscls P

and mrws: $v \in w\text{-values } P (\lambda\cdot. \{\}) (\text{map snd } (\text{lift-start-obs } \text{start-tid } \text{start-heap-obs})) (ad, al)$

shows $\exists T. P, \text{start-heap} \vdash ad@al : T \wedge P, \text{start-heap} \vdash v : \leq T$

⟨proof⟩

lemma start-state-vs-conf:

$wf\text{-syscls } P \implies vs\text{-conf } P \text{ start-heap } (w\text{-values } P (\lambda\cdot. \{\}) (\text{map snd } (\text{lift-start-obs } \text{start-tid } \text{start-heap-obs})))$
 ⟨proof⟩

end

8.11.1 JMM traces for Jinja semantics

context multithreaded-base begin

inductive-set $\mathcal{E} :: ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow ('t \times 'o) \text{ llist set}$

for $\sigma :: ('l, 't, 'x, 'm, 'w) \text{ state}$

where

$mthr.\text{Runs } \sigma E'$

$\implies lconcat (lmap (\lambda(t, ta). \text{llist-of } (\text{map } (Pair t) \{\{ta\}_o\})) E') \in \mathcal{E} \sigma$

lemma actions- $\mathcal{E}E\text{-aux}$:

fixes $\sigma E'$

defines $E == lconcat (lmap (\lambda(t, ta). \text{llist-of } (\text{map } (Pair t) \{\{ta\}_o\})) E')$

assumes $mthr.\text{Runs } \sigma E'$

and $a: \text{enat } a < \text{llength } E$

obtains $m n t ta$

where $\text{lnth } E a = (t, \{\{ta\}_o ! n)$

and $n < \text{length } \{\{ta\}_o$ and $\text{enat } m < \text{llength } E'$

and $a = (\sum_{i < m. \text{length } \{\{snd } (\text{lnth } E' i)\}_o}) + n$

and $\text{lnth } E' m = (t, ta)$

⟨proof⟩

lemma actions- $\mathcal{E}E$:

assumes $E: E \in \mathcal{E} \sigma$

and $a: \text{enat } a < \text{llength } E$

obtains $E' m n t ta$

where $E = lconcat (lmap (\lambda(t, ta). \text{llist-of } (\text{map } (Pair t) \{\{ta\}_o\})) E')$

and $mthr.\text{Runs } \sigma E'$

and $\text{lnth } E a = (t, \{\{ta\}_o ! n)$

and $n < \text{length } \{\{ta\}_o$ and $\text{enat } m < \text{llength } E'$

and $a = (\sum_{i < m. \text{length } \{\{snd } (\text{lnth } E' i)\}_o}) + n$

and $\text{lnth } E' m = (t, ta)$

⟨proof⟩

end

context τ multithreaded-wf **begin**

Alternative characterisation for \mathcal{E}

lemma \mathcal{E} -conv-Runs:

$\mathcal{E} \sigma = \text{lconcat } \text{lmap } (\lambda(t, ta). \text{lList-of } (\text{map } (\text{Pair } t) \{\{ta\}_o\})) \text{ 'lList-of-tlList } \{E. \text{mthr}.\tau\text{Runs } \sigma E\}$
 (is ?lhs = ?rhs)
 ⟨proof⟩

end

Running threads have been started before

definition $\text{Status-no-wait-locks} :: ('l, 't, \text{status} \times 'x) \text{ thread-info} \Rightarrow \text{bool}$

where

$\text{Status-no-wait-locks } ts \longleftrightarrow$
 $(\forall t \text{ status } x \text{ ln}. ts \ t = \llbracket ((\text{status}, x), \text{ln}) \rrbracket \longrightarrow \text{status} \neq \text{Running} \longrightarrow \text{ln} = \text{no-wait-locks})$

lemma $\text{Status-no-wait-locks-PreStartD}$:

$\bigwedge \text{ln}. \llbracket \text{Status-no-wait-locks } ts; ts \ t = \llbracket ((\text{PreStart}, x), \text{ln}) \rrbracket \rrbracket \Longrightarrow \text{ln} = \text{no-wait-locks}$
 ⟨proof⟩

lemma $\text{Status-no-wait-locks-FinishedD}$:

$\bigwedge \text{ln}. \llbracket \text{Status-no-wait-locks } ts; ts \ t = \llbracket ((\text{Finished}, x), \text{ln}) \rrbracket \rrbracket \Longrightarrow \text{ln} = \text{no-wait-locks}$
 ⟨proof⟩

lemma $\text{Status-no-wait-locksI}$:

$(\bigwedge t \text{ status } x \text{ ln}. \llbracket ts \ t = \llbracket ((\text{status}, x), \text{ln}) \rrbracket; \text{status} = \text{PreStart} \vee \text{status} = \text{Finished} \rrbracket \Longrightarrow \text{ln} = \text{no-wait-locks})$
 $\Longrightarrow \text{Status-no-wait-locks } ts$
 ⟨proof⟩

context heap-base **begin**

lemma $\text{Status-no-wait-locks-start-state}$:

$\text{Status-no-wait-locks } (\text{thr } (\text{init-fin-lift-state } \text{status } (\text{start-state } f \ P \ C \ M \ vs)))$
 ⟨proof⟩

end

context multithreaded-base **begin**

lemma $\text{init-fin-preserve-Status-no-wait-locks}$:

assumes $ok: \text{Status-no-wait-locks } (\text{thr } s)$
and $\text{redT}: \text{multithreaded-base.redT } \text{init-fin-final } \text{init-fin } (\text{map } \text{NormalAction} \circ \text{convert-RA}) \ s \ \text{tta } \ s'$
shows $\text{Status-no-wait-locks } (\text{thr } s')$
 ⟨proof⟩

lemma $\text{init-fin-Running-InitialThreadAction}$:

assumes $\text{redT}: \text{multithreaded-base.redT } \text{init-fin-final } \text{init-fin } (\text{map } \text{NormalAction} \circ \text{convert-RA}) \ s \ \text{tta } \ s'$
and $\text{not-running}: \bigwedge x \ \text{ln}. \text{thr } s \ t \neq \llbracket ((\text{Running}, x), \text{ln}) \rrbracket$
and $\text{running}: \text{thr } s' \ t = \llbracket ((\text{Running}, x'), \text{ln}') \rrbracket$
shows $\text{tta} = (t, \{\{ \text{InitialThreadAction} \})$
 ⟨proof⟩

end

context *if-multithreaded* begin

lemma *init-fin-Trsys-preserve-Status-no-wait-locks*:

assumes *ok*: *Status-no-wait-locks* (*thr s*)

and *Trsys*: *if.mthr.Trsys s ttas s'*

shows *Status-no-wait-locks* (*thr s'*)

<proof>

lemma *init-fin-Trsys-Running-InitialThreadAction*:

assumes *redT*: *if.mthr.Trsys s ttas s'*

and *not-running*: $\bigwedge x \text{ ln. } \text{thr } s \ t \neq [((\text{Running}, x), \text{ln})]$

and *running*: $\text{thr } s' \ t = [((\text{Running}, x'), \text{ln}')]]$

shows $(t, \{\text{InitialThreadAction}\}) \in \text{set } \text{ttas}$

<proof>

end

locale *heap-multithreaded-base* =

heap-base

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate typeof-addr heap-read heap-write

+

mthr: multithreaded-base final r convert-RA

for *addr2thread-id* :: (*'addr* :: *addr*) \Rightarrow *'thread-id*

and *thread-id2addr* :: *'thread-id* \Rightarrow *'addr*

and *spurious-wakeups* :: *bool*

and *empty-heap* :: *'heap*

and *allocate* :: *'heap* \Rightarrow *htype* \Rightarrow (*'heap* \times *'addr*) *set*

and *typeof-addr* :: *'heap* \Rightarrow *'addr* \rightarrow *htype*

and *heap-read* :: *'heap* \Rightarrow *'addr* \Rightarrow *addr-loc* \Rightarrow *'addr val* \Rightarrow *bool*

and *heap-write* :: *'heap* \Rightarrow *'addr* \Rightarrow *addr-loc* \Rightarrow *'addr val* \Rightarrow *'heap* \Rightarrow *bool*

and *final* :: *'x* \Rightarrow *bool*

and *r* :: (*'addr*, *'thread-id*, *'x*, *'heap*, *'addr*, (*'addr*, *'thread-id*) *obs-event*) *semantics* ($\langle \vdash - \dashrightarrow \rightarrow$
[50,0,0,50] 80)

and *convert-RA* :: *'addr released-locks* \Rightarrow (*'addr*, *'thread-id*) *obs-event list*

sublocale *heap-multithreaded-base* < *mthr: if-multithreaded-base final r convert-RA*

<proof>

context *heap-multithreaded-base* begin

abbreviation *E-start* ::

(*cname* \Rightarrow *mname* \Rightarrow *ty list* \Rightarrow *ty* \Rightarrow *'md* \Rightarrow *'addr val list* \Rightarrow *'x*)

\Rightarrow *'md prog* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *'addr val list* \Rightarrow *status*

\Rightarrow (*'thread-id* \times (*'addr*, *'thread-id*) *obs-event action*) *llist set*

where

E-start *f P C M vs status* \equiv

lappend (*llist-of* (*lift-start-obs start-tid start-heap-obs*)) '

mthr.if.E (*init-fin-lift-state status* (*start-state f P C M vs*))

end

locale *heap-multithreaded* =

heap-multithreaded-base
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate typeof-addr heap-read heap-write
final r convert-RA

+

heap
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate typeof-addr heap-read heap-write
P

+

mthr: multithreaded final r convert-RA

for *addr2thread-id* :: ('addr :: addr) ⇒ 'thread-id

and *thread-id2addr* :: 'thread-id ⇒ 'addr

and *spurious-wakeups* :: bool

and *empty-heap* :: 'heap

and *allocate* :: 'heap ⇒ htype ⇒ ('heap × 'addr) set

and *typeof-addr* :: 'heap ⇒ 'addr → htype

and *heap-read* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool

and *heap-write* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool

and *final* :: 'x ⇒ bool

and *r* :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics (⟦ - ⟧ - - -> →) [50,0,0,50] 80

and *convert-RA* :: 'addr released-locks ⇒ ('addr, 'thread-id) obs-event list

and *P* :: 'md prog

sublocale *heap-multithreaded* < *mthr: if-multithreaded final r convert-RA*

⟨*proof*⟩

sublocale *heap-multithreaded* < *if: jmm-multithreaded*

mthr.init-fin-final mthr.init-fin map NormalAction o convert-RA P

⟨*proof*⟩

context *heap-multithreaded begin*

lemma *thread-start-actions-ok-init-fin-RedT*:

assumes *Red: mthr.if.RedT (init-fin-lift-state status (start-state f P C M vs)) ttas s'*

(**is** *mthr.if.RedT ?start-state - -*)

shows *thread-start-actions-ok (llist-of (lift-start-obs start-tid start-heap-obs @ concat (map (λ(t, ta). map (Pair t) {ta}_o) ttas)))*

(**is** *thread-start-actions-ok (llist-of (?obs-prefix @ ?E'))*)

⟨*proof*⟩

lemma *thread-start-actions-ok-init-fin*:

assumes *E: E ∈ mthr.if.ℰ (init-fin-lift-state status (start-state f P C M vs))*

shows *thread-start-actions-ok (lappend (llist-of (lift-start-obs start-tid start-heap-obs)) E)*

(**is** *thread-start-actions-ok ?E*)

<proof>

end

In the subsequent locales, *convert-RA* refers to *convert-RA* and is no longer a parameter!

lemma *convert-RA-not-write*:

$\bigwedge ln. ob \in set (convert-RA\ ln) \implies \neg is-write-action (NormalAction\ ob)$
<proof>

lemma *ta-seq-consist-convert-RA*:

fixes *ln* **shows**
 $ta-seq-consist\ P\ vs\ (l\ list-of\ ((map\ NormalAction\ \circ\ convert-RA)\ ln))$
<proof>

lemma *ta-hb-consistent-convert-RA*:

$\bigwedge ln. ta-hb-consistent\ P\ E\ (l\ list-of\ (map\ (Pair\ t)\ ((map\ NormalAction\ \circ\ convert-RA)\ ln)))$
<proof>

locale *allocated-multithreaded* =

allocated-heap
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate typeof-addr heap-read heap-write
allocated
P
 +
mthr: multithreaded final r convert-RA

for *addr2thread-id* :: ('addr :: addr) \Rightarrow 'thread-id
and *thread-id2addr* :: 'thread-id \Rightarrow 'addr
and *spurious-wakeups* :: bool
and *empty-heap* :: 'heap
and *allocate* :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) set
and *typeof-addr* :: 'heap \Rightarrow 'addr \rightarrow htype
and *heap-read* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow bool
and *heap-write* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow 'heap \Rightarrow bool
and *allocated* :: 'heap \Rightarrow 'addr set
and *final* :: 'x \Rightarrow bool
and *r* :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics ($\leftarrow \vdash - \dashrightarrow \rightarrow$)
 [50,0,0,50] 80)
and *P* :: 'md prog
 +
assumes *red-allocated-mono*: $t \vdash (x, m) -ta\rightarrow (x', m') \implies allocated\ m \subseteq allocated\ m'$
and *red-New-allocatedD*:
 $\llbracket t \vdash (x, m) -ta\rightarrow (x', m'); NewHeapElem\ ad\ CTn \in set\ \{ta\}_o \rrbracket$
 $\implies ad \in allocated\ m' \wedge ad \notin allocated\ m$
and *red-allocated-NewD*:
 $\llbracket t \vdash (x, m) -ta\rightarrow (x', m'); ad \in allocated\ m'; ad \notin allocated\ m \rrbracket$
 $\implies \exists CTn. NewHeapElem\ ad\ CTn \in set\ \{ta\}_o$
and *red-New-same-addr-same*:
 $\llbracket t \vdash (x, m) -ta\rightarrow (x', m');$
 $\{ta\}_o ! i = NewHeapElem\ a\ CTn; i < length\ \{ta\}_o;$

$$\{ta\}_o ! j = \text{NewHeapElem } a \text{ } CTn'; j < \text{length } \{ta\}_o] \\ \implies i = j$$

sublocale *allocated-multithreaded* < *heap-multithreaded*
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate typeof-addr heap-read heap-write
final r convert-RA P
 ⟨*proof*⟩

context *allocated-multithreaded* **begin**

lemma *redT-allocated-mono*:
assumes *mthr.redT* σ (*t*, *ta*) σ'
shows *allocated* (*shr* σ) \subseteq *allocated* (*shr* σ')
 ⟨*proof*⟩

lemma *RedT-allocated-mono*:
assumes *mthr.RedT* σ *ttas* σ'
shows *allocated* (*shr* σ) \subseteq *allocated* (*shr* σ')
 ⟨*proof*⟩

lemma *init-fin-allocated-mono*:
 $t \vdash (x, m) -ta \rightarrow i (x', m') \implies \text{allocated } m \subseteq \text{allocated } m'$
 ⟨*proof*⟩

lemma *init-fin-redT-allocated-mono*:
assumes *mthr.if.redT* σ (*t*, *ta*) σ'
shows *allocated* (*shr* σ) \subseteq *allocated* (*shr* σ')
 ⟨*proof*⟩

lemma *init-fin-RedT-allocated-mono*:
assumes *mthr.if.RedT* σ *ttas* σ'
shows *allocated* (*shr* σ) \subseteq *allocated* (*shr* σ')
 ⟨*proof*⟩

lemma *init-fin-red-New-allocatedD*:
assumes $t \vdash (x, m) -ta \rightarrow i (x', m') \text{NormalAction } (\text{NewHeapElem } ad \text{ } CTn) \in \text{set } \{ta\}_o$
shows $ad \in \text{allocated } m' \wedge ad \notin \text{allocated } m$
 ⟨*proof*⟩

lemma *init-fin-red-allocated-NewD*:
assumes $t \vdash (x, m) -ta \rightarrow i (x', m') ad \in \text{allocated } m' ad \notin \text{allocated } m$
shows $\exists CTn. \text{NormalAction } (\text{NewHeapElem } ad \text{ } CTn) \in \text{set } \{ta\}_o$
 ⟨*proof*⟩

lemma *init-fin-red-New-same-addr-same*:
assumes $t \vdash (x, m) -ta \rightarrow i (x', m')$
and $\{ta\}_o ! i = \text{NormalAction } (\text{NewHeapElem } a \text{ } CTn) i < \text{length } \{ta\}_o$
and $\{ta\}_o ! j = \text{NormalAction } (\text{NewHeapElem } a \text{ } CTn') j < \text{length } \{ta\}_o$
shows $i = j$
 ⟨*proof*⟩

lemma *init-fin-redT-allocated-NewHeapElemD*:

assumes *mthr.if.redT s (t, ta) s'*

and *ad ∈ allocated (shr s')*

and *ad ∉ allocated (shr s)*

shows $\exists CTn. \text{NormalAction } (\text{NewHeapElem } ad \ CTn) \in \text{set } \{ta\}_o$

<proof>

lemma *init-fin-RedT-allocated-NewHeapElemD*:

assumes *mthr.if.RedT s ttas s'*

and *ad ∈ allocated (shr s')*

and *ad ∉ allocated (shr s)*

shows $\exists t \ ta \ CTn. (t, ta) \in \text{set } ttas \wedge \text{NormalAction } (\text{NewHeapElem } ad \ CTn) \in \text{set } \{ta\}_o$

<proof>

lemma *E-new-actions-for-unique*:

assumes *E: E ∈ E-start f P C M vs status*

and *a: a ∈ new-actions-for P E adal*

and *a': a' ∈ new-actions-for P E adal*

shows *a = a'*

<proof>

end

Knowledge of addresses of a multithreaded state

fun *ka-Val* :: *'addr val ⇒ 'addr set*

where

ka-Val (Addr a) = {a}

| *ka-Val - = {}*

fun *new-obs-addr* :: *('addr, 'thread-id) obs-event ⇒ 'addr set*

where

new-obs-addr (ReadMem ad al (Addr ad')) = {ad'}

| *new-obs-addr (NewHeapElem ad hT) = {ad}*

| *new-obs-addr - = {}*

lemma *new-obs-addr-cases*[*consumes 1, case-names ReadMem NewHeapElem, cases set*]:

assumes *ad ∈ new-obs-addr ob*

obtains *ad' al* **where** *ob = ReadMem ad' al (Addr ad)*

| *CTn* **where** *ob = NewHeapElem ad CTn*

<proof>

definition *new-obs-addr*s :: *('addr, 'thread-id) obs-event list ⇒ 'addr set*

where

*new-obs-addr*s *obs = ⋃ (new-obs-addr ' set obs)*

fun *new-obs-addr-if* :: *('addr, 'thread-id) obs-event action ⇒ 'addr set*

where

new-obs-addr-if (NormalAction a) = new-obs-addr a

| *new-obs-addr-if - = {}*

definition *new-obs-addr*s-*if* :: *('addr, 'thread-id) obs-event action list ⇒ 'addr set*

where

*new-obs-addr*s-*if* *obs = ⋃ (new-obs-addr-if ' set obs)*

lemma *ka-Val-subset-new-obs-Addr-ReadMem*:

$ka\text{-Val } v \subseteq new\text{-obs-addr } (ReadMem \text{ ad } al \ v)$

<proof>

lemma *typeof-ka*: $typeof \ v \neq None \implies ka\text{-Val } v = \{\}$

<proof>

lemma *ka-Val-undefined-value* [*simp*]:

$ka\text{-Val } undefined\text{-value} = \{\}$

<proof>

locale *known-addr-base* =

fixes *known-addr* :: $'t \Rightarrow 'x \Rightarrow 'addr \ set$

begin

definition *known-addr-thr* :: $('l, 't, 'x) \ thread\text{-info} \Rightarrow 'addr \ set$

where $known\text{-addr-thr } ts = (\bigcup t \in dom \ ts. known\text{-addr} \ t \ (fst \ (the \ (ts \ t))))$

definition *known-addr-state* :: $('l, 't, 'x, 'm, 'w) \ state \Rightarrow 'addr \ set$

where $known\text{-addr-state } s = known\text{-addr-thr} \ (thr \ s)$

lemma *known-addr-state-simps* [*simp*]:

$known\text{-addr-state} \ (ls, (ts, m), ws) = known\text{-addr-thr} \ ts$

<proof>

lemma *known-addr-thr-cases*[*consumes 1, case-names known-addr, cases set: known-addr-thr*]:

assumes $ad \in known\text{-addr-thr} \ ts$

and $\bigwedge t \ x \ ln. \llbracket ts \ t = [(x, ln)]; ad \in known\text{-addr} \ t \ x \rrbracket \implies thesis$

shows *thesis*

<proof>

lemma *known-addr-stateI*:

$\bigwedge ln. \llbracket ad \in known\text{-addr} \ t \ x; thr \ s \ t = [(x, ln)] \rrbracket \implies ad \in known\text{-addr-state} \ s$

<proof>

fun *known-addr-if* :: $'t \Rightarrow status \times 'x \Rightarrow 'addr \ set$

where $known\text{-addr-if} \ t \ (s, x) = known\text{-addr} \ t \ x$

end

locale *if-known-addr-base* =

$known\text{-addr-base} \ known\text{-addr}$

+

$multithreaded\text{-base} \ final \ r \ convert\text{-RA}$

for *known-addr* :: $'t \Rightarrow 'x \Rightarrow 'addr \ set$

and *final* :: $'x \Rightarrow bool$

and *r* :: $('addr, 't, 'x, 'heap, 'addr, 'obs) \ semantics \ (\leftarrow \vdash \ - \ \longrightarrow \rightarrow [50,0,0,50] \ 80)$

and *convert-RA* :: $'addr \ released\text{-locks} \Rightarrow 'obs \ list$

sublocale *if-known-addr-base* < *if*: $known\text{-addr-base} \ known\text{-addr-if}$ *<proof>*

locale *known-addr* =

$allocated\text{-multithreaded}$

$addr2thread\text{-id} \ thread\text{-id}2addr$

spurious-wakeups
empty-heap allocate typeof-addr heap-read heap-write
allocated
final r
P
+
if-known-addr-base known-addr final r convert-RA

for *addr2thread-id* :: ('addr :: addr) ⇒ 'thread-id
and *thread-id2addr* :: 'thread-id ⇒ 'addr
and *spurious-wakeups* :: bool
and *empty-heap* :: 'heap
and *allocate* :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and *typeof-addr* :: 'heap ⇒ 'addr → htype
and *heap-read* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and *heap-write* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and *allocated* :: 'heap ⇒ 'addr set
and *known-addr* :: 'thread-id ⇒ 'x ⇒ 'addr set
and *final* :: 'x ⇒ bool
and *r* :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics (◁ ⊢ - - - -> →)
[50,0,0,50] 80)
and *P* :: 'md prog
+
assumes *red-known-addr-new*:
 $t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle$
 $\implies \text{known-addr } t \ x' \subseteq \text{known-addr } t \ x \cup \text{new-obs-addr } \{ta\}_o$
and *red-known-addr-new-thread*:
 $\llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; \text{NewThread } t' \ x'' \ m'' \in \text{set } \{ta\}_t \rrbracket$
 $\implies \text{known-addr } t' \ x'' \subseteq \text{known-addr } t \ x$
and *red-read-knows-addr*:
 $\llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; \text{ReadMem } ad \ al \ v \in \text{set } \{ta\}_o \rrbracket$
 $\implies ad \in \text{known-addr } t \ x$
and *red-write-knows-addr*:
 $\llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; \{ta\}_o ! n = \text{WriteMem } ad \ al \ (\text{Addr } ad'); n < \text{length } \{ta\}_o \rrbracket$
 $\implies ad' \in \text{known-addr } t \ x \vee ad' \in \text{new-obs-addr } (\text{take } n \ \{ta\}_o)$
— second possibility necessary for *heap-clone*

begin

notation *mthr.redT-syntax1* (◁ -▷-→ → [50,0,0,50] 80)

lemma *if-red-known-addr-new*:
assumes $t \vdash \langle x, m \rangle -ta \rightarrow i \langle x', m' \rangle$
shows $\text{known-addr-if } t \ x' \subseteq \text{known-addr-if } t \ x \cup \text{new-obs-addr-if } \{ta\}_o$
⟨proof⟩

lemma *if-red-known-addr-new-thread*:
assumes $t \vdash \langle x, m \rangle -ta \rightarrow i \langle x', m' \rangle$ $\text{NewThread } t' \ x'' \ m'' \in \text{set } \{ta\}_t$
shows $\text{known-addr-if } t' \ x'' \subseteq \text{known-addr-if } t \ x$
⟨proof⟩

lemma *if-red-read-knows-addr*:
assumes $t \vdash \langle x, m \rangle -ta \rightarrow i \langle x', m' \rangle$ $\text{NormalAction } (\text{ReadMem } ad \ al \ v) \in \text{set } \{ta\}_o$
shows $ad \in \text{known-addr-if } t \ x$
⟨proof⟩

lemma *if-red-write-knows-addr*:

assumes $t \vdash (x, m) -ta \rightarrow i (x', m')$
and $\{ta\}_o ! n = \text{NormalAction } (\text{WriteMem } ad \ al \ (\text{Addr } ad')) \ n < \text{length } \{ta\}_o$
shows $ad' \in \text{known-addr-if } t \ x \vee ad' \in \text{new-obs-addr-if } (\text{take } n \ \{ta\}_o)$

<proof>

lemma *if-redT-known-addr-new*:

assumes $\text{redT}: \text{mthr.if.redT } s \ (t, ta) \ s'$
shows $\text{if.known-addr-state } s' \subseteq \text{if.known-addr-state } s \cup \text{new-obs-addr-if } \{ta\}_o$

<proof>

lemma *if-redT-read-knows-addr*:

assumes $\text{redT}: \text{mthr.if.redT } s \ (t, ta) \ s'$
and $\text{read}: \text{NormalAction } (\text{ReadMem } ad \ al \ v) \in \text{set } \{ta\}_o$
shows $ad \in \text{if.known-addr-state } s$

<proof>

lemma *init-fin-redT-known-addr-subset*:

assumes $\text{mthr.if.redT } s \ (t, ta) \ s'$
shows $\text{if.known-addr-state } s' \subseteq \text{if.known-addr-state } s \cup \text{known-addr-if } t \ (\text{fst } (\text{the } (\text{thr } s' \ t)))$

<proof>

lemma *w-values-no-write-unchanged*:

assumes $\text{no-write}: \bigwedge w. \llbracket w \in \text{set obs}; \text{is-write-action } w; adal \in \text{action-loc-aux } P \ w \rrbracket \implies \text{False}$
shows $w\text{-values } P \ vs \ \text{obs } adal = vs \ adal$

<proof>

lemma *redT-non-speculative-known-addr-allocated*:

assumes $\text{red}: \text{mthr.if.redT } s \ (t, ta) \ s'$
and $\text{tasc}: \text{non-speculative } P \ vs \ (\text{l-list-of } \{ta\}_o)$
and $ka: \text{if.known-addr-state } s \subseteq \text{allocated } (\text{shr } s)$
and $vs: w\text{-addrs } vs \subseteq \text{allocated } (\text{shr } s)$
shows $\text{if.known-addr-state } s' \subseteq \text{allocated } (\text{shr } s') \ (\text{is } ?thesis1)$
and $w\text{-addrs } (w\text{-values } P \ vs \ \{ta\}_o) \subseteq \text{allocated } (\text{shr } s') \ (\text{is } ?thesis2)$

<proof>

lemma *RedT-non-speculative-known-addr-allocated*:

assumes $\text{red}: \text{mthr.if.RedT } s \ t\text{tas } s'$
and $\text{tasc}: \text{non-speculative } P \ vs \ (\text{l-list-of } (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) \ t\text{tas})))$
and $ka: \text{if.known-addr-state } s \subseteq \text{allocated } (\text{shr } s)$
and $vs: w\text{-addrs } vs \subseteq \text{allocated } (\text{shr } s)$
shows $\text{if.known-addr-state } s' \subseteq \text{allocated } (\text{shr } s') \ (\text{is } ?thesis1 \ s')$
and $w\text{-addrs } (w\text{-values } P \ vs \ (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) \ t\text{tas}))) \subseteq \text{allocated } (\text{shr } s') \ (\text{is } ?thesis2$

$s' \ t\text{tas}$)

<proof>

lemma *read-ex-NewHeapElem [consumes 5, case-names start Red]*:

assumes $\text{RedT}: \text{mthr.if.RedT } (\text{init-fin-lift-state } \text{status } (\text{start-state } f \ P \ C \ M \ vs)) \ t\text{tas } s$
and $\text{red}: \text{mthr.if.redT } s \ (t, ta) \ s'$
and $\text{read}: \text{NormalAction } (\text{ReadMem } ad \ al \ v) \in \text{set } \{ta\}_o$
and $sc: \text{non-speculative } P \ (\lambda-. \ \{ \}) \ (\text{l-list-of } (\text{map } \text{snd } (\text{lift-start-obs } \text{start-tid } \text{start-heap-obs}) \ @ \ \text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) \ t\text{tas})))$

and *known*: *known-addr*s *start-tid* (*f* (*fst* (*method P C M*)) *M* (*fst* (*snd* (*method P C M*))) (*fst* (*snd* (*snd* (*method P C M*)))) (*the* (*snd* (*snd* (*snd* (*method P C M*)))))) *vs*) \subseteq *allocated start-heap*
obtains (*start*) *CTn* **where** *NewHeapElem ad CTn* \in *set start-heap-obs*
| (*Red*) *ttas' s'' t' ta' s''' ttas'' CTn*
where *mthr.if.RedT* (*init-fin-lift-state status* (*start-state f P C M vs*)) *ttas' s''*
and *mthr.if.RedT* *s''* (*t', ta'*) *s'''*
and *mthr.if.RedT* *s'''* *ttas'' s*
and *ttas* = *ttas' @ (t', ta') # ttas''*
and *NormalAction* (*NewHeapElem ad CTn*) \in *set* $\{\{ta'\}_o\}$
⟨*proof*⟩

end

locale *known-addr*s-*typing* =
*known-addr*s
 addr2thread-id thread-id2addr
 spurious-wakeups
 empty-heap allocate typeof-addr heap-read heap-write
 *allocated known-addr*s
 final r P
for *addr2thread-id* :: (*'addr* :: *addr*) \Rightarrow *'thread-id*
and *thread-id2addr* :: *'thread-id* \Rightarrow *'addr*
and *spurious-wakeups* :: *bool*
and *empty-heap* :: *'heap*
and *allocate* :: *'heap* \Rightarrow *h*type \Rightarrow (*'heap* \times *'addr*) *set*
and *typeof-addr* :: *'heap* \Rightarrow *'addr* \rightarrow *h*type
and *heap-read* :: *'heap* \Rightarrow *'addr* \Rightarrow *addr-loc* \Rightarrow *'addr val* \Rightarrow *bool*
and *heap-write* :: *'heap* \Rightarrow *'addr* \Rightarrow *addr-loc* \Rightarrow *'addr val* \Rightarrow *'heap* \Rightarrow *bool*
and *allocated* :: *'heap* \Rightarrow *'addr* *set*
and *known-addr*s :: *'thread-id* \Rightarrow *'x* \Rightarrow *'addr* *set*
and *final* :: *'x* \Rightarrow *bool*
and *r* :: (*'addr*, *'thread-id*, *'x*, *'heap*, *'addr*, (*'addr*, *'thread-id*) *obs-event*) *semantics* ($\langle \vdash \dashv \dashrightarrow \rightarrow$
[50,0,0,50] 80)
and *wfx* :: *'thread-id* \Rightarrow *'x* \Rightarrow *'heap* \Rightarrow *bool*
and *P* :: *'md prog*
+
assumes *wfs-non-speculative-invar*:
[[*t* \vdash (*x*, *m*) $\dashv\text{ta}$ \rightarrow (*x'*, *m'*); *wfx t x m*;
 vs-conf P m vs; *non-speculative P vs* (*l*list-of (*map NormalAction* $\{\{ta\}_o\}$))]
 \Rightarrow *wfx t x' m'*
and *wfs-non-speculative-spawn*:
[[*t* \vdash (*x*, *m*) $\dashv\text{ta}$ \rightarrow (*x'*, *m'*); *wfx t x m*;
 vs-conf P m vs; *non-speculative P vs* (*l*list-of (*map NormalAction* $\{\{ta\}_o\}$));
 NewThread t'' x'' m'' \in *set* $\{\{ta\}_t\}$]
 \Rightarrow *wfx t'' x'' m''*
and *wfs-non-speculative-other*:
[[*t* \vdash (*x*, *m*) $\dashv\text{ta}$ \rightarrow (*x'*, *m'*); *wfx t x m*;
 vs-conf P m vs; *non-speculative P vs* (*l*list-of (*map NormalAction* $\{\{ta\}_o\}$));
 wfx t'' x'' m]
 \Rightarrow *wfx t'' x'' m'*
and *wfs-non-speculative-vs-conf*:
[[*t* \vdash (*x*, *m*) $\dashv\text{ta}$ \rightarrow (*x'*, *m'*); *wfx t x m*;
 vs-conf P m vs; *non-speculative P vs* (*l*list-of (*take n* (*map NormalAction* $\{\{ta\}_o\}$)))]
 \Rightarrow *vs-conf P m'* (*w-values P vs* (*take n* (*map NormalAction* $\{\{ta\}_o\}$)))

and *red-read-typeable*:

$\llbracket t \vdash (x, m) -ta \rightarrow (x', m'); wfx\ t\ x\ m; ReadMem\ ad\ al\ v \in set\ \{ta\}_o \rrbracket$
 $\implies \exists T. P, m \vdash ad@al : T$

and *red-NewHeapElemD*:

$\llbracket t \vdash (x, m) -ta \rightarrow (x', m'); wfx\ t\ x\ m; NewHeapElem\ ad\ hT \in set\ \{ta\}_o \rrbracket$
 $\implies typeof-addr\ m'\ ad = \lfloor hT \rfloor$

and *red-hext-incr*:

$\llbracket t \vdash (x, m) -ta \rightarrow (x', m'); wfx\ t\ x\ m;$
 $vs-conf\ P\ m\ vs; non-speculative\ P\ vs\ (l\ list-of\ (map\ NormalAction\ \{ta\}_o)) \rrbracket$
 $\implies m \sqsubseteq m'$

begin

lemma *redT-wfs-non-speculative-invar*:

assumes *redT*: $mthr.redT\ s\ (t, ta)\ s'$

and *wfx*: $ts-ok\ wfx\ (thr\ s)\ (shr\ s)$

and *vs*: $vs-conf\ P\ (shr\ s)\ vs$

and *ns*: $non-speculative\ P\ vs\ (l\ list-of\ (map\ NormalAction\ \{ta\}_o))$

shows $ts-ok\ wfx\ (thr\ s')\ (shr\ s')$

$\langle proof \rangle$

lemma *redT-wfs-non-speculative-vs-conf*:

assumes *redT*: $mthr.redT\ s\ (t, ta)\ s'$

and *wfx*: $ts-ok\ wfx\ (thr\ s)\ (shr\ s)$

and *conf*: $vs-conf\ P\ (shr\ s)\ vs$

and *ns*: $non-speculative\ P\ vs\ (l\ list-of\ (take\ n\ (map\ NormalAction\ \{ta\}_o)))$

shows $vs-conf\ P\ (shr\ s')\ (w-values\ P\ vs\ (take\ n\ (map\ NormalAction\ \{ta\}_o)))$

$\langle proof \rangle$

lemma *if-redT-non-speculative-invar*:

assumes *red*: $mthr.if.redT\ s\ (t, ta)\ s'$

and *ts-ok*: $ts-ok\ (init-fin-lift\ wfx)\ (thr\ s)\ (shr\ s)$

and *sc*: $non-speculative\ P\ vs\ (l\ list-of\ \{ta\}_o)$

and *vs*: $vs-conf\ P\ (shr\ s)\ vs$

shows $ts-ok\ (init-fin-lift\ wfx)\ (thr\ s')\ (shr\ s')$

$\langle proof \rangle$

lemma *if-redT-non-speculative-vs-conf*:

assumes *red*: $mthr.if.redT\ s\ (t, ta)\ s'$

and *ts-ok*: $ts-ok\ (init-fin-lift\ wfx)\ (thr\ s)\ (shr\ s)$

and *sc*: $non-speculative\ P\ vs\ (l\ list-of\ (take\ n\ \{ta\}_o))$

and *vs*: $vs-conf\ P\ (shr\ s)\ vs$

shows $vs-conf\ P\ (shr\ s')\ (w-values\ P\ vs\ (take\ n\ \{ta\}_o))$

$\langle proof \rangle$

lemma *if-RedT-non-speculative-invar*:

assumes *red*: $mthr.if.RedT\ s\ ttas\ s'$

and *tsok*: $ts-ok\ (init-fin-lift\ wfx)\ (thr\ s)\ (shr\ s)$

and *sc*: $non-speculative\ P\ vs\ (l\ list-of\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas)))$

and *vs*: $vs-conf\ P\ (shr\ s)\ vs$

shows $ts-ok\ (init-fin-lift\ wfx)\ (thr\ s')\ (shr\ s')$ (**is** *?thesis1*)

and $vs-conf\ P\ (shr\ s')\ (w-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas)))$ (**is** *?thesis2*)

$\langle proof \rangle$

lemma *init-fin-hext-incr*:

assumes $t \vdash (x, m) -ta \rightarrow i (x', m')$
and *init-fin-lift wfx* $t x m$
and *non-speculative P vs* (*llist-of* $\{ta\}_o$)
and *vs-conf P m vs*
shows $m \leq m'$
<proof>

lemma *init-fin-redT-heat-incr*:
assumes *mthr.if.redT s* $(t, ta) s'$
and *ts-ok (init-fin-lift wfx) (thr s) (shr s)*
and *non-speculative P vs* (*llist-of* $\{ta\}_o$)
and *vs-conf P (shr s) vs*
shows $shr s \leq shr s'$
<proof>

lemma *init-fin-RedT-heat-incr*:
assumes *mthr.if.RedT s ttas s'*
and *ts-ok (init-fin-lift wfx) (thr s) (shr s)*
and *sc: non-speculative P vs* (*llist-of* (*concat* (*map* $(\lambda(t, ta). \{ta\}_o)$ *ttas*)))
and *vs: vs-conf P (shr s) vs*
shows $shr s \leq shr s'$
<proof>

lemma *init-fin-red-read-typeable*:
assumes $t \vdash (x, m) -ta \rightarrow i (x', m')$
and *init-fin-lift wfx* $t x m$ *NormalAction (ReadMem ad al v) \in set $\{ta\}_o$*
shows $\exists T. P, m \vdash ad@al : T$
<proof>

lemma *Ex-new-action-for*:
assumes *wf: wf-syscls P*
and *wfx-start: ts-ok wfx (thr (start-state f P C M vs)) start-heap*
and *ka: known-addr start-tid (f (fst (method P C M)) M (fst (snd (method P C M))) (fst (snd (snd (method P C M)))) (the (snd (snd (snd (method P C M)))))) vs) \subseteq allocated start-heap*
and *E: E \in \mathcal{E} -start f P C M vs status*
and *read: ra \in read-actions E*
and *aloc: adal \in action-loc P E ra*
and *sc: non-speculative P ($\lambda-. \{\}$) (ltake (enat ra) (lmap snd E))*
shows $\exists wa. wa \in$ *new-actions-for P E adal* $\wedge wa < ra$
<proof>

lemma *executions-sc-hb*:
assumes *wf-syscls P*
and *ts-ok wfx (thr (start-state f P C M vs)) start-heap*
and *known-addr start-tid (f (fst (method P C M)) M (fst (snd (method P C M))) (fst (snd (snd (method P C M)))) (the (snd (snd (snd (method P C M)))))) vs) \subseteq allocated start-heap*
shows
executions-sc-hb (\mathcal{E} -start f P C M vs status) P
(is executions-sc-hb ?E P)
<proof>

lemma *executions-aux*:
assumes *wf: wf-syscls P*
and *wfx-start: ts-ok wfx (thr (start-state f P C M vs)) start-heap (is ts-ok wfx (thr ?start-state) -)*

and *ka*: *known-addr*s *start-tid* (*f* (*fst* (*method* *P C M*)) *M* (*fst* (*snd* (*method* *P C M*))) (*fst* (*snd* (*snd* (*method* *P C M*)))))) (*the* (*snd* (*snd* (*snd* (*method* *P C M*)))))) *vs*) \subseteq *allocated start-heap*
shows *executions-aux* (\mathcal{E} -*start* *f P C M vs status*) *P*
 (**is** *executions-aux* ? \mathcal{E} *P*)
 \langle *proof* \rangle

lemma *drf*:

assumes *cut-and-update*:

if.cut-and-update

(*init-fin-lift-state status* (*start-state* *f P C M vs*))

(*mrw-values* *P Map.empty* (*map snd* (*lift-start-obs start-tid start-heap-obs*)))

(**is** *if.cut-and-update* ?*start-state* (*mrw-values* - - (*map* - ?*start-heap-obs*)))

and *wf*: *wf-syscls* *P*

and *wfx-start*: *ts-ok wfx* (*thr* (*start-state* *f P C M vs*)) *start-heap*

and *ka*: *known-addr*s *start-tid* (*f* (*fst* (*method* *P C M*)) *M* (*fst* (*snd* (*method* *P C M*))) (*fst* (*snd* (*snd* (*method* *P C M*)))))) (*the* (*snd* (*snd* (*snd* (*method* *P C M*)))))) *vs*) \subseteq *allocated start-heap*

shows *drf* (\mathcal{E} -*start* *f P C M vs status*) *P* (**is** *drf* ? \mathcal{E} -)

\langle *proof* \rangle

lemma *sc-legal*:

assumes *hb-completion*:

if.hb-completion (*init-fin-lift-state status* (*start-state* *f P C M vs*)) (*lift-start-obs start-tid start-heap-obs*)

(**is** *if.hb-completion* ?*start-state* ?*start-heap-obs*)

and *wf*: *wf-syscls* *P*

and *wfx-start*: *ts-ok wfx* (*thr* (*start-state* *f P C M vs*)) *start-heap*

and *ka*: *known-addr*s *start-tid* (*f* (*fst* (*method* *P C M*)) *M* (*fst* (*snd* (*method* *P C M*))) (*fst* (*snd* (*snd* (*method* *P C M*)))))) (*the* (*snd* (*snd* (*snd* (*method* *P C M*)))))) *vs*) \subseteq *allocated start-heap*

shows *sc-legal* (\mathcal{E} -*start* *f P C M vs status*) *P*

(**is** *sc-legal* ? \mathcal{E} *P*)

\langle *proof* \rangle

end

lemma *w-value-mrw-value-conf*:

assumes *set-option* (*vs'* *adal*) \subseteq *vs adal* \times *UNIV*

shows *set-option* (*mrw-value* *P vs' ob adal*) \subseteq *w-value* *P vs ob adal* \times *UNIV*

\langle *proof* \rangle

lemma *w-values-mrw-values-conf*:

assumes *set-option* (*vs'* *adal*) \subseteq *vs adal* \times *UNIV*

shows *set-option* (*mrw-values* *P vs' obs adal*) \subseteq *w-values* *P vs obs adal* \times *UNIV*

\langle *proof* \rangle

lemma *w-value-mrw-value-dom-eq-preserve*:

assumes *dom vs'* = {*adal. vs adal* \neq {}}

shows *dom* (*mrw-value* *P vs' ob*) = {*adal. w-value* *P vs ob adal* \neq {}}

\langle *proof* \rangle

lemma *w-values-mrw-values-dom-eq-preserve*:

assumes *dom vs'* = {*adal. vs adal* \neq {}}

shows *dom* (*mrw-values* *P vs' obs*) = {*adal. w-values* *P vs obs adal* \neq {}}

\langle *proof* \rangle

context *jmm-multithreaded begin*

definition *non-speculative-read* ::

$nat \Rightarrow ('l, 'thread-id, 'x, 'm, 'w) state \Rightarrow ('addr \times addr-loc \Rightarrow 'addr\ val\ set) \Rightarrow bool$

where

$non-speculative-read\ n\ s\ vs \longleftrightarrow$

$(\forall\ ttas\ s'\ t\ x\ ta\ x'\ m'\ i\ ad\ al\ v\ v'.)$

$s \rightarrow ttas \rightarrow * s' \longrightarrow non-speculative\ P\ vs\ (l\list-of\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas))) \longrightarrow$

$thr\ s'\ t = \lfloor(x, no-wait-locks)\rfloor \longrightarrow t \vdash (x, shr\ s') -ta \rightarrow (x', m') \longrightarrow actions-ok\ s'\ t\ ta \longrightarrow$

$i < length\ \{ta\}_o \longrightarrow$

$non-speculative\ P\ (w-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas)))\ (l\list-of\ (take\ i\ \{ta\}_o))$

\longrightarrow

$\{ta\}_o ! i = NormalAction\ (ReadMem\ ad\ al\ v) \longrightarrow$

$v' \in w-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas)\ @\ take\ i\ \{ta\}_o\ (ad, al) \longrightarrow$

$(\exists\ ta'\ x''\ m''.\ t \vdash (x, shr\ s') -ta' \rightarrow (x'', m'') \wedge actions-ok\ s'\ t\ ta' \wedge$

$i < length\ \{ta'\}_o \wedge take\ i\ \{ta'\}_o = take\ i\ \{ta\}_o \wedge \{ta'\}_o ! i = NormalAction$

$(ReadMem\ ad\ al\ v') \wedge$

$length\ \{ta'\}_o \leq max\ n\ (length\ \{ta\}_o))$

lemma *non-speculative-readI* [intro?]:

$(\wedge\ ttas\ s'\ t\ x\ ta\ x'\ m'\ i\ ad\ al\ v\ v'.)$

$\llbracket s \rightarrow ttas \rightarrow * s'; non-speculative\ P\ vs\ (l\list-of\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas));$

$thr\ s'\ t = \lfloor(x, no-wait-locks)\rfloor; t \vdash (x, shr\ s') -ta \rightarrow (x', m'); actions-ok\ s'\ t\ ta;$

$i < length\ \{ta\}_o; non-speculative\ P\ (w-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas)))\ (l\list-of$

$(take\ i\ \{ta\}_o));$

$\{ta\}_o ! i = NormalAction\ (ReadMem\ ad\ al\ v);$

$v' \in w-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas)\ @\ take\ i\ \{ta\}_o\ (ad, al) \rrbracket$

$\implies \exists\ ta'\ x''\ m''.\ t \vdash (x, shr\ s') -ta' \rightarrow (x'', m'') \wedge actions-ok\ s'\ t\ ta' \wedge$

$i < length\ \{ta'\}_o \wedge take\ i\ \{ta'\}_o = take\ i\ \{ta\}_o \wedge \{ta'\}_o ! i = NormalAction$

$(ReadMem\ ad\ al\ v') \wedge$

$length\ \{ta'\}_o \leq max\ n\ (length\ \{ta\}_o)$

$\implies non-speculative-read\ n\ s\ vs$

$\langle proof \rangle$

lemma *non-speculative-readD*:

$\llbracket non-speculative-read\ n\ s\ vs; s \rightarrow ttas \rightarrow * s'; non-speculative\ P\ vs\ (l\list-of\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas));$

$thr\ s'\ t = \lfloor(x, no-wait-locks)\rfloor; t \vdash (x, shr\ s') -ta \rightarrow (x', m'); actions-ok\ s'\ t\ ta;$

$i < length\ \{ta\}_o; non-speculative\ P\ (w-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas)))\ (l\list-of$

$(take\ i\ \{ta\}_o));$

$\{ta\}_o ! i = NormalAction\ (ReadMem\ ad\ al\ v);$

$v' \in w-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas)\ @\ take\ i\ \{ta\}_o\ (ad, al) \rrbracket$

$\implies \exists\ ta'\ x''\ m''.\ t \vdash (x, shr\ s') -ta' \rightarrow (x'', m'') \wedge actions-ok\ s'\ t\ ta' \wedge$

$i < length\ \{ta'\}_o \wedge take\ i\ \{ta'\}_o = take\ i\ \{ta\}_o \wedge \{ta'\}_o ! i = NormalAction$

$(ReadMem\ ad\ al\ v') \wedge$

$length\ \{ta'\}_o \leq max\ n\ (length\ \{ta\}_o)$

$\langle proof \rangle$

end

8.11.2 *non-speculative generalises cut-and-update and ta-hb-consistent*

context *known-addr-typing* **begin**

lemma *read-non-speculative-new-actions-for*:

```

fixes status f C M params E
defines E  $\equiv$  lift-start-obs start-tid start-heap-obs
and vs  $\equiv$  w-values P ( $\lambda$ -.  $\{\}$ ) (map snd E)
and s  $\equiv$  init-fin-lift-state status (start-state f P C M params)
assumes wf: wf-syscls P
and RedT: mthr.if.RedT s ttas s'
and redT: mthr.if.redT s' (t, ta') s''
and read: NormalAction (ReadMem ad al v)  $\in$  set  $\{\{ta\}_o\}$ 
and ns: non-speculative P ( $\lambda$ -.  $\{\}$ ) (llist-of (map snd E  $\@$  concat (map ( $\lambda(t, ta)$ .  $\{\{ta\}_o\}$ ) ttas)))
and ka: known-addrs start-tid (f (fst (method P C M)) M (fst (snd (method P C M))) (fst (snd (snd (method P C M)))))) (the (snd (snd (snd (method P C M)))))) params)  $\subseteq$  allocated start-heap
and wt: ts-ok (init-fin-lift wfx) (thr s) (shr s)
and type-adal: P, shr s'  $\vdash$  ad@al : T
shows  $\exists w$ . w  $\in$  new-actions-for P (llist-of (E  $\@$  concat (map ( $\lambda(t, ta)$ . map (Pair t)  $\{\{ta\}_o\}$ ) ttas)))
(ad, al)
  (is  $\exists w$ . ?new-w w)
<proof>

```

lemma *non-speculative-read-into-cut-and-update*:

```

fixes status f C M params E
defines E  $\equiv$  lift-start-obs start-tid start-heap-obs
and vs  $\equiv$  w-values P ( $\lambda$ -.  $\{\}$ ) (map snd E)
and s  $\equiv$  init-fin-lift-state status (start-state f P C M params)
and vs'  $\equiv$  mrw-values P Map.empty (map snd E)
assumes wf: wf-syscls P
and nsr: if.non-speculative-read n s vs
and wt: ts-ok (init-fin-lift wfx) (thr s) (shr s)
and ka: known-addrs start-tid (f (fst (method P C M)) M (fst (snd (method P C M))) (fst (snd (snd (method P C M)))))) (the (snd (snd (snd (method P C M)))))) params)  $\subseteq$  allocated start-heap
shows if.cut-and-update s vs'
<proof>

```

lemma *non-speculative-read-into-hb-completion*:

```

fixes status f C M params E
defines E  $\equiv$  lift-start-obs start-tid start-heap-obs
and vs  $\equiv$  w-values P ( $\lambda$ -.  $\{\}$ ) (map snd E)
and s  $\equiv$  init-fin-lift-state status (start-state f P C M params)
assumes wf: wf-syscls P
and nsr: if.non-speculative-read n s vs
and wt: ts-ok (init-fin-lift wfx) (thr s) (shr s)
and ka: known-addrs start-tid (f (fst (method P C M)) M (fst (snd (method P C M))) (fst (snd (snd (method P C M)))))) (the (snd (snd (snd (method P C M)))))) params)  $\subseteq$  allocated start-heap
shows if.hb-completion s E
<proof>

```

end

end

8.12 Type-safety proof for the Java memory model

theory *JMM-Typesafe*

imports

JMM-Framework

begin

Create a dynamic list *heap-independent* of theorems for replacing heap-dependent constants by heap-independent ones.

$\langle ML \rangle$

locale *heap-base'* =

h: *heap-base*

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate $\lambda\cdot$. *typeof-addr heap-read heap-write*

for *addr2thread-id* :: ('*addr* :: *addr*) \Rightarrow '*thread-id*

and *thread-id2addr* :: '*thread-id* \Rightarrow '*addr*

and *spurious-wakeups* :: *bool*

and *empty-heap* :: '*heap*

and *allocate* :: '*heap* \Rightarrow *h**type* \Rightarrow ('*heap* \times '*addr*) *set*

and *typeof-addr* :: '*addr* \rightarrow *h**type*

and *heap-read* :: '*heap* \Rightarrow '*addr* \Rightarrow *addr-loc* \Rightarrow '*addr val* \Rightarrow *bool*

and *heap-write* :: '*heap* \Rightarrow '*addr* \Rightarrow *addr-loc* \Rightarrow '*addr val* \Rightarrow '*heap* \Rightarrow *bool*

begin

definition *typeof-h* :: '*addr val* \Rightarrow *ty option*

where *typeof-h* = *h.typeof-h undefined*

lemma *typeof-h-conv-typeof-h* [*heap-independent, iff*]: *h.typeof-h h* = *typeof-h*

$\langle proof \rangle$

lemmas *typeof-h-simps* [*simp*] = *h.typeof-h.simps* [*unfolded heap-independent*]

definition *cname-of* :: '*addr* \Rightarrow *cname*

where *cname-of* = *h.cname-of undefined*

lemma *cname-of-conv-cname-of* [*heap-independent, iff*]: *h.cname-of h* = *cname-of*

$\langle proof \rangle$

definition *addr-loc-type* :: '*m prog* \Rightarrow '*addr* \Rightarrow *addr-loc* \Rightarrow *ty* \Rightarrow *bool*

where *addr-loc-type P* = *h.addr-loc-type P undefined*

notation *addr-loc-type* ($\langle \vdash \vdash \text{-@-} : \rightarrow [50, 50, 50, 50] 51$)

lemma *addr-loc-type-conv-addr-loc-type* [*heap-independent, iff*]:

h.addr-loc-type P h = *addr-loc-type P*

$\langle proof \rangle$

lemmas *addr-loc-type-cases* [*cases pred: addr-loc-type*] =

h.addr-loc-type.cases[*unfolded heap-independent*]

lemmas *addr-loc-type-intros* = *h.addr-loc-type.intros*[*unfolded heap-independent*]

definition *typeof-addr-loc* :: '*m prog* \Rightarrow '*addr* \Rightarrow *addr-loc* \Rightarrow *ty*

where *typeof-addr-loc P* = *h.typeof-addr-loc P undefined*

lemma *typeof-addr-loc-conv-typeof-addr-loc* [*heap-independent, iff*]:

h.typeof-addr-loc P h = *typeof-addr-loc P*

$\langle proof \rangle$

definition *conf* :: '*a prog* \Rightarrow '*addr val* \Rightarrow *ty* \Rightarrow *bool*

where *conf P* \equiv *h.conf P undefined*

notation *conf* ($\langle \vdash \vdash \text{-} \leq \rightarrow [51, 51, 51] 50$)

lemma *conf-conv-conf* [*heap-independent, iff*]: *h.conf P h* = *conf P*

$\langle proof \rangle$

lemmas *defval-conf* [*simp*] = *h.defval-conf*[*unfolded heap-independent*]

definition *lconf* :: 'm prog \Rightarrow (vname \rightarrow 'addr val) \Rightarrow (vname \rightarrow ty) \Rightarrow bool

where *lconf* P = *h.lconf* P *undefined*

notation *lconf* ($\langle \cdot \vdash \cdot \text{'(:}\leq\text{'}) \rightarrow [51,51,51] 50$)

lemma *lconf-conv-lconf* [*heap-independent, iff*]: *h.lconf* P *h* = *lconf* P

\langle *proof* \rangle

definition *confs* :: 'm prog \Rightarrow 'addr val list \Rightarrow ty list \Rightarrow bool

where *confs* P = *h.confs* P *undefined*

notation *confs* ($\langle \cdot \vdash \cdot \text{[:}\leq\text{]} \rightarrow [51,51,51] 50$)

lemma *confs-conv-confs* [*heap-independent, iff*]: *h.confs* P *h* = *confs* P

\langle *proof* \rangle

definition *tconf* :: 'm prog \Rightarrow 'thread-id \Rightarrow bool

where *tconf* P = *h.tconf* P *undefined*

notation *tconf* ($\langle \cdot \vdash \cdot \sqrt{\cdot} \rangle [51,51] 50$)

lemma *tconf-conv-tconf* [*heap-independent, iff*]: *h.tconf* P *h* = *tconf* P

\langle *proof* \rangle

definition *vs-conf* :: 'm prog \Rightarrow ('addr \times addr-loc \Rightarrow 'addr val set) \Rightarrow bool

where *vs-conf* P = *h.vs-conf* P *undefined*

lemma *vs-conf-conv-vs-conf* [*heap-independent, iff*]: *h.vs-conf* P *h* = *vs-conf* P

\langle *proof* \rangle

lemmas *vs-confI* = *h.vs-confI*[*unfolded heap-independent*]

lemmas *vs-confD* = *h.vs-confD*[*unfolded heap-independent*]

use non-speculativity to express that only type-correct values are read

primrec *vs-type-all* :: 'm prog \Rightarrow 'addr \times addr-loc \Rightarrow 'addr val set

where *vs-type-all* P (ad, al) = {v. $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$ }

lemma *vs-conf-vs-type-all* [*simp*]: *vs-conf* P (*vs-type-all* P)

\langle *proof* \rangle

lemma *w-addr-vs-type-all*: *w-addr* (*vs-type-all* P) \subseteq *dom typeof-addr*

\langle *proof* \rangle

lemma *w-addr-vs-type-all-in-vs-type-all*:

$(\bigcup ad \in w\text{-addr} (vs\text{-type-all } P). \{(ad, al)|al. \exists T. P \vdash ad@al : T\}) \subseteq \{adal. vs\text{-type-all } P adal \neq \{\}\}$

\langle *proof* \rangle

declare *vs-type-all.simps* [*simp del*]

lemmas *vs-conf-insert-iff* = *h.vs-conf-insert-iff*[*unfolded heap-independent*]

end

locale *heap'* =

h: *heap*

addr2thread-id thread-id2addr

spurious-wakeups

```

    empty-heap allocate  $\lambda$ -. typeof-addr heap-read heap-write
  P
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and P :: 'm prog

sublocale heap' < heap-base' <proof>

context heap' begin

lemma vs-conf-w-value-WriteMemD:
   $\llbracket$  vs-conf P (w-value P vs ob); ob = NormalAction (WriteMem ad al v)  $\rrbracket$ 
   $\Longrightarrow$   $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$ 
<proof>

lemma vs-conf-w-values-WriteMemD:
   $\llbracket$  vs-conf P (w-values P vs obs); NormalAction (WriteMem ad al v)  $\in$  set obs  $\rrbracket$ 
   $\Longrightarrow$   $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$ 
<proof>

lemma w-values-vs-type-all-start-heap-obs:
  assumes wf: wf-syscls P
  shows w-values P (vs-type-all P) (map snd (lift-start-obs h.start-tid h.start-heap-obs)) = vs-type-all
  P
  (is ?lhs = ?rhs)
<proof>

end

lemma lprefix-lappend2I: lprefix xs ys  $\Longrightarrow$  lprefix xs (lappend ys zs)
<proof>

locale known-addr-typing' =
  h: known-addr-typing
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate  $\lambda$ -. typeof-addr heap-read heap-write
  allocated known-addr
  final r wfx
  P
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool

```

and *heap-write* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and *allocated* :: 'heap ⇒ 'addr set
and *known-addr*s :: 'thread-id ⇒ 'x ⇒ 'addr set
and *final* :: 'x ⇒ bool
and *r* :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics (⊢ - - -> →)
[50,0,0,50] 80)
and *wfx* :: 'thread-id ⇒ 'x ⇒ 'heap ⇒ bool
and *P* :: 'md prog
+
assumes *NewHeapElem-typed*: — Should this be moved to known_addr_typing?
[[*t* ⊢ (*x*, *h*) -*ta*→ (*x'*, *h'*); *NewHeapElem* *ad CTn* ∈ set {*ta*}_o; *typeof-addr ad* ≠ None]
⇒ *typeof-addr ad* = [*CTn*]

sublocale *known-addr-typing'* < *heap'* ⟨proof⟩

context *known-addr-typing'* **begin**

lemma *known-addr-typeable-in-vs-type-all*:

h.if.known-addr-state s ⊆ *dom typeof-addr*
⇒ (⋃ *a* ∈ *h.if.known-addr-state s*. {(*a*, *al*) | *al*. ∃ *T*. *P* ⊢ *a*@*al* : *T*}) ⊆ {*adal*. *vs-type-all P adal* ≠ {} }
⟨proof⟩

lemma *if-NewHeapElem-typed*:

[[*t* ⊢ *xh* -*ta*→*i* *x'h'*; *NormalAction* (*NewHeapElem ad CTn*) ∈ set {*ta*}_o; *typeof-addr ad* ≠ None]
⇒ *typeof-addr ad* = [*CTn*]
⟨proof⟩

lemma *if-redT-NewHeapElem-typed*:

[[*h.mthr.if.redT s* (*t*, *ta*) *s'*; *NormalAction* (*NewHeapElem ad CTn*) ∈ set {*ta*}_o; *typeof-addr ad* ≠ None]
⇒ *typeof-addr ad* = [*CTn*]
⟨proof⟩

lemma *non-speculative-written-value-typeable*:

assumes *wfx-start: ts-ok wfx* (*thr* (*h.start-state f P C M vs*)) *h.start-heap*
and *wfP*: *wf-syscls P*
and *E*: *E* ∈ *h.ℰ-start f P C M vs status*
and *write*: *w* ∈ *write-actions E*
and *adal*: (*ad*, *al*) ∈ *action-loc P E w*
and *ns*: *non-speculative P* (*vs-type-all P*) (*lmap snd* (*ltake* (*enat w*) *E*))
shows ∃ *T*. *P* ⊢ *ad*@*al* : *T* ∧ *P* ⊢ *value-written P E w* (*ad*, *al*) :≤ *T*
⟨proof⟩

lemma *hb-read-value-typeable*:

assumes *wfx-start: ts-ok wfx* (*thr* (*h.start-state f P C M vs*)) *h.start-heap*
(is *ts-ok wfx* (*thr ?start-state*) -)
and *wfP*: *wf-syscls P*
and *E*: *E* ∈ *h.ℰ-start f P C M vs status*
and *wf*: *P* ⊢ (*E*, *ws*) ✓
and *races*: ∧ *a ad al v*. [[*enat a* < *llength E*; *action-obs E a* = *NormalAction* (*ReadMem ad al v*); ⊢ *P, E* ⊢ *ws a* ≤*hb a*]
⇒ ∃ *T*. *P* ⊢ *ad*@*al* : *T* ∧ *P* ⊢ *v* :≤ *T*
and *r*: *enat a* < *llength E*

and *read*: *action-obs* $E\ a = \text{NormalAction } (\text{ReadMem } ad\ al\ v)$
shows $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$
 <proof>

theorem

assumes *wfx-start*: *ts-ok wfx* (*thr* (*h.start-state* $f\ P\ C\ M\ vs$)) *h.start-heap*
and *wfP*: *wf-syscls* P
and *justified*: $P \vdash (E, ws)$ *weakly-justified-by* J
and J : *range* (*justifying-exec* $\circ J$) $\subseteq h.\mathcal{E}\text{-start } f\ P\ C\ M\ vs\ status$
shows *read-value-typeable-justifying*:
 [$0 < n$; *enat* $a < llength$ (*justifying-exec* ($J\ n$));
action-obs (*justifying-exec* ($J\ n$)) $a = \text{NormalAction } (\text{ReadMem } ad\ al\ v)$]
 $\implies \exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$
and *read-value-typeable-justified*:
 [$E \in h.\mathcal{E}\text{-start } f\ P\ C\ M\ vs\ status$; $P \vdash (E, ws) \surd$;
enat $a < llength\ E$; *action-obs* $E\ a = \text{NormalAction } (\text{ReadMem } ad\ al\ v)$]
 $\implies \exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$
 <proof>

corollary *weakly-legal-read-value-typeable*:

assumes *wfx-start*: *ts-ok wfx* (*thr* (*h.start-state* $f\ P\ C\ M\ vs$)) *h.start-heap*
and *wfP*: *wf-syscls* P
and *legal*: *weakly-legal-execution* P (*h.E-start* $f\ P\ C\ M\ vs\ status$) (E, ws)
and a : *enat* $a < llength\ E$
and *read*: *action-obs* $E\ a = \text{NormalAction } (\text{ReadMem } ad\ al\ v)$
shows $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$
 <proof>

corollary *legal-read-value-typeable*:

[*ts-ok wfx* (*thr* (*h.start-state* $f\ P\ C\ M\ vs$)) *h.start-heap*; *wf-syscls* P ;
legal-execution P (*h.E-start* $f\ P\ C\ M\ vs\ status$) (E, ws);
enat $a < llength\ E$; *action-obs* $E\ a = \text{NormalAction } (\text{ReadMem } ad\ al\ v)$]
 $\implies \exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$
 <proof>

end

end

8.13 JMM Instantiation with Jinja – common parts

theory *JMM-Common*

imports

JMM-Framework

JMM-Typesafe

../Common/BinOp

../Common/ExternalCallWF

begin

context *heap* **begin**

lemma *heap-copy-loc-not-New*: **assumes** *heap-copy-loc* $a\ a'\ al\ h\ ob\ h'$
shows *NewHeapElem* $a''\ x \in set\ ob \implies False$

<proof>

lemma *heap-copies-not-New*:

assumes *heap-copies* $a\ a'\ als\ h\ obs\ h'$

and *NewHeapElem* $a''\ x \in\ set\ obs$

shows *False*

<proof>

lemma *heap-clone-New-same-addr-same*:

assumes *heap-clone* $P\ h\ a\ h' [(obs, a')]$

and $obs\ !\ i = NewHeapElem\ a''\ x\ i < length\ obs$

and $obs\ !\ j = NewHeapElem\ a''\ x'\ j < length\ obs$

shows $i = j$

<proof>

lemma *red-external-New-same-addr-same*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext\ \langle va, h' \rangle;$

$\{ta\}_o\ !\ i = NewHeapElem\ a'\ x; i < length\ \{ta\}_o;$

$\{ta\}_o\ !\ j = NewHeapElem\ a'\ x'; j < length\ \{ta\}_o \rrbracket$

$\implies i = j$

<proof>

lemma *red-external-aggr-New-same-addr-same*:

$\llbracket (ta, va, h') \in red-external-aggr\ P\ t\ a\ M\ vs\ h;$

$\{ta\}_o\ !\ i = NewHeapElem\ a'\ x; i < length\ \{ta\}_o;$

$\{ta\}_o\ !\ j = NewHeapElem\ a'\ x'; j < length\ \{ta\}_o \rrbracket$

$\implies i = j$

<proof>

lemma *heap-copy-loc-read-typeable*:

assumes *heap-copy-loc* $a\ a'\ al\ h\ obs\ h'$

and *ReadMem* $ad\ al'\ v \in\ set\ obs$

and $P, h \vdash a@al : T$

shows $ad = a \wedge al' = al$

<proof>

lemma *heap-copies-read-typeable*:

assumes *heap-copies* $a\ a'\ als\ h\ obs\ h'$

and *ReadMem* $ad\ al'\ v \in\ set\ obs$

and *list-all2* $(\lambda al\ T.\ P, h \vdash a@al : T)\ als\ Ts$

shows $ad = a \wedge al' \in\ set\ als$

<proof>

lemma *heap-clone-read-typeable*:

assumes *clone*: *heap-clone* $P\ h\ a\ h' [(obs, a')]$

and *read*: *ReadMem* $ad\ al\ v \in\ set\ obs$

shows $ad = a \wedge (\exists T'. P, h \vdash ad@al : T')$

<proof>

lemma *red-external-read-mem-typeable*:

assumes *red*: $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext\ \langle va, h' \rangle$

and *read*: *ReadMem* $ad\ al\ v \in\ set\ \{ta\}_o$

shows $\exists T'. P, h \vdash ad@al : T'$

<proof>

end

context *heap-conf* begin

lemma *heap-clone-typeof-addrD*:

assumes *heap-clone* P h a h' $[(obs, a')]$

and *hconf* h

shows $NewHeapElem\ a''\ x \in set\ obs \implies a'' = a' \wedge typeof\text{-}addr\ h'\ a' = Some\ x$

$\langle proof \rangle$

lemma *red-external-New-typeof-addrD*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext\ \langle va, h' \rangle; NewHeapElem\ a'\ x \in set\ \{ta\}_o; hconf\ h \rrbracket$

$\implies typeof\text{-}addr\ h'\ a' = Some\ x$

$\langle proof \rangle$

lemma *red-external-aggr-New-typeof-addrD*:

$\llbracket (ta, va, h') \in red\text{-}external\text{-}aggr\ P\ t\ a\ M\ vs\ h; NewHeapElem\ a'\ x \in set\ \{ta\}_o;$

$is\text{-}native\ P\ (the\ (typeof\text{-}addr\ h\ a))\ M; hconf\ h \rrbracket$

$\implies typeof\text{-}addr\ h'\ a' = Some\ x$

$\langle proof \rangle$

end

context *heap-conf* begin

lemma *heap-copy-loc-non-speculative-typeable*:

assumes *copy*: *heap-copy-loc* ad ad' al h obs h'

and *sc*: *non-speculative* P vs (*l*list-of (*map* *NormalAction* obs))

and *vs*: *vs-conf* P h vs

and *hconf*: *hconf* h

and *wt*: $P, h \vdash ad@al : T$ $P, h \vdash ad'@al : T$

shows *heap-base.heap-copy-loc* (*heap-read-typed* P) *heap-write* ad ad' al h obs h'

$\langle proof \rangle$

lemma *heap-copy-loc-non-speculative-vs-conf*:

assumes *copy*: *heap-copy-loc* ad ad' al h obs h'

and *sc*: *non-speculative* P vs (*l*list-of (*take* n (*map* *NormalAction* obs)))

and *vs*: *vs-conf* P h vs

and *hconf*: *hconf* h

and *wt*: $P, h \vdash ad@al : T$ $P, h \vdash ad'@al : T$

shows *vs-conf* P h' (*w-values* P vs (*take* n (*map* *NormalAction* obs)))

$\langle proof \rangle$

lemma *heap-copies-non-speculative-typeable*:

assumes *heap-copies* ad ad' als h obs h'

and *non-speculative* P vs (*l*list-of (*map* *NormalAction* obs))

and *vs-conf* P h vs

and *hconf* h

and *list-all2* ($\lambda al\ T. P, h \vdash ad@al : T$) *als* Ts *list-all2* ($\lambda al\ T. P, h \vdash ad'@al : T$) *als* Ts

shows *heap-base.heap-copies* (*heap-read-typed* P) *heap-write* ad ad' als h obs h'

$\langle proof \rangle$

lemma *heap-copies-non-speculative-vs-conf*:

assumes *heap-copies* $ad\ ad'\ als\ h\ obs\ h'$
and *non-speculative* $P\ vs\ (l\text{list-of}\ (take\ n\ (map\ NormalAction\ obs)))$
and *vs-conf* $P\ h\ vs$
and *hconf* h
and *list-all2* $(\lambda al\ T.\ P, h \vdash ad@al : T)\ als\ Ts\ list-all2\ (\lambda al\ T.\ P, h \vdash ad'@al : T)\ als\ Ts$
shows *vs-conf* $P\ h'$ (*w-values* $P\ vs\ (take\ n\ (map\ NormalAction\ obs))$)
 $\langle proof \rangle$

lemma *heap-clone-non-speculative-typeable-Some*:
assumes *clone*: *heap-clone* $P\ h\ ad\ h'\ [(obs, ad')]$
and *sc*: *non-speculative* $P\ vs\ (l\text{list-of}\ (map\ NormalAction\ obs))$
and *vs*: *vs-conf* $P\ h\ vs$
and *hconf*: *hconf* h
shows *heap-base.heap-clone* *allocate* *typeof-addr* (*heap-read-typed* P) *heap-write* $P\ h\ ad\ h'\ [(obs, ad')]$
 $\langle proof \rangle$

lemma *heap-clone-non-speculative-vs-conf-Some*:
assumes *clone*: *heap-clone* $P\ h\ ad\ h'\ [(obs, ad')]$
and *sc*: *non-speculative* $P\ vs\ (l\text{list-of}\ (take\ n\ (map\ NormalAction\ obs)))$
and *vs*: *vs-conf* $P\ h\ vs$
and *hconf*: *hconf* h
shows *vs-conf* $P\ h'$ (*w-values* $P\ vs\ (take\ n\ (map\ NormalAction\ obs))$)
 $\langle proof \rangle$

lemma *heap-clone-non-speculative-typeable-None*:
assumes *heap-clone* $P\ h\ ad\ h'\ None$
shows *heap-base.heap-clone* *allocate* *typeof-addr* (*heap-read-typed* P) *heap-write* $P\ h\ ad\ h'\ None$
 $\langle proof \rangle$

lemma *red-external-non-speculative-typeable*:
assumes *red*: $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$
and *sc*: *non-speculative* $P\ Vs\ (l\text{list-of}\ (map\ NormalAction\ \{ta\}_o))$
and *vs*: *vs-conf* $P\ h\ Vs$
and *hconf*: *hconf* h
shows *heap-base.red-external* *addr2thread-id* *thread-id2addr* *spurious-wakeups* *empty-heap* *allocate* *typeof-addr* (*heap-read-typed* P) *heap-write* $P\ t\ h\ a\ M\ vs\ ta\ va\ h'$
 $\langle proof \rangle$

lemma *red-external-non-speculative-vs-conf*:
assumes *red*: $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$
and *sc*: *non-speculative* $P\ Vs\ (l\text{list-of}\ (take\ n\ (map\ NormalAction\ \{ta\}_o)))$
and *vs*: *vs-conf* $P\ h\ Vs$
and *hconf*: *hconf* h
shows *vs-conf* $P\ h'$ (*w-values* $P\ Vs\ (take\ n\ (map\ NormalAction\ \{ta\}_o))$)
 $\langle proof \rangle$

lemma *red-external-aggr-non-speculative-typeable*:
assumes *red*: $(ta, va, h') \in red\text{-external-aggr}\ P\ t\ a\ M\ vs\ h$
and *sc*: *non-speculative* $P\ Vs\ (l\text{list-of}\ (map\ NormalAction\ \{ta\}_o))$
and *vs*: *vs-conf* $P\ h\ Vs$
and *hconf*: *hconf* h
and *native*: *is-native* $P\ (the\ (typeof\text{-addr}\ h\ a))\ M$
shows $(ta, va, h') \in heap\text{-base.red-external-aggr}\ addr2thread\text{-id}\ thread\text{-id2addr}\ spurious\text{-wakeups}\ empty\text{-heap}\ allocate\ typeof\text{-addr}\ (heap\text{-read-typed}\ P)\ heap\text{-write}\ P\ t\ a\ M\ vs\ h$

<proof>

lemma *red-external-aggr-non-speculative-vs-conf*:

assumes *red*: $(ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h$
and *sc*: *non-speculative* $P \ Vs$ (*l*list-of (*t*ake n (*m*ap *NormalAction* $\{ta\}_o$)))
and *vs*: *vs-conf* $P \ h \ Vs$
and *hconf*: *hconf* h
and *native*: *is-native* P (*the* (*typeof-addr* $h \ a$)) M
shows *vs-conf* $P \ h'$ (*w-values* $P \ Vs$ (*t*ake n (*m*ap *NormalAction* $\{ta\}_o$)))

<proof>

end

declare *split-paired-Ex* [*simp del*]

declare *eq-upto-seq-inconsist-simps* [*simp*]

context *heap-progress* **begin**

lemma *heap-copy-loc-non-speculative-read*:

assumes *hrt*: *heap-read-typeable* *hconf* P
and *vs*: *vs-conf* $P \ h \ vs$
and *type*: $P, h \vdash a@al : T \ P, h \vdash a'@al : T$
and *hconf*: *hconf* h
and *copy*: *heap-copy-loc* $a \ a' \ al \ h \ obs \ h'$
and *i*: $i < \text{length } obs$
and *read*: $obs \ ! \ i = \text{ReadMem } a'' \ al'' \ v$
and *v*: $v' \in \text{w-values } P \ vs$ (*m*ap *NormalAction* (*t*ake i *obs*)) (a'', al'')
shows $\exists obs' \ h''. \text{heap-copy-loc } a \ a' \ al \ h \ obs' \ h'' \wedge i < \text{length } obs' \wedge \text{take } i \ obs' = \text{take } i \ obs \wedge$
 $obs' \ ! \ i = \text{ReadMem } a'' \ al'' \ v' \wedge \text{length } obs' \leq \text{length } obs \wedge$
non-speculative $P \ vs$ (*l*list-of (*m*ap *NormalAction* *obs'*))

<proof>

lemma *heap-copies-non-speculative-read*:

assumes *hrt*: *heap-read-typeable* *hconf* P
and *copies*: *heap-copies* $a \ a' \ als \ h \ obs \ h'$
and *vs*: *vs-conf* $P \ h \ vs$
and *type1*: *list-all2* $(\lambda al \ T. \ P, h \vdash a@al : T) \ als \ Ts$
and *type2*: *list-all2* $(\lambda al \ T. \ P, h \vdash a'@al : T) \ als \ Ts$
and *hconf*: *hconf* h
and *i*: $i < \text{length } obs$
and *read*: $obs \ ! \ i = \text{ReadMem } a'' \ al'' \ v$
and *v*: $v' \in \text{w-values } P \ vs$ (*m*ap *NormalAction* (*t*ake i *obs*)) (a'', al'')
and *ns*: *non-speculative* $P \ vs$ (*l*list-of (*m*ap *NormalAction* (*t*ake i *obs*)))
shows $\exists obs' \ h''. \text{heap-copies } a \ a' \ als \ h \ obs' \ h'' \wedge i < \text{length } obs' \wedge \text{take } i \ obs' = \text{take } i \ obs \wedge$
 $obs' \ ! \ i = \text{ReadMem } a'' \ al'' \ v' \wedge \text{length } obs' \leq \text{length } obs$

(**is** *?concl* *als* $h \ obs \ vs \ i$)

<proof>

lemma *heap-clone-non-speculative-read*:

assumes *hrt*: *heap-read-typeable* *hconf* P
and *clone*: *heap-clone* $P \ h \ a \ h' \ [(obs, a')]$
and *vs*: *vs-conf* $P \ h \ vs$
and *hconf*: *hconf* h
and *i*: $i < \text{length } obs$

and *read*: $obs ! i = ReadMem\ a''\ al''\ v$
and *v*: $v' \in w\text{-values}\ P\ vs\ (map\ NormalAction\ (take\ i\ obs))\ (a'',\ al'')$
and *ns*: *non-speculative* $P\ vs\ (l\text{list-of}\ (map\ NormalAction\ (take\ i\ obs)))$
shows $\exists\ obs'\ h''.\ heap\text{-clone}\ P\ h\ a\ h''\ [(obs',\ a')] \wedge i < length\ obs' \wedge take\ i\ obs' = take\ i\ obs \wedge$
 $obs' ! i = ReadMem\ a''\ al''\ v' \wedge length\ obs' \leq length\ obs$

<proof>

lemma *red-external-non-speculative-read*:

assumes *hrt*: *heap-read-typeable* $hconf\ P$

and *vs*: *vs-conf* $P\ (shr\ s)\ vs$

and *red*: $P, t \vdash \langle a.M(vs'), shr\ s \rangle -ta \rightarrow ext\ \langle va, h' \rangle$

and *aok*: *final-thread.actions-ok* $final\ s\ t\ ta$

and *hconf*: $hconf\ (shr\ s)$

and *i*: $i < length\ \{ta\}_o$

and *read*: $\{ta\}_o ! i = ReadMem\ a''\ al''\ v$

and *v*: $v' \in w\text{-values}\ P\ vs\ (map\ NormalAction\ (take\ i\ \{ta\}_o))\ (a'',\ al'')$

and *ns*: *non-speculative* $P\ vs\ (l\text{list-of}\ (map\ NormalAction\ (take\ i\ \{ta\}_o)))$

shows $\exists\ ta''\ va''\ h''.\ P, t \vdash \langle a.M(vs'), shr\ s \rangle -ta'' \rightarrow ext\ \langle va'', h' \rangle \wedge final\text{-thread.actions-ok}\ final\ s\ t$
 $ta'' \wedge$

$$i < length\ \{ta''\}_o \wedge take\ i\ \{ta''\}_o = take\ i\ \{ta\}_o \wedge \\ \{ta''\}_o ! i = ReadMem\ a''\ al''\ v' \wedge length\ \{ta''\}_o \leq length\ \{ta\}_o$$

<proof>

lemma *red-external-aggr-non-speculative-read*:

assumes *hrt*: *heap-read-typeable* $hconf\ P$

and *vs*: *vs-conf* $P\ (shr\ s)\ vs$

and *red*: $(ta, va, h') \in red\text{-external-aggr}\ P\ t\ a\ M\ vs'\ (shr\ s)$

and *native*: *is-native* $P\ (the\ (typeof\text{-addr}\ (shr\ s)\ a))\ M$

and *aok*: *final-thread.actions-ok* $final\ s\ t\ ta$

and *hconf*: $hconf\ (shr\ s)$

and *i*: $i < length\ \{ta\}_o$

and *read*: $\{ta\}_o ! i = ReadMem\ a''\ al''\ v$

and *v*: $v' \in w\text{-values}\ P\ vs\ (map\ NormalAction\ (take\ i\ \{ta\}_o))\ (a'',\ al'')$

and *ns*: *non-speculative* $P\ vs\ (l\text{list-of}\ (map\ NormalAction\ (take\ i\ \{ta\}_o)))$

shows $\exists\ ta''\ va''\ h''.\ (ta'', va'', h') \in red\text{-external-aggr}\ P\ t\ a\ M\ vs'\ (shr\ s) \wedge final\text{-thread.actions-ok}$
 $final\ s\ t\ ta'' \wedge$

$$i < length\ \{ta''\}_o \wedge take\ i\ \{ta''\}_o = take\ i\ \{ta\}_o \wedge \\ \{ta''\}_o ! i = ReadMem\ a''\ al''\ v' \wedge length\ \{ta''\}_o \leq length\ \{ta\}_o$$

<proof>

end

declare *split-paired-Ex* [*simp*]

declare *eq-upto-seq-inconsist-simps* [*simp del*]

context *allocated-heap* **begin**

lemma *heap-copy-loc-allocated-same*:

assumes *heap-copy-loc* $a\ a'\ al\ h\ obs\ h'$

shows $allocated\ h' = allocated\ h$

<proof>

lemma *heap-copy-loc-allocated-mono*:

heap-copy-loc $a' \text{ al } h \text{ obs } h' \implies \text{allocated } h \subseteq \text{allocated } h'$
 ⟨proof⟩

lemma *heap-copies-allocated-same*:
assumes *heap-copies* $a' \text{ al } h \text{ obs } h'$
shows $\text{allocated } h' = \text{allocated } h$
 ⟨proof⟩

lemma *heap-copies-allocated-mono*:
heap-copies $a' \text{ al } h \text{ obs } h' \implies \text{allocated } h \subseteq \text{allocated } h'$
 ⟨proof⟩

lemma *heap-clone-allocated-mono*:
assumes *heap-clone* $P \ h \ a \ h' \ \text{aobs}$
shows $\text{allocated } h \subseteq \text{allocated } h'$
 ⟨proof⟩

lemma *red-external-allocated-mono*:
assumes $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h^\wedge \rangle$
shows $\text{allocated } h \subseteq \text{allocated } h'$
 ⟨proof⟩

lemma *red-external-aggr-allocated-mono*:
 $\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h; \text{is-native } P \ (\text{the } (\text{typeof-addr } h \ a)) \ M \rrbracket$
 $\implies \text{allocated } h \subseteq \text{allocated } h'$
 ⟨proof⟩

lemma *heap-clone-allocatedD*:
assumes *heap-clone* $P \ h \ a \ h' \ \llbracket (\text{obs}, a') \rrbracket$
and $\text{NewHeapElem } a'' \ x \in \text{set } \text{obs}$
shows $a'' \in \text{allocated } h' \wedge a'' \notin \text{allocated } h$
 ⟨proof⟩

lemma *red-external-allocatedD*:
 $\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h^\wedge \rangle; \text{NewHeapElem } a' \ x \in \text{set } \llbracket ta \rrbracket_o \rrbracket$
 $\implies a' \in \text{allocated } h' \wedge a' \notin \text{allocated } h$
 ⟨proof⟩

lemma *red-external-aggr-allocatedD*:
 $\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h; \text{NewHeapElem } a' \ x \in \text{set } \llbracket ta \rrbracket_o; \text{is-native } P \ (\text{the } (\text{typeof-addr } h \ a)) \ M \rrbracket$
 $\implies a' \in \text{allocated } h' \wedge a' \notin \text{allocated } h$
 ⟨proof⟩

lemma *heap-clone-NewHeapElemD*:
assumes *heap-clone* $P \ h \ a \ h' \ \llbracket (\text{obs}, a') \rrbracket$
and $ad \in \text{allocated } h'$
and $ad \notin \text{allocated } h$
shows $\exists CTn. \text{NewHeapElem } ad \ CTn \in \text{set } \text{obs}$
 ⟨proof⟩

lemma *heap-clone-fail-allocated-same*:
assumes *heap-clone* $P \ h \ a \ h' \ \text{None}$
shows $\text{allocated } h' = \text{allocated } h$

<proof>

lemma *red-external-NewHeapElemD:*

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; a' \in allocated\ h'; a' \notin allocated\ h \rrbracket$
 $\implies \exists CTn. NewHeapElem\ a'\ CTn \in set\ \{\{ta\}_o\}$

<proof>

lemma *red-external-aggr-NewHeapElemD:*

$\llbracket (ta, va, h') \in red-external-aggr\ P\ t\ a\ M\ vs\ h; a' \in allocated\ h'; a' \notin allocated\ h;$
 $is-native\ P\ (the\ (typeof-addr\ h\ a))\ M \rrbracket$
 $\implies \exists CTn. NewHeapElem\ a'\ CTn \in set\ \{\{ta\}_o\}$

<proof>

end

context *heap-base begin*

lemma *binop-known-addr:*

assumes *ok: start-heap-ok*

shows $binop\ bop\ v1\ v2 = [Inl\ v] \implies ka-Val\ v \subseteq ka-Val\ v1 \cup ka-Val\ v2 \cup set\ start-addr$

and $binop\ bop\ v1\ v2 = [Inr\ a] \implies a \in ka-Val\ v1 \cup ka-Val\ v2 \cup set\ start-addr$

<proof>

lemma *heap-copy-loc-known-addr-ReadMem:*

assumes *heap-copy-loc a a' al h ob h'*

and *ReadMem ad al' v ∈ set ob*

shows $ad = a$

<proof>

lemma *heap-copies-known-addr-ReadMem:*

assumes *heap-copies a a' als h obs h'*

and *ReadMem ad al v ∈ set obs*

shows $ad = a$

<proof>

lemma *heap-clone-known-addr-ReadMem:*

assumes *heap-clone P h a h' [(obs, a')]*

and *ReadMem ad al v ∈ set obs*

shows $ad = a$

<proof>

lemma *red-external-known-addr-ReadMem:*

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; ReadMem\ ad\ al\ v \in set\ \{\{ta\}_o\} \rrbracket$

$\implies ad \in \{thread-id2addr\ t, a\} \cup (\bigcup (ka-Val\ 'set\ vs)) \cup set\ start-addr$

<proof>

lemma *red-external-aggr-known-addr-ReadMem:*

$\llbracket (ta, va, h') \in red-external-aggr\ P\ t\ a\ M\ vs\ h; ReadMem\ ad\ al\ v \in set\ \{\{ta\}_o\} \rrbracket$

$\implies ad \in \{thread-id2addr\ t, a\} \cup (\bigcup (ka-Val\ 'set\ vs)) \cup set\ start-addr$

<proof>

lemma *heap-copy-loc-known-addr-WriteMem:*

assumes *heap-copy-loc a a' al h ob h'*

and $ob\ !\ n = WriteMem\ ad\ al'\ (Addr\ a'')\ n < length\ ob$

shows $a'' \in \text{new-obs-addr}$ s (take n ob)
 ⟨proof⟩

lemma *heap-copies-known-addr*-WriteMem:
assumes *heap-copies* a a' *als* h *obs* h'
and $\text{obs} ! n = \text{WriteMem } ad \ al \ (\text{Addr } a'') \ n < \text{length } \text{obs}$
shows $a'' \in \text{new-obs-addr}$ s (take n obs)
 ⟨proof⟩

lemma *heap-clone-known-addr*-WriteMem:
assumes *heap-clone* P h a h' $[(\text{obs}, a')]$
and $\text{obs} ! n = \text{WriteMem } ad \ al \ (\text{Addr } a'') \ n < \text{length } \text{obs}$
shows $a'' \in \text{new-obs-addr}$ s (take n obs)
 ⟨proof⟩

lemma *red-external-known-addr*-WriteMem:
 $\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; \{\!|ta|\!\}_o ! n = \text{WriteMem } ad \ al \ (\text{Addr } a'); \ n < \text{length } \{\!|ta|\!\}_o \rrbracket$
 $\implies a' \in \{\text{thread-id2addr } t, a\} \cup (\bigcup (ka\text{-Val } ' \text{ set } vs)) \cup \text{set } \text{start-addr}$ s $\cup \text{new-obs-addr}$ s (take n $\{\!|ta|\!\}_o$)
 ⟨proof⟩

lemma *red-external-aggr-known-addr*-WriteMem:
 $\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h;$
 $\{\!|ta|\!\}_o ! n = \text{WriteMem } ad \ al \ (\text{Addr } a'); \ n < \text{length } \{\!|ta|\!\}_o \rrbracket$
 $\implies a' \in \{\text{thread-id2addr } t, a\} \cup (\bigcup (ka\text{-Val } ' \text{ set } vs)) \cup \text{set } \text{start-addr}$ s $\cup \text{new-obs-addr}$ s (take n $\{\!|ta|\!\}_o$)
 ⟨proof⟩

lemma *red-external-known-addr*-mono:
assumes *ok*: *start-heap-ok*
and *red*: $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$
shows (case va of $\text{RetVal } v \Rightarrow ka\text{-Val } v \mid \text{RetExc } a \Rightarrow \{a\} \mid \text{RetStaySame} \Rightarrow \{\}$) $\subseteq \{\text{thread-id2addr } t, a\} \cup (\bigcup (ka\text{-Val } ' \text{ set } vs)) \cup \text{set } \text{start-addr}$ s $\cup \text{new-obs-addr}$ s $\{\!|ta|\!\}_o$
 ⟨proof⟩

lemma *red-external-aggr-known-addr*-mono:
assumes *ok*: *start-heap-ok*
and *red*: $(ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h$ *is-native* P (the (typeof-addr h a)) M
shows (case va of $\text{RetVal } v \Rightarrow ka\text{-Val } v \mid \text{RetExc } a \Rightarrow \{a\} \mid \text{RetStaySame} \Rightarrow \{\}$) $\subseteq \{\text{thread-id2addr } t, a\} \cup (\bigcup (ka\text{-Val } ' \text{ set } vs)) \cup \text{set } \text{start-addr}$ s $\cup \text{new-obs-addr}$ s $\{\!|ta|\!\}_o$
 ⟨proof⟩

lemma *red-external-NewThread-idD*:
 $\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; \text{NewThread } t' \ (C, M', a') \ h'' \in \text{set } \{\!|ta|\!\}_t \rrbracket$
 $\implies t' = \text{addr2thread-id } a \wedge a' = a$
 ⟨proof⟩

lemma *red-external-aggr-NewThread-idD*:
 $\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h;$
 $\text{NewThread } t' \ (C, M', a') \ h'' \in \text{set } \{\!|ta|\!\}_t \rrbracket$
 $\implies t' = \text{addr2thread-id } a \wedge a' = a$
 ⟨proof⟩

end

```

locale heap'' =
  heap'
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and P :: 'm prog
  +
assumes allocate-typeof-addr-SomeD: [(h', a) ∈ allocate h hT; typeof-addr a ≠ None] ⇒ typeof-addr
a = [hT]
begin

lemma heap-copy-loc-New-type-match:
  [(h.heap-copy-loc a a' al h obs h'; NewHeapElem ad CTn ∈ set obs; typeof-addr ad ≠ None]
  ⇒ typeof-addr ad = [CTn]
⟨proof⟩

lemma heap-copies-New-type-match:
  [(h.heap-copies a a' als h obs h'; NewHeapElem ad CTn ∈ set obs; typeof-addr ad ≠ None]
  ⇒ typeof-addr ad = [CTn]
⟨proof⟩

lemma heap-clone-New-type-match:
  [(h.heap-clone P h a h' [(obs, a)]]; NewHeapElem ad CTn ∈ set obs; typeof-addr ad ≠ None]
  ⇒ typeof-addr ad = [CTn]
⟨proof⟩

lemma red-external-New-type-match:
  [(h.red-external P t a M vs h ta va h'; NewHeapElem ad CTn ∈ set {ta}_o; typeof-addr ad ≠ None]
  ⇒ typeof-addr ad = [CTn]
⟨proof⟩

lemma red-external-aggr-New-type-match:
  [(ta, va, h') ∈ h.red-external-aggr P t a M vs h; NewHeapElem ad CTn ∈ set {ta}_o; typeof-addr ad
≠ None]
  ⇒ typeof-addr ad = [CTn]
⟨proof⟩

end

end
theory JMM-J
imports
  JMM-Framework
  ../J/Threaded

```

begin

sublocale *J-heap-base* < *red-mthr*:

heap-multithreaded-base

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate typeof-addr heap-read heap-write

final-expr mred P convert-RA

for *P*

<proof>

context *J-heap-base* **begin**

abbreviation *J- \mathcal{E}* ::

'addr J-prog \Rightarrow cname \Rightarrow mname \Rightarrow 'addr val list \Rightarrow status

\Rightarrow ('thread-id \times ('addr, 'thread-id) obs-event action) llist set

where

J- \mathcal{E} P \equiv red-mthr. \mathcal{E} -start P J-local-start P

end

end

8.14 JMM Instantiation for J

theory *DRF-J*

imports

JMM-Common

JMM-J

../J/ProgressThreaded

SC-Legal

begin

primrec *ka* :: *'addr expr \Rightarrow 'addr set*

and *kas* :: *'addr expr list \Rightarrow 'addr set*

where

ka (new C) = {}

| ka (newA T[e]) = ka e

| ka (Cast T e) = ka e

| ka (e instanceof T) = ka e

| ka (Val v) = ka-Val v

| ka (Var V) = {}

| ka (e1 «bop» e2) = ka e1 \cup ka e2

| ka (V := e) = ka e

| ka (a[e]) = ka a \cup ka e

| ka (a[e] := e') = ka a \cup ka e \cup ka e'

| ka (a.length) = ka a

| ka (e.F{D}) = ka e

| ka (e.F{D} := e') = ka e \cup ka e'

| ka (e.compareAndSwap(D.F, e', e'')) = ka e \cup ka e' \cup ka e''

| ka (e.M(es)) = ka e \cup kas es

| ka {V:T=vo; e} = ka e \cup (case vo of None \Rightarrow {} | Some v \Rightarrow ka-Val v)

| ka (Synchronized x e e') = ka e \cup ka e'

$| ka (InSynchronized\ x\ a\ e) = insert\ a\ (ka\ e)$
 $| ka\ (e;\; e') = ka\ e \cup ka\ e'$
 $| ka\ (if\ (e)\ e1\ else\ e2) = ka\ e \cup ka\ e1 \cup ka\ e2$
 $| ka\ (while\ (b)\ e) = ka\ b \cup ka\ e$
 $| ka\ (throw\ e) = ka\ e$
 $| ka\ (try\ e\ catch\ (C\ V)\ e') = ka\ e \cup ka\ e'$

$| kas\ [] = \{\}$
 $| kas\ (e\ \# \ es) = ka\ e \cup kas\ es$

definition *ka-locals* :: 'addr locals \Rightarrow 'addr set
where *ka-locals* *xs* = {*a*. Addr *a* \in ran *xs*}

lemma *ka-Val-subset-ka-locals*:
 $xs\ V = [v] \implies ka\ Val\ v \subseteq ka\ locals\ xs$
 <proof>

lemma *ka-locals-update-subset*:
 $ka\ locals\ (xs\ (V := None)) \subseteq ka\ locals\ xs$
 $ka\ locals\ (xs\ (V \mapsto v)) \subseteq ka\ Val\ v \cup ka\ locals\ xs$
 <proof>

lemma *ka-locals-empty* [*simp*]: *ka-locals* *Map.empty* = $\{\}$
 <proof>

lemma *kas-append* [*simp*]: $kas\ (es\ @\ es') = kas\ es \cup kas\ es'$
 <proof>

lemma *kas-map-Val* [*simp*]: $kas\ (map\ Val\ vs) = \bigcup (ka\ Val\ 'set\ vs)$
 <proof>

lemma *ka-blocks*:
 $\llbracket length\ pns = length\ Ts; length\ vs = length\ Ts \rrbracket$
 $\implies ka\ (blocks\ pns\ Ts\ vs\ body) = \bigcup (ka\ Val\ 'set\ vs) \cup ka\ body$
 <proof>

lemma *WT-ka*: $P, E \vdash e :: T \implies ka\ e = \{\}$
and *WTs-kas*: $P, E \vdash es [::] Ts \implies kas\ es = \{\}$
 <proof>

context *J-heap-base* **begin**

primrec *J-known-addr*s :: 'thread-id \Rightarrow 'addr expr \times 'addr locals \Rightarrow 'addr set
where *J-known-addr*s *t* (*e*, *xs*) = insert (thread-id2addr *t*) (ka *e* \cup *ka-locals* *xs* \cup set *start-addr*s)

lemma **assumes** *wf*: *wf-J-prog* *P*
and *ok*: *start-heap-ok*
shows *red-known-addr*s-*mono*:
 $P, t \vdash \langle e, s \rangle \text{-}ta \rightarrow \langle e', s' \rangle \implies J\text{-known-addr}\ s\ t\ (e',\ lcl\ s') \subseteq J\text{-known-addr}\ s\ t\ (e,\ lcl\ s) \cup new\text{-obs-addr}\ s\ \{\!\{ta\}\!\}_o$
and *reds-known-addr*s-*mono*:
 $P, t \vdash \langle es, s \rangle \text{-}ta \rightarrow \langle es', s' \rangle \implies kas\ es'\ \cup\ ka\ locals\ (lcl\ s') \subseteq insert\ (thread\text{-id}2addr\ t)\ (kas\ es\ \cup\ ka\ locals\ (lcl\ s)) \cup new\text{-obs-addr}\ s\ \{\!\{ta\}\!\}_o \cup set\ start\text{-addr}\ s$
 <proof>

lemma *red-known-addr-ReadMem*:

$\llbracket P, t \vdash \langle e, s \rangle \text{-ta} \rightarrow \langle e', s' \rangle; \text{ReadMem } ad \text{ al } v \in \text{set } \{\{ta\}_o \} \rrbracket \implies ad \in J\text{-known-addr } t (e, \text{lcl } s)$

and *reds-known-addr-ReadMem*:

$\llbracket P, t \vdash \langle es, s \rangle \text{-ta} \rightarrow \langle es', s' \rangle; \text{ReadMem } ad \text{ al } v \in \text{set } \{\{ta\}_o \} \rrbracket$

$\implies ad \in \text{insert } (\text{thread-id2addr } t) (kas \text{ es} \cup ka\text{-locals } (\text{lcl } s)) \cup \text{set } \text{start-addr}$

$\langle \text{proof} \rangle$

lemma *red-known-addr-WriteMem*:

$\llbracket P, t \vdash \langle e, s \rangle \text{-ta} \rightarrow \langle e', s' \rangle; \{\{ta\}_o \} ! n = \text{WriteMem } ad \text{ al } (\text{Addr } a); n < \text{length } \{\{ta\}_o \} \rrbracket$

$\implies a \in J\text{-known-addr } t (e, \text{lcl } s) \vee a \in \text{new-obs-addr } (\text{take } n \{\{ta\}_o \})$

and *reds-known-addr-WriteMem*:

$\llbracket P, t \vdash \langle es, s \rangle \text{-ta} \rightarrow \langle es', s' \rangle; \{\{ta\}_o \} ! n = \text{WriteMem } ad \text{ al } (\text{Addr } a); n < \text{length } \{\{ta\}_o \} \rrbracket$

$\implies a \in \text{insert } (\text{thread-id2addr } t) (kas \text{ es} \cup ka\text{-locals } (\text{lcl } s)) \cup \text{set } \text{start-addr} \cup \text{new-obs-addr } (\text{take } n \{\{ta\}_o \})$

$\langle \text{proof} \rangle$

end

context *J-heap* **begin**

lemma

assumes *wf*: *wf-J-prog* *P*

and *ok*: *start-heap-ok*

shows *red-known-addr-new-thread*:

$\llbracket P, t \vdash \langle e, s \rangle \text{-ta} \rightarrow \langle e', s' \rangle; \text{NewThread } t' \text{ } x' \text{ } h' \in \text{set } \{\{ta\}_t \} \rrbracket$

$\implies J\text{-known-addr } t' \text{ } x' \subseteq J\text{-known-addr } t (e, \text{lcl } s)$

and *reds-known-addr-new-thread*:

$\llbracket P, t \vdash \langle es, s \rangle \text{-ta} \rightarrow \langle es', s' \rangle; \text{NewThread } t' \text{ } x' \text{ } h' \in \text{set } \{\{ta\}_t \} \rrbracket$

$\implies J\text{-known-addr } t' \text{ } x' \subseteq \text{insert } (\text{thread-id2addr } t) (kas \text{ es} \cup ka\text{-locals } (\text{lcl } s)) \cup \text{set } \text{start-addr}$

$\langle \text{proof} \rangle$

lemma *red-New-same-addr-same*:

$\llbracket \text{convert-extTA } extTA, P, t \vdash \langle e, s \rangle \text{-ta} \rightarrow \langle e', s' \rangle;$

$\{\{ta\}_o \} ! i = \text{NewHeapElem } a \text{ } x; i < \text{length } \{\{ta\}_o \};$

$\{\{ta\}_o \} ! j = \text{NewHeapElem } a \text{ } x'; j < \text{length } \{\{ta\}_o \} \rrbracket$

$\implies i = j$

and *reds-New-same-addr-same*:

$\llbracket \text{convert-extTA } extTA, P, t \vdash \langle es, s \rangle \text{-ta} \rightarrow \langle es', s' \rangle;$

$\{\{ta\}_o \} ! i = \text{NewHeapElem } a \text{ } x; i < \text{length } \{\{ta\}_o \};$

$\{\{ta\}_o \} ! j = \text{NewHeapElem } a \text{ } x'; j < \text{length } \{\{ta\}_o \} \rrbracket$

$\implies i = j$

$\langle \text{proof} \rangle$

end

locale *J-allocated-heap* = *allocated-heap* +

constrains *addr2thread-id* :: ('*addr* :: *addr*) \Rightarrow '*thread-id*

and *thread-id2addr* :: '*thread-id* \Rightarrow '*addr*

and *spurious-wakeups* :: *bool*

and *empty-heap* :: '*heap*

and *allocate* :: '*heap* \Rightarrow *h*type \Rightarrow ('*heap* \times '*addr*) *set*

and *typeof-addr* :: '*heap* \Rightarrow '*addr* \rightarrow *h*type

and *heap-read* :: '*heap* \Rightarrow '*addr* \Rightarrow *addr-loc* \Rightarrow '*addr val* \Rightarrow *bool*

and *heap-write* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and *P* :: 'addr J-prog

sublocale *J-allocated-heap* < *J-heap*
 ⟨proof⟩

context *J-allocated-heap* **begin**

lemma *red-allocated-mono*: $P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{allocated } (hp\ s) \subseteq \text{allocated } (hp\ s')$
and *reds-allocated-mono*: $P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \text{allocated } (hp\ s) \subseteq \text{allocated } (hp\ s')$
 ⟨proof⟩

lemma *red-allocatedD*:

$\llbracket P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o \rrbracket \implies ad \in \text{allocated } (hp\ s') \wedge ad \notin \text{allocated } (hp\ s)$

and *reds-allocatedD*:

$\llbracket P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o \rrbracket \implies ad \in \text{allocated } (hp\ s') \wedge ad \notin \text{allocated } (hp\ s)$

⟨proof⟩

lemma *red-allocated-NewHeapElemD*:

$\llbracket P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; ad \in \text{allocated } (hp\ s'); ad \notin \text{allocated } (hp\ s) \rrbracket \implies \exists CTn. \text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o$

and *reds-allocated-NewHeapElemD*:

$\llbracket P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; ad \in \text{allocated } (hp\ s'); ad \notin \text{allocated } (hp\ s) \rrbracket \implies \exists CTn. \text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o$

⟨proof⟩

lemma *mred-allocated-multithreaded*:

allocated-multithreaded addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write allocated final-expr (mred P) P

⟨proof⟩

end

sublocale *J-allocated-heap* < *red-mthr*: *allocated-multithreaded*
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate typeof-addr heap-read heap-write allocated
final-expr mred P
P

⟨proof⟩

context *J-allocated-heap* **begin**

lemma *mred-known-addr*s:

assumes *wf*: *wf-J-prog P*

and *ok*: *start-heap-ok*

shows *known-addr*s *addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write allocated J-known-addr*s *final-expr (mred P) P*

⟨proof⟩

end

context *J-heap begin*

lemma *red-read-typeable*:

$\llbracket \text{convert-extTA } \text{extTA}, P, t \vdash \langle e, s \rangle \text{--ta} \rightarrow \langle e', s' \rangle; P, E, hp \ s \vdash e : T; \text{ReadMem } ad \ al \ v \in \text{set } \{ta\}_o \rrbracket$
 $\implies \exists T'. P, hp \ s \vdash ad@al : T'$

and *reds-read-typeable*:

$\llbracket \text{convert-extTA } \text{extTA}, P, t \vdash \langle es, s \rangle \text{[-ta} \rightarrow] \langle es', s' \rangle; P, E, hp \ s \vdash es \text{[:] } Ts; \text{ReadMem } ad \ al \ v \in \text{set } \{ta\}_o \rrbracket$

$\implies \exists T'. P, hp \ s \vdash ad@al : T'$

<proof>

end

primrec *new-types* :: ('a, 'b, 'addr) exp \Rightarrow ty set

and *new-typess* :: ('a, 'b, 'addr) exp list \Rightarrow ty set

where

new-types (new *C*) = {Class *C*}

| *new-types* (newA *T* [*e*]) = insert (*T*[]) (*new-types* *e*)

| *new-types* (Cast *T* *e*) = *new-types* *e*

| *new-types* (*e* instanceof *T*) = *new-types* *e*

| *new-types* (Val *v*) = {}

| *new-types* (Var *V*) = {}

| *new-types* (*e*1 «bop» *e*2) = *new-types* *e*1 \cup *new-types* *e*2

| *new-types* (*V* := *e*) = *new-types* *e*

| *new-types* (*a* [*e*]) = *new-types* *a* \cup *new-types* *e*

| *new-types* (*a* [*e*] := *e'*) = *new-types* *a* \cup *new-types* *e* \cup *new-types* *e'*

| *new-types* (*a*.length) = *new-types* *a*

| *new-types* (*e*.F{*D*}) = *new-types* *e*

| *new-types* (*e*.F{*D*} := *e'*) = *new-types* *e* \cup *new-types* *e'*

| *new-types* (*e*.compareAndSwap(*D*.F, *e'*, *e''*)) = *new-types* *e* \cup *new-types* *e'* \cup *new-types* *e''*

| *new-types* (*e*.M(*es*)) = *new-types* *e* \cup *new-typess* *es*

| *new-types* {*V*:*T*=*vo*; *e*} = *new-types* *e*

| *new-types* (Synchronized *x* *e* *e'*) = *new-types* *e* \cup *new-types* *e'*

| *new-types* (InSynchronized *x* *a* *e*) = *new-types* *e*

| *new-types* (*e*;; *e'*) = *new-types* *e* \cup *new-types* *e'*

| *new-types* (if (*e*) *e*1 else *e*2) = *new-types* *e* \cup *new-types* *e*1 \cup *new-types* *e*2

| *new-types* (while (*b*) *e*) = *new-types* *b* \cup *new-types* *e*

| *new-types* (throw *e*) = *new-types* *e*

| *new-types* (try *e* catch(*C* *V*) *e'*) = *new-types* *e* \cup *new-types* *e'*

| *new-typess* [] = {}

| *new-typess* (*e* # *es*) = *new-types* *e* \cup *new-typess* *es*

lemma *new-types-blocks*:

$\llbracket \text{length } pns = \text{length } Ts; \text{length } vs = \text{length } Ts \rrbracket \implies \text{new-types } (\text{blocks } pns \text{ vs } Ts \ e) = \text{new-types } e$
<proof>

context *J-heap-base begin*

lemma *WTrt-new-types-types*: $P, E, h \vdash e : T \implies \text{new-types } e \subseteq \text{types } P$

and *WTrts-new-typess-types*: $P, E, h \vdash es \text{[:] } Ts \implies \text{new-typess } es \subseteq \text{types } P$

<proof>

end

lemma *WT-new-types-types*: $P, E \vdash e :: T \implies \text{new-types } e \subseteq \text{types } P$
and *WTs-new-types-types*: $P, E \vdash es [::] Ts \implies \text{new-typess } es \subseteq \text{types } P$
 ⟨proof⟩

context *J-heap-conf* **begin**

lemma *red-New-typeof-addrD*:

[[*convert-extTA* *extTA*, $P, t \vdash \langle e, s \rangle \text{--}ta \rightarrow \langle e', s' \rangle$; *new-types* $e \subseteq \text{types } P$; *hconf* ($hp\ s$); *NewHeapElem*
 $a\ x \in \text{set } \{ta\}_o$]]

$\implies \text{typeof-addr } (hp\ s')\ a = \text{Some } x$

and *reds-New-typeof-addrD*:

[[*convert-extTA* *extTA*, $P, t \vdash \langle es, s \rangle \text{--}[-ta \rightarrow] \langle es', s' \rangle$; *new-typess* $es \subseteq \text{types } P$; *hconf* ($hp\ s$);
NewHeapElem $a\ x \in \text{set } \{ta\}_o$]]

$\implies \text{typeof-addr } (hp\ s')\ a = \text{Some } x$

⟨proof⟩

lemma *J-conf-read-heap-read-typed*:

J-conf-read *addr2thread-id* *thread-id2addr* *empty-heap* *allocate* *typeof-addr* (*heap-read-typed* P) *heap-write*
hconf P

⟨proof⟩

lemma *red-non-speculative-vs-conf*:

[[*convert-extTA* *extTA*, $P, t \vdash \langle e, s \rangle \text{--}ta \rightarrow \langle e', s' \rangle$; $P, E, hp\ s \vdash e : T$;
non-speculative $P\ vs\ (\text{l-list-of } (\text{take } n\ (\text{map } \text{NormalAction } \{ta\}_o)))$; *vs-conf* $P\ (hp\ s)\ vs$; *hconf* ($hp\ s$)]]

$\implies \text{vs-conf } P\ (hp\ s')\ (w\text{-values } P\ vs\ (\text{take } n\ (\text{map } \text{NormalAction } \{ta\}_o)))$

and *reds-non-speculative-vs-conf*:

[[*convert-extTA* *extTA*, $P, t \vdash \langle es, s \rangle \text{--}[-ta \rightarrow] \langle es', s' \rangle$; $P, E, hp\ s \vdash es [::] Ts$;
non-speculative $P\ vs\ (\text{l-list-of } (\text{map } \text{NormalAction } \{ta\}_o))$; *vs-conf* $P\ (hp\ s)\ vs$; *hconf* ($hp\ s$)]]

$\implies \text{vs-conf } P\ (hp\ s')\ (w\text{-values } P\ vs\ (\text{take } n\ (\text{map } \text{NormalAction } \{ta\}_o)))$

⟨proof⟩

lemma *red-non-speculative-typeable*:

[[*convert-extTA* *extTA*, $P, t \vdash \langle e, s \rangle \text{--}ta \rightarrow \langle e', s' \rangle$; $P, E, hp\ s \vdash e : T$;
non-speculative $P\ vs\ (\text{l-list-of } (\text{map } \text{NormalAction } \{ta\}_o))$; *vs-conf* $P\ (hp\ s)\ vs$; *hconf* ($hp\ s$)]]
 $\implies \text{J-heap-base.red } \text{addr2thread-id } \text{thread-id2addr } \text{spurious-wakeups } \text{empty-heap } \text{allocate } \text{typeof-addr}$
 $(\text{heap-read-typed } P)\ \text{heap-write } (\text{convert-extTA } \text{extTA})\ P\ t\ e\ s\ ta\ e'\ s'$

and *reds-non-speculative-typeable*:

[[*convert-extTA* *extTA*, $P, t \vdash \langle es, s \rangle \text{--}[-ta \rightarrow] \langle es', s' \rangle$; $P, E, hp\ s \vdash es [::] Ts$;
non-speculative $P\ vs\ (\text{l-list-of } (\text{map } \text{NormalAction } \{ta\}_o))$; *vs-conf* $P\ (hp\ s)\ vs$; *hconf* ($hp\ s$)]]
 $\implies \text{J-heap-base.reds } \text{addr2thread-id } \text{thread-id2addr } \text{spurious-wakeups } \text{empty-heap } \text{allocate } \text{typeof-addr}$
 $(\text{heap-read-typed } P)\ \text{heap-write } (\text{convert-extTA } \text{extTA})\ P\ t\ es\ s\ ta\ es'\ s'$

⟨proof⟩

end

sublocale *J-heap-base* < *red-mthr*:

if-multithreaded

final-expr

mred P

convert-RA

for P
 $\langle proof \rangle$

locale J -allocated-heap-conf =

J -heap-conf
 addr2thread-id thread-id2addr
 spurious-wakeups
 empty-heap allocate typeof-addr heap-read heap-write hconf
 P

+

J -allocated-heap
 addr2thread-id thread-id2addr
 spurious-wakeups
 empty-heap allocate typeof-addr heap-read heap-write
 allocated
 P

for addr2thread-id :: ('addr :: addr) \Rightarrow 'thread-id
and thread-id2addr :: 'thread-id \Rightarrow 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) set
and typeof-addr :: 'heap \Rightarrow 'addr \rightarrow htype
and heap-read :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow bool
and heap-write :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow 'heap \Rightarrow bool
and hconf :: 'heap \Rightarrow bool
and allocated :: 'heap \Rightarrow 'addr set
and P :: 'addr J -prog

begin

lemma mred-known-addr-typing:

assumes wf: wf- J -prog P

and ok: start-heap-ok

shows known-addr-typing addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write
 allocated J -known-addr final-expr (mred P) ($\lambda t x h. \exists ET. sconf$ -type-ok $ET t x h$) P

$\langle proof \rangle$

end

context J -allocated-heap-conf **begin**

lemma executions-sc:

assumes wf: wf- J -prog P

and wf-start: wf-start-state $P C M vs$

and vs2: $\bigcup (ka$ -Val ' set vs) \subseteq set start-addr

shows executions-sc-hb (J - \mathcal{E} $P C M vs$ status) P

(**is** executions-sc-hb ? E P)

$\langle proof \rangle$

end

declare split-paired-Ex [*simp del*]

context J -progress **begin**

lemma *ex-WTrt-simps*:

$P, E, h \vdash e : T \implies \exists E T. P, E, h \vdash e : T$
 $\langle \text{proof} \rangle$

abbreviation (*input*) *J-non-speculative-read-bound* :: nat

where *J-non-speculative-read-bound* $\equiv 2$

lemma **assumes** *hrt*: heap-read-typeable *hconf* *P*

and *vs*: vs-conf *P* (*shr s*) *vs*

and *hconf*: *hconf* (*shr s*)

shows *red-non-speculative-read*:

$\llbracket P, t \vdash \langle e, (\text{shr } s, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle; \exists E T. P, E, \text{shr } s \vdash e : T;$
 $\text{red-mthr.mthr.if.actions-ok } s \ t \ ta;$
 $I < \text{length } \{\!\{ta\}\!\}_o; \{\!\{ta\}\!\}_o ! I = \text{ReadMem } a'' \ al'' \ v; v' \in w\text{-values } P \ \text{vs } (\text{map } \text{NormalAction } (\text{take } I \ \{\!\{ta\}\!\}_o)) \ (a'', \ al'');$
 $\text{non-speculative } P \ \text{vs } (\text{llist-of } (\text{map } \text{NormalAction } (\text{take } I \ \{\!\{ta\}\!\}_o))) \rrbracket$
 $\implies \exists ta' \ e'' \ xs'' \ h''. P, t \vdash \langle e, (\text{shr } s, xs) \rangle -ta' \rightarrow \langle e'', (h'', xs'') \rangle \wedge$
 $\text{red-mthr.mthr.if.actions-ok } s \ t \ ta' \wedge$
 $I < \text{length } \{\!\{ta'\}\!\}_o \wedge \text{take } I \ \{\!\{ta'\}\!\}_o = \text{take } I \ \{\!\{ta\}\!\}_o \wedge$
 $\{\!\{ta'\}\!\}_o ! I = \text{ReadMem } a'' \ al'' \ v' \wedge \text{length } \{\!\{ta'\}\!\}_o \leq \max \ J\text{-non-speculative-read-bound } (\text{length } \{\!\{ta\}\!\}_o)$

and *reds-non-speculative-read*:

$\llbracket P, t \vdash \langle es, (\text{shr } s, xs) \rangle [-ta \rightarrow] \langle es', (h', xs') \rangle; \exists E Ts. P, E, \text{shr } s \vdash es \ [:] \ Ts;$
 $\text{red-mthr.mthr.if.actions-ok } s \ t \ ta;$
 $I < \text{length } \{\!\{ta\}\!\}_o; \{\!\{ta\}\!\}_o ! I = \text{ReadMem } a'' \ al'' \ v; v' \in w\text{-values } P \ \text{vs } (\text{map } \text{NormalAction } (\text{take } I \ \{\!\{ta\}\!\}_o)) \ (a'', \ al'');$
 $\text{non-speculative } P \ \text{vs } (\text{llist-of } (\text{map } \text{NormalAction } (\text{take } I \ \{\!\{ta\}\!\}_o))) \rrbracket$
 $\implies \exists ta' \ es'' \ xs'' \ h''. P, t \vdash \langle es, (\text{shr } s, xs) \rangle [-ta' \rightarrow] \langle es'', (h'', xs'') \rangle \wedge$
 $\text{red-mthr.mthr.if.actions-ok } s \ t \ ta' \wedge$
 $I < \text{length } \{\!\{ta'\}\!\}_o \wedge \text{take } I \ \{\!\{ta'\}\!\}_o = \text{take } I \ \{\!\{ta\}\!\}_o \wedge$
 $\{\!\{ta'\}\!\}_o ! I = \text{ReadMem } a'' \ al'' \ v' \wedge \text{length } \{\!\{ta'\}\!\}_o \leq \max \ J\text{-non-speculative-read-bound } (\text{length } \{\!\{ta\}\!\}_o)$

$\{\!\{ta\}\!\}_o$

$\langle \text{proof} \rangle$

end

sublocale *J-allocated-heap-conf* < *if-known-addr-base*

J-known-addr

final-expr *mred* *P* *convert-RA*

$\langle \text{proof} \rangle$

declare *split-paired-Ex* [*simp*]

declare *eq-upto-seq-inconsist-simps* [*simp del*]

locale *J-allocated-progress* =

J-progress

addr2thread-id *thread-id2addr*

spurious-wakeups

empty-heap *allocate* *typeof-addr* *heap-read* *heap-write* *hconf*

P

+

J-allocated-heap-conf
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate typeof-addr heap-read heap-write hconf
allocated
P
for *addr2thread-id* :: ('addr :: addr) ⇒ 'thread-id
and *thread-id2addr* :: 'thread-id ⇒ 'addr
and *spurious-wakeups* :: bool
and *empty-heap* :: 'heap
and *allocate* :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and *typeof-addr* :: 'heap ⇒ 'addr → htype
and *heap-read* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and *heap-write* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and *hconf* :: 'heap ⇒ bool
and *allocated* :: 'heap ⇒ 'addr set
and *P* :: 'addr J-prog
begin

lemma *non-speculative-read*:
assumes *wf*: wf-J-prog *P*
and *hrt*: heap-read-typeable *hconf P*
and *wf-start*: wf-start-state *P C M vs*
and *ka*: $\bigcup (ka\text{-Val } 'set\ vs) \subseteq set\ start\ addrs$
shows *red-mthr.if.non-speculative-read J-non-speculative-read-bound*
(init-fin-lift-state status (J-start-state P C M vs))
(w-values P (λ-. {})) (map snd (lift-start-obs start-tid start-heap-obs)))
(is red-mthr.if.non-speculative-read - ?start-state ?start-vs)
 ⟨proof⟩

lemma *J-cut-and-update*:
assumes *wf*: wf-J-prog *P*
and *hrt*: heap-read-typeable *hconf P*
and *wf-start*: wf-start-state *P C M vs*
and *ka*: $\bigcup (ka\text{-Val } 'set\ vs) \subseteq set\ start\ addrs$
shows *red-mthr.if.cut-and-update (init-fin-lift-state status (J-start-state P C M vs))*
(mrw-values P Map.empty (map snd (lift-start-obs start-tid start-heap-obs)))
 ⟨proof⟩

lemma *J-drf*:
assumes *wf*: wf-J-prog *P*
and *hrt*: heap-read-typeable *hconf P*
and *wf-start*: wf-start-state *P C M vs*
and *ka*: $\bigcup (ka\text{-Val } 'set\ vs) \subseteq set\ start\ addrs$
shows *drf (J- \mathcal{E} P C M vs status) P*
 ⟨proof⟩

lemma *J-sc-legal*:
assumes *wf*: wf-J-prog *P*
and *hrt*: heap-read-typeable *hconf P*
and *wf-start*: wf-start-state *P C M vs*
and *ka*: $\bigcup (ka\text{-Val } 'set\ vs) \subseteq set\ start\ addrs$
shows *sc-legal (J- \mathcal{E} P C M vs status) P*
 ⟨proof⟩

lemma *J-jmm-consistent*:

assumes *wf*: *wf-J-prog P*
and *hrt*: *heap-read-typeable hconf P*
and *wf-start*: *wf-start-state P C M vs*
and *ka*: $\bigcup (ka\text{-Val } 'set\ vs) \subseteq set\ start\ addrs$
shows *jmm-consistent (J- \mathcal{E} P C M vs status) P*
(is jmm-consistent ? \mathcal{E} P)

<proof>

lemma *J-ex-sc-exec*:

assumes *wf*: *wf-J-prog P*
and *hrt*: *heap-read-typeable hconf P*
and *wf-start*: *wf-start-state P C M vs*
and *ka*: $\bigcup (ka\text{-Val } 'set\ vs) \subseteq set\ start\ addrs$
shows $\exists E\ ws. E \in J\text{-}\mathcal{E}\ P\ C\ M\ vs\ status \wedge P \vdash (E, ws) \checkmark \wedge sequentially\ consistent\ P\ (E, ws)$
(is $\exists E\ ws. - \in ?\mathcal{E} \wedge -$)

<proof>

theorem *J-consistent*:

assumes *wf*: *wf-J-prog P*
and *hrt*: *heap-read-typeable hconf P*
and *wf-start*: *wf-start-state P C M vs*
and *ka*: $\bigcup (ka\text{-Val } 'set\ vs) \subseteq set\ start\ addrs$
shows $\exists E\ ws. legal\ execution\ P\ (J\text{-}\mathcal{E}\ P\ C\ M\ vs\ status)\ (E, ws)$

<proof>

end

end

theory *JMM-JVM*

imports

JMM-Framework
../JVM/JVMThreaded

begin

sublocale *JVM-heap-base* < *execd-mthr*:

heap-multithreaded-base
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate typeof-addr heap-read heap-write
JVM-final mexecd P convert-RA

for *P*

<proof>

context *JVM-heap-base* **begin**

abbreviation *JVMd- \mathcal{E}* ::

'addr jvm-prog \Rightarrow cname \Rightarrow mname \Rightarrow 'addr val list \Rightarrow status
 \Rightarrow (*'thread-id \times ('addr, 'thread-id) obs-event action*) *llist set*

where *JVMd- \mathcal{E} P \equiv execd-mthr. \mathcal{E} -start P JVM-local-start P*

end

end

8.15 JMM Instantiation for bytecode

theory *DRF-JVM*

imports

JMM-Common

JMM-JVM

../BV/BVProgressThreaded

SC-Legal

begin

8.15.1 DRF guarantee for the JVM

abbreviation (*input*) *ka-xcp* :: 'addr option \Rightarrow 'addr set

where *ka-xcp* \equiv *set-option*

primrec *jvm-ka* :: 'addr *jvm-thread-state* \Rightarrow 'addr set

where

jvm-ka (*xcp*, *frs*) =

ka-xcp xcp \cup (\bigcup (*stk*, *loc*, *C*, *M*, *pc*) \in *set frs*. (\bigcup *v* \in *set stk*. *ka-Val v*) \cup (\bigcup *v* \in *set loc*. *ka-Val v*))

context *heap* begin

lemma *red-external-aggr-read-mem-typeable*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h; \text{ReadMem } ad \ al \ v \in \text{set } \{ta\}_o \rrbracket$

$\implies \exists T'. P, h \vdash ad@al : T'$

<proof>

end

context *JVM-heap-base* begin

definition *jvm-known-addr*s :: 'thread-id \Rightarrow 'addr *jvm-thread-state* \Rightarrow 'addr set

where *jvm-known-addr*s *t xcpfrs* = {*thread-id2addr t*} \cup *jvm-ka xcpfrs* \cup *set start-addr*s

end

context *JVM-heap* begin

lemma *exec-instr-known-addr*s:

assumes *ok*: *start-heap-ok*

and *exec*: (*ta*, *xcp'*, *h'*, *frs'*) \in *exec-instr* *i P t h stk loc C M pc frs*

and *check*: *check-instr* *i P h stk loc C M pc frs*

shows *jvm-known-addr*s *t (xcp', frs')* \subseteq *jvm-known-addr*s *t (None, (stk, loc, C, M, pc) # frs)* \cup *new-obs-addr*s $\{ta\}_o$

<proof>

lemma *exec-d-known-addr*s-*mono*:

assumes *ok*: *start-heap-ok*

and *exec*: *mexecd* *P t (xcpfrs, h) ta (xcpfrs', h')*

shows *jvm-known-addr*s *t xcpfrs'* \subseteq *jvm-known-addr*s *t xcpfrs* \cup *new-obs-addr*s $\{ta\}_o$

<proof>

lemma *exec-instr-known-addr-ReadMem*:

assumes $exec: (ta, xcp', h', frs') \in exec\text{-instr } i P t h stk loc C M pc frs$
and check: $check\text{-instr } i P h stk loc C M pc frs$
and read: $ReadMem ad al v \in set \{ta\}_o$
shows $ad \in jvm\text{-known-addr } t (None, (stk, loc, C, M, pc) \# frs)$

$\langle proof \rangle$

lemma *mexecd-known-addr-ReadMem*:

$\llbracket mexecd P t (xcpfrs, h) ta (xcpfrs', h'); ReadMem ad al v \in set \{ta\}_o \rrbracket$
 $\implies ad \in jvm\text{-known-addr } t xcpfrs$

$\langle proof \rangle$

lemma *exec-instr-known-addr-WriteMem*:

assumes $exec: (ta, xcp', h', frs') \in exec\text{-instr } i P t h stk loc C M pc frs$
and check: $check\text{-instr } i P h stk loc C M pc frs$
and write: $\{ta\}_o ! n = WriteMem ad al (Addr a) n < length \{ta\}_o$
shows $a \in jvm\text{-known-addr } t (None, (stk, loc, C, M, pc) \# frs) \vee a \in new\text{-obs-addr } (take n \{ta\}_o)$

$\langle proof \rangle$

lemma *mexecd-known-addr-WriteMem*:

$\llbracket mexecd P t (xcpfrs, h) ta (xcpfrs', h'); \{ta\}_o ! n = WriteMem ad al (Addr a); n < length \{ta\}_o \rrbracket$
 $\implies a \in jvm\text{-known-addr } t xcpfrs \vee a \in new\text{-obs-addr } (take n \{ta\}_o)$

$\langle proof \rangle$

lemma *exec-instr-known-addr-new-thread*:

assumes $exec: (ta, xcp', h', frs') \in exec\text{-instr } i P t h stk loc C M pc frs$
and check: $check\text{-instr } i P h stk loc C M pc frs$
and new: $NewThread t' x' h'' \in set \{ta\}_t$
shows $jvm\text{-known-addr } t' x' \subseteq jvm\text{-known-addr } t (None, (stk, loc, C, M, pc) \# frs)$

$\langle proof \rangle$

lemma *mexecd-known-addr-new-thread*:

$\llbracket mexecd P t (xcpfrs, h) ta (xcpfrs', h'); NewThread t' x' h'' \in set \{ta\}_t \rrbracket$
 $\implies jvm\text{-known-addr } t' x' \subseteq jvm\text{-known-addr } t xcpfrs$

$\langle proof \rangle$

lemma *exec-instr-New-same-addr-same*:

$\llbracket (ta, xcp', h', frs') \in exec\text{-instr } ins P t h stk loc C M pc frs;$
 $\{ta\}_o ! i = NewHeapElem a x; i < length \{ta\}_o;$
 $\{ta\}_o ! j = NewHeapElem a x'; j < length \{ta\}_o \rrbracket$
 $\implies i = j$

$\langle proof \rangle$

lemma *exec-New-same-addr-same*:

$\llbracket (ta, xcp', h', frs') \in exec P t (xcp, h, frs);$
 $\{ta\}_o ! i = NewHeapElem a x; i < length \{ta\}_o;$
 $\{ta\}_o ! j = NewHeapElem a x'; j < length \{ta\}_o \rrbracket$
 $\implies i = j$

$\langle proof \rangle$

lemma *exec-1-d-New-same-addr-same*:

$\llbracket P, t \vdash Normal (xcp, h, frs) \text{-}ta\text{-}jvmd \rightarrow Normal (xcp', h', frs');$
 $\{ta\}_o ! i = NewHeapElem a x; i < length \{ta\}_o;$
 $\{ta\}_o ! j = NewHeapElem a x'; j < length \{ta\}_o \rrbracket$

$\implies i = j$
 $\langle \text{proof} \rangle$

end

locale *JVM-allocated-heap* = *allocated-heap* +
constrains *addr2thread-id* :: ('addr :: addr) \Rightarrow 'thread-id
and *thread-id2addr* :: 'thread-id \Rightarrow 'addr
and *spurious-wakeups* :: bool
and *empty-heap* :: 'heap
and *allocate* :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) set
and *typeof-addr* :: 'heap \Rightarrow 'addr \rightarrow htype
and *heap-read* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow bool
and *heap-write* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow 'heap \Rightarrow bool
and *allocated* :: 'heap \Rightarrow 'addr set
and *P* :: 'addr jvm-prog

sublocale *JVM-allocated-heap* < *JVM-heap*
 $\langle \text{proof} \rangle$

context *JVM-allocated-heap* **begin**

lemma *exec-instr-allocated-mono*:

$\llbracket (ta, xcp', h', frs') \in \text{exec-instr } i \ P \ t \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs}; \ \text{check-instr } i \ P \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs} \rrbracket$
 $\implies \text{allocated } h \subseteq \text{allocated } h'$
 $\langle \text{proof} \rangle$

lemma *mexecd-allocated-mono*:

$mexecd \ P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h') \implies \text{allocated } h \subseteq \text{allocated } h'$
 $\langle \text{proof} \rangle$

lemma *exec-instr-allocatedD*:

$\llbracket (ta, xcp', h', frs') \in \text{exec-instr } i \ P \ t \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs};$
 $\text{check-instr } i \ P \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs}; \ \text{NewHeapElem } ad \ CTn \in \text{set } \{ta\}_o \rrbracket$
 $\implies ad \in \text{allocated } h' \wedge ad \notin \text{allocated } h$
 $\langle \text{proof} \rangle$

lemma *mexecd-allocatedD*:

$\llbracket mexecd \ P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h'); \ \text{NewHeapElem } ad \ CTn \in \text{set } \{ta\}_o \rrbracket$
 $\implies ad \in \text{allocated } h' \wedge ad \notin \text{allocated } h$
 $\langle \text{proof} \rangle$

lemma *exec-instr-NewHeapElemD*:

$\llbracket (ta, xcp', h', frs') \in \text{exec-instr } i \ P \ t \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs}; \ \text{check-instr } i \ P \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs};$
 $ad \in \text{allocated } h'; \ ad \notin \text{allocated } h \rrbracket$
 $\implies \exists CTn. \ \text{NewHeapElem } ad \ CTn \in \text{set } \{ta\}_o$
 $\langle \text{proof} \rangle$

lemma *mexecd-NewHeapElemD*:

$\llbracket mexecd \ P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h'); \ ad \in \text{allocated } h'; \ ad \notin \text{allocated } h \rrbracket$
 $\implies \exists CTn. \ \text{NewHeapElem } ad \ CTn \in \text{set } \{ta\}_o$
 $\langle \text{proof} \rangle$

lemma *mexecd-allocated-multithreaded*:

allocated-multithreaded addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write allocated JVM-final (mexecd P) P
 ⟨proof⟩

end

sublocale *JVM-allocated-heap* < *execd-mthr*: *allocated-multithreaded*

addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate typeof-addr heap-read heap-write allocated
JVM-final mexecd P
P
 ⟨proof⟩

context *JVM-allocated-heap* **begin**

lemma *mexecd-known-addr*:

assumes *wf*: *wf-prog wfmd P*
and *ok*: *start-heap-ok*
shows *known-addr addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write allocated jvm-known-addr JVM-final (mexecd P) P*
 ⟨proof⟩

end

context *JVM-heap* **begin**

lemma *exec-instr-read-typeable*:

assumes *exec*: $(ta, xcp', h', frs') \in \text{exec-instr } i \ P \ t \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs}$
and *check*: *check-instr i P h stk loc C M pc frs*
and *read*: *ReadMem ad al v ∈ set {ta}o*
shows $\exists T'. P, h \vdash ad@al : T'$
 ⟨proof⟩

lemma *exec-1-d-read-typeable*:

$\llbracket P, t \vdash \text{Normal } (xcp, h, frs) \text{ --ta-jvmd--} \rightarrow \text{Normal } (xcp', h', frs');$
 $\text{ReadMem } ad \ al \ v \in \text{set } \{ta\}_o \rrbracket$
 $\implies \exists T'. P, h \vdash ad@al : T'$
 ⟨proof⟩

end

sublocale *JVM-heap-base* < *execd-mthr*:

if-multithreaded
JVM-final
mexecd P
convert-RA
for *P*
 ⟨proof⟩

context *JVM-heap-conf* **begin**

lemma *JVM-conf-read-heap-read-typed*:

JVM-conf-read addr2thread-id thread-id2addr empty-heap allocate typeof-addr (heap-read-typed P) heap-write hconf P
 ⟨proof⟩

lemma *exec-instr-New-typeof-addrD*:

[[$(ta, xcp', h', frs') \in \text{exec-instr } i P t h \text{ stk loc } C M pc frs$;
 $\text{check-instr } i P h \text{ stk loc } C M pc frs; hconf h$;
 $\text{NewHeapElem } a x \in \text{set } \{ta\}_o$]]
 $\implies \text{typeof-addr } h' a = \text{Some } x$
 ⟨proof⟩

lemma *exec-1-d-New-typeof-addrD*:

[[$P, t \vdash \text{Normal } (xcp, h, frs) -ta-jvmd \rightarrow \text{Normal } (xcp', h', frs')$; $\text{NewHeapElem } a x \in \text{set } \{ta\}_o$;
 $hconf h$]]
 $\implies \text{typeof-addr } h' a = \text{Some } x$
 ⟨proof⟩

lemma *exec-instr-non-speculative-typeable*:

assumes *exec*: $(ta, xcp', h', frs') \in \text{exec-instr } i P t h \text{ stk loc } C M pc frs$
and *check*: $\text{check-instr } i P h \text{ stk loc } C M pc frs$
and *sc*: $\text{non-speculative } P \text{ vs } (\text{l-list-of } (\text{map } \text{NormalAction } \{ta\}_o))$
and *vs-conf*: $\text{vs-conf } P h \text{ vs}$
and *hconf*: $hconf h$
shows $(ta, xcp', h', frs') \in \text{JVM-heap-base.exec-instr addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-read-typed P) heap-write } i P t h \text{ stk loc } C M pc frs$
 ⟨proof⟩

lemma *exec-instr-non-speculative-vs-conf*:

assumes *exec*: $(ta, xcp', h', frs') \in \text{exec-instr } i P t h \text{ stk loc } C M pc frs$
and *check*: $\text{check-instr } i P h \text{ stk loc } C M pc frs$
and *sc*: $\text{non-speculative } P \text{ vs } (\text{l-list-of } (\text{take } n (\text{map } \text{NormalAction } \{ta\}_o)))$
and *vs-conf*: $\text{vs-conf } P h \text{ vs}$
and *hconf*: $hconf h$
shows $\text{vs-conf } P h' (\text{w-values } P \text{ vs } (\text{take } n (\text{map } \text{NormalAction } \{ta\}_o)))$
 ⟨proof⟩

lemma *mexecd-non-speculative-typeable*:

[[$P, t \vdash \text{Normal } (xcp, h, stk) -ta-jvmd \rightarrow \text{Normal } (xcp', h', frs')$; $\text{non-speculative } P \text{ vs } (\text{l-list-of } (\text{map } \text{NormalAction } \{ta\}_o))$;
 $\text{vs-conf } P h \text{ vs}; hconf h$]]
 $\implies \text{JVM-heap-base.exec-1-d addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-read-typed P) heap-write } P t (\text{Normal } (xcp, h, stk)) ta (\text{Normal } (xcp', h', frs'))$
 ⟨proof⟩

lemma *mexecd-non-speculative-vs-conf*:

[[$P, t \vdash \text{Normal } (xcp, h, stk) -ta-jvmd \rightarrow \text{Normal } (xcp', h', frs')$;
 $\text{non-speculative } P \text{ vs } (\text{l-list-of } (\text{take } n (\text{map } \text{NormalAction } \{ta\}_o)))$;
 $\text{vs-conf } P h \text{ vs}; hconf h$]]
 $\implies \text{vs-conf } P h' (\text{w-values } P \text{ vs } (\text{take } n (\text{map } \text{NormalAction } \{ta\}_o)))$
 ⟨proof⟩

end

```

locale JVM-allocated-heap-conf =
  JVM-heap-conf
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write hconf
  P
+
  JVM-allocated-heap
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  allocated
  P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and allocated :: 'heap ⇒ 'addr set
and P :: 'addr jvm-prog
begin

lemma mexecd-known-addr-typing:
  assumes wf: wf-jvm-progΦ P
  and ok: start-heap-ok
  shows known-addr-typing addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write
allocated jvm-known-addr JVM-final (mexecd P) (λt (xcp, frstls) h. Φ ⊢ t: (xcp, h, frstls) √) P
  ⟨proof⟩

lemma executions-sc:
  assumes wf: wf-jvm-progΦ P
  and wf-start: wf-start-state P C M vs
  and vs2: ⋃ (ka-Val ' set vs) ⊆ set start-addr
  shows executions-sc-hb (JVMd- $\mathcal{E}$  P C M vs status) P
  (is executions-sc-hb ?E P)
  ⟨proof⟩

end

declare split-paired-Ex [simp del]
declare eq-upto-seq-inconsist-simps [simp]

context JVM-progress begin

abbreviation (input) jvm-non-speculative-read-bound :: nat where
  jvm-non-speculative-read-bound ≡ 2

lemma exec-instr-non-speculative-read:
  assumes hrt: heap-read-typeable hconf P
  and vs: vs-conf P (shr s) vs

```

and *hconf*: *hconf* (*shr s*)
and *exec-i*: $(ta, xcp', h', frs') \in \text{exec-instr } i \ P \ t \ (shr \ s) \ stk \ loc \ C \ M \ pc \ frs$
and *check*: *check-instr* *i P* (*shr s*) *stk loc C M pc frs*
and *aok*: *execd-mthr.mthr.if.actions-ok s t ta*
and *i*: $I < \text{length } \{ta\}_o$
and *read*: $\{ta\}_o ! I = \text{ReadMem } a'' \ al'' \ v$
and *v'*: $v' \in w\text{-values } P \ vs \ (\text{map } \text{NormalAction} \ (\text{take } I \ \{ta\}_o)) \ (a'', \ al'')$
and *ns*: *non-speculative P vs (llist-of (map NormalAction (take I {ta}_o)))*
shows $\exists ta' \ xcp'' \ h'' \ frs'' . (ta', xcp'', h'', frs'') \in \text{exec-instr } i \ P \ t \ (shr \ s) \ stk \ loc \ C \ M \ pc \ frs \wedge$
 $\text{execd-mthr.mthr.if.actions-ok } s \ t \ ta' \wedge$
 $I < \text{length } \{ta'\}_o \wedge \text{take } I \ \{ta'\}_o = \text{take } I \ \{ta\}_o \wedge$
 $\{ta'\}_o ! I = \text{ReadMem } a'' \ al'' \ v' \wedge$
 $\text{length } \{ta'\}_o \leq \text{max jvm-non-speculative-read-bound } (\text{length } \{ta\}_o)$
 <proof>

lemma *exec-1-d-non-speculative-read*:

assumes *hrt*: *heap-read-typeable hconf P*
and *vs*: *vs-conf P* (*shr s*) *vs*
and *exec*: $P, t \vdash \text{Normal} \ (xcp, shr \ s, frs) \ -ta\text{-jvmd} \rightarrow \text{Normal} \ (xcp', h', frs')$
and *aok*: *execd-mthr.mthr.if.actions-ok s t ta*
and *hconf*: *hconf* (*shr s*)
and *i*: $I < \text{length } \{ta\}_o$
and *read*: $\{ta\}_o ! I = \text{ReadMem } a'' \ al'' \ v$
and *v'*: $v' \in w\text{-values } P \ vs \ (\text{map } \text{NormalAction} \ (\text{take } I \ \{ta\}_o)) \ (a'', \ al'')$
and *ns*: *non-speculative P vs (llist-of (map NormalAction (take I {ta}_o)))*
shows $\exists ta' \ xcp'' \ h'' \ frs'' . P, t \vdash \text{Normal} \ (xcp, shr \ s, frs) \ -ta'\text{-jvmd} \rightarrow \text{Normal} \ (xcp'', h'', frs'') \wedge$
 $\text{execd-mthr.mthr.if.actions-ok } s \ t \ ta' \wedge$
 $I < \text{length } \{ta'\}_o \wedge \text{take } I \ \{ta'\}_o = \text{take } I \ \{ta\}_o \wedge$
 $\{ta'\}_o ! I = \text{ReadMem } a'' \ al'' \ v' \wedge$
 $\text{length } \{ta'\}_o \leq \text{max jvm-non-speculative-read-bound } (\text{length } \{ta\}_o)$
 <proof>

end

declare *split-paired-Ex* [*simp*]

declare *eq-upto-seq-inconsist-simps* [*simp del*]

locale *JVM-allocated-progress* =

JVM-progress

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate typeof-addr heap-read heap-write hconf

P

+

JVM-allocated-heap-conf

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate typeof-addr heap-read heap-write hconf

allocated

P

for *addr2thread-id* :: $('addr :: \text{addr}) \Rightarrow 'thread\text{-id}$

and *thread-id2addr* :: $'thread\text{-id} \Rightarrow 'addr$

and *spurious-wakeups* :: *bool*

and *empty-heap* :: *'heap*

```

and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and allocated :: 'heap  $\Rightarrow$  'addr set
and P :: 'addr jvm-prog
begin

```

lemma *non-speculative-read*:

```

assumes wf: wf-jvm-prog $\Phi$  P
and hrt: heap-read-typeable hconf P
and wf-start: wf-start-state P C M vs
and ka:  $\bigcup$  (ka-Val ' set vs)  $\subseteq$  set start-addr
shows execd-mthr.if.non-speculative-read jvm-non-speculative-read-bound
  (init-fin-lift-state status (JVM-start-state P C M vs))
  (w-values P ( $\lambda$ -. {})) (map snd (lift-start-obs start-tid start-heap-obs)))
(is execd-mthr.if.non-speculative-read - ?start-state ?start-vs)
<proof>

```

lemma *JVM-cut-and-update*:

```

assumes wf: wf-jvm-prog $\Phi$  P
and hrt: heap-read-typeable hconf P
and wf-start: wf-start-state P C M vs
and ka:  $\bigcup$  (ka-Val ' set vs)  $\subseteq$  set start-addr
shows execd-mthr.if.cut-and-update (init-fin-lift-state status (JVM-start-state P C M vs))
  (mrw-values P Map.empty (map snd (lift-start-obs start-tid start-heap-obs)))
<proof>

```

lemma *JVM-drf*:

```

assumes wf: wf-jvm-prog $\Phi$  P
and hrt: heap-read-typeable hconf P
and wf-start: wf-start-state P C M vs
and ka:  $\bigcup$  (ka-Val ' set vs)  $\subseteq$  set start-addr
shows drf (JVMd- $\mathcal{E}$  P C M vs status) P
<proof>

```

lemma *JVM-sc-legal*:

```

assumes wf: wf-jvm-prog $\Phi$  P
and hrt: heap-read-typeable hconf P
and wf-start: wf-start-state P C M vs
and ka:  $\bigcup$  (ka-Val ' set vs)  $\subseteq$  set start-addr
shows sc-legal (JVMd- $\mathcal{E}$  P C M vs status) P
<proof>

```

lemma *JVM-jmm-consistent*:

```

assumes wf: wf-jvm-prog $\Phi$  P
and hrt: heap-read-typeable hconf P
and wf-start: wf-start-state P C M vs
and ka:  $\bigcup$  (ka-Val ' set vs)  $\subseteq$  set start-addr
shows jmm-consistent (JVMd- $\mathcal{E}$  P C M vs status) P
  (is jmm-consistent ? $\mathcal{E}$  P)
<proof>

```

lemma *JVM-ex-sc-exec*:

assumes *wf*: *wf-jvm-prog*_Φ *P*
and *hrt*: *heap-read-typeable hconf P*
and *wf-start*: *wf-start-state P C M vs*
and *ka*: $\bigcup (ka\text{-Val } 'set\ vs) \subseteq set\ start\ addrs$
shows $\exists E\ ws. E \in JVMd\text{-}\mathcal{E}\ P\ C\ M\ vs\ status \wedge P \vdash (E, ws) \surd \wedge sequentially\text{-consistent}\ P\ (E, ws)$
(is $\exists E\ ws. - \in ?\mathcal{E} \wedge -)$

<proof>

theorem *JVM-consistent*:

assumes *wf*: *wf-jvm-prog*_Φ *P*
and *hrt*: *heap-read-typeable hconf P*
and *wf-start*: *wf-start-state P C M vs*
and *ka*: $\bigcup (ka\text{-Val } 'set\ vs) \subseteq set\ start\ addrs$
shows $\exists E\ ws. legal\text{-execution}\ P\ (JVMd\text{-}\mathcal{E}\ P\ C\ M\ vs\ status)\ (E, ws)$

<proof>

end

One could now also prove that the aggressive JVM satisfies *drf*. The key would be that *welldtyped-commute* also holds for *non-speculative* prefixes from start.

end

8.16 JMM heap implementation 1

theory *JMM-Type*

imports

../Common/ExternalCallWF
../Common/ConformThreaded
JMM-Heap

begin

8.16.1 Definitions

The JMM heap only stores type information.

type-synonym *'addr JMM-heap* = *'addr* \rightarrow *htype*

translations (*type*) *'addr JMM-heap* \leq (*type*) *'addr* \Rightarrow *htype option*

abbreviation *jmm-empty* :: *'addr JMM-heap* **where** *jmm-empty* == *Map.empty*

definition *jmm-allocate* :: *'addr JMM-heap* \Rightarrow *htype* \Rightarrow (*'addr JMM-heap* \times *'addr*) *set*
where *jmm-allocate* *h hT* = ($\lambda a. (h(a \mapsto hT), a)$) ' {*a. h a* = *None*}

definition *jmm-typeof-addr* :: *'addr JMM-heap* \Rightarrow *'addr* \rightarrow *htype*

where *jmm-typeof-addr* *h* = *h*

definition *jmm-heap-read* :: *'addr JMM-heap* \Rightarrow *'addr* \Rightarrow *addr-loc* \Rightarrow *'addr val* \Rightarrow *bool*

where *jmm-heap-read* *h a ad v* = *True*

context

notes [*inductive-internals*]

begin

inductive *jmm-heap-write* :: 'addr JMM-heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow 'addr JMM-heap \Rightarrow bool

where *jmm-heap-write* *h a ad v h*

end

definition *jmm-hconf* :: 'm prog \Rightarrow 'addr JMM-heap \Rightarrow bool ($\langle \cdot \vdash_{jmm} \cdot \sqrt{\cdot} \rangle$ [51,51] 50)

where $P \vdash_{jmm} h \sqrt{\cdot} \iff ty\text{-of}\text{-h}\text{type } \text{'ran } h \subseteq \{T. \text{is-type } P\ T\}$

definition *jmm-allocated* :: 'addr JMM-heap \Rightarrow 'addr set

where *jmm-allocated* *h* = dom (*jmm-typeof-addr* *h*)

definition *jmm-spurious-wakeups* :: bool

where *jmm-spurious-wakeups* = True

lemmas *jmm-heap-ops-defs* =

jmm-allocate-def jmm-typeof-addr-def

jmm-heap-read-def jmm-heap-write-def

jmm-allocated-def jmm-spurious-wakeups-def

type-synonym 'addr thread-id = 'addr

abbreviation (*input*) *addr2thread-id* :: 'addr \Rightarrow 'addr thread-id

where *addr2thread-id* $\equiv \lambda x. x$

abbreviation (*input*) *thread-id2addr* :: 'addr thread-id \Rightarrow 'addr

where *thread-id2addr* $\equiv \lambda x. x$

interpretation *jmm*: heap-base

addr2thread-id thread-id2addr

jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-read jmm-heap-write

$\langle \text{proof} \rangle$

notation *jmm.heap* ($\langle \cdot \triangleleft_{jmm} \cdot \rangle$ [51,51] 50)

notation *jmm.conf* ($\langle \cdot, \cdot \vdash_{jmm} \cdot \text{:} \leq \cdot \rangle$ [51,51,51,51] 50)

notation *jmm.addr-loc-type* ($\langle \cdot, \cdot \vdash_{jmm} \cdot \text{-}@ \cdot \text{:} \rightarrow [50, 50, 50, 50, 50] 51 \rangle$)

notation *jmm.conf*s ($\langle \cdot, \cdot \vdash_{jmm} \cdot \text{[:} \leq \cdot \rangle$ [51,51,51,51] 50)

notation *jmm.tconf* ($\langle \cdot, \cdot \vdash_{jmm} \cdot \sqrt{\cdot} \rangle$ [51,51,51] 50)

Now a variation of the JMM with a different read operation that permits to read only type-conformant values

interpretation *jmm'*: heap-base

addr2thread-id thread-id2addr

jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write

for *P* $\langle \text{proof} \rangle$

notation *jmm'.heap* ($\langle \cdot \triangleleft_{jmm''} \cdot \rangle$ [51,51] 50)

notation *jmm'.conf* ($\langle \cdot, \cdot \vdash_{jmm''} \cdot \text{:} \leq \cdot \rangle$ [51,51,51,51] 50)

notation *jmm'.addr-loc-type* ($\langle \cdot, \cdot \vdash_{jmm''} \cdot \text{-}@ \cdot \text{:} \rightarrow [50, 50, 50, 50, 50] 51 \rangle$)

notation *jmm'.conf*s ($\langle \cdot, \cdot \vdash_{jmm''} \cdot \text{[:} \leq \cdot \rangle$ [51,51,51,51] 50)

notation *jmm'.tconf* ($\langle \cdot, \cdot \vdash_{jmm''} \cdot \sqrt{\cdot} \rangle$ [51,51,51] 50)

8.16.2 Heap locale interpretations

8.16.3 Locale *heap*

lemma *jmm-heap*: *heap addr2thread-id thread-id2addr jmm-allocate jmm-typeof-addr jmm-heap-write*
P
 ⟨*proof*⟩

interpretation *jmm*: *heap*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-read jmm-heap-write
P
for *P*
 ⟨*proof*⟩

declare *jmm.typeof-addr-thread-id2-addr-addr2thread-id* [*simp del*]

lemmas *jmm'-heap = jmm-heap*

interpretation *jmm'*: *heap*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write
P
for *P*
 ⟨*proof*⟩

declare *jmm'.typeof-addr-thread-id2-addr-addr2thread-id* [*simp del*]

8.16.4 Locale *heap-conf*

interpretation *jmm*: *heap-conf-base*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-read jmm-heap-write jmm-hconf P
P
for *P* ⟨*proof*⟩

abbreviation (*input*) *jmm'-hconf* :: '*m prog* ⇒ '*addr JMM-heap* ⇒ *bool* (↪ ⊢ *jmm''* - √) [51,51] 50)
where *jmm'-hconf* == *jmm-hconf*

interpretation *jmm'*: *heap-conf-base*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write jmm'-hconf P
P
for *P* ⟨*proof*⟩

abbreviation *jmm-heap-read-typeable* :: ('*addr* :: *addr*) *itself* ⇒ '*m prog* ⇒ *bool*
where *jmm-heap-read-typeable tytok P* ≡ *jmm.heap-read-typeable* (*jmm-hconf P* :: '*addr JMM-heap* ⇒ *bool*) *P*

abbreviation *jmm'-heap-read-typeable* :: ('*addr* :: *addr*) *itself* ⇒ '*m prog* ⇒ *bool*
where *jmm'-heap-read-typeable tytok P* ≡ *jmm'.heap-read-typeable* *TYPE('m) P* (*jmm-hconf P* :: '*addr*

$JMM\text{-heap} \Rightarrow \text{bool}) P$

lemma *jmm-heap-read-typeable*: *jmm-heap-read-typeable* *tytok* *P*
 ⟨*proof*⟩

lemma *jmm'-heap-read-typeable*: *jmm'-heap-read-typeable* *tytok* *P*
 ⟨*proof*⟩

lemma *jmm-heap-conf*:
heap-conf *addr2thread-id* *thread-id2addr* *jmm-empty* *jmm-allocate* *jmm-typeof-addr* *jmm-heap-write*
 (*jmm-hconf* *P*) *P*
 ⟨*proof*⟩

interpretation *jmm*: *heap-conf*
addr2thread-id *thread-id2addr*
jmm-spurious-wakeups
jmm-empty *jmm-allocate* *jmm-typeof-addr* *jmm-heap-read* *jmm-heap-write* *jmm-hconf* *P*
P
for *P*
 ⟨*proof*⟩

lemmas *jmm'-heap-conf* = *jmm-heap-conf*

interpretation *jmm'*: *heap-conf*
addr2thread-id *thread-id2addr*
jmm-spurious-wakeups
jmm-empty *jmm-allocate* *jmm-typeof-addr* *jmm.heap-read-typed* *P* *jmm-heap-write* *jmm'-hconf* *P*
P
for *P*
 ⟨*proof*⟩

8.16.5 Locale *heap-progress*

lemma *jmm-heap-progress*:
heap-progress *addr2thread-id* *thread-id2addr* *jmm-empty* *jmm-allocate* *jmm-typeof-addr* *jmm-heap-read*
jmm-heap-write (*jmm-hconf* *P*) *P*
 ⟨*proof*⟩

interpretation *jmm*: *heap-progress*
addr2thread-id *thread-id2addr*
jmm-spurious-wakeups
jmm-empty *jmm-allocate* *jmm-typeof-addr* *jmm-heap-read* *jmm-heap-write* *jmm-hconf* *P*
P
for *P*
 ⟨*proof*⟩

lemma *jmm'-heap-progress*:
heap-progress *addr2thread-id* *thread-id2addr* *jmm-empty* *jmm-allocate* *jmm-typeof-addr* (*jmm.heap-read-typed*
P) *jmm-heap-write* (*jmm'-hconf* *P*) *P*
 ⟨*proof*⟩

interpretation *jmm'*: *heap-progress*
addr2thread-id *thread-id2addr*
jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write jmm'-hconf P
P
for P
 ⟨proof⟩

8.16.6 Locale *heap-conf-read*

lemma *jmm'-heap-conf-read*:
heap-conf-read addr2thread-id thread-id2addr jmm-empty jmm-allocate jmm-typeof-addr (jmm.heap-read-typed
P) jmm-heap-write (jmm'-hconf P) P
 ⟨proof⟩

interpretation *jmm': heap-conf-read*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write jmm'-hconf P
P
for P
 ⟨proof⟩

interpretation *jmm': heap-typesafe*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write jmm'-hconf P
P
for P
 ⟨proof⟩

8.16.7 Locale *allocated-heap*

lemma *jmm-allocated-heap*:
allocated-heap addr2thread-id thread-id2addr jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-write
jmm-allocated P
 ⟨proof⟩

interpretation *jmm: allocated-heap*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-read jmm-heap-write
jmm-allocated
P
for P
 ⟨proof⟩

lemmas *jmm'-allocated-heap = jmm-allocated-heap*

interpretation *jmm': allocated-heap*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write
jmm-allocated
P
for P
 ⟨proof⟩

8.16.8 Syntax translations

notation $jmm'.external-WT'$ ($\langle -, - \vdash jmm'' (-\cdot-)'(-) \rangle : \rightarrow [50, 0, 0, 0, 50]$ 60)

abbreviation $jmm'.red-external$::

$'m \text{ prog} \Rightarrow 'addr \text{ thread-id} \Rightarrow 'addr \text{ JMM-heap} \Rightarrow 'addr \Rightarrow mname \Rightarrow 'addr \text{ val list}$
 $\Rightarrow ('addr :: addr, 'addr \text{ thread-id}, 'addr \text{ JMM-heap}) \text{ external-thread-action}$
 $\Rightarrow 'addr \text{ extCallRet} \Rightarrow 'addr \text{ JMM-heap} \Rightarrow bool$

where $jmm'.red-external P \equiv jmm'.red-external (TYPE('m)) P P$

abbreviation $jmm'.red-external-syntax$::

$'m \text{ prog} \Rightarrow 'addr \text{ thread-id} \Rightarrow 'addr \Rightarrow mname \Rightarrow 'addr \text{ val list} \Rightarrow 'addr \text{ JMM-heap}$
 $\Rightarrow ('addr :: addr, 'addr \text{ thread-id}, 'addr \text{ JMM-heap}) \text{ external-thread-action}$
 $\Rightarrow 'addr \text{ extCallRet} \Rightarrow 'addr \text{ JMM-heap} \Rightarrow bool$

$(\langle -, - \vdash jmm'' (\langle (-\cdot-)'(-), /- \rangle) \dashrightarrow \text{ext} (\langle (-), /(-) \rangle) \rangle [50, 0, 0, 0, 0, 0, 0, 0, 0] 51)$

where

$P, t \vdash jmm' \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext} \langle va, h' \rangle \equiv jmm'.red-external P t h a M vs ta va h'$

abbreviation $jmm'.red-external-aggr$::

$'m \text{ prog} \Rightarrow 'addr \text{ thread-id} \Rightarrow 'addr \Rightarrow mname \Rightarrow 'addr \text{ val list} \Rightarrow 'addr \text{ JMM-heap}$
 $\Rightarrow (('addr :: addr, 'addr \text{ thread-id}, 'addr \text{ JMM-heap}) \text{ external-thread-action} \times 'addr \text{ extCallRet} \times$
 $'addr \text{ JMM-heap}) \text{ set}$

where $jmm'.red-external-aggr P \equiv jmm'.red-external-aggr TYPE('m) P P$

abbreviation $jmm'.heap-copy-loc$::

$'m \text{ prog} \Rightarrow 'addr \Rightarrow 'addr \Rightarrow addr\text{-loc} \Rightarrow 'addr \text{ JMM-heap}$
 $\Rightarrow ('addr :: addr, 'addr \text{ thread-id}) \text{ obs-event list} \Rightarrow 'addr \text{ JMM-heap} \Rightarrow bool$

where $jmm'.heap-copy-loc \equiv jmm'.heap-copy-loc TYPE('m)$

abbreviation $jmm'.heap-copies$::

$'m \text{ prog} \Rightarrow 'addr \Rightarrow 'addr \Rightarrow addr\text{-loc list} \Rightarrow 'addr \text{ JMM-heap}$
 $\Rightarrow ('addr :: addr, 'addr \text{ thread-id}) \text{ obs-event list} \Rightarrow 'addr \text{ JMM-heap} \Rightarrow bool$

where $jmm'.heap-copies \equiv jmm'.heap-copies TYPE('m)$

abbreviation $jmm'.heap-clone$::

$'m \text{ prog} \Rightarrow 'addr \text{ JMM-heap} \Rightarrow 'addr \Rightarrow 'addr \text{ JMM-heap}$
 $\Rightarrow (('addr :: addr, 'addr \text{ thread-id}) \text{ obs-event list} \times 'addr) \text{ option} \Rightarrow bool$

where $jmm'.heap-clone P \equiv jmm'.heap-clone TYPE('m) P P$

end

8.17 Compiler correctness for the JMM

theory $JMM\text{-Compiler}$ imports

$JMM\text{-J}$

$JMM\text{-JVM}$

$\dots/Compiler/Correctness$

$\dots/Framework/FWBisimLift$

begin

lemma $action\text{-loc-aux-comp}P$ [$simp$]: $action\text{-loc-aux} (compP f P) = action\text{-loc-aux} P$
 $\langle proof \rangle$

lemma $action\text{-loc-comp}P$: $action\text{-loc} (compP f P) = action\text{-loc} P$

$\langle \text{proof} \rangle$

lemma *is-volatile-compP* [simp]: *is-volatile* (compP f P) = *is-volatile* P
 $\langle \text{proof} \rangle$

lemma *saction-compP* [simp]: *saction* (compP f P) = *saction* P
 $\langle \text{proof} \rangle$

lemma *sactions-compP* [simp]: *sactions* (compP f P) = *sactions* P
 $\langle \text{proof} \rangle$

lemma *addr-locs-compP* [simp]: *addr-locs* (compP f P) = *addr-locs* P
 $\langle \text{proof} \rangle$

lemma *synchronizes-with-compP* [simp]: *synchronizes-with* (compP f P) = *synchronizes-with* P
 $\langle \text{proof} \rangle$

lemma *sync-order-compP* [simp]: *sync-order* (compP f P) = *sync-order* P
 $\langle \text{proof} \rangle$

lemma *sync-with-compP* [simp]: *sync-with* (compP f P) = *sync-with* P
 $\langle \text{proof} \rangle$

lemma *po-sw-compP* [simp]: *po-sw* (compP f P) = *po-sw* P
 $\langle \text{proof} \rangle$

lemma *happens-before-compP*: *happens-before* (compP f P) = *happens-before* P
 $\langle \text{proof} \rangle$

lemma *addr-loc-default-compP* [simp]: *addr-loc-default* (compP f P) = *addr-loc-default* P
 $\langle \text{proof} \rangle$

lemma *value-written-aux-compP* [simp]: *value-written-aux* (compP f P) = *value-written-aux* P
 $\langle \text{proof} \rangle$

lemma *value-written-compP* [simp]: *value-written* (compP f P) = *value-written* P
 $\langle \text{proof} \rangle$

lemma *is-write-seen-compP* [simp]: *is-write-seen* (compP f P) = *is-write-seen* P
 $\langle \text{proof} \rangle$

lemma *justification-well-formed-compP* [simp]:
justification-well-formed (compP f P) = *justification-well-formed* P
 $\langle \text{proof} \rangle$

lemma *happens-before-committed-compP* [simp]:
happens-before-committed (compP f P) = *happens-before-committed* P
 $\langle \text{proof} \rangle$

lemma *happens-before-committed-weak-compP* [simp]:
happens-before-committed-weak (compP f P) = *happens-before-committed-weak* P
 $\langle \text{proof} \rangle$

lemma *sync-order-committed-compP* [simp]:

sync-order-committed (*compP f P*) = *sync-order-committed P*
 ⟨*proof*⟩

lemma *value-written-committed-compP* [*simp*]:
value-written-committed (*compP f P*) = *value-written-committed P*
 ⟨*proof*⟩

lemma *uncommitted-reads-see-hb-compP* [*simp*]:
uncommitted-reads-see-hb (*compP f P*) = *uncommitted-reads-see-hb P*
 ⟨*proof*⟩

lemma *external-actions-committed-compP* [*simp*]:
external-actions-committed (*compP f P*) = *external-actions-committed P*
 ⟨*proof*⟩

lemma *is-justified-by-compP* [*simp*]: *is-justified-by* (*compP f P*) = *is-justified-by P*
 ⟨*proof*⟩

lemma *is-weakly-justified-by-compP* [*simp*]: *is-weakly-justified-by* (*compP f P*) = *is-weakly-justified-by P*
 ⟨*proof*⟩

lemma *legal-execution-compP*: *legal-execution* (*compP f P*) = *legal-execution P*
 ⟨*proof*⟩

lemma *weakly-legal-execution-compP*: *weakly-legal-execution* (*compP f P*) = *weakly-legal-execution P*
 ⟨*proof*⟩

lemma *most-recent-write-for-compP* [*simp*]:
most-recent-write-for (*compP f P*) = *most-recent-write-for P*
 ⟨*proof*⟩

lemma *sequentially-consistent-compP* [*simp*]:
sequentially-consistent (*compP f P*) = *sequentially-consistent P*
 ⟨*proof*⟩

lemma *conflict-compP* [*simp*]: *non-volatile-conflict* (*compP f P*) = *non-volatile-conflict P*
 ⟨*proof*⟩

lemma *correctly-synchronized-compP* [*simp*]:
correctly-synchronized (*compP f P*) = *correctly-synchronized P*
 ⟨*proof*⟩

lemma (**in** *heap-base*) *heap-read-typed-compP* [*simp*]:
heap-read-typed (*compP f P*) = *heap-read-typed P*
 ⟨*proof*⟩

context *J-JVM-heap-conf-base* **begin**

definition *if-bisimJ2JVM* ::

((*'addr, 'thread-id, status × 'addr expr × 'addr locals, 'heap, 'addr*) *state*,
 (*'addr, 'thread-id, status × 'addr option × 'addr frame list, 'heap, 'addr*) *state*) *bisim*

where

if-bisimJ2JVM =

$FWbisimulation\text{-}base.mbisim\ red\text{-}red0.init\text{-}fin\text{-}bisim\ red\text{-}red0.init\text{-}fin\text{-}bisim\text{-}wait \circ_B$
 $FWbisimulation\text{-}base.mbisim\ red0\text{-}Red1'.init\text{-}fin\text{-}bisim\ red0\text{-}Red1'.init\text{-}fin\text{-}bisim\text{-}wait \circ_B$
 $if\text{-}mbisim\text{-}Red1'\text{-}Red1 \circ_B$
 $FWbisimulation\text{-}base.mbisim\ Red1\text{-}execd.init\text{-}fin\text{-}bisim\ Red1\text{-}execd.init\text{-}fin\text{-}bisim\text{-}wait$

definition $if\text{-}tlsimJ2JVM ::$

$(\text{'thread-id} \times (\text{'addr}, \text{'thread-id}, \text{status} \times \text{'addr expr} \times \text{'addr locals},$
 $\quad \text{'heap}, \text{'addr}, (\text{'addr}, \text{'thread-id}) \text{obs-event action}) \text{thread-action},$
 $\quad \text{'thread-id} \times (\text{'addr}, \text{'thread-id}, \text{status} \times \text{'addr jvm-thread-state},$
 $\quad \text{'heap}, \text{'addr}, (\text{'addr}, \text{'thread-id}) \text{obs-event action}) \text{thread-action}) \text{bisim}$

where

$if\text{-}tlsimJ2JVM =$
 $FWbisimulation\text{-}base.mta\text{-}bisim\ red\text{-}red0.init\text{-}fin\text{-}bisim \circ_B$
 $FWbisimulation\text{-}base.mta\text{-}bisim\ red0\text{-}Red1'.init\text{-}fin\text{-}bisim \circ_B (=) \circ_B$
 $FWbisimulation\text{-}base.mta\text{-}bisim\ Red1\text{-}execd.init\text{-}fin\text{-}bisim$

end

sublocale $J\text{-}JVM\text{-}conf\text{-}read < red\text{-}mthr: if\text{-}\tau\text{multithreaded}\text{-}wf\ final\text{-}expr\ mred\ P\ convert\text{-}RA\ \tau\text{MOVE}$
 P

$\langle proof \rangle$

sublocale $J\text{-}JVM\text{-}conf\text{-}read < execd\text{-}mthr:$

$if\text{-}\tau\text{multithreaded}\text{-}wf$
 $JVM\text{-}final$
 $mexecd\ (compP2\ (compP1\ P))$
 $convert\text{-}RA$
 $\tau\text{MOVE2}\ (compP2\ (compP1\ P))$

$\langle proof \rangle$

context $J\text{-}JVM\text{-}conf\text{-}read$ **begin**

theorem $if\text{-}bisimJ2JVM\text{-}weak\text{-}bisim:$

assumes $wf: wf\text{-}J\text{-}prog\ P$

shows $delay\text{-}bisimulation\text{-}diverge\text{-}final$

$(red\text{-}mthr.mthr.if.redT\ P)\ (execd\text{-}mthr.mthr.if.redT\ (J2JVM\ P))\ if\text{-}bisimJ2JVM\ if\text{-}tlsimJ2JVM$
 $red\text{-}mthr.if.m\tau\text{move}\ execd\text{-}mthr.if.m\tau\text{move}\ red\text{-}mthr.mthr.if.mfinal\ execd\text{-}mthr.mthr.if.mfinal$

$\langle proof \rangle$

lemma $if\text{-}bisimJ2JVM\text{-}start:$

assumes $wf: wf\text{-}J\text{-}prog\ P$

and $wf\text{-}start: wf\text{-}start\text{-}state\ P\ C\ M\ vs$

shows $if\text{-}bisimJ2JVM\ (init\text{-}fin\text{-}lift\text{-}state\ Running\ (J\text{-}start\text{-}state\ P\ C\ M\ vs))$

$(init\text{-}fin\text{-}lift\text{-}state\ Running\ (JVM\text{-}start\text{-}state\ (J2JVM\ P)\ C\ M\ vs))$

$\langle proof \rangle$

lemma $red\text{-}Runs\text{-}eq\text{-}mexecd\text{-}Runs:$

fixes $C\ M\ vs$

defines $s: s \equiv init\text{-}fin\text{-}lift\text{-}state\ Running\ (J\text{-}start\text{-}state\ P\ C\ M\ vs)$

and $comps: cs \equiv init\text{-}fin\text{-}lift\text{-}state\ Running\ (JVM\text{-}start\text{-}state\ (J2JVM\ P)\ C\ M\ vs)$

assumes $wf: wf\text{-}J\text{-}prog\ P$

and $wf\text{-}start: wf\text{-}start\text{-}state\ P\ C\ M\ vs$

shows $red\text{-}mthr.mthr.if.\mathcal{E}\ P\ s = execd\text{-}mthr.mthr.if.\mathcal{E}\ (J2JVM\ P)\ cs$

$\langle proof \rangle$

lemma *red- \mathcal{E} -eq-mexecd- \mathcal{E}* :

[[*wf-J-prog* P ; *wf-start-state* P C M *vs*]]
 \implies *J- \mathcal{E}* P C M *vs* *Running* = *JVMd- \mathcal{E}* (*J2JVM* P) C M *vs* *Running*
 <proof>

theorem *J2JVM-jmm-correct*:

assumes *wf*: *wf-J-prog* P
and *wf-start*: *wf-start-state* P C M *vs*
shows *legal-execution* P (*J- \mathcal{E}* P C M *vs* *Running*) (E , ws) \longleftrightarrow
legal-execution (*J2JVM* P) (*JVMd- \mathcal{E}* (*J2JVM* P) C M *vs* *Running*) (E , ws)
 <proof>

theorem *J2JVM-jmm-correct-weak*:

assumes *wf*: *wf-J-prog* P
and *wf-start*: *wf-start-state* P C M *vs*
shows *weakly-legal-execution* P (*J- \mathcal{E}* P C M *vs* *Running*) (E , ws) \longleftrightarrow
weakly-legal-execution (*J2JVM* P) (*JVMd- \mathcal{E}* (*J2JVM* P) C M *vs* *Running*) (E , ws)
 <proof>

theorem *J2JVM-jmm-correctly-synchronized*:

assumes *wf*: *wf-J-prog* P
and *wf-start*: *wf-start-state* P C M *vs*
shows *correctly-synchronized* (*J2JVM* P) (*JVMd- \mathcal{E}* (*J2JVM* P) C M *vs* *Running*) \longleftrightarrow
correctly-synchronized P (*J- \mathcal{E}* P C M *vs* *Running*)
 <proof>

end

end

8.18 JMM heap implementation 2

theory *JMM-Type2*

imports

../Common/ExternalCallWF
 ../Common/ConformThreaded
 JMM-Heap

begin

8.18.1 Definitions

datatype *addr* = *Address* *htype* *nat* — heap type and sequence number

lemma *rec-addr-conv-case-addr* [*simp*]: *rec-addr* = *case-addr*
 <proof>

instantiation *addr* :: *addr* **begin**

definition *hash-addr* (a :: *addr*) = (*case* a of *Address* ht n \Rightarrow *int* n)

definition *monitor-finfun-to-list* (ls :: *addr* \Rightarrow *f* *nat*) = (*SOME* xs . *set* xs = { x . *finfun-dom* ls \$ x })

instance

<proof>

end

primrec *the-Address* :: *addr* \Rightarrow *hType* \times *nat*
where *the-Address* (*Address* *hT* *n*) = (*hT*, *n*)

The JMM heap only stores which sequence numbers of a given *hType* have already been allocated.

type-synonym *JMM-heap* = *hType* \Rightarrow *nat* *set*

translations (*type*) *JMM-heap* \leq (*type*) *hType* \Rightarrow *nat* *set*

definition *jmm-allocate* :: *JMM-heap* \Rightarrow *hType* \Rightarrow (*JMM-heap* \times *addr*) *set*
where *jmm-allocate* *h* *hT* = (let *hhT* = *h* *hT* in ($\lambda n. (h(hT := insert\ n\ hhT), Address\ hT\ n)$) ‘(-*hhT*))

abbreviation *jmm-empty* :: *JMM-heap* **where** *jmm-empty* == ($\lambda -. \{\}$)

definition *jmm-typeof-addr* :: '*m prog* \Rightarrow *JMM-heap* \Rightarrow *addr* \rightarrow *hType*
where *jmm-typeof-addr* *P* *h* = ($\lambda hT. if\ is-hType\ P\ hT\ then\ Some\ hT\ else\ None$) \circ *fst* \circ *the-Address*

definition *jmm-typeof-addr'* :: '*m prog* \Rightarrow *addr* \rightarrow *hType*
where *jmm-typeof-addr'* *P* = ($\lambda hT. if\ is-hType\ P\ hT\ then\ Some\ hT\ else\ None$) \circ *fst* \circ *the-Address*

lemma *jmm-typeof-addr'-conv-jmm-type-addr*: *jmm-typeof-addr'* *P* = *jmm-typeof-addr* *P* *h*
 $\langle proof \rangle$

lemma *jmm-typeof-addr'-conv-jmm-typeof-addr*: ($\lambda -. jmm-typeof-addr'\ P$) = *jmm-typeof-addr* *P*
 $\langle proof \rangle$

lemma *jmm-typeof-addr-conv-jmm-typeof-addr'*: *jmm-typeof-addr* = ($\lambda P -. jmm-typeof-addr'\ P$)
 $\langle proof \rangle$

definition *jmm-heap-read* :: *JMM-heap* \Rightarrow *addr* \Rightarrow *addr-loc* \Rightarrow *addr val* \Rightarrow *bool*
where *jmm-heap-read* *h* *a* *ad* *v* = *True*

context
notes [[*inductive-internals*]]
begin

inductive *jmm-heap-write* :: *JMM-heap* \Rightarrow *addr* \Rightarrow *addr-loc* \Rightarrow *addr val* \Rightarrow *JMM-heap* \Rightarrow *bool*
where *jmm-heap-write* *h* *a* *ad* *v* *h*

end

definition *jmm-hconf* :: *JMM-heap* \Rightarrow *bool*
where *jmm-hconf* *h* \longleftrightarrow *True*

definition *jmm-allocated* :: *JMM-heap* \Rightarrow *addr* *set*
where *jmm-allocated* *h* = {*Address* *CTn* *n* | *CTn* *n*. *n* \in *h* *CTn*}

definition *jmm-spurious-wakeups* :: *bool*
where *jmm-spurious-wakeups* = *True*

lemmas *jmm-heap-ops-defs* =
jmm-allocate-def *jmm-typeof-addr-def*
jmm-heap-read-def *jmm-heap-write-def*

jmm-allocated-def jmm-spurious-wakeups-def

type-synonym *thread-id* = *addr*

abbreviation (*input*) *addr2thread-id* :: *addr* ⇒ *thread-id*

where *addr2thread-id* ≡ λ*x*. *x*

abbreviation (*input*) *thread-id2addr* :: *thread-id* ⇒ *addr*

where *thread-id2addr* ≡ λ*x*. *x*

interpretation *jmm*: *heap-base*

addr2thread-id thread-id2addr

jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read jmm-heap-write

for *P*

⟨*proof*⟩

abbreviation *jmm-heap* :: '*m prog* ⇒ *JMM-heap* ⇒ *JMM-heap* ⇒ *bool* (⟨- ⊢ - ≤_{jmm} -⟩ [51,51,51] 50)

where *jmm-heap* ≡ *jmm.heap* TYPE('m)

abbreviation *jmm-conf* :: '*m prog* ⇒ *JMM-heap* ⇒ *addr val* ⇒ *ty* ⇒ *bool*

(⟨-, ⊢_{jmm} - :≤ -⟩ [51,51,51,51] 50)

where *jmm-conf* *P* ≡ *jmm.conf* TYPE('m) *P P*

abbreviation *jmm-addr-loc-type* :: '*m prog* ⇒ *JMM-heap* ⇒ *addr* ⇒ *addr-loc* ⇒ *ty* ⇒ *bool*

(⟨-, ⊢_{jmm} -@- : -⟩ [50, 50, 50, 50, 50] 51)

where *jmm-addr-loc-type* *P* ≡ *jmm.addr-loc-type* TYPE('m) *P P*

abbreviation *jmm-confs* :: '*m prog* ⇒ *JMM-heap* ⇒ *addr val list* ⇒ *ty list* ⇒ *bool*

(⟨-, ⊢_{jmm} - [:≤] -⟩ [51,51,51,51] 50)

where *jmm-confs* *P* ≡ *jmm.confs* TYPE('m) *P P*

abbreviation *jmm-tconf* :: '*m prog* ⇒ *JMM-heap* ⇒ *addr* ⇒ *bool* (⟨-, ⊢_{jmm} - √_t⟩ [51,51,51] 50)

where *jmm-tconf* *P* ≡ *jmm.tconf* TYPE('m) *P P*

interpretation *jmm*: *allocated-heap-base*

addr2thread-id thread-id2addr

jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read jmm-heap-write

jmm-allocated

for *P*

⟨*proof*⟩

Now a variation of the JMM with a different read operation that permits to read only type-conformant values

abbreviation *jmm-heap-read-typed* :: '*m prog* ⇒ *JMM-heap* ⇒ *addr* ⇒ *addr-loc* ⇒ *addr val* ⇒ *bool*

where *jmm-heap-read-typed* *P* ≡ *jmm.heap-read-typed* TYPE('m) *P P*

interpretation *jmm'*: *heap-base*

addr2thread-id thread-id2addr

jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write

for *P* ⟨*proof*⟩

abbreviation $jmm'\text{-heax} :: 'm \text{ prog} \Rightarrow \text{JMM-heap} \Rightarrow \text{JMM-heap} \Rightarrow \text{bool} (\langle \cdot \vdash - \trianglelefteq jmm'' \rangle \rightarrow [51, 51, 51] 50)$

where $jmm'\text{-heax} \equiv jmm'.\text{heax} \text{ TYPE}('m)$

abbreviation $jmm'\text{-conf} :: 'm \text{ prog} \Rightarrow \text{JMM-heap} \Rightarrow \text{addr val} \Rightarrow \text{ty} \Rightarrow \text{bool} (\langle \cdot, - \vdash jmm'' \rangle - : \leq \rightarrow [51, 51, 51, 51] 50)$

where $jmm'\text{-conf} P \equiv jmm'.\text{conf} \text{ TYPE}('m) P P$

abbreviation $jmm'\text{-addr-loc-type} :: 'm \text{ prog} \Rightarrow \text{JMM-heap} \Rightarrow \text{addr} \Rightarrow \text{addr-loc} \Rightarrow \text{ty} \Rightarrow \text{bool} (\langle \cdot, - \vdash jmm'' \rangle - @ - : \rightarrow [50, 50, 50, 50, 50] 51)$

where $jmm'\text{-addr-loc-type} P \equiv jmm'.\text{addr-loc-type} \text{ TYPE}('m) P P$

abbreviation $jmm'\text{-confs} :: 'm \text{ prog} \Rightarrow \text{JMM-heap} \Rightarrow \text{addr val list} \Rightarrow \text{ty list} \Rightarrow \text{bool} (\langle \cdot, - \vdash jmm'' \rangle - [:\leq] \rightarrow [51, 51, 51, 51] 50)$

where $jmm'\text{-confs} P \equiv jmm'.\text{confs} \text{ TYPE}('m) P P$

abbreviation $jmm'\text{-tconf} :: 'm \text{ prog} \Rightarrow \text{JMM-heap} \Rightarrow \text{addr} \Rightarrow \text{bool} (\langle \cdot, - \vdash jmm'' \rangle - \sqrt{t} \rangle [51, 51, 51] 50)$

where $jmm'\text{-tconf} P \equiv jmm'.\text{tconf} \text{ TYPE}('m) P P$

8.18.2 Heap locale interpretations

8.18.3 Locale heap

lemma $jmm\text{-heap}: \text{heap addr2thread-id thread-id2addr jmm-allocate (jmm-typeof-addr } P) \text{ jmm-heap-write } P$

$\langle \text{proof} \rangle$

interpretation $jmm: \text{heap}$

$\text{addr2thread-id thread-id2addr}$

$\text{jmm-spurious-wakeups}$

$\text{jmm-empty jmm-allocate jmm-typeof-addr } P \text{ jmm-heap-read jmm-heap-write}$

P

for P

$\langle \text{proof} \rangle$

declare $\text{jmm.typeof-addr-thread-id2-addr-addr2thread-id} [\text{simp del}]$

lemmas $\text{jmm}'\text{-heap} = \text{jmm-heap}$

interpretation $jmm': \text{heap}$

$\text{addr2thread-id thread-id2addr}$

$\text{jmm-spurious-wakeups}$

$\text{jmm-empty jmm-allocate jmm-typeof-addr } P \text{ jmm-heap-read-typed } P \text{ jmm-heap-write}$

P

for P

$\langle \text{proof} \rangle$

declare $\text{jmm}'.\text{typeof-addr-thread-id2-addr-addr2thread-id} [\text{simp del}]$

lemma $\text{jmm-heap-read-typed-default-val}:$

$\text{heap-base.heap-read-typed typeof-addr jmm-heap-read } P \text{ h a al}$

$(\text{default-val } (THE T. \text{heap-base.addr-loc-type typeof-addr } P \text{ h a al } T))$

$\langle \text{proof} \rangle$

lemma *jmm-allocate-Eps*:

(*SOME* *ha*. *ha* \in *jmm-allocate* *h* *hT*) = (*h'*, *a'*)
 \implies *jmm-allocate* *h* *hT* \neq {} \longrightarrow (*h'*, *a'*) \in *jmm-allocate* *h* *hT*
 <proof>

lemma *jmm-allocate-eq-empty*: *jmm-allocate* *h* *hT* = {} \longleftrightarrow *h* *hT* = *UNIV*
 <proof>

lemma *jmm-allocate-otherD*:

(*h'*, *a*) \in *jmm-allocate* *h* *hT* \implies \forall *hT'*. *hT'* \neq *hT* \longrightarrow *h'* *hT'* = *h* *hT'*
 <proof>

lemma *jmm-start-heap-ok*: *jmm.start-heap-ok*
 <proof>

8.18.4 Locale *heap-conf*

interpretation *jmm*: *heap-conf-base*

addr2thread-id *thread-id2addr*
jmm-spurious-wakeups
jmm-empty *jmm-allocate* *jmm-typeof-addr* *P* *jmm-heap-read* *jmm-heap-write* *jmm-hconf*
P
for *P* <proof>

abbreviation (*input*) *jmm'-hconf* :: *JMM-heap* \Rightarrow *bool*
where *jmm'-hconf* == *jmm-hconf*

interpretation *jmm'*: *heap-conf-base*

addr2thread-id *thread-id2addr*
jmm-spurious-wakeups
jmm-empty *jmm-allocate* *jmm-typeof-addr* *P* *jmm-heap-read-typed* *P* *jmm-heap-write* *jmm'-hconf*
P
for *P* <proof>

abbreviation *jmm-heap-read-typeable* :: *'m prog* \Rightarrow *bool*

where *jmm-heap-read-typeable* *P* \equiv *jmm.heap-read-typeable* *TYPE('m)* *P* *jmm-hconf* *P*

abbreviation *jmm'-heap-read-typeable* :: *'m prog* \Rightarrow *bool*

where *jmm'-heap-read-typeable* *P* \equiv *jmm'.heap-read-typeable* *TYPE('m)* *P* *jmm-hconf* *P*

lemma *jmm-heap-read-typeable*: *jmm-heap-read-typeable* *P*
 <proof>

lemma *jmm'-heap-read-typeable*: *jmm'-heap-read-typeable* *P*
 <proof>

lemma *jmm-heap-conf*:

heap-conf *addr2thread-id* *thread-id2addr* *jmm-empty* *jmm-allocate* (*jmm-typeof-addr* *P*) *jmm-heap-write*
jmm-hconf *P*
 <proof>

interpretation *jmm*: *heap-conf*

addr2thread-id *thread-id2addr*

jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read jmm-heap-write jmm-hconf
P
for *P*
 ⟨*proof*⟩

lemmas *jmm'-heap-conf = jmm-heap-conf*

interpretation *jmm'*: *heap-conf*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write jmm'-hconf
P
for *P*
 ⟨*proof*⟩

8.18.5 Locale *heap-progress*

lemma *jmm-heap-progress*:
heap-progress addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) jmm-heap-read
jmm-heap-write jmm-hconf P
 ⟨*proof*⟩

interpretation *jmm*: *heap-progress*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read jmm-heap-write jmm-hconf
P
for *P*
 ⟨*proof*⟩

lemma *jmm'-heap-progress*:
heap-progress addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) (jmm-heap-read-typed
P) jmm-heap-write jmm'-hconf P
 ⟨*proof*⟩

interpretation *jmm'*: *heap-progress*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write jmm'-hconf
P
for *P*
 ⟨*proof*⟩

8.18.6 Locale *heap-conf-read*

lemma *jmm'-heap-conf-read*:
heap-conf-read addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) (jmm-heap-read-typed
P) jmm-heap-write jmm'-hconf P
 ⟨*proof*⟩

interpretation *jmm'*: *heap-conf-read*
addr2thread-id thread-id2addr
jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write jmm'-hconf
P
for *P*
 ⟨proof⟩

interpretation *jmm': heap-typesafe*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write jmm'-hconf
P
for *P*
 ⟨proof⟩

8.18.7 Locale *allocated-heap*

lemma *jmm-allocated-heap*:
allocated-heap addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) jmm-heap-write
jmm-allocated P
 ⟨proof⟩

interpretation *jmm: allocated-heap*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read jmm-heap-write
jmm-allocated
P
for *P*
 ⟨proof⟩

lemmas *jmm'-allocated-heap = jmm-allocated-heap*

interpretation *jmm': allocated-heap*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write
jmm-allocated
P
for *P*
 ⟨proof⟩

8.18.8 Syntax translations

notation *jmm'.external-WT'* ($\langle -, - \vdash jmm'' (-) \rangle : \rightarrow [50, 0, 0, 0, 50]$ 60)

abbreviation *jmm'-red-external* ::
'm prog \Rightarrow thread-id \Rightarrow JMM-heap \Rightarrow addr \Rightarrow mname \Rightarrow addr val list
 \Rightarrow (addr, thread-id, JMM-heap) external-thread-action
 \Rightarrow addr extCallRet \Rightarrow JMM-heap \Rightarrow bool
where *jmm'-red-external P \equiv jmm'.red-external (TYPE('m)) P P*

abbreviation *jmm'-red-external-syntax* ::
'm prog \Rightarrow thread-id \Rightarrow addr \Rightarrow mname \Rightarrow addr val list \Rightarrow JMM-heap
 \Rightarrow (addr, thread-id, JMM-heap) external-thread-action
 \Rightarrow addr extCallRet \Rightarrow JMM-heap \Rightarrow bool

$jmm'\text{-conf } (compP2 (compP1 P)) = jmm'\text{-conf } P$
 ⟨proof⟩

lemma $jmm'\text{-J-JVM-conf-read}$:

$J\text{-JVM-conf-read } addr2thread\text{-id } thread\text{-id}2addr\ jmm\text{-empty } jmm\text{-allocate } (jmm\text{-typeof-addr } P) (jmm\text{-heap-read-typed } P) jmm\text{-heap-write } jmm\text{-hconf } P$
 ⟨proof⟩

lemma $jmm\text{-J-allocated-progress}$:

$J\text{-allocated-progress } addr2thread\text{-id } thread\text{-id}2addr\ jmm\text{-empty } jmm\text{-allocate } (jmm\text{-typeof-addr } P) jmm\text{-heap-read } jmm\text{-heap-write } jmm\text{-hconf } jmm\text{-allocated } P$
 ⟨proof⟩

lemma $jmm'\text{-J-allocated-progress}$:

$J\text{-allocated-progress } addr2thread\text{-id } thread\text{-id}2addr\ jmm\text{-empty } jmm\text{-allocate } (jmm\text{-typeof-addr } P) (jmm\text{-heap-read-typed } P) jmm\text{-heap-write } jmm\text{-hconf } jmm\text{-allocated } P$
 ⟨proof⟩

lemma $jmm\text{-JVM-allocated-progress}$:

$JVM\text{-allocated-progress } addr2thread\text{-id } thread\text{-id}2addr\ jmm\text{-empty } jmm\text{-allocate } (jmm\text{-typeof-addr } P) jmm\text{-heap-read } jmm\text{-heap-write } jmm\text{-hconf } jmm\text{-allocated } P$
 ⟨proof⟩

lemma $jmm'\text{-JVM-allocated-progress}$:

$JVM\text{-allocated-progress } addr2thread\text{-id } thread\text{-id}2addr\ jmm\text{-empty } jmm\text{-allocate } (jmm\text{-typeof-addr } P) (jmm\text{-heap-read-typed } P) jmm\text{-heap-write } jmm\text{-hconf } jmm\text{-allocated } P$
 ⟨proof⟩

end

8.19 Specialize type safety for JMM heap implementation 2

theory $JMM\text{-Typesafe2}$

imports

$JMM\text{-Type2}$

$JMM\text{-Common}$

begin

interpretation jmm : $heap'$

$addr2thread\text{-id } thread\text{-id}2addr$

$jmm\text{-spurious-wakeups}$

$jmm\text{-empty } jmm\text{-allocate } jmm\text{-typeof-addr}' P jmm\text{-heap-read } jmm\text{-heap-write}$

for P

⟨proof⟩

abbreviation $jmm\text{-addr-loc-type}' :: 'm\ prog \Rightarrow addr \Rightarrow addr\text{-loc} \Rightarrow ty \Rightarrow bool$ ($\langle \vdash jmm \text{-@-} : \rightarrow [50, 50, 50, 50] 51$)

where $jmm\text{-addr-loc-type}' P \equiv jmm.\text{addr-loc-type } TYPE('m) P P$

lemma $jmm\text{-addr-loc-type-conv-}jmm\text{-addr-loc-type}'$ [$simp, heap\text{-independent}$]:

$jmm\text{-addr-loc-type } P h = jmm\text{-addr-loc-type}' P$

⟨proof⟩

abbreviation $jmm\text{-conf}' :: 'm\ prog \Rightarrow addr\ val \Rightarrow ty \Rightarrow bool$ ($\langle \cdot \vdash jmm - : \leq \cdot \rangle [51,51,51] 50$)
where $jmm\text{-conf}' P \equiv jmm.conf\ TYPE('m)\ P\ P$

lemma $jmm\text{-conf}\text{-conv}\text{-}jmm\text{-conf}'$ [*simp, heap-independent*]:

$jmm\text{-conf}\ P\ h = jmm\text{-conf}'\ P$
 $\langle proof \rangle$

lemma $jmm\text{-heap}''$: $heap''\ addr2thread\text{-}id\ thread\text{-}id2addr\ jmm\text{-allocate}\ (jmm\text{-typeof}\text{-}addr'\ P)\ jmm\text{-heap}\text{-}write\ P$

$\langle proof \rangle$

interpretation jmm : $heap''$

$addr2thread\text{-}id\ thread\text{-}id2addr$
 $jmm\text{-spurious}\text{-}wakeups$
 $jmm\text{-empty}\ jmm\text{-allocate}\ jmm\text{-typeof}\text{-}addr'\ P\ jmm\text{-heap}\text{-}read\ jmm\text{-heap}\text{-}write$
for P
 $\langle proof \rangle$

interpretation jmm' : $heap''$

$addr2thread\text{-}id\ thread\text{-}id2addr$
 $jmm\text{-spurious}\text{-}wakeups$
 $jmm\text{-empty}\ jmm\text{-allocate}\ jmm\text{-typeof}\text{-}addr'\ P\ jmm\text{-heap}\text{-}read\text{-}typed\ P\ jmm\text{-heap}\text{-}write$
for P
 $\langle proof \rangle$

abbreviation $jmm\text{-wf}\text{-}start\text{-}state :: 'm\ prog \Rightarrow cname \Rightarrow mname \Rightarrow addr\ val\ list \Rightarrow bool$

where $jmm\text{-wf}\text{-}start\text{-}state\ P \equiv jmm.wf\text{-}start\text{-}state\ TYPE('m)\ P\ P$

abbreviation $if\text{-}heap\text{-}read\text{-}typed ::$

$('x \Rightarrow bool) \Rightarrow ('l, 't, 'x, 'heap, 'w, ('addr :: addr, 'thread\text{-}id)\ obs\text{-}event)\ semantics$
 $\Rightarrow ('addr \Rightarrow htype\ option)$
 $\Rightarrow 'm\ prog \Rightarrow ('l, 't, status \times 'x, 'heap, 'w, ('addr, 'thread\text{-}id)\ obs\text{-}event\ action)\ semantics$

where

$\bigwedge final. if\text{-}heap\text{-}read\text{-}typed\ final\ r\ typeof\text{-}addr\ P\ t\ xh\ ta\ x'h' \equiv$
 $multithreaded\text{-}base.init\text{-}fin\ final\ r\ t\ xh\ ta\ x'h' \wedge$
 $(\forall ad\ al\ v\ T. NormalAction\ (ReadMem\ ad\ al\ v) \in set\ \{\!|ta|\!\}_o \longrightarrow heap\text{-}base'.addr\text{-}loc\text{-}type\ TYPE('heap)$
 $typeof\text{-}addr\ P\ ad\ al\ T \longrightarrow heap\text{-}base'.conf\ TYPE('heap)\ typeof\text{-}addr\ P\ v\ T)$

lemma $if\text{-}mthr\text{-}Runs\text{-}heap\text{-}read\text{-}typedI$:

fixes $final$ **and** $r :: ('addr, 't, 'x, 'heap, 'w, ('addr :: addr, 'thread\text{-}id)\ obs\text{-}event)\ semantics$
assumes $trsys.Runs\ (multithreaded\text{-}base.redT\ (final\text{-}thread.init\text{-}fin\text{-}final\ final)\ (multithreaded\text{-}base.init\text{-}fin\ final\ r)\ (map\ NormalAction\ \circ\ convert\text{-}RA))\ s\ \xi$
(is $trsys.Runs\ ?redT\ -\ -)$
and $\bigwedge ad\ al\ v\ T. \llbracket NormalAction\ (ReadMem\ ad\ al\ v) \in lset\ (lconcat\ (lmap\ (l\text{-}list\text{-}of\ \circ\ obs\text{-}a\ \circ\ snd)\ \xi)); heap\text{-}base'.addr\text{-}loc\text{-}type\ TYPE('heap)\ typeof\text{-}addr\ P\ ad\ al\ T \rrbracket \Longrightarrow heap\text{-}base'.conf\ TYPE('heap)\ typeof\text{-}addr\ P\ v\ T$
(is $\bigwedge ad\ al\ v\ T. \llbracket ?obs\ \xi\ ad\ al\ v; ?adal\ ad\ al\ T \rrbracket \Longrightarrow ?conf\ v\ T$)
shows $trsys.Runs\ (multithreaded\text{-}base.redT\ (final\text{-}thread.init\text{-}fin\text{-}final\ final)\ (if\text{-}heap\text{-}read\text{-}typed\ final\ r\ typeof\text{-}addr\ P)\ (map\ NormalAction\ \circ\ convert\text{-}RA))\ s\ \xi$
(is $trsys.Runs\ ?redT'\ -\ -)$
 $\langle proof \rangle$

lemma $if\text{-}mthr\text{-}Runs\text{-}heap\text{-}read\text{-}typedD$:

fixes *final* **and** $r :: ('addr, 't, 'x, 'heap, 'w, ('addr :: addr, 'thread-id) obs-event)$ semantics
assumes *Runs'*: $trsys.Runs (multithreaded-base.redT (final-thread.init-fin-final final) (if-heap-read-typed final r typeof-addr P) (map NormalAction \circ convert-RA)) s \xi$
(is $?Runs' s \xi$)
and *stuck*: $\bigwedge tta s' tta s''.$ \llbracket
 $multithreaded-base.RedT (final-thread.init-fin-final final) (if-heap-read-typed final r typeof-addr P)$
 $(map NormalAction \circ convert-RA) s tta s';$
 $multithreaded-base.redT (final-thread.init-fin-final final) (multithreaded-base.init-fin final r) (map$
 $NormalAction \circ convert-RA) s' tta s'' \rrbracket$
 $\implies \exists tta s''. multithreaded-base.redT (final-thread.init-fin-final final) (if-heap-read-typed final r typeof-addr$
 $P) (map NormalAction \circ convert-RA) s' tta s''$
(is $\bigwedge tta s' tta s''. \llbracket ?redT' s tta s'; ?redT s' tta s'' \rrbracket \implies \exists tta s''. ?redT' s' tta s''$)
shows $trsys.Runs (multithreaded-base.redT (final-thread.init-fin-final final) (multithreaded-base.init-fin final r) (map NormalAction \circ convert-RA)) s \xi$
(is $?Runs s \xi$)
 $\langle proof \rangle$

lemma *heap-copy-loc-heap-read-typed*:

$heap-base.heap-copy-loc (heap-base.heap-read-typed (\lambda - :: 'heap. typeof-addr) heap-read P) heap-write a a' al h obs h' \longleftrightarrow$
 $heap-base.heap-copy-loc heap-read heap-write a a' al h obs h' \wedge$
 $(\forall ad al v T. ReadMem ad al v \in set obs \longrightarrow heap-base'.addr-loc-type TYPE('heap) typeof-addr P ad al T \longrightarrow heap-base'.conf TYPE('heap) typeof-addr P v T)$
 $\langle proof \rangle$

lemma *heap-copies-heap-read-typed*:

$heap-base.heap-copies (heap-base.heap-read-typed (\lambda - :: 'heap. typeof-addr) heap-read P) heap-write a a' als h obs h' \longleftrightarrow$
 $heap-base.heap-copies heap-read heap-write a a' als h obs h' \wedge$
 $(\forall ad al v T. ReadMem ad al v \in set obs \longrightarrow heap-base'.addr-loc-type TYPE('heap) typeof-addr P ad al T \longrightarrow heap-base'.conf TYPE('heap) typeof-addr P v T)$
(is $?lhs \longleftrightarrow ?rhs$)
 $\langle proof \rangle$

lemma *heap-clone-heap-read-typed*:

$heap-base.heap-clone allocate (\lambda - :: 'heap. typeof-addr) (heap-base.heap-read-typed (\lambda - :: 'heap. typeof-addr) heap-read P) heap-write P a h h' obs \longleftrightarrow$
 $heap-base.heap-clone allocate (\lambda - :: 'heap. typeof-addr) heap-read heap-write P a h h' obs \wedge$
 $(\forall ad al v T obs' a'. obs = \llbracket (obs', a') \rrbracket \longrightarrow ReadMem ad al v \in set obs' \longrightarrow heap-base'.addr-loc-type TYPE('heap) typeof-addr P ad al T \longrightarrow heap-base'.conf TYPE('heap) typeof-addr P v T)$
 $\langle proof \rangle$

lemma *red-external-heap-read-typed*:

$heap-base.red-external addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate (\lambda - :: 'heap. typeof-addr) (heap-base.heap-read-typed (\lambda - :: 'heap. typeof-addr) heap-read P) heap-write P t h a M vs ta va h' \longleftrightarrow$
 $heap-base.red-external addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate (\lambda - :: 'heap. typeof-addr) heap-read heap-write P t h a M vs ta va h' \wedge$
 $(\forall ad al v T obs' a'. ReadMem ad al v \in set \{\{ta\}\}_o \longrightarrow heap-base'.addr-loc-type TYPE('heap) typeof-addr P ad al T \longrightarrow heap-base'.conf TYPE('heap) typeof-addr P v T)$
 $\langle proof \rangle$

lemma *red-external-aggr-heap-read-typed*:

$(ta, va, h') \in heap-base.red-external-aggr addr2thread-id thread-id2addr spurious-wakeups empty-heap$

$allocate (\lambda- :: 'heap. \text{typeof-addr}) (\text{heap-base.heap-read-typed } (\lambda- :: 'heap. \text{typeof-addr}) \text{ heap-read } P)$
 $\text{heap-write } P \text{ t h a } M \text{ vs } \longleftrightarrow$
 $(ta, va, h') \in \text{heap-base.red-external-aggr } \text{addr2thread-id } \text{thread-id2addr } \text{spurious-wakeups } \text{empty-heap}$
 $allocate (\lambda- :: 'heap. \text{typeof-addr}) \text{ heap-read } \text{heap-write } P \text{ t h a } M \text{ vs } \wedge$
 $(\forall ad \text{ al } v \text{ T } \text{obs}' a'. \text{ReadMem } ad \text{ al } v \in \text{set } \{\{ta\}\}_o \longrightarrow \text{heap-base'.addr-loc-type } \text{TYPE}('heap)$
 $\text{typeof-addr } P \text{ ad al } T \longrightarrow \text{heap-base'.conf } \text{TYPE}('heap) \text{typeof-addr } P \text{ v } T)$
 $\langle \text{proof} \rangle$

lemma *jmm'-heap-copy-locI:*

$\exists \text{obs } h'. \text{heap-base.heap-copy-loc } (\text{heap-base.heap-read-typed } \text{typeof-addr } \text{jmm-heap-read } P) \text{ jmm-heap-write}$
 $a \text{ a}' \text{ al } h \text{ obs } h'$
 $\langle \text{proof} \rangle$

lemma *jmm'-heap-copiesI:*

$\exists \text{obs} :: (\text{addr}, 'thread-id) \text{obs-event list.}$
 $\exists h'. \text{heap-base.heap-copies } (\text{heap-base.heap-read-typed } \text{typeof-addr } \text{jmm-heap-read } P) \text{ jmm-heap-write}$
 $a \text{ a}' \text{ als } h \text{ obs } h'$
 $\langle \text{proof} \rangle$

lemma *jmm'-heap-cloneI:*

fixes $\text{obsa} :: ((\text{addr}, 'thread-id) \text{obs-event list} \times \text{addr}) \text{option}$
assumes $\text{heap-base.heap-clone } \text{allocate } \text{typeof-addr } \text{jmm-heap-read } \text{jmm-heap-write } P \text{ h a h' obsa}$
shows $\exists h'. \exists \text{obsa} :: ((\text{addr}, 'thread-id) \text{obs-event list} \times \text{addr}) \text{option.}$
 $\text{heap-base.heap-clone } \text{allocate } \text{typeof-addr } (\text{heap-base.heap-read-typed } \text{typeof-addr } \text{jmm-heap-read}$
 $P) \text{ jmm-heap-write } P \text{ h a h' obsa}$
 $\langle \text{proof} \rangle$

lemma *jmm'-red-externalI:*

$\bigwedge \text{final.}$
 $\llbracket \text{heap-base.red-external } \text{addr2thread-id } \text{thread-id2addr } \text{spurious-wakeups } \text{empty-heap } \text{allocate } \text{typeof-addr}$
 $\text{jmm-heap-read } \text{jmm-heap-write } P \text{ t h a } M \text{ vs } ta \text{ va } h';$
 $\text{final-thread.actions-ok } \text{final } s \text{ t } ta \rrbracket$
 $\implies \exists ta \text{ va } h'. \text{heap-base.red-external } \text{addr2thread-id } \text{thread-id2addr } \text{spurious-wakeups } \text{empty-heap}$
 $\text{allocate } \text{typeof-addr } (\text{heap-base.heap-read-typed } \text{typeof-addr } \text{jmm-heap-read } P) \text{ jmm-heap-write } P \text{ t h a}$
 $M \text{ vs } ta \text{ va } h' \wedge \text{final-thread.actions-ok } \text{final } s \text{ t } ta$
 $\langle \text{proof} \rangle$

lemma *red-external-aggr-heap-read-typedI:*

$\bigwedge \text{final.}$
 $\llbracket (ta, vah') \in \text{heap-base.red-external-aggr } \text{addr2thread-id } \text{thread-id2addr } \text{spurious-wakeups } \text{empty-heap}$
 $\text{allocate } \text{typeof-addr } \text{jmm-heap-read } \text{jmm-heap-write } P \text{ t h a } M \text{ vs};$
 $\text{final-thread.actions-ok } \text{final } s \text{ t } ta$
 \rrbracket
 $\implies \exists ta \text{ vah}'. (ta, vah') \in \text{heap-base.red-external-aggr } \text{addr2thread-id } \text{thread-id2addr } \text{spurious-wakeups}$
 $\text{empty-heap } \text{allocate } \text{typeof-addr } (\text{heap-base.heap-read-typed } \text{typeof-addr } \text{jmm-heap-read } P) \text{ jmm-heap-write}$
 $P \text{ t h a } M \text{ vs } \wedge \text{final-thread.actions-ok } \text{final } s \text{ t } ta$
 $\langle \text{proof} \rangle$

end

8.20 JMM type safety for source code

```

theory JMM-J-Typesafe imports
  JMM-Typesafe2
  DRF-J
begin

locale J-allocated-heap-conf' =
  h: J-heap-conf
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate λ-. typeof-addr heap-read heap-write hconf
  P
  +
  h: J-allocated-heap
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate λ-. typeof-addr heap-read heap-write
  allocated
  P
  +
  heap''
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  P
  for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'addr → htype
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
  and hconf :: 'heap ⇒ bool
  and allocated :: 'heap ⇒ 'addr set
  and P :: 'addr J-prog

sublocale J-allocated-heap-conf' < h: J-allocated-heap-conf
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate λ-. typeof-addr heap-read heap-write hconf allocated
  P
  ⟨proof⟩

context J-allocated-heap-conf' begin

lemma red-New-type-match:
  [[ h.red' P t e s ta e' s'; NewHeapElem ad CTn ∈ set {ta}0; typeof-addr ad ≠ None ]
  ⇒ typeof-addr ad = [ CTn ]
  and reds-New-type-match:
  [[ h.reds' P t e s s ta es' s'; NewHeapElem ad CTn ∈ set {ta}0; typeof-addr ad ≠ None ]
  ⇒ typeof-addr ad = [ CTn ]
  ⟨proof⟩

```

lemma *mred-known-addr-typing'*:

assumes *wf*: *wf-J-prog P*

and *ok*: *h.start-heap-ok*

shows *known-addr-typing' addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write allocated h.J-known-addr final-expr (h.mred P) ($\lambda t x h. \exists ET. h.sconf-type-ok ET t x h$) P*

<proof>

lemma *J-legal-read-value-typeable*:

assumes *wf*: *wf-J-prog P*

and *wf-start*: *h.wf-start-state P C M vs*

and *legal*: *weakly-legal-execution P (h.J- \mathcal{E} P C M vs status) (E, ws)*

and *a*: *enat a < llength E*

and *read*: *action-obs E a = NormalAction (ReadMem ad al v)*

shows $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$

<proof>

end

8.20.1 Specific part for JMM implementation 2

abbreviation *jmm-J- \mathcal{E}*

$:: addr\ J\text{-prog} \Rightarrow cname \Rightarrow mname \Rightarrow addr\ val\ list \Rightarrow status \Rightarrow (addr \times (addr, addr)\ obs\ event\ action)\ llist\ set$

where

jmm-J- \mathcal{E} P \equiv

J-heap-base.J- \mathcal{E} addr2thread-id thread-id2addr jmm-spurious-wakeups jmm-empty jmm-allocate (jmm-typeof-addr P) jmm-heap-read jmm-heap-write P

abbreviation *jmm'-J- \mathcal{E}*

$:: addr\ J\text{-prog} \Rightarrow cname \Rightarrow mname \Rightarrow addr\ val\ list \Rightarrow status \Rightarrow (addr \times (addr, addr)\ obs\ event\ action)\ llist\ set$

where

jmm'-J- \mathcal{E} P \equiv

J-heap-base.J- \mathcal{E} addr2thread-id thread-id2addr jmm-spurious-wakeups jmm-empty jmm-allocate (jmm-typeof-addr P) (jmm-heap-read-typed P) jmm-heap-write P

lemma *jmm-J-heap-conf*:

J-heap-conf addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) jmm-heap-write jmm-hconf P

<proof>

lemma *jmm-J-allocated-heap-conf*: *J-allocated-heap-conf addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) jmm-heap-write jmm-hconf jmm-allocated P*

<proof>

lemma *jmm-J-allocated-heap-conf'*:

J-allocated-heap-conf' addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr' P) jmm-heap-write jmm-hconf jmm-allocated P

<proof>

lemma *red-heap-read-typedD*:

$J\text{-heap-base.red}' \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda\text{-} :: 'heap. \text{typeof-addr}) (\text{heap-base.heap-read-typed } (\lambda\text{-} :: 'heap. \text{typeof-addr}) \text{ heap-read } P) \text{ heap-write } P \text{ t e s ta e' s}' \longleftrightarrow$

$J\text{-heap-base.red}' \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda\text{-} :: 'heap. \text{typeof-addr}) \text{ heap-read heap-write } P \text{ t e s ta e' s}' \wedge$

$(\forall ad \text{ al } v \text{ T. ReadMem } ad \text{ al } v \in \text{set } \{\text{ta}\}_o \longrightarrow \text{heap-base'.addr-loc-type TYPE('heap) typeof-addr } P \text{ ad al } T \longrightarrow \text{heap-base'.conf TYPE('heap) typeof-addr } P \text{ v } T)$

(**is** $?lhs1 \longleftrightarrow ?rhs1a \wedge ?rhs1b$)

and *reds-heap-read-typedD*:

$J\text{-heap-base.reds}' \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda\text{-} :: 'heap. \text{typeof-addr}) (\text{heap-base.heap-read-typed } (\lambda\text{-} :: 'heap. \text{typeof-addr}) \text{ heap-read } P) \text{ heap-write } P \text{ t e s s ta es' s}' \longleftrightarrow$

$J\text{-heap-base.reds}' \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda\text{-} :: 'heap. \text{typeof-addr}) \text{ heap-read heap-write } P \text{ t e s s ta es' s}' \wedge$

$(\forall ad \text{ al } v \text{ T. ReadMem } ad \text{ al } v \in \text{set } \{\text{ta}\}_o \longrightarrow \text{heap-base'.addr-loc-type TYPE('heap) typeof-addr } P \text{ ad al } T \longrightarrow \text{heap-base'.conf TYPE('heap) typeof-addr } P \text{ v } T)$

(**is** $?lhs2 \longleftrightarrow ?rhs2a \wedge ?rhs2b$)

$\langle \text{proof} \rangle$

lemma *if-mred-heap-read-typedD*:

$\text{multithreaded-base.init-fin final-expr } (J\text{-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda\text{-} :: 'heap. \text{typeof-addr}) (\text{heap-base.heap-read-typed } (\lambda\text{-} :: 'heap. \text{typeof-addr}) \text{ heap-read } P) \text{ heap-write } P) \text{ t xh ta x'h}' \longleftrightarrow$

$\text{if-heap-read-typed final-expr } (J\text{-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda\text{-} :: 'heap. \text{typeof-addr}) \text{ heap-read heap-write } P) \text{ typeof-addr } P \text{ t xh ta x'h}'$

$\langle \text{proof} \rangle$

lemma *J- \mathcal{E} -heap-read-typedI*:

$\llbracket E \in J\text{-heap-base.J-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda\text{-} :: 'heap. \text{typeof-addr}) \text{ heap-read heap-write } P \text{ C M vs status};$

$\bigwedge ad \text{ al } v \text{ T. } \llbracket \text{NormalAction } (\text{ReadMem } ad \text{ al } v) \in \text{snd 'lset } E; \text{heap-base'.addr-loc-type TYPE('heap) typeof-addr } P \text{ ad al } T \rrbracket \implies \text{heap-base'.conf TYPE('heap) typeof-addr } P \text{ v } T \rrbracket$

$\implies E \in J\text{-heap-base.J-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda\text{-} :: 'heap. \text{typeof-addr}) (\text{heap-base.heap-read-typed } (\lambda\text{-} :: 'heap. \text{typeof-addr}) \text{ heap-read } P) \text{ heap-write } P \text{ C M vs status}$

$\langle \text{proof} \rangle$

lemma *jmm'-redI*:

$\llbracket J\text{-heap-base.red}' \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write } P \text{ t e s ta e' s}';$

$\text{final-thread.actions-ok } (\text{final-thread.init-fin-final final-expr}) \text{ S t ta } \rrbracket$

$\implies \exists ta \text{ e' s}'. J\text{-heap-base.red}' \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr } (\text{heap-base.heap-read-typed } \text{typeof-addr } \text{jmm-heap-read } P) \text{ jmm-heap-write } P \text{ t e s ta e' s}' \wedge \text{final-thread.actions-ok } (\text{final-thread.init-fin-final final-expr}) \text{ S t ta}$

(**is** $\llbracket ?red'; ?aok \rrbracket \implies ?concl$)

and *jmm'-redsI*:

$\llbracket J\text{-heap-base.reds}' \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write } P \text{ t e s s ta es' s}';$

$\text{final-thread.actions-ok } (\text{final-thread.init-fin-final final-expr}) \text{ S t ta } \rrbracket$

$\implies \exists ta \text{ es' s}'. J\text{-heap-base.reds}' \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr } (\text{heap-base.heap-read-typed } \text{typeof-addr } \text{jmm-heap-read } P) \text{ jmm-heap-write } P \text{ t e s s ta es' s}' \wedge$

$\text{final-thread.actions-ok } (\text{final-thread.init-fin-final final-expr}) \text{ S t ta}$

(is $\llbracket ?reds'; ?aoks \rrbracket \implies ?concls$)
 ⟨proof⟩

lemma *if-mred-heap-read-not-stuck*:

$\llbracket \text{multithreaded-base.init-fin final-expr } (J\text{-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write } P) t \text{ xh ta } x'h';$
 $\text{final-thread.actions-ok (final-thread.init-fin-final final-expr) } s t \text{ ta} \rrbracket$
 \implies
 $\exists ta \ x'h'. \text{multithreaded-base.init-fin final-expr } (J\text{-heap-base.mred addr2thread-id thread-id2addr spu-}$
 $\text{rious-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read}$
 $P) \text{ jmm-heap-write } P) t \text{ xh ta } x'h' \wedge \text{final-thread.actions-ok (final-thread.init-fin-final final-expr) } s t \text{ ta}$
 ⟨proof⟩

lemma *if-mredT-heap-read-not-stuck*:

$\text{multithreaded-base.redT (final-thread.init-fin-final final-expr) (multithreaded-base.init-fin final-expr}$
 $(J\text{-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read}$
 $\text{jmm-heap-write } P)) \text{convert-RA' } s \text{ tta } s'$
 $\implies \exists tta \ s'. \text{multithreaded-base.redT (final-thread.init-fin-final final-expr) (multithreaded-base.init-fin}$
 $\text{final-expr } (J\text{-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr}$
 $(\text{heap-base.heap-read-typed typeof-addr jmm-heap-read } P) \text{ jmm-heap-write } P)) \text{convert-RA' } s \text{ tta } s'$
 ⟨proof⟩

lemma *J- \mathcal{E} -heap-read-typedD*:

$E \in J\text{-heap-base.J-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda-. \text{typeof-addr})$
 $(\text{heap-base.heap-read-typed } (\lambda-. \text{typeof-addr}) \text{ jmm-heap-read } P) \text{ jmm-heap-write } P \ C \ M \ \text{vs status}$
 $\implies E \in J\text{-heap-base.J-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda-. \text{typeof-addr})$
 $\text{jmm-heap-read jmm-heap-write } P \ C \ M \ \text{vs status}$
 ⟨proof⟩

lemma *J- \mathcal{E} -typesafe-subset*: $\text{jmm}'\text{-J-}\mathcal{E} \ P \ C \ M \ \text{vs status} \subseteq \text{jmm-J-}\mathcal{E} \ P \ C \ M \ \text{vs status}$

⟨proof⟩

lemma *J-legal-typesafe1*:

assumes $\text{wfP: wf-J-prog } P$
and $\text{ok: jmm-wf-start-state } P \ C \ M \ \text{vs}$
and $\text{legal: legal-execution } P \ (\text{jmm-J-}\mathcal{E} \ P \ C \ M \ \text{vs status}) \ (E, \ \text{ws})$
shows $\text{legal-execution } P \ (\text{jmm}'\text{-J-}\mathcal{E} \ P \ C \ M \ \text{vs status}) \ (E, \ \text{ws})$
 ⟨proof⟩

lemma *J-weakly-legal-typesafe1*:

assumes $\text{wfP: wf-J-prog } P$
and $\text{ok: jmm-wf-start-state } P \ C \ M \ \text{vs}$
and $\text{legal: weakly-legal-execution } P \ (\text{jmm-J-}\mathcal{E} \ P \ C \ M \ \text{vs status}) \ (E, \ \text{ws})$
shows $\text{weakly-legal-execution } P \ (\text{jmm}'\text{-J-}\mathcal{E} \ P \ C \ M \ \text{vs status}) \ (E, \ \text{ws})$
 ⟨proof⟩

lemma *J-legal-typesafe2*:

assumes $\text{legal: legal-execution } P \ (\text{jmm}'\text{-J-}\mathcal{E} \ P \ C \ M \ \text{vs status}) \ (E, \ \text{ws})$
shows $\text{legal-execution } P \ (\text{jmm-J-}\mathcal{E} \ P \ C \ M \ \text{vs status}) \ (E, \ \text{ws})$
 ⟨proof⟩

lemma *J-weakly-legal-typesafe2*:

assumes $\text{legal: weakly-legal-execution } P \ (\text{jmm}'\text{-J-}\mathcal{E} \ P \ C \ M \ \text{vs status}) \ (E, \ \text{ws})$
shows $\text{weakly-legal-execution } P \ (\text{jmm-J-}\mathcal{E} \ P \ C \ M \ \text{vs status}) \ (E, \ \text{ws})$

<proof>

theorem *J-weakly-legal-typesafe:*

assumes *wf-J-prog P*

and *jmm-wf-start-state P C M vs*

shows *weakly-legal-execution P (jmm-J- \mathcal{E} P C M vs status) = weakly-legal-execution P (jmm'-J- \mathcal{E} P C M vs status)*

<proof>

theorem *J-legal-typesafe:*

assumes *wf-J-prog P*

and *jmm-wf-start-state P C M vs*

shows *legal-execution P (jmm-J- \mathcal{E} P C M vs status) = legal-execution P (jmm'-J- \mathcal{E} P C M vs status)*

<proof>

end

8.21 JMM type safety for bytecode

theory *JMM-JVM-Typesafe*

imports

JMM-Typesafe2

DRF-JVM

begin

locale *JVM-allocated-heap-conf' =*

h: JVM-heap-conf

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate λ -. typeof-addr heap-read heap-write hconf

P

+

h: JVM-allocated-heap

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate λ -. typeof-addr heap-read heap-write

allocated

P

+

heap''

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate typeof-addr heap-read heap-write

P

for *addr2thread-id :: ('addr :: addr) \Rightarrow 'thread-id*

and *thread-id2addr :: 'thread-id \Rightarrow 'addr*

and *spurious-wakeups :: bool*

and *empty-heap :: 'heap*

and *allocate :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) set*

and *typeof-addr :: 'addr \rightarrow htype*

and *heap-read :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow bool*

and *heap-write :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow 'heap \Rightarrow bool*

and *hconf :: 'heap \Rightarrow bool*

and *allocated* :: 'heap \Rightarrow 'addr set
and *P* :: 'addr jvm-prog

sublocale *JVM-allocated-heap-conf'* < *h*: *JVM-allocated-heap-conf*
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate λ -. *typeof-addr heap-read heap-write hconf allocated*
P
 <proof>

context *JVM-allocated-heap-conf'* **begin**

lemma *exec-instr-New-type-match*:

$\llbracket (ta, s') \in h.exec\text{-instr } i \ P \ t \ h \ stk \ loc \ C \ M \ pc \ frs; \ NewHeapElem \ ad \ CTn \in \ set \ \{ta\}_o; \ typeof\text{-addr} \ ad \neq \ None \rrbracket$
 $\implies \ typeof\text{-addr } ad = \lfloor CTn \rfloor$
 <proof>

lemma *mexecd-New-type-match*:

$\llbracket h.mexecd \ P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h'); \ NewHeapElem \ ad \ CTn \in \ set \ \{ta\}_o; \ typeof\text{-addr } ad \neq \ None \rrbracket$
 $\implies \ typeof\text{-addr } ad = \lfloor CTn \rfloor$
 <proof>

lemma *mexecd-known-addr-typing'*:

assumes *wf*: *wf-jvm-prog* Φ *P*
and *ok*: *h.start-heap-ok*
shows *known-addr-typing'* *addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write allocated h.jvm-known-addr JVM-final (h.mexecd P) ($\lambda t \ (xcp, frs) \ h. \ h.correct\text{-state } \Phi \ t \ (xcp, h, frs)$)*
P
 <proof>

lemma *JVM-weakly-legal-read-value-typeable*:

assumes *wf*: *wf-jvm-prog* Φ *P*
and *wf-start*: *h.wf-start-state P C M vs*
and *legal*: *weakly-legal-execution P (h.JVMd- \mathcal{E} P C M vs status) (E, ws)*
and *a*: *enat a < llength E*
and *read*: *action-obs E a = NormalAction (ReadMem ad al v)*
shows $\exists T. \ P \vdash \ ad@al : T \wedge \ P \vdash \ v : \leq T$
 <proof>

end

abbreviation *jmm-JVMd- \mathcal{E}*

:: *addr jvm-prog* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *addr val list* \Rightarrow *status* \Rightarrow (*addr* \times (*addr*, *addr*)) *obs-event action*) *llist set*

where

jmm-JVMd- \mathcal{E} P \equiv
JVM-heap-base.JVMd- \mathcal{E} addr2thread-id thread-id2addr jmm-spurious-wakeups jmm-empty jmm-allocate
 (*jmm-typeof-addr P*) *jmm-heap-read jmm-heap-write P*

abbreviation *jmm'-JVMd- \mathcal{E}*

:: *addr jvm-prog* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *addr val list* \Rightarrow *status* \Rightarrow (*addr* \times (*addr*, *addr*)) *obs-event*

action) llist set

where

$jmm'\text{-JVMd-}\mathcal{E} P \equiv$
 $JVM\text{-heap-base.JVMd-}\mathcal{E} \text{ addr2thread-id thread-id2addr jmm-spurious-wakeups jmm-empty jmm-allocate}$
 $(jmm\text{-typeof-addr } P) (jmm\text{-heap-read-typed } P) jmm\text{-heap-write } P$

abbreviation $jmm\text{-JVM-start-state}$

$:: \text{ addr jvm-prog} \Rightarrow \text{ cname} \Rightarrow \text{ mname} \Rightarrow \text{ addr val list} \Rightarrow (\text{ addr, thread-id, addr jvm-thread-state, JMM-heap, addr})$
 state

where $jmm\text{-JVM-start-state} \equiv JVM\text{-heap-base.JVM-start-state addr2thread-id jmm-empty jmm-allocate}$

lemma $jmm\text{-JVM-heap-conf}$:

$JVM\text{-heap-conf addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm\text{-typeof-addr } P) jmm\text{-heap-write}$
 $jmm\text{-hconf } P$
 $\langle \text{proof} \rangle$

lemma $jmm\text{-JVMd-allocated-heap-conf}'$:

$JVM\text{-allocated-heap-conf}' \text{ addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm\text{-typeof-addr}'$
 $P) jmm\text{-heap-write jmm-hconf jmm-allocated } P$
 $\langle \text{proof} \rangle$

lemma $\text{exec-instr-heap-read-typed}$:

$(ta, xcphfrs') \in JVM\text{-heap-base.exec-instr addr2thread-id thread-id2addr spurious-wakeups empty-heap}$
 $\text{allocate } (\lambda \cdot :: \text{'heap. typeof-addr}) (\text{heap-base.heap-read-typed } (\lambda \cdot :: \text{'heap. typeof-addr}) \text{ heap-read } P)$
 $\text{heap-write } i P t h \text{ stk loc } C M \text{ pc frs} \longleftrightarrow$
 $(ta, xcphfrs') \in JVM\text{-heap-base.exec-instr addr2thread-id thread-id2addr spurious-wakeups empty-heap}$
 $\text{allocate } (\lambda \cdot :: \text{'heap. typeof-addr}) \text{ heap-read heap-write } i P t h \text{ stk loc } C M \text{ pc frs} \wedge$
 $(\forall ad \text{ al } v T. \text{ReadMem } ad \text{ al } v \in \text{set } \{\{ta\}\}_o \longrightarrow \text{heap-base'.addr-loc-type TYPE('heap) typeof-addr}$
 $P \text{ ad al } T \longrightarrow \text{heap-base'.conf TYPE('heap) typeof-addr } P v T)$
 $\langle \text{proof} \rangle$

lemma $\text{exec-heap-read-typed}$:

$(ta, xcphfrs') \in JVM\text{-heap-base.exec addr2thread-id thread-id2addr spurious-wakeups empty-heap al}$
 $\text{locate } (\lambda \cdot :: \text{'heap. typeof-addr}) (\text{heap-base.heap-read-typed } (\lambda \cdot \text{ typeof-addr}) \text{ heap-read } P) \text{ heap-write } P t$
 $t xcphfrs \longleftrightarrow$
 $(ta, xcphfrs') \in JVM\text{-heap-base.exec addr2thread-id thread-id2addr spurious-wakeups empty-heap}$
 $\text{allocate } (\lambda \cdot :: \text{'heap. typeof-addr}) \text{ heap-read heap-write } P t xcphfrs \wedge$
 $(\forall ad \text{ al } v T. \text{ReadMem } ad \text{ al } v \in \text{set } \{\{ta\}\}_o \longrightarrow \text{heap-base'.addr-loc-type TYPE('heap) typeof-addr}$
 $P \text{ ad al } T \longrightarrow \text{heap-base'.conf TYPE('heap) typeof-addr } P v T)$
 $\langle \text{proof} \rangle$

lemma $\text{exec-1-d-heap-read-typed}$:

$JVM\text{-heap-base.exec-1-d addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda \cdot ::$
 $\text{'heap. typeof-addr}) (\text{heap-base.heap-read-typed } (\lambda \cdot \text{ typeof-addr}) \text{ heap-read } P) \text{ heap-write } P t (\text{Normal}$
 $xcphfrs) ta (\text{Normal } xcphfrs') \longleftrightarrow$
 $JVM\text{-heap-base.exec-1-d addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda \cdot ::$
 $\text{'heap. typeof-addr}) \text{ heap-read heap-write } P t (\text{Normal } xcphfrs) ta (\text{Normal } xcphfrs') \wedge$
 $(\forall ad \text{ al } v T. \text{ReadMem } ad \text{ al } v \in \text{set } \{\{ta\}\}_o \longrightarrow \text{heap-base'.addr-loc-type TYPE('heap) typeof-addr}$
 $P \text{ ad al } T \longrightarrow \text{heap-base'.conf TYPE('heap) typeof-addr } P v T)$
 $\langle \text{proof} \rangle$

lemma $\text{mexecd-heap-read-typed}$:

$JVM\text{-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda \cdot ::$

'heap. typeof-addr) (heap-base.heap-read-typed ($\lambda-$:: 'heap. typeof-addr) heap-read P) heap-write P t xcpfrsh ta xcpfrsh' \longleftrightarrow

JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate ($\lambda-$:: 'heap. typeof-addr) heap-read heap-write P t xcpfrsh ta xcpfrsh' \wedge

($\forall ad al v T. \text{ReadMem } ad \text{ al } v \in \text{set } \{\text{ta}\}_o \longrightarrow \text{heap-base'.addr-loc-type TYPE('heap) typeof-addr } P \text{ ad al } T \longrightarrow \text{heap-base'.conf TYPE('heap) typeof-addr } P \text{ v } T$)

$\langle \text{proof} \rangle$

lemma *if-mexecd-heap-read-typed*:

multithreaded-base.init-fin JVM-final (JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate ($\lambda-$:: 'heap. typeof-addr) (heap-base.heap-read-typed ($\lambda-$:: 'heap. typeof-addr) heap-read P) heap-write P) t xh ta x'h' \longleftrightarrow

if-heap-read-typed JVM-final (JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate ($\lambda-$:: 'heap. typeof-addr) heap-read heap-write P) typeof-addr P t xh ta x'h'

$\langle \text{proof} \rangle$

lemma *JVMd- \mathcal{E} -heap-read-typedI*:

$\llbracket E \in \text{JVM-heap-base.JVMd-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda- :: \text{'heap. typeof-addr}) \text{ heap-read heap-write } P \text{ C M vs status};$

$\bigwedge ad al v T. \llbracket \text{NormalAction } (\text{ReadMem } ad \text{ al } v) \in \text{snd 'lset } E; \text{heap-base'.addr-loc-type TYPE('heap) typeof-addr } P \text{ ad al } T \rrbracket \implies \text{heap-base'.conf TYPE('heap) typeof-addr } P \text{ v } T \rrbracket$

$\implies E \in \text{JVM-heap-base.JVMd-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda- :: \text{'heap. typeof-addr}) \text{ (heap-base.heap-read-typed } (\lambda- :: \text{'heap. typeof-addr}) \text{ heap-read } P) \text{ heap-write } P \text{ C M vs status}$

$\langle \text{proof} \rangle$

lemma *jmm'-exec-instrI*:

$\llbracket (ta, xcpfrs) \in \text{JVM-heap-base.exec-instr addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write } i \text{ P t h stk loc C M pc frs};$

$\text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) s t ta} \rrbracket$

$\implies \exists ta xcpfrs. (ta, xcpfrs) \in \text{JVM-heap-base.exec-instr addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read } P) \text{ jmm-heap-write } i \text{ P t h stk loc C M pc frs} \wedge \text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) s t ta}$

$\langle \text{proof} \rangle$

lemma *jmm'-execI*:

$\llbracket (ta, xcpfrs') \in \text{JVM-heap-base.exec addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write } P \text{ t xcpfrs};$

$\text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) s t ta} \rrbracket$

$\implies \exists ta xcpfrs'. (ta, xcpfrs') \in \text{JVM-heap-base.exec addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read } P) \text{ jmm-heap-write } P \text{ t xcpfrs} \wedge \text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) s t ta}$

$\langle \text{proof} \rangle$

lemma *jmm'-execI*:

$\llbracket \text{JVM-heap-base.exec-1-d addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write } P \text{ t (Normal xcpfrs) ta (Normal xcpfrs')};$

$\text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) s t ta} \rrbracket$

$\implies \exists ta xcpfrs'. \text{JVM-heap-base.exec-1-d addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read } P) \text{ jmm-heap-write } P \text{ t (Normal xcpfrs) ta (Normal xcpfrs')} \wedge \text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) s t ta}$

$\langle \text{proof} \rangle$

lemma *jmm'-mexecdI*:

\llbracket *JVM-heap-base.mexecd* *addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write P t xcpfrsh ta xcpfrsh'*;
final-thread.actions-ok (*final-thread.init-fin-final JVM-final*) *s t ta* \rrbracket
 $\implies \exists ta xcpfrsh'. \text{JVM-heap-base.mexecd } \text{addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read P) jmm-heap-write P t xcpfrsh ta xcpfrsh}' \wedge \text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) s t ta}$
 $\langle \text{proof} \rangle$

lemma *if-mexecd-heap-read-not-stuck*:

\llbracket *multithreaded-base.init-fin JVM-final* (*JVM-heap-base.mexecd* *addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write P*) *t xh ta x'h'*;
final-thread.actions-ok (*final-thread.init-fin-final JVM-final*) *s t ta* \rrbracket
 $\implies \exists ta x'h'. \text{multithreaded-base.init-fin JVM-final (JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read P) jmm-heap-write P) t xh ta x'h}' \wedge \text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) s t ta}$
 $\langle \text{proof} \rangle$

lemma *if-mExecd-heap-read-not-stuck*:

multithreaded-base.redT (*final-thread.init-fin-final JVM-final*) (*multithreaded-base.init-fin JVM-final* (*JVM-heap-base.mexecd* *addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write P*)) *convert-RA' s tta s'*
 $\implies \exists tta s'. \text{multithreaded-base.redT (final-thread.init-fin-final JVM-final) (multithreaded-base.init-fin JVM-final (JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read P) jmm-heap-write P)) convert-RA' s tta s'}$
 $\langle \text{proof} \rangle$

lemma *JVM-legal-typesafe1*:

assumes *wfP*: *wf-jvm-prog P*
and *ok*: *jmm-wf-start-state P C M vs*
and *legal*: *legal-execution P (jmm-JVMd- \mathcal{E} P C M vs status) (E, ws)*
shows *legal-execution P (jmm'-JVMd- \mathcal{E} P C M vs status) (E, ws)*
 $\langle \text{proof} \rangle$

lemma *JVM-weakly-legal-typesafe1*:

assumes *wfP*: *wf-jvm-prog P*
and *ok*: *jmm-wf-start-state P C M vs*
and *legal*: *weakly-legal-execution P (jmm-JVMd- \mathcal{E} P C M vs status) (E, ws)*
shows *weakly-legal-execution P (jmm'-JVMd- \mathcal{E} P C M vs status) (E, ws)*
 $\langle \text{proof} \rangle$

lemma *JVMd- \mathcal{E} -heap-read-typedD*:

$E \in \text{JVM-heap-base.JVMd-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda-. \text{typeof-addr}) \text{ (heap-base.heap-read-typed } (\lambda-. \text{typeof-addr}) \text{ jmm-heap-read P) jmm-heap-write P C M vs status}$
 $\implies E \in \text{JVM-heap-base.JVMd-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda-. \text{typeof-addr}) \text{ jmm-heap-read jmm-heap-write P C M vs status}$
 $\langle \text{proof} \rangle$

lemma *JVMd- \mathcal{E} -typesafe-subset*: *jmm'-JVMd- \mathcal{E} P C M vs status* \subseteq *jmm-JVMd- \mathcal{E} P C M vs status*

$\langle \text{proof} \rangle$

lemma *JVMd-legal-typesafe2*:

assumes *legal*: *legal-execution* P (*jmm'-JVMd- \mathcal{E}* P C M *vs status*) (E , ws)

shows *legal-execution* P (*jmm-JVMd- \mathcal{E}* P C M *vs status*) (E , ws)

<proof>

theorem *JVMd-weakly-legal-typesafe2*:

assumes *legal*: *weakly-legal-execution* P (*jmm'-JVMd- \mathcal{E}* P C M *vs status*) (E , ws)

shows *weakly-legal-execution* P (*jmm-JVMd- \mathcal{E}* P C M *vs status*) (E , ws)

<proof>

theorem *JVMd-weakly-legal-typesafe*:

assumes *wf-jvm-prog* P

and *jmm-wf-start-state* P C M *vs*

shows *weakly-legal-execution* P (*jmm-JVMd- \mathcal{E}* P C M *vs status*) = *weakly-legal-execution* P (*jmm'-JVMd- \mathcal{E}* P C M *vs status*)

<proof>

theorem *JVMd-legal-typesafe*:

assumes *wf-jvm-prog* P

and *jmm-wf-start-state* P C M *vs*

shows *legal-execution* P (*jmm-JVMd- \mathcal{E}* P C M *vs status*) = *legal-execution* P (*jmm'-JVMd- \mathcal{E}* P C M *vs status*)

<proof>

end

8.22 Compiler correctness for JMM heap implementation 2

theory *JMM-Compiler-Type2*

imports

JMM-Compiler

JMM-J-Typesafe

JMM-JVM-Typesafe

JMM-Interp

begin

theorem *J2JVM-jmm-correct*:

assumes *wf*: *wf-J-prog* P

and *wf-start*: *jmm-wf-start-state* P C M *vs*

shows *legal-execution* P (*jmm-J- \mathcal{E}* P C M *vs Running*) (E , ws) \longleftrightarrow

legal-execution (*J2JVM* P) (*jmm-JVMd- \mathcal{E}* (*J2JVM* P) C M *vs Running*) (E , ws)

<proof>

theorem *J2JVM-jmm-correct-weak*:

assumes *wf*: *wf-J-prog* P

and *wf-start*: *jmm-wf-start-state* P C M *vs*

shows *weakly-legal-execution* P (*jmm-J- \mathcal{E}* P C M *vs Running*) (E , ws) \longleftrightarrow

weakly-legal-execution (*J2JVM* P) (*jmm-JVMd- \mathcal{E}* (*J2JVM* P) C M *vs Running*) (E , ws)

<proof>

theorem *J2JVM-jmm-correctly-synchronized*:

assumes *wf*: *wf-J-prog* P

and *wf-start*: *jmm-wf-start-state* P C M *vs*

and $ka: \bigcup (ka\text{-Val } \textit{set vs}) \subseteq \textit{set jmm.start-addr}$
shows $\textit{correctly-synchronized (J2JVM P) (jmm-JVMd-}\mathcal{E} \textit{ (J2JVM P) C M vs Running)} \longleftrightarrow$
 $\textit{correctly-synchronized P (jmm-J-}\mathcal{E} \textit{ P C M vs Running)}$
(is $?lhs \longleftrightarrow ?rhs$)
 <proof>

end

theory *JMM*

imports

JMM-DRF

SC-Legal

DRF-J

DRF-JVM

JMM-Type

JMM-Interp

JMM-Typesafe

JMM-J-Typesafe

JMM-JVM-Typesafe

JMM-Compiler-Type2

begin

end

theory *MM-Main*

imports

SC

SC-Interp

SC-Collections

JMM

begin

end

Chapter 9

Schedulers

9.1 Refinement for multithreaded states

```
theory State-Refinement
imports
  ../Framework/FWSemantics
  ../Common/StartConfig
begin

type-synonym
  ('l,'t,'m,'m-t,'m-w,'s-i) state-refine = ('l,'t) locks × ('m-t × 'm) × 'm-w × 's-i

locale state-refine-base =
  fixes final :: 'x ⇒ bool
  and r :: 't ⇒ ('x × 'm) ⇒ (('l,'t,'x,'m,'w,'o) thread-action × 'x × 'm) Predicate.pred
  and convert-RA :: 'l released-locks ⇒ 'o list
  and thr-α :: 'm-t ⇒ ('l,'t,'x) thread-info
  and thr-invar :: 'm-t ⇒ bool
  and ws-α :: 'm-w ⇒ ('w,'t) wait-sets
  and ws-invar :: 'm-w ⇒ bool
  and is-α :: 's-i ⇒ 't interrupts
  and is-invar :: 's-i ⇒ bool
begin

fun state-α :: ('l,'t,'m,'m-t,'m-w, 's-i) state-refine ⇒ ('l,'t,'x,'m,'w) state
where state-α (ls, (ts, m), ws, is) = (ls, (thr-α ts, m), ws-α ws, is-α is)

lemma state-α-conv [simp]:
  locks (state-α s) = locks s
  thr (state-α s) = thr-α (thr s)
  shr (state-α s) = shr s
  wset (state-α s) = ws-α (wset s)
  interrupts (state-α s) = is-α (interrupts s)
⟨proof⟩

inductive state-invar :: ('l,'t,'m,'m-t,'m-w,'s-i) state-refine ⇒ bool
where [| thr-invar ts; ws-invar ws; is-invar is |] ⇒ state-invar (ls, (ts, m), ws, is)

inductive-simps state-invar-simps [simp]:
  state-invar (ls, (ts, m), ws, is)
```

```

lemma state-invarD [simp]:
  assumes state-invar s
  shows thr-invar (thr s) ws-invar (wset s) is-invar (interrupts s)
  ⟨proof⟩

```

end

```

sublocale state-refine-base <  $\alpha$ : final-thread final ⟨proof⟩

```

```

sublocale state-refine-base <  $\alpha$ :
  multithreaded-base
  final
   $\lambda t xm ta x'm'. \text{Predicate.eval } (r t xm) (ta, x'm')$ 
  ⟨proof⟩

```

definition (**in** *heap-base*) *start-state-refine* ::

```

  'm-t  $\Rightarrow$  ('thread-id  $\Rightarrow$  ('x  $\times$  'addr released-locks)  $\Rightarrow$  'm-t  $\Rightarrow$  'm-t)  $\Rightarrow$  'm-w  $\Rightarrow$  's-i
   $\Rightarrow$  (cname  $\Rightarrow$  mname  $\Rightarrow$  ty list  $\Rightarrow$  ty  $\Rightarrow$  'md  $\Rightarrow$  'addr val list  $\Rightarrow$  'x)  $\Rightarrow$  'md prog  $\Rightarrow$  cname  $\Rightarrow$  mname
   $\Rightarrow$  'addr val list
   $\Rightarrow$  ('addr, 'thread-id, 'heap, 'm-t, 'm-w, 's-i) state-refine

```

where

```

   $\bigwedge is-empty.$ 
  start-state-refine thr-empty thr-update ws-empty is-empty f P C M vs =
  (let (D, Ts, T, m) = method P C M
    in (K$ None, (thr-update start-tid (f D M Ts T (the m) vs, no-wait-locks) thr-empty, start-heap),
    ws-empty, is-empty))

```

definition *Jinja-output* ::

```

  's  $\Rightarrow$  'thread-id  $\Rightarrow$  ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) thread-action
   $\Rightarrow$  ('thread-id  $\times$  ('addr, 'thread-id) obs-event list) option

```

where *Jinja-output* $\sigma t ta =$ (if $\{ta\}_o = []$ then None else Some (t, $\{ta\}_o$))

lemmas [*code*] =

```

  heap-base.start-state-refine-def

```

end

9.2 Abstract scheduler

theory *Scheduler*

imports

```

  State-Refinement
  ../Framework/FWProgressAux
  ../Framework/FWLTS

  ../Basic/JT-ICF

```

begin

Define an unfold operation that puts everything into one function to avoid duplicate evaluation.

definition *unfold-tllist'* :: ('a \Rightarrow 'b \times 'a + 'c) \Rightarrow 'a \Rightarrow ('b, 'c) *tllist*

where [*code del*]:

unfold-tllist' f =
unfold-tllist ($\lambda a. \exists c. f a = \text{Inr } c$) (*projr* \circ *f*) (*fst* \circ *projl* \circ *f*) (*snd* \circ *projl* \circ *f*)

lemma *unfold-tllist' [code]:*

unfold-tllist' f a =
(case f a of Inr c \Rightarrow TNil c | Inl (b, a') \Rightarrow TCons b (unfold-tllist' f a'))
 <proof>

type-synonym

$(l, t, x, m, w, o, m-t, m-w, s-i, s)$ *scheduler* =
 $'s \Rightarrow (l, t, m, m-t, m-w, s-i)$ *state-refine* $\Rightarrow (t \times ((l, t, x, m, w, o)$ *thread-action* $\times 'x \times 'm)$ *option*
 $\times 's)$ *option*

locale *scheduler-spec-base =*

state-refine-base
final r convert-RA
thr- α thr-invar
ws- α ws-invar
is- α is-invar
for *final* :: $'x \Rightarrow \text{bool}$
and *r* :: $'t \Rightarrow ('x \times 'm) \Rightarrow ((l, t, x, m, w, o)$ *thread-action* $\times 'x \times 'm)$ *Predicate.pred*
and *convert-RA* :: $'l$ *released-locks* $\Rightarrow 'o$ *list*
and *schedule* :: $(l, t, x, m, w, o, m-t, m-w, s-i, s)$ *scheduler*
and *σ -invar* :: $'s \Rightarrow 't$ *set* $\Rightarrow \text{bool}$
and *thr- α* :: $'m-t \Rightarrow (l, t, x)$ *thread-info*
and *thr-invar* :: $'m-t \Rightarrow \text{bool}$
and *ws- α* :: $'m-w \Rightarrow (w, t)$ *wait-sets*
and *ws-invar* :: $'m-w \Rightarrow \text{bool}$
and *is- α* :: $'s-i \Rightarrow 't$ *interrupts*
and *is-invar* :: $'s-i \Rightarrow \text{bool}$

locale *scheduler-spec =*

scheduler-spec-base
final r convert-RA
schedule σ -invar
thr- α thr-invar
ws- α ws-invar
is- α is-invar
for *final* :: $'x \Rightarrow \text{bool}$
and *r* :: $'t \Rightarrow ('x \times 'm) \Rightarrow ((l, t, x, m, w, o)$ *thread-action* $\times 'x \times 'm)$ *Predicate.pred*
and *convert-RA* :: $'l$ *released-locks* $\Rightarrow 'o$ *list*
and *schedule* :: $(l, t, x, m, w, o, m-t, m-w, s-i, s)$ *scheduler*
and *σ -invar* :: $'s \Rightarrow 't$ *set* $\Rightarrow \text{bool}$
and *thr- α* :: $'m-t \Rightarrow (l, t, x)$ *thread-info*
and *thr-invar* :: $'m-t \Rightarrow \text{bool}$
and *ws- α* :: $'m-w \Rightarrow (w, t)$ *wait-sets*
and *ws-invar* :: $'m-w \Rightarrow \text{bool}$
and *is- α* :: $'s-i \Rightarrow 't$ *interrupts*
and *is-invar* :: $'s-i \Rightarrow \text{bool}$
 +
fixes *invariant* :: (l, t, x, m, w) *state set*
assumes *schedule-NoneD:*
 [[*schedule* σ $s = \text{None}$; *state-invar* s ; *σ -invar* σ (*dom* (*thr- α* (*thr* s))); *state- α* $s \in \text{invariant}$]]

$\implies \alpha.\text{active-threads}(\text{state-}\alpha s) = \{\}$
and *schedule-Some-NoneD*:
 $\llbracket \text{schedule } \sigma s = \llbracket (t, \text{None}, \sigma') \rrbracket; \text{state-invar } s; \sigma\text{-invar } \sigma (\text{dom } (\text{thr-}\alpha (\text{thr } s))); \text{state-}\alpha s \in \text{invariant} \rrbracket$
 $\implies \exists x \text{ ln } n. \text{thr-}\alpha (\text{thr } s) t = \llbracket (x, \text{ln}) \rrbracket \wedge \text{ln } \$ n > 0 \wedge \neg \text{waiting} (\text{ws-}\alpha (\text{wset } s) t) \wedge \text{may-acquire-all} (\text{locks } s) t \text{ ln}$
and *schedule-Some-SomeD*:
 $\llbracket \text{schedule } \sigma s = \llbracket (t, \llbracket (ta, x', m') \rrbracket, \sigma') \rrbracket; \text{state-invar } s; \sigma\text{-invar } \sigma (\text{dom } (\text{thr-}\alpha (\text{thr } s))); \text{state-}\alpha s \in \text{invariant} \rrbracket$
 $\implies \exists x. \text{thr-}\alpha (\text{thr } s) t = \llbracket (x, \text{no-wait-locks}) \rrbracket \wedge \text{Predicate.eval} (r t (x, \text{shr } s)) (ta, x', m') \wedge \alpha.\text{actions-ok} (\text{state-}\alpha s) t ta$
and *schedule-invar-None*:
 $\llbracket \text{schedule } \sigma s = \llbracket (t, \text{None}, \sigma') \rrbracket; \text{state-invar } s; \sigma\text{-invar } \sigma (\text{dom } (\text{thr-}\alpha (\text{thr } s))); \text{state-}\alpha s \in \text{invariant} \rrbracket$
 $\implies \sigma\text{-invar } \sigma' (\text{dom } (\text{thr-}\alpha (\text{thr } s)))$
and *schedule-invar-Some*:
 $\llbracket \text{schedule } \sigma s = \llbracket (t, \llbracket (ta, x', m') \rrbracket, \sigma') \rrbracket; \text{state-invar } s; \sigma\text{-invar } \sigma (\text{dom } (\text{thr-}\alpha (\text{thr } s))); \text{state-}\alpha s \in \text{invariant} \rrbracket$
 $\implies \sigma\text{-invar } \sigma' (\text{dom } (\text{thr-}\alpha (\text{thr } s)) \cup \{t. \exists x m. \text{NewThread } t x m \in \text{set } \{\llbracket ta \rrbracket_t\}\})$

locale *pick-wakeup-spec-base* =
state-refine-base
final *r* *convert-RA*
thr-}\alpha *thr-invar*
ws-}\alpha *ws-invar*
is-}\alpha *is-invar*
for *final* :: 'x \Rightarrow bool
and *r* :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l, 't, 'x, 'm, 'w, 'o) *thread-action* \times 'x \times 'm) *Predicate.pred*
and *convert-RA* :: 'l *released-locks* \Rightarrow 'o *list*
and *pick-wakeup* :: 's \Rightarrow 't \Rightarrow 'w \Rightarrow 'm-w \Rightarrow 't *option*
and *\sigma-invar* :: 's \Rightarrow 't *set* \Rightarrow bool
and *thr-}\alpha* :: 'm-t \Rightarrow ('l, 't, 'x) *thread-info*
and *thr-invar* :: 'm-t \Rightarrow bool
and *ws-}\alpha* :: 'm-w \Rightarrow ('w, 't) *wait-sets*
and *ws-invar* :: 'm-w \Rightarrow bool
and *is-}\alpha* :: 's-i \Rightarrow 't *interrupts*
and *is-invar* :: 's-i \Rightarrow bool

locale *pick-wakeup-spec* =
pick-wakeup-spec-base
final *r* *convert-RA*
pick-wakeup *\sigma-invar*
thr-}\alpha *thr-invar*
ws-}\alpha *ws-invar*
is-}\alpha *is-invar*
for *final* :: 'x \Rightarrow bool
and *r* :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l, 't, 'x, 'm, 'w, 'o) *thread-action* \times 'x \times 'm) *Predicate.pred*
and *convert-RA* :: 'l *released-locks* \Rightarrow 'o *list*
and *pick-wakeup* :: 's \Rightarrow 't \Rightarrow 'w \Rightarrow 'm-w \Rightarrow 't *option*
and *\sigma-invar* :: 's \Rightarrow 't *set* \Rightarrow bool
and *thr-}\alpha* :: 'm-t \Rightarrow ('l, 't, 'x) *thread-info*
and *thr-invar* :: 'm-t \Rightarrow bool
and *ws-}\alpha* :: 'm-w \Rightarrow ('w, 't) *wait-sets*
and *ws-invar* :: 'm-w \Rightarrow bool


```

and is- $\alpha$  :: 's-i  $\Rightarrow$  't interrupts
and is-invar :: 's-i  $\Rightarrow$  bool
+
assumes pick-wakeup-NoneD:
[[ pick-wakeup  $\sigma$  t w ws = None; ws-invar ws;  $\sigma$ -invar  $\sigma$  T; dom (ws- $\alpha$  ws)  $\subseteq$  T; t  $\in$  T ]]
 $\Rightarrow$  InWS w  $\notin$  ran (ws- $\alpha$  ws)
and pick-wakeup-SomeD:
[[ pick-wakeup  $\sigma$  t w ws = [t']; ws-invar ws;  $\sigma$ -invar  $\sigma$  T; dom (ws- $\alpha$  ws)  $\subseteq$  T; t  $\in$  T ]]
 $\Rightarrow$  ws- $\alpha$  ws t' = [InWS w]

```

```

locale scheduler-base-aux =
  state-refine-base
  final r convert-RA
  thr- $\alpha$  thr-invar
  ws- $\alpha$  ws-invar
  is- $\alpha$  is-invar
for final :: 'x  $\Rightarrow$  bool
and r :: 't  $\Rightarrow$  ('x  $\times$  'm)  $\Rightarrow$  (('l,'t,'x,'m,'w,'o) thread-action  $\times$  'x  $\times$  'm) Predicate.pred
and convert-RA :: 'l released-locks  $\Rightarrow$  'o list
and thr- $\alpha$  :: 'm-t  $\Rightarrow$  ('l,'t,'x) thread-info
and thr-invar :: 'm-t  $\Rightarrow$  bool
and thr-lookup :: 't  $\Rightarrow$  'm-t  $\rightarrow$  ('x  $\times$  'l released-locks)
and thr-update :: 't  $\Rightarrow$  'x  $\times$  'l released-locks  $\Rightarrow$  'm-t  $\Rightarrow$  'm-t
and ws- $\alpha$  :: 'm-w  $\Rightarrow$  ('w,'t) wait-sets
and ws-invar :: 'm-w  $\Rightarrow$  bool
and ws-lookup :: 't  $\Rightarrow$  'm-w  $\rightarrow$  'w wait-set-status
and is- $\alpha$  :: 's-i  $\Rightarrow$  't interrupts
and is-invar :: 's-i  $\Rightarrow$  bool
and is-memb :: 't  $\Rightarrow$  's-i  $\Rightarrow$  bool
and is-ins :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
and is-delete :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
begin

```

```

definition free-thread-id :: 'm-t  $\Rightarrow$  't  $\Rightarrow$  bool
where free-thread-id ts t  $\longleftrightarrow$  thr-lookup t ts = None

```

```

fun redT-updT :: 'm-t  $\Rightarrow$  ('t,'x,'m) new-thread-action  $\Rightarrow$  'm-t
where
  redT-updT ts (NewThread t' x m) = thr-update t' (x, no-wait-locks) ts
| redT-updT ts - = ts

```

```

definition redT-updTs :: 'm-t  $\Rightarrow$  ('t,'x,'m) new-thread-action list  $\Rightarrow$  'm-t
where redT-updTs = foldl redT-updT

```

```

primrec thread-ok :: 'm-t  $\Rightarrow$  ('t,'x,'m) new-thread-action  $\Rightarrow$  bool
where
  thread-ok ts (NewThread t x m) = free-thread-id ts t
| thread-ok ts (ThreadExists t b) = (b  $\neq$  free-thread-id ts t)

```

We use *local.redT-updT* in *thread-ok* instead of *redT-updT'* like in theory *JinjaThreads.FWThread*. This fixes 'x in the ('t, 'x, 'm) *new-thread-action list* type, but avoids *undefined*, which raises an exception during execution in the generated code.

```

primrec thread-oks :: 'm-t  $\Rightarrow$  ('t,'x,'m) new-thread-action list  $\Rightarrow$  bool
where

```

$thread\text{-}oks\ ts\ [] = True$
 $| thread\text{-}oks\ ts\ (ta\#\ tas) = (thread\text{-}ok\ ts\ ta \wedge thread\text{-}oks\ (redT\text{-}updT\ ts\ ta)\ tas)$

definition $wset\text{-}actions\text{-}ok :: 'm\text{-}w \Rightarrow 't \Rightarrow ('t, 'w)\ \text{wait-set-action list} \Rightarrow bool$

where

$wset\text{-}actions\text{-}ok\ ws\ t\ was \longleftrightarrow$
 $ws\text{-}lookup\ t\ ws =$
 (if $Notified \in set\ was$ then $[PostWS\ WSNotified]$
 else if $WokenUp \in set\ was$ then $[PostWS\ WSWokenUp]$
 else $None$)

primrec $cond\text{-}action\text{-}ok :: ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)\ \text{state-refine} \Rightarrow 't \Rightarrow 't\ \text{conditional-action} \Rightarrow bool$

where

$\bigwedge ln.\ cond\text{-}action\text{-}ok\ s\ t\ (Join\ T) =$
 (case $thr\text{-}lookup\ T\ (thr\ s)$
 of $None \Rightarrow True$
 $| [(x, ln)] \Rightarrow t \neq T \wedge final\ x \wedge ln = no\text{-}wait\text{-}locks \wedge ws\text{-}lookup\ T\ (wset\ s) = None$)
 $| cond\text{-}action\text{-}ok\ s\ t\ Yield = True$

definition $cond\text{-}action\text{-}oks :: ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)\ \text{state-refine} \Rightarrow 't \Rightarrow 't\ \text{conditional-action list} \Rightarrow bool$

where

$cond\text{-}action\text{-}oks\ s\ t\ cts = list\text{-}all\ (cond\text{-}action\text{-}ok\ s\ t)\ cts$

primrec $redT\text{-}updI :: 's\text{-}i \Rightarrow 't\ \text{interrupt-action} \Rightarrow 's\text{-}i$

where

$redT\text{-}updI\ is\ (Interrupt\ t) = is\text{-}ins\ t\ is$
 $| redT\text{-}updI\ is\ (ClearInterrupt\ t) = is\text{-}delete\ t\ is$
 $| redT\text{-}updI\ is\ (IsInterrupted\ t\ b) = is$

primrec $redT\text{-}updIs :: 's\text{-}i \Rightarrow 't\ \text{interrupt-action list} \Rightarrow 's\text{-}i$

where

$redT\text{-}updIs\ is\ [] = is$
 $| redT\text{-}updIs\ is\ (ia\ \#\ ias) = redT\text{-}updIs\ (redT\text{-}updI\ is\ ia)\ ias$

primrec $interrupt\text{-}action\text{-}ok :: 's\text{-}i \Rightarrow 't\ \text{interrupt-action} \Rightarrow bool$

where

$interrupt\text{-}action\text{-}ok\ is\ (Interrupt\ t) = True$
 $| interrupt\text{-}action\text{-}ok\ is\ (ClearInterrupt\ t) = True$
 $| interrupt\text{-}action\text{-}ok\ is\ (IsInterrupted\ t\ b) = (b = (is\text{-}memb\ t\ is))$

primrec $interrupt\text{-}actions\text{-}ok :: 's\text{-}i \Rightarrow 't\ \text{interrupt-action list} \Rightarrow bool$

where

$interrupt\text{-}actions\text{-}ok\ is\ [] = True$
 $| interrupt\text{-}actions\text{-}ok\ is\ (ia\ \#\ ias) \longleftrightarrow interrupt\text{-}action\text{-}ok\ is\ ia \wedge interrupt\text{-}actions\text{-}ok\ (redT\text{-}updI\ is\ ia)\ ias$

definition $actions\text{-}ok :: ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)\ \text{state-refine} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o')\ \text{thread-action} \Rightarrow bool$

where

$actions\text{-}ok\ s\ t\ ta \longleftrightarrow$
 $lock\text{-}ok\text{-}las\ (locks\ s)\ t\ \{\!\!|ta\!\!\}_l \wedge$
 $thread\text{-}oks\ (thr\ s)\ \{\!\!|ta\!\!\}_t \wedge$
 $cond\text{-}action\text{-}oks\ s\ t\ \{\!\!|ta\!\!\}_c \wedge$

wset-actions-ok (*wset s*) *t* $\{\{ta\}\}_w \wedge$
interrupt-actions-ok (*interrupts s*) $\{\{ta\}\}_i$

end

locale *scheduler-base* =

scheduler-base-aux

final r convert-RA

thr- α thr-invar thr-lookup thr-update

ws- α ws-invar ws-lookup

is- α is-invar is-memb is-ins is-delete

+

scheduler-spec-base

final r convert-RA

schedule σ -invar

thr- α thr-invar

ws- α ws-invar

is- α is-invar

+

pick-wakeup-spec-base

final r convert-RA

pick-wakeup σ -invar

thr- α thr-invar

ws- α ws-invar

is- α is-invar

for *final* :: '*x* \Rightarrow *bool*

and *r* :: '*t* \Rightarrow ('*x* \times '*m*) \Rightarrow (('l, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action* \times '*x* \times '*m*) *Predicate.pred*

and *convert-RA* :: '*l* *released-locks* \Rightarrow '*o* *list*

and *schedule* :: ('l, '*t*, '*x*, '*m*, '*w*, '*o*, '*m-t*, '*m-w*, '*s-i*, '*s*) *scheduler*

and *output* :: '*s* \Rightarrow '*t* \Rightarrow ('l, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action* \Rightarrow '*q* *option*

and *pick-wakeup* :: '*s* \Rightarrow '*t* \Rightarrow '*w* \Rightarrow '*m-w* \Rightarrow '*t* *option*

and *σ -invar* :: '*s* \Rightarrow '*t* *set* \Rightarrow *bool*

and *thr- α* :: '*m-t* \Rightarrow ('l, '*t*, '*x*) *thread-info*

and *thr-invar* :: '*m-t* \Rightarrow *bool*

and *thr-lookup* :: '*t* \Rightarrow '*m-t* \rightarrow ('*x* \times '*l* *released-locks*)

and *thr-update* :: '*t* \Rightarrow '*x* \times '*l* *released-locks* \Rightarrow '*m-t* \Rightarrow '*m-t*

and *ws- α* :: '*m-w* \Rightarrow ('*w*, '*t*) *wait-sets*

and *ws-invar* :: '*m-w* \Rightarrow *bool*

and *ws-lookup* :: '*t* \Rightarrow '*m-w* \rightarrow '*w* *wait-set-status*

and *ws-update* :: '*t* \Rightarrow '*w* *wait-set-status* \Rightarrow '*m-w* \Rightarrow '*m-w*

and *ws-delete* :: '*t* \Rightarrow '*m-w* \Rightarrow '*m-w*

and *ws-iterate* :: '*m-w* \Rightarrow ('*t* \times '*w* *wait-set-status*, '*m-w*) *set-iterator*

and *is- α* :: '*s-i* \Rightarrow '*t* *interrupts*

and *is-invar* :: '*s-i* \Rightarrow *bool*

and *is-memb* :: '*t* \Rightarrow '*s-i* \Rightarrow *bool*

and *is-ins* :: '*t* \Rightarrow '*s-i* \Rightarrow '*s-i*

and *is-delete* :: '*t* \Rightarrow '*s-i* \Rightarrow '*s-i*

begin

primrec *exec-updW* :: '*s* \Rightarrow '*t* \Rightarrow '*m-w* \Rightarrow ('*t*, '*w*) *wait-set-action* \Rightarrow '*m-w*

where

exec-updW σ *t* *ws* (*Notify w*) =

(*case pick-wakeup* σ *t* *w* *ws*

of None \Rightarrow *ws*

$|$ Some $t \Rightarrow$ ws -update t ($PostWS$ $WSNotified$) ws)
 $|$ $exec\text{-}updW$ σ t ws ($NotifyAll$ w) =
 ws -iterate ws ($\lambda\cdot$. $True$) ($\lambda(t, w')$ ws' . if $w' = InWS$ w then ws -update t ($PostWS$ $WSNotified$) ws'
else ws')
 ws
 $|$ $exec\text{-}updW$ σ t ws ($Suspend$ w) = ws -update t ($InWS$ w) ws
 $|$ $exec\text{-}updW$ σ t ws ($WakeUp$ t') =
(case ws -lookup t' ws of $[InWS$ $w] \Rightarrow$ ws -update t' ($PostWS$ $WSWokenUp$) ws $|$ - \Rightarrow ws)
 $|$ $exec\text{-}updW$ σ t ws $Notified$ = ws -delete t ws
 $|$ $exec\text{-}updW$ σ t ws $WokenUp$ = ws -delete t ws

definition $exec\text{-}updWs$:: $'s \Rightarrow 't \Rightarrow 'm\text{-}w \Rightarrow ('t, 'w)$ wait-set-action list $\Rightarrow 'm\text{-}w$
where $exec\text{-}updWs$ σ t = $foldl$ ($exec\text{-}updW$ σ t)

definition $exec\text{-}upd$::

$'s \Rightarrow ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)$ state-refine $\Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o)$ thread-action $\Rightarrow 'x \Rightarrow 'm$
 $\Rightarrow ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)$ state-refine

where [$simp$]:

$exec\text{-}upd$ σ s t x' m' =
($redT\text{-}updLs$ ($locks$ s) t $\{ta\}_l$,
(thr -update t (x' , $redT\text{-}updLns$ ($locks$ s) t (snd (the (thr -lookup t (thr s)))) $\{ta\}_l$) ($redT\text{-}updTs$ (thr
 s) $\{ta\}_t$), m'),
 $exec\text{-}updWs$ σ t ($wset$ s) $\{ta\}_w$, $redT\text{-}updIs$ ($interrupts$ s) $\{ta\}_i$)

definition $execT$::

$'s \Rightarrow ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)$ state-refine
 $\Rightarrow ('s \times 't \times ('l, 't, 'x, 'm, 'w, 'o)$ thread-action $\times ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)$ state-refine) option

where

$execT$ σ s =
(do {
(t , $tax'm'$, σ') \leftarrow schedule σ s ;
case $tax'm'$ of
None \Rightarrow
(let (x , ln) = the (thr -lookup t (thr s));
 ta = ($K\$$ \square , \square , \square , \square , \square , $convert\text{-}RA$ ln);
 s' = ($acquire\text{-}all$ ($locks$ s) t ln , (thr -update t (x , $no\text{-}wait\text{-}locks$) (thr s), shr s), $wset$ s , $interrupts$
 s)
in $[(\sigma', t, ta, s')]$)
 $|$ $[(ta, x', m')] \Rightarrow [(\sigma', t, ta, exec\text{-}upd$ σ s t ta x' $m')$
})

primrec $exec\text{-}step$::

$'s \times ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)$ state-refine \Rightarrow
($'s \times 't \times ('l, 't, 'x, 'm, 'w, 'o)$ thread-action $\times 's \times ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)$ state-refine + ($'l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i$)
state-refine

where

$exec\text{-}step$ (σ , s) =
(case $execT$ σ s of
None \Rightarrow Inr s
 $|$ Some (σ' , t , ta , s') \Rightarrow Inl ((σ , t , ta), σ' , s')

declare $exec\text{-}step.simps$ [$simp$ del]

definition $exec\text{-}aux$::

's × ('l,'t,'m,'m-t,'m-w,'s-i) state-refine
 ⇒ ('s × 't × ('l,'t,'x,'m,'w,'o) thread-action, ('l,'t,'m,'m-t,'m-w,'s-i) state-refine) tllist

where

exec-aux σ s = unfold-tllist' exec-step σ s

definition exec :: 's ⇒ ('l,'t,'m,'m-t,'m-w,'s-i) state-refine ⇒ ('q, ('l,'t,'m,'m-t,'m-w,'s-i) state-refine)
 tllist

where

exec σ s = tmap the id (tfilter undefined (λq. q ≠ None) (tmap (λ(σ, t, ta). output σ t ta) id
 (exec-aux (σ, s))))

end

Implement pick-wakeup by map-sel'

definition pick-wakeup-via-sel ::

('m-w ⇒ ('t ⇒ 'w wait-set-status ⇒ bool) → 't × 'w wait-set-status)
 ⇒ 's ⇒ 't ⇒ 'w ⇒ 'm-w ⇒ 't option

where pick-wakeup-via-sel ws-sel σ t w ws = map-option fst (ws-sel ws (λt w'. w' = InWS w))

lemma pick-wakeup-spec-via-sel:

assumes sel: map-sel' ws-α ws-invar ws-sel

shows pick-wakeup-spec (pick-wakeup-via-sel (λs P. ws-sel s (λ(k,v). P k v))) σ-invar ws-α ws-invar
 ⟨proof⟩

locale scheduler-ext-base =

scheduler-base-aux

final r convert-RA

thr-α thr-invar thr-lookup thr-update

ws-α ws-invar ws-lookup

is-α is-invar is-memb is-ins is-delete

for final :: 'x ⇒ bool

and r :: 't ⇒ ('x × 'm) ⇒ (('l,'t,'x,'m,'w,'o) thread-action × 'x × 'm) Predicate.pred

and convert-RA :: 'l released-locks ⇒ 'o list

and thr-α :: 'm-t ⇒ ('l,'t,'x) thread-info

and thr-invar :: 'm-t ⇒ bool

and thr-lookup :: 't ⇒ 'm-t → ('x × 'l released-locks)

and thr-update :: 't ⇒ 'x × 'l released-locks ⇒ 'm-t ⇒ 'm-t

and thr-iterate :: 'm-t ⇒ ('t × ('x × 'l released-locks), 's-t) set-iterator

and ws-α :: 'm-w ⇒ ('w,'t) wait-sets

and ws-invar :: 'm-w ⇒ bool

and ws-lookup :: 't ⇒ 'm-w → 'w wait-set-status

and ws-update :: 't ⇒ 'w wait-set-status ⇒ 'm-w ⇒ 'm-w

and ws-sel :: 'm-w ⇒ ('t × 'w wait-set-status ⇒ bool) → ('t × 'w wait-set-status)

and is-α :: 's-i ⇒ 't interrupts

and is-invar :: 's-i ⇒ bool

and is-memb :: 't ⇒ 's-i ⇒ bool

and is-ins :: 't ⇒ 's-i ⇒ 's-i

and is-delete :: 't ⇒ 's-i ⇒ 's-i

+

fixes thr'-α :: 's-t ⇒ 't set

and thr'-invar :: 's-t ⇒ bool

and thr'-empty :: unit ⇒ 's-t

and thr'-ins-dj :: 't ⇒ 's-t ⇒ 's-t

begin

abbreviation *pick-wakeup* :: 's ⇒ 't ⇒ 'w ⇒ 'm-w ⇒ 't option
where *pick-wakeup* ≡ *pick-wakeup-via-sel* (λs P. ws-sel s (λ(k,v). P k v))

fun *active-threads* :: ('l,'t,'m,'m-t,'m-w,'s-i) state-refine ⇒ 's-t

where

active-threads (ls, (ts, m), ws, is) =

thr-iterate ts (λ-. True)

(λ(t, (x, ln)) ts'. if ln = *no-wait-locks*

then if *Predicate.holds*

(do {

(ta, -) ← r t (x, m);

Predicate.if-pred (*actions-ok* (ls, (ts, m), ws, is) t ta)

})

then *thr'-ins-dj* t ts'

else ts'

else if ¬ *waiting* (*ws-lookup* t ws) ∧ *may-acquire-all* ls t ln then *thr'-ins-dj* t ts' else

ts')

(*thr'-empty* ())

end

locale *scheduler-aux* =

scheduler-base-aux

final r *convert-RA*

thr-α *thr-invar* *thr-lookup* *thr-update*

ws-α *ws-invar* *ws-lookup*

is-α *is-invar* *is-memb* *is-ins* *is-delete*

+

thr: *finite-map* *thr-α* *thr-invar* +

thr: *map-lookup* *thr-α* *thr-invar* *thr-lookup* +

thr: *map-update* *thr-α* *thr-invar* *thr-update* +

ws: *map* *ws-α* *ws-invar* +

ws: *map-lookup* *ws-α* *ws-invar* *ws-lookup* +

is: *set* *is-α* *is-invar* +

is: *set-memb* *is-α* *is-invar* *is-memb* +

is: *set-ins* *is-α* *is-invar* *is-ins* +

is: *set-delete* *is-α* *is-invar* *is-delete*

for *final* :: 'x ⇒ bool

and r :: 't ⇒ ('x × 'm) ⇒ (('l,'t,'x,'m,'w,'o) *thread-action* × 'x × 'm) *Predicate.pred*

and *convert-RA* :: 'l *released-locks* ⇒ 'o list

and *thr-α* :: 'm-t ⇒ ('l,'t,'x) *thread-info*

and *thr-invar* :: 'm-t ⇒ bool

and *thr-lookup* :: 't ⇒ 'm-t → ('x × 'l *released-locks*)

and *thr-update* :: 't ⇒ 'x × 'l *released-locks* ⇒ 'm-t ⇒ 'm-t

and *ws-α* :: 'm-w ⇒ ('w,'t) *wait-sets*

and *ws-invar* :: 'm-w ⇒ bool

and *ws-lookup* :: 't ⇒ 'm-w → 'w *wait-set-status*

and *is-α* :: 's-i ⇒ 't *interrupts*

and *is-invar* :: 's-i ⇒ bool

and *is-memb* :: 't ⇒ 's-i ⇒ bool

and *is-ins* :: 't ⇒ 's-i ⇒ 's-i

and *is-delete* :: 't ⇒ 's-i ⇒ 's-i

begin

lemma *free-thread-id-correct* [*simp*]:

thr-invar ts \implies *free-thread-id ts* = *FWThread.free-thread-id (thr- α ts)*
 \langle *proof* \rangle

lemma *redT-updT-correct* [*simp*]:

assumes *thr-invar ts*
shows *thr- α (redT-updT ts nta)* = *FWThread.redT-updT (thr- α ts) nta*
and *thr-invar (redT-updT ts nta)*
 \langle *proof* \rangle

lemma *redT-updTs-correct* [*simp*]:

assumes *thr-invar ts*
shows *thr- α (redT-updTs ts ntas)* = *FWThread.redT-updTs (thr- α ts) ntas*
and *thr-invar (redT-updTs ts ntas)*
 \langle *proof* \rangle

lemma *thread-ok-correct* [*simp*]:

thr-invar ts \implies *thread-ok ts nta* \longleftrightarrow *FWThread.thread-ok (thr- α ts) nta*
 \langle *proof* \rangle

lemma *thread-oks-correct* [*simp*]:

thr-invar ts \implies *thread-oks ts ntas* \longleftrightarrow *FWThread.thread-oks (thr- α ts) ntas*
 \langle *proof* \rangle

lemma *wset-actions-ok-correct* [*simp*]:

ws-invar ws \implies *wset-actions-ok ws t was* \longleftrightarrow *FWWait.wset-actions-ok (ws- α ws) t was*
 \langle *proof* \rangle

lemma *cond-action-ok-correct* [*simp*]:

state-invar s \implies *cond-action-ok s t cta* \longleftrightarrow *α .cond-action-ok (state- α s) t cta*
 \langle *proof* \rangle

lemma *cond-action-oks-correct* [*simp*]:

assumes *state-invar s*
shows *cond-action-oks s t ctas* \longleftrightarrow *α .cond-action-oks (state- α s) t ctas*
 \langle *proof* \rangle

lemma *redT-updI-correct* [*simp*]:

assumes *is-invar is*
shows *is- α (redT-updI is ia)* = *FWInterrupt.redT-updI (is- α is) ia*
and *is-invar (redT-updI is ia)*
 \langle *proof* \rangle

lemma *redT-updIs-correct* [*simp*]:

assumes *is-invar is*
shows *is- α (redT-updIs is ias)* = *FWInterrupt.redT-updIs (is- α is) ias*
and *is-invar (redT-updIs is ias)*
 \langle *proof* \rangle

lemma *interrupt-action-ok-correct* [*simp*]:

is-invar is \implies *interrupt-action-ok is ia* \longleftrightarrow *FWInterrupt.interrupt-action-ok (is- α is) ia*
 \langle *proof* \rangle

lemma *interrupt-actions-ok-correct* [simp]:

is-invar is \implies *interrupt-actions-ok is ias* \longleftrightarrow *FWInterrupt.interrupt-actions-ok (is- α is) ias*
 ⟨proof⟩

lemma *actions-ok-correct* [simp]:

state-invar s \implies *actions-ok s t ta* \longleftrightarrow α .*actions-ok (state- α s) t ta*
 ⟨proof⟩

end

locale *scheduler* =

scheduler-base

final r convert-RA

schedule output pick-wakeup σ -invar

thr- α thr-invar thr-lookup thr-update

ws- α ws-invar ws-lookup ws-update ws-delete ws-iterate

is- α is-invar is-memb is-ins is-delete

+

scheduler-aux

final r convert-RA

thr- α thr-invar thr-lookup thr-update

ws- α ws-invar ws-lookup

is- α is-invar is-memb is-ins is-delete

+

scheduler-spec

final r convert-RA

schedule σ -invar

thr- α thr-invar

ws- α ws-invar

is- α is-invar

invariant

+

pick-wakeup-spec

final r convert-RA

pick-wakeup σ -invar

thr- α thr-invar

ws- α ws-invar

is- α is-invar

+

ws: map-update ws- α ws-invar ws-update +

ws: map-delete ws- α ws-invar ws-delete +

ws: map-iteratei ws- α ws-invar ws-iterate

for *final* :: *'x* \Rightarrow *bool*

and *r* :: *'t* \Rightarrow (*'x* \times *'m*) \Rightarrow ((*'l, 't, 'x, 'm, 'w, 'o*) *thread-action* \times *'x* \times *'m*) *Predicate.pred*

and *convert-RA* :: *'l released-locks* \Rightarrow *'o list*

and *schedule* :: (*'l, 't, 'x, 'm, 'w, 'o, 'm-t, 'm-w, 's-i, 's*) *scheduler*

and *output* :: *'s* \Rightarrow *'t* \Rightarrow (*'l, 't, 'x, 'm, 'w, 'o*) *thread-action* \Rightarrow *'q option*

and *pick-wakeup* :: *'s* \Rightarrow *'t* \Rightarrow *'w* \Rightarrow *'m-w* \Rightarrow *'t option*

and *σ -invar* :: *'s* \Rightarrow *'t set* \Rightarrow *bool*

and *thr- α* :: *'m-t* \Rightarrow (*'l, 't, 'x*) *thread-info*

and *thr-invar* :: *'m-t* \Rightarrow *bool*

and *thr-lookup* :: *'t* \Rightarrow *'m-t* \rightarrow (*'x* \times *'l released-locks*)

and *thr-update* :: *'t* \Rightarrow *'x* \times *'l released-locks* \Rightarrow *'m-t* \Rightarrow *'m-t*

and *ws- α* :: *'m-w* \Rightarrow (*'w, 't*) *wait-sets*

and $ws\text{-invar} :: 'm\text{-}w \Rightarrow \text{bool}$
and $ws\text{-lookup} :: 't \Rightarrow 'm\text{-}w \rightarrow 'w \text{ wait-set-status}$
and $ws\text{-update} :: 't \Rightarrow 'w \text{ wait-set-status} \Rightarrow 'm\text{-}w \Rightarrow 'm\text{-}w$
and $ws\text{-delete} :: 't \Rightarrow 'm\text{-}w \Rightarrow 'm\text{-}w$
and $ws\text{-iterate} :: 'm\text{-}w \Rightarrow ('t \times 'w \text{ wait-set-status}, 'm\text{-}w) \text{ set-iterator}$
and $is\text{-}\alpha :: 's\text{-}i \Rightarrow 't \text{ interrupts}$
and $is\text{-invar} :: 's\text{-}i \Rightarrow \text{bool}$
and $is\text{-memb} :: 't \Rightarrow 's\text{-}i \Rightarrow \text{bool}$
and $is\text{-ins} :: 't \Rightarrow 's\text{-}i \Rightarrow 's\text{-}i$
and $is\text{-delete} :: 't \Rightarrow 's\text{-}i \Rightarrow 's\text{-}i$
and $invariant :: ('l, 't, 'x, 'm, 'w) \text{ state set}$
 +
assumes $invariant: invariant3p \alpha.redT \text{ invariant}$
begin

lemma $exec\text{-upd}W\text{-correct}$:

assumes $invar: ws\text{-invar } ws \sigma\text{-invar } \sigma T \text{ dom } (ws\text{-}\alpha \text{ } ws) \subseteq T t \in T$
shows $redT\text{-upd}W t (ws\text{-}\alpha \text{ } ws) wa (ws\text{-}\alpha (exec\text{-upd}W \sigma t \text{ } ws \text{ } wa))$ (**is** $?thesis1$)
and $ws\text{-invar } (exec\text{-upd}W \sigma t \text{ } ws \text{ } wa)$ (**is** $?thesis2$)
 <proof>

lemma $exec\text{-upd}Ws\text{-correct}$:

assumes $ws\text{-invar } ws \sigma\text{-invar } \sigma T \text{ dom } (ws\text{-}\alpha \text{ } ws) \subseteq T t \in T$
shows $redT\text{-upd}Ws t (ws\text{-}\alpha \text{ } ws) was (ws\text{-}\alpha (exec\text{-upd}Ws \sigma t \text{ } ws \text{ } was))$ (**is** $?thesis1$)
and $ws\text{-invar } (exec\text{-upd}Ws \sigma t \text{ } ws \text{ } was)$ (**is** $?thesis2$)
 <proof>

lemma $exec\text{-upd}\text{-correct}$:

assumes $state\text{-invar } s \sigma\text{-invar } \sigma (\text{dom } (thr\text{-}\alpha (thr \text{ } s))) t \in (\text{dom } (thr\text{-}\alpha (thr \text{ } s)))$
and $wset\text{-thread-ok } (ws\text{-}\alpha (wset \text{ } s)) (thr\text{-}\alpha (thr \text{ } s))$
shows $redT\text{-upd } (state\text{-}\alpha \text{ } s) t \text{ ta } x' m' (state\text{-}\alpha (exec\text{-upd } \sigma s t \text{ ta } x' m'))$
and $state\text{-invar } (exec\text{-upd } \sigma s t \text{ ta } x' m')$
 <proof>

lemma $execT\text{-None}$:

assumes $invar: state\text{-invar } s \sigma\text{-invar } \sigma (\text{dom } (thr\text{-}\alpha (thr \text{ } s))) state\text{-}\alpha \text{ } s \in invariant$
and $exec: execT \sigma s = \text{None}$
shows $\alpha.active\text{-threads } (state\text{-}\alpha \text{ } s) = \{\}$
 <proof>

lemma $execT\text{-Some}$:

assumes $invar: state\text{-invar } s \sigma\text{-invar } \sigma (\text{dom } (thr\text{-}\alpha (thr \text{ } s))) state\text{-}\alpha \text{ } s \in invariant$
and $wstok: wset\text{-thread-ok } (ws\text{-}\alpha (wset \text{ } s)) (thr\text{-}\alpha (thr \text{ } s))$
and $exec: execT \sigma s = \lfloor (\sigma', t, ta, s') \rfloor$
shows $\alpha.redT (state\text{-}\alpha \text{ } s) (t, ta) (state\text{-}\alpha \text{ } s')$ (**is** $?thesis1$)
and $state\text{-invar } s'$ (**is** $?thesis2$)
and $\sigma\text{-invar } \sigma' (\text{dom } (thr\text{-}\alpha (thr \text{ } s')))$ (**is** $?thesis3$)
 <proof>

lemma $exec\text{-step}\text{-into}\text{-red}T$:

assumes $invar: state\text{-invar } s \sigma\text{-invar } \sigma (\text{dom } (thr\text{-}\alpha (thr \text{ } s))) state\text{-}\alpha \text{ } s \in invariant$
and $wstok: wset\text{-thread-ok } (ws\text{-}\alpha (wset \text{ } s)) (thr\text{-}\alpha (thr \text{ } s))$
and $exec: exec\text{-step } (\sigma, s) = \text{Inl } ((\sigma'', t, ta), \sigma', s')$
shows $\alpha.redT (state\text{-}\alpha \text{ } s) (t, ta) (state\text{-}\alpha \text{ } s') \sigma'' = \sigma$

and *state-invar* s' *σ -invar* σ' ($\text{dom}(\text{thr-}\alpha(\text{thr } s'))$) *state- α* $s' \in \text{invariant}$
 <proof>

lemma *exec-step-InrD*:

assumes *state-invar* s *σ -invar* σ ($\text{dom}(\text{thr-}\alpha(\text{thr } s))$) *state- α* $s \in \text{invariant}$

and *exec-step* $(\sigma, s) = \text{Inr } s'$

shows $\alpha.\text{active-threads}(\text{state-}\alpha s) = \{\}$

and $s' = s$

<proof>

lemma (**in** *multithreaded-base*) *red-in-active-threads*:

assumes $s -t \triangleright ta \rightarrow s'$

shows $t \in \text{active-threads } s$

<proof>

lemma *exec-aux-into-Runs*:

assumes *state-invar* s *σ -invar* σ ($\text{dom}(\text{thr-}\alpha(\text{thr } s))$) *state- α* $s \in \text{invariant}$

and *wset-thread-ok* ($\text{ws-}\alpha(\text{wset } s)$) ($\text{thr-}\alpha(\text{thr } s)$)

shows $\alpha.\text{mthr.Runs}(\text{state-}\alpha s) (\text{lmap } \text{snd} (\text{llist-of-tl} (\text{exec-aux } (\sigma, s))))$ (**is** *?thesis1*)

and *tfinite* ($\text{exec-aux } (\sigma, s)) \implies \text{state-invar}(\text{terminal}(\text{exec-aux } (\sigma, s)))$ (**is** $- \implies$ *?thesis2*)

<proof>

end

locale *scheduler-ext-aux* =

scheduler-ext-base

final r *convert-RA*

thr- α *thr-invar* *thr-lookup* *thr-update* *thr-iterate*

ws- α *ws-invar* *ws-lookup* *ws-update* *ws-sel*

is- α *is-invar* *is-memb* *is-ins* *is-delete*

thr'- α *thr'-invar* *thr'-empty* *thr'-ins-dj*

+

scheduler-aux

final r *convert-RA*

thr- α *thr-invar* *thr-lookup* *thr-update*

ws- α *ws-invar* *ws-lookup*

is- α *is-invar* *is-memb* *is-ins* *is-delete*

+

thr: *map-iteratei* *thr- α* *thr-invar* *thr-iterate* +

ws: *map-update* *ws- α* *ws-invar* *ws-update* +

ws: *map-sel'* *ws- α* *ws-invar* *ws-sel* +

thr': *finite-set* *thr'- α* *thr'-invar* +

thr': *set-empty* *thr'- α* *thr'-invar* *thr'-empty* +

thr': *set-ins-dj* *thr'- α* *thr'-invar* *thr'-ins-dj*

for *final* :: $'x \Rightarrow \text{bool}$

and r :: $'t \Rightarrow ('x \times 'm) \Rightarrow (('l, 't, 'x, 'm, 'w, 'o) \text{thread-action} \times 'x \times 'm) \text{Predicate.pred}$

and *convert-RA* :: $'l \text{released-locks} \Rightarrow 'o \text{list}$

and *thr- α* :: $'m-t \Rightarrow ('l, 't, 'x) \text{thread-info}$

and *thr-invar* :: $'m-t \Rightarrow \text{bool}$

and *thr-lookup* :: $'t \Rightarrow 'm-t \rightarrow ('x \times 'l \text{released-locks})$

and *thr-update* :: $'t \Rightarrow 'x \times 'l \text{released-locks} \Rightarrow 'm-t \Rightarrow 'm-t$

and *thr-iterate* :: $'m-t \Rightarrow ('t \times ('x \times 'l \text{released-locks}), 's-t) \text{set-iterator}$

and *ws- α* :: $'m-w \Rightarrow ('w, 't) \text{wait-sets}$

and *ws-invar* :: $'m-w \Rightarrow \text{bool}$

```

and ws-lookup :: 't ⇒ 'm-w → 'w wait-set-status
and ws-update :: 't ⇒ 'w wait-set-status ⇒ 'm-w ⇒ 'm-w
and ws-sel :: 'm-w ⇒ (('t × 'w wait-set-status) ⇒ bool) → ('t × 'w wait-set-status)
and is-α :: 's-i ⇒ 't interrupts
and is-invar :: 's-i ⇒ bool
and is-memb :: 't ⇒ 's-i ⇒ bool
and is-ins :: 't ⇒ 's-i ⇒ 's-i
and is-delete :: 't ⇒ 's-i ⇒ 's-i
and thr'-α :: 's-t ⇒ 't set
and thr'-invar :: 's-t ⇒ bool
and thr'-empty :: unit ⇒ 's-t
and thr'-ins-dj :: 't ⇒ 's-t ⇒ 's-t
begin

```

lemma *active-threads-correct* [*simp*]:

```

  assumes state-invar s
  shows thr'-α (active-threads s) = α.active-threads (state-α s) (is ?thesis1)
  and thr'-invar (active-threads s) (is ?thesis2)
  ⟨proof⟩

```

end

locale *scheduler-ext* =

scheduler-ext-aux

final r convert-RA

thr-α thr-invar thr-lookup thr-update thr-iterate

ws-α ws-invar ws-lookup ws-update ws-sel

is-α is-invar is-memb is-ins is-delete

thr'-α thr'-invar thr'-empty thr'-ins-dj

+

scheduler-spec

final r convert-RA

schedule σ-invar

thr-α thr-invar

ws-α ws-invar

is-α is-invar

invariant

+

ws: map-delete ws-α ws-invar ws-delete +

ws: map-iteratei ws-α ws-invar ws-iterate

for *final* :: 'x ⇒ bool

and *r* :: 't ⇒ ('x × 'm) ⇒ (('l, 't, 'x, 'm, 'w, 'o) *thread-action* × 'x × 'm) *Predicate.pred*

and *convert-RA* :: 'l *released-locks* ⇒ 'o *list*

and *schedule* :: ('l, 't, 'x, 'm, 'w, 'o, 'm-t, 'm-w, 's-i, 's) *scheduler*

and *output* :: 's ⇒ 't ⇒ ('l, 't, 'x, 'm, 'w, 'o) *thread-action* ⇒ 'q *option*

and *σ-invar* :: 's ⇒ 't *set* ⇒ bool

and *thr-α* :: 'm-t ⇒ ('l, 't, 'x) *thread-info*

and *thr-invar* :: 'm-t ⇒ bool

and *thr-lookup* :: 't ⇒ 'm-t → ('x × 'l *released-locks*)

and *thr-update* :: 't ⇒ 'x × 'l *released-locks* ⇒ 'm-t ⇒ 'm-t

and *thr-iterate* :: 'm-t ⇒ ('t × ('x × 'l *released-locks*), 's-t) *set-iterator*

and *ws-α* :: 'm-w ⇒ ('w, 't) *wait-sets*

and *ws-invar* :: 'm-w ⇒ bool

and *ws-empty* :: unit ⇒ 'm-w

```

and ws-lookup :: 't ⇒ 'm-w → 'w wait-set-status
and ws-update :: 't ⇒ 'w wait-set-status ⇒ 'm-w ⇒ 'm-w
and ws-delete :: 't ⇒ 'm-w ⇒ 'm-w
and ws-iterate :: 'm-w ⇒ ('t × 'w wait-set-status, 'm-w) set-iterator
and ws-sel :: 'm-w ⇒ ('t × 'w wait-set-status ⇒ bool) → ('t × 'w wait-set-status)
and is-α :: 's-i ⇒ 't interrupts
and is-invar :: 's-i ⇒ bool
and is-memb :: 't ⇒ 's-i ⇒ bool
and is-ins :: 't ⇒ 's-i ⇒ 's-i
and is-delete :: 't ⇒ 's-i ⇒ 's-i
and thr'-α :: 's-t ⇒ 't set
and thr'-invar :: 's-t ⇒ bool
and thr'-empty :: unit ⇒ 's-t
and thr'-ins-dj :: 't ⇒ 's-t ⇒ 's-t
and invariant :: ('l, 't, 'x, 'm, 'w) state set
+
assumes invariant: invariant3p α.redT invariant

```

```

sublocale scheduler-ext <
  pick-wakeup-spec
  final r convert-RA
  pick-wakeup σ-invar
  thr-α thr-invar
  ws-α ws-invar
  ⟨proof⟩

```

```

sublocale scheduler-ext <
  scheduler
  final r convert-RA
  schedule output pick-wakeup σ-invar
  thr-α thr-invar thr-lookup thr-update
  ws-α ws-invar ws-lookup ws-update ws-delete ws-iterate
  is-α is-invar is-memb is-ins is-delete
  invariant
  ⟨proof⟩

```

9.2.1 Schedulers for deterministic small-step semantics

The default code equations for *Predicate.the* impose the type class constraint *eq* on the predicate elements. For the semantics, which contains the heap, there might be no such instance, so we use new constants for which other code equations can be used. These do not add the type class constraint, but may fail more often with non-uniqueness exception.

definition *singleton2* **where** [*simp*]: *singleton2* = *Predicate.singleton*

definition *the-only2* **where** [*simp*]: *the-only2* = *Predicate.the-only*

definition *the2* **where** [*simp*]: *the2* = *Predicate.the*

context *multithreaded-base* **begin**

definition *step-thread* ::

```

((('l, 't, 'x, 'm, 'w, 'o) thread-action ⇒ 's) ⇒ ('l, 't, 'x, 'm, 'w) state ⇒ 't
⇒ ('t × (('l, 't, 'x, 'm, 'w, 'o) thread-action × 'x × 'm) option × 's) option

```

where

```

∧ln. step-thread update-state s t =

```

```

(case thr s t of
  [(x, ln)] ⇒
  if ln = no-wait-locks then
    if ∃ ta x' m'. t ⊢ (x, shr s) -ta→ (x', m') ∧ actions-ok s t ta then
      let
        (ta, x', m') = THE (ta, x', m'). t ⊢ (x, shr s) -ta→ (x', m') ∧ actions-ok s t ta
      in
        [(t, [(ta, x', m')], update-state ta)]
    else
      None
  else if may-acquire-all (locks s) t ln ∧ ¬ waiting (wset s t) then
    [(t, None, update-state (K$ [], [], [], [], [], convert-RA ln))]
  else
    None
| None ⇒ None)

```

lemma *step-thread-NoneD*:

step-thread update-state s t = None ⇒ t ∉ active-threads s
 ⟨proof⟩

lemma *inactive-step-thread-eq-NoneI*:

t ∉ active-threads s ⇒ step-thread update-state s t = None
 ⟨proof⟩

lemma *step-thread-eq-None-conv*:

step-thread update-state s t = None ⇔ t ∉ active-threads s
 ⟨proof⟩

lemma *step-thread-eq-Some-activeD*:

step-thread update-state s t = [(t', taxmσ')]
 ⇒ *t' = t ∧ t ∈ active-threads s*
 ⟨proof⟩

declare *actions-ok-iff* [simp del]

declare *actions-ok.cases* [rule del]

lemma *step-thread-Some-NoneD*:

step-thread update-state s t' = [(t, None, σ')]
 ⇒ ∃ x ln n. *thr s t = [(x, ln)] ∧ ln \$ n > 0 ∧ ¬ waiting (wset s t) ∧ may-acquire-all (locks s) t ln*
 ∧ *σ' = update-state (K\$ [], [], [], [], [], convert-RA ln)*
 ⟨proof⟩

lemma *step-thread-Some-SomeD*:

[[*deterministic I*; *step-thread update-state s t' = [(t, [(ta, x', m')], σ')]*; *s ∈ I*]]
 ⇒ ∃ x. *thr s t = [(x, no-wait-locks)] ∧ t ⊢ (x, shr s) -ta→ (x', m') ∧ actions-ok s t ta ∧ σ' =*
update-state ta
 ⟨proof⟩

end

context *scheduler-base-aux* **begin**

definition *step-thread* ::

((l',t',x',m',w',o) thread-action ⇒ 's) ⇒ (l',t',m',m-t,'m-w,'s-i) state-refine ⇒ 't ⇒

$(t \times ((l, t, x, m, w, o) \text{ thread-action} \times x \times m) \text{ option} \times s) \text{ option}$
where
 $\wedge \text{ln. step-thread update-state } s \ t =$
 (case *thr-lookup* t (*thr* s) of
 $\llbracket (x, \text{ln}) \rrbracket \Rightarrow$
 if $\text{ln} = \text{no-wait-locks}$ then
 let
 $\text{reds} = \text{do } \{$
 $(ta, x', m') \leftarrow r \ t \ (x, \text{shr } s);$
 if $\text{actions-ok } s \ t \ ta$ then $\text{Predicate.single } (ta, x', m')$ else bot
 }
 in
 if $\text{Predicate.holds } (\text{reds} \gg (\lambda-. \text{Predicate.single } ()))$ then
 let
 $(ta, x', m') = \text{the2 } \text{reds}$
 in
 $\llbracket (t, \llbracket (ta, x', m') \rrbracket, \text{update-state } ta) \rrbracket$
 else
 None
 else if $\text{may-acquire-all } (\text{locks } s) \ t \ \text{ln} \wedge \neg \text{waiting } (\text{ws-lookup } t \ (\text{wset } s))$ then
 $\llbracket (t, \text{None}, \text{update-state } (K\$ \ [], [], [], [], [], \text{convert-RA } \text{ln})) \rrbracket$
 else
 None
 | $\text{None} \Rightarrow \text{None}$)

end

context scheduler-aux begin

lemma deterministic-THE2:

assumes $\alpha.\text{deterministic } I$
and $\text{tst: thr-}\alpha \ (\text{thr } s) \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket$
and $\text{red: Predicate.eval } (r \ t \ (x, \text{shr } s)) \ (ta, x', m')$
and $\text{aok: } \alpha.\text{actions-ok } (\text{state-}\alpha \ s) \ t \ ta$
and $I: \text{state-}\alpha \ s \in I$
shows $\text{Predicate.the } (r \ t \ (x, \text{shr } s) \gg (\lambda(ta, x', m'). \text{if } \alpha.\text{actions-ok } (\text{state-}\alpha \ s) \ t \ ta \text{ then } \text{Predicate.single } (ta, x', m') \text{ else } \text{bot})) = (ta, x', m')$
 <proof>

lemma step-thread-correct:

assumes $\text{det: } \alpha.\text{deterministic } I$
and $\text{invar: } \sigma\text{-invar } \sigma \ (\text{dom } (\text{thr-}\alpha \ (\text{thr } s))) \ \text{state-invar } s \ \text{state-}\alpha \ s \in I$
shows
 $\text{map-option } (\text{apsnd } (\text{apsnd } \sigma\text{-}\alpha)) \ (\text{step-thread update-state } s \ t) = \alpha.\text{step-thread } (\sigma\text{-}\alpha \circ \text{update-state}) \ (\text{state-}\alpha \ s) \ t$ (**is** *?thesis1*)
and $(\wedge ta. \text{FWThread.thread-oks } (\text{thr-}\alpha \ (\text{thr } s)) \ \{\!| \text{ta} \!\}_t \Longrightarrow \sigma\text{-invar } (\text{update-state } ta) \ (\text{dom } (\text{thr-}\alpha \ (\text{thr } s)) \cup \{t. \exists x \ m. \text{NewThread } t \ x \ m \in \text{set } \{\!| \text{ta} \!\}_t\})) \Longrightarrow \text{case-option True } (\lambda(t, \text{taxm}, \sigma). \sigma\text{-invar } \sigma \ (\text{case } \text{taxm} \text{ of } \text{None} \Rightarrow \text{dom } (\text{thr-}\alpha \ (\text{thr } s)) \mid \text{Some } (ta, x', m') \Rightarrow \text{dom } (\text{thr-}\alpha \ (\text{thr } s)) \cup \{t. \exists x \ m. \text{NewThread } t \ x \ m \in \text{set } \{\!| \text{ta} \!\}_t\})) \ (\text{step-thread update-state } s \ t)$
 (**is** $(\wedge ta. \text{?tso } ta \Longrightarrow \text{?inv } ta) \Longrightarrow \text{?thesis2}$)
 <proof>

lemma step-thread-eq-None-conv:

assumes $\text{det: } \alpha.\text{deterministic } I$

and *invar*: $state\text{-}invar\ s\ state\text{-}\alpha\ s \in I$
shows $step\text{-}thread\ update\text{-}state\ s\ t = None \longleftrightarrow t \notin \alpha.active\text{-}threads\ (state\text{-}\alpha\ s)$
 ⟨proof⟩

lemma *step-thread-Some-NoneD*:

assumes *det*: $\alpha.deterministic\ I$
and *step*: $step\text{-}thread\ update\text{-}state\ s\ t' = [(t, None, \sigma')]$
and *invar*: $state\text{-}invar\ s\ state\text{-}\alpha\ s \in I$
shows $\exists x\ ln\ n. thr\text{-}\alpha\ (thr\ s)\ t = [(x, ln)] \wedge ln\ \$\ n > 0 \wedge \neg waiting\ (ws\text{-}\alpha\ (wset\ s)\ t) \wedge may\text{-}acquire\text{-}all\ (locks\ s)\ t\ ln \wedge \sigma' = update\text{-}state\ (K\$ \ [], \ [], \ [], \ [], \ [],\ convert\text{-}RA\ ln)$
 ⟨proof⟩

lemma *step-thread-Some-SomeD*:

assumes *det*: $\alpha.deterministic\ I$
and *step*: $step\text{-}thread\ update\text{-}state\ s\ t' = [(t, [(ta, x', m'), \sigma'])]$
and *invar*: $state\text{-}invar\ s\ state\text{-}\alpha\ s \in I$
shows $\exists x. thr\text{-}\alpha\ (thr\ s)\ t = [(x, no\text{-}wait\text{-}locks)] \wedge Predicate.eval\ (r\ t\ (x, shr\ s))\ (ta, x', m') \wedge actions\text{-}ok\ s\ t\ ta \wedge \sigma' = update\text{-}state\ ta$
 ⟨proof⟩

end

9.2.2 Code Generator setup

lemmas [*code*] =
scheduler-base-aux.free-thread-id-def
scheduler-base-aux.redT-updT.simps
scheduler-base-aux.redT-updTs-def
scheduler-base-aux.thread-ok.simps
scheduler-base-aux.thread-oks.simps
scheduler-base-aux.wset-actions-ok-def
scheduler-base-aux.cond-action-ok.simps
scheduler-base-aux.cond-action-oks-def
scheduler-base-aux.redT-updI.simps
scheduler-base-aux.redT-updIs.simps
scheduler-base-aux.interrupt-action-ok.simps
scheduler-base-aux.interrupt-actions-ok.simps
scheduler-base-aux.actions-ok-def
scheduler-base-aux.step-thread-def

lemmas [*code*] =
scheduler-base.exec-updW.simps
scheduler-base.exec-updWs-def
scheduler-base.exec-upd-def
scheduler-base.execT-def
scheduler-base.exec-step.simps
scheduler-base.exec-aux-def
scheduler-base.exec-def

lemmas [*code*] =
scheduler-ext-base.active-threads.simps

lemma *singleton2-code* [*code*]:
singleton2\ dfault\ (Predicate.Seq\ f) =

```

(case f () of
  Predicate.Empty ⇒ dfault ()
| Predicate.Insert x P ⇒
  if Predicate.is-empty P then x else Code.abort (STR "singleton2 not unique") (λ-. singleton2 dfault
(Predicate.Seq f))
| Predicate.Join P xq ⇒
  if Predicate.is-empty P then
    the-only2 dfault xq
  else if Predicate.null xq then singleton2 dfault P else Code.abort (STR "singleton2 not unique")
(λ-. singleton2 dfault (Predicate.Seq f)))
⟨proof⟩

```

lemma *the-only2-code* [code]:

```

the-only2 dfault Predicate.Empty = Code.abort (STR "the-only2 empty") dfault
the-only2 dfault (Predicate.Insert x P) =
  (if Predicate.is-empty P then x else Code.abort (STR "the-only2 not unique") (λ-. the-only2 dfault
(Predicate.Insert x P)))
the-only2 dfault (Predicate.Join P xq) =
  (if Predicate.is-empty P then
    the-only2 dfault xq
  else if Predicate.null xq then
    singleton2 dfault P
  else
    Code.abort (STR "the-only2 not unique") (λ-. the-only2 dfault (Predicate.Join P xq)))
⟨proof⟩

```

lemma *the2-eq* [code]:

```

the2 A = singleton2 (λx. Code.abort (STR "not-unique") (λ-. the2 A)) A
⟨proof⟩

```

end

9.3 Random scheduler

theory *Random-Scheduler*

imports

Scheduler

begin

type-synonym *random-scheduler* = *Random.seed*

abbreviation (*input*)

random-scheduler-invar :: *random-scheduler* ⇒ 't set ⇒ bool

where *random-scheduler-invar* ≡ λ- -. True

locale *random-scheduler-base* =

scheduler-ext-base

final *r* *convert-RA*

thr-α *thr-invar* *thr-lookup* *thr-update* *thr-iterate*

ws-α *ws-invar* *ws-lookup* *ws-update* *ws-sel*

is-α *is-invar* *is-memb* *is-ins* *is-delete*

thr'-α *thr'-invar* *thr'-empty* *thr'-ins-dj*

for *final* :: 'x ⇒ bool


```

and  $r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ Predicate.pred}$ 
and  $\text{convert-RA} :: 'l \text{ released-locks} \Rightarrow 'o \text{ list}$ 
and  $\text{output} :: \text{random-scheduler} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow 'q \text{ option}$ 
and  $\text{thr-}\alpha :: 'm\text{-}t \Rightarrow ('l, 't, 'x) \text{ thread-info}$ 
and  $\text{thr-invar} :: 'm\text{-}t \Rightarrow \text{bool}$ 
and  $\text{thr-lookup} :: 't \Rightarrow 'm\text{-}t \rightarrow ('x \times 'l \text{ released-locks})$ 
and  $\text{thr-update} :: 't \Rightarrow 'x \times 'l \text{ released-locks} \Rightarrow 'm\text{-}t \Rightarrow 'm\text{-}t$ 
and  $\text{thr-iterate} :: 'm\text{-}t \Rightarrow ('t \times ('x \times 'l \text{ released-locks}), 's\text{-}t) \text{ set-iterator}$ 
and  $\text{ws-}\alpha :: 'm\text{-}w \Rightarrow ('w, 't) \text{ wait-sets}$ 
and  $\text{ws-invar} :: 'm\text{-}w \Rightarrow \text{bool}$ 
and  $\text{ws-lookup} :: 't \Rightarrow 'm\text{-}w \rightarrow 'w \text{ wait-set-status}$ 
and  $\text{ws-update} :: 't \Rightarrow 'w \text{ wait-set-status} \Rightarrow 'm\text{-}w \Rightarrow 'm\text{-}w$ 
and  $\text{ws-delete} :: 't \Rightarrow 'm\text{-}w \Rightarrow 'm\text{-}w$ 
and  $\text{ws-iterate} :: 'm\text{-}w \Rightarrow ('t \times 'w \text{ wait-set-status}, 'm\text{-}w) \text{ set-iterator}$ 
and  $\text{ws-sel} :: 'm\text{-}w \Rightarrow ('t \times 'w \text{ wait-set-status} \Rightarrow \text{bool}) \rightarrow ('t \times 'w \text{ wait-set-status})$ 
and  $\text{is-}\alpha :: 's\text{-}i \Rightarrow 't \text{ interrupts}$ 
and  $\text{is-invar} :: 's\text{-}i \Rightarrow \text{bool}$ 
and  $\text{is-memb} :: 't \Rightarrow 's\text{-}i \Rightarrow \text{bool}$ 
and  $\text{is-ins} :: 't \Rightarrow 's\text{-}i \Rightarrow 's\text{-}i$ 
and  $\text{is-delete} :: 't \Rightarrow 's\text{-}i \Rightarrow 's\text{-}i$ 
and  $\text{thr}'\text{-}\alpha :: 's\text{-}t \Rightarrow 't \text{ set}$ 
and  $\text{thr}'\text{-invar} :: 's\text{-}t \Rightarrow \text{bool}$ 
and  $\text{thr}'\text{-empty} :: \text{unit} \Rightarrow 's\text{-}t$ 
and  $\text{thr}'\text{-ins-dj} :: 't \Rightarrow 's\text{-}t \Rightarrow 's\text{-}t$ 
+
fixes  $\text{thr}'\text{-to-list} :: 's\text{-}t \Rightarrow 't \text{ list}$ 

```

begin

```

definition  $\text{next-thread} :: \text{random-scheduler} \Rightarrow 's\text{-}t \Rightarrow ('t \times \text{random-scheduler}) \text{ option}$ 
where
   $\text{next-thread seed active} =$ 
   $(\text{let } ts = \text{thr}'\text{-to-list active}$ 
   $\text{ in if } ts = [] \text{ then None else Some (Random.select (thr}'\text{-to-list active) seed)})$ 

```

```

definition  $\text{random-scheduler} :: ('l, 't, 'x, 'm, 'w, 'o, 'm\text{-}t, 'm\text{-}w, 's\text{-}i, \text{random-scheduler}) \text{ scheduler}$ 
where

```

```

   $\text{random-scheduler seed } s =$ 
   $(\text{do } \{$ 
     $(t, \text{seed}') \leftarrow \text{next-thread seed (active-threads } s);$ 
     $\text{step-thread } (\lambda t a. \text{seed}') s t$ 
   $\})$ 

```

end

```

locale  $\text{random-scheduler} =$ 
   $\text{random-scheduler-base}$ 
   $\text{final } r \text{ convert-RA output}$ 
   $\text{thr-}\alpha \text{ thr-invar thr-lookup thr-update thr-iterate}$ 
   $\text{ws-}\alpha \text{ ws-invar ws-lookup ws-update ws-delete ws-iterate ws-sel}$ 
   $\text{is-}\alpha \text{ is-invar is-memb is-ins is-delete}$ 
   $\text{thr}'\text{-}\alpha \text{ thr}'\text{-invar thr}'\text{-empty thr}'\text{-ins-dj thr}'\text{-to-list}$ 
+
   $\text{scheduler-ext-aux}$ 
   $\text{final } r \text{ convert-RA}$ 

```

```

  thr- $\alpha$  thr-invar thr-lookup thr-update thr-iterate
  ws- $\alpha$  ws-invar ws-lookup ws-update ws-sel
  is- $\alpha$  is-invar is-memb is-ins is-delete
  thr'- $\alpha$  thr'-invar thr'-empty thr'-ins-dj
+
  ws: map-delete ws- $\alpha$  ws-invar ws-delete +
  ws: map-iteratei ws- $\alpha$  ws-invar ws-iterate +
  thr': set-to-list thr'- $\alpha$  thr'-invar thr'-to-list
for final :: 'x  $\Rightarrow$  bool
and r :: 't  $\Rightarrow$  ('x  $\times$  'm)  $\Rightarrow$  (('l,'t,'x,'m,'w,'o) thread-action  $\times$  'x  $\times$  'm) Predicate.pred
and convert-RA :: 'l released-locks  $\Rightarrow$  'o list
and output :: random-scheduler  $\Rightarrow$  't  $\Rightarrow$  ('l,'t,'x,'m,'w,'o) thread-action  $\Rightarrow$  'q option
and thr- $\alpha$  :: 'm-t  $\Rightarrow$  ('l,'t,'x) thread-info
and thr-invar :: 'm-t  $\Rightarrow$  bool
and thr-lookup :: 't  $\Rightarrow$  'm-t  $\rightarrow$  ('x  $\times$  'l released-locks)
and thr-update :: 't  $\Rightarrow$  'x  $\times$  'l released-locks  $\Rightarrow$  'm-t  $\Rightarrow$  'm-t
and thr-iterate :: 'm-t  $\Rightarrow$  ('t  $\times$  ('x  $\times$  'l released-locks), 's-t) set-iterator
and ws- $\alpha$  :: 'm-w  $\Rightarrow$  ('w,'t) wait-sets
and ws-invar :: 'm-w  $\Rightarrow$  bool
and ws-lookup :: 't  $\Rightarrow$  'm-w  $\rightarrow$  'w wait-set-status
and ws-update :: 't  $\Rightarrow$  'w wait-set-status  $\Rightarrow$  'm-w  $\Rightarrow$  'm-w
and ws-delete :: 't  $\Rightarrow$  'm-w  $\Rightarrow$  'm-w
and ws-iterate :: 'm-w  $\Rightarrow$  ('t  $\times$  'w wait-set-status, 'm-w) set-iterator
and ws-sel :: 'm-w  $\Rightarrow$  ('t  $\times$  'w wait-set-status  $\Rightarrow$  bool)  $\rightarrow$  ('t  $\times$  'w wait-set-status)
and is- $\alpha$  :: 's-i  $\Rightarrow$  't interrupts
and is-invar :: 's-i  $\Rightarrow$  bool
and is-memb :: 't  $\Rightarrow$  's-i  $\Rightarrow$  bool
and is-ins :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
and is-delete :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
and thr'- $\alpha$  :: 's-t  $\Rightarrow$  't set
and thr'-invar :: 's-t  $\Rightarrow$  bool
and thr'-empty :: unit  $\Rightarrow$  's-t
and thr'-ins-dj :: 't  $\Rightarrow$  's-t  $\Rightarrow$  's-t
and thr'-to-list :: 's-t  $\Rightarrow$  't list
begin

```

```

lemma next-thread-eq-None-iff:
  assumes thr'-invar active random-scheduler-invar seed T
  shows next-thread seed active = None  $\longleftrightarrow$  thr'- $\alpha$  active = {}
  <proof>

```

```

lemma next-thread-eq-SomeD:
  assumes next-thread seed active = Some (t, seed')
  and thr'-invar active random-scheduler-invar seed T
  shows t  $\in$  thr'- $\alpha$  active
  <proof>

```

```

lemma random-scheduler-spec:
  assumes det:  $\alpha$ .deterministic I
  shows scheduler-spec final r random-scheduler random-scheduler-invar thr- $\alpha$  thr-invar ws- $\alpha$  ws-invar
  is- $\alpha$  is-invar I
  <proof>

```

```

end

```

```

sublocale random-scheduler-base <
  scheduler-base
  final r convert-RA
  random-scheduler output pick-wakeup-via-sel ( $\lambda s P. ws-sel s (\lambda(k,v). P k v)$ ) random-scheduler-invar
  thr- $\alpha$  thr-invar thr-lookup thr-update
  ws- $\alpha$  ws-invar ws-lookup ws-update ws-delete ws-iterate
  is- $\alpha$  is-invar is-memb is-ins is-delete
for n0 <proof>

```

```

sublocale random-scheduler <
  pick-wakeup-spec
  final r convert-RA
  pick-wakeup-via-sel ( $\lambda s P. ws-sel s (\lambda(k,v). P k v)$ ) random-scheduler-invar
  thr- $\alpha$  thr-invar
  ws- $\alpha$  ws-invar
  is- $\alpha$  is-invar
<proof>

```

```

context random-scheduler begin

```

```

lemma random-scheduler-scheduler:

```

```

assumes det:  $\alpha$ .deterministic I

```

```

shows

```

```

scheduler

```

```

  final r convert-RA

```

```

  random-scheduler (pick-wakeup-via-sel ( $\lambda s P. ws-sel s (\lambda(k,v). P k v)$ )) random-scheduler-invar

```

```

  thr- $\alpha$  thr-invar thr-lookup thr-update

```

```

  ws- $\alpha$  ws-invar ws-lookup ws-update ws-delete ws-iterate

```

```

  is- $\alpha$  is-invar is-memb is-ins is-delete

```

```

  I

```

```

<proof>

```

```

end

```

9.3.1 Code generator setup

```

lemmas [code] =

```

```

  random-scheduler-base.next-thread-def

```

```

  random-scheduler-base.random-scheduler-def

```

```

end

```

9.4 Round robin scheduler

```

theory Round-Robin

```

```

imports

```

```

  Scheduler

```

```

begin

```

A concrete scheduler must pick one possible reduction step from the small-step semantics for individual threads. Currently, this is only possible if there is only one such by using *Predicate.the*.

9.4.1 Concrete schedulers

9.4.2 Round-robin schedulers

type-synonym $'queue\ round\ robin = 'queue \times nat$

— Waiting queue of threads and remaining number of steps of the first thread until it has to return resources

primrec $enqueue\ new\ thread :: 't\ list \Rightarrow ('t, 'x, 'm)\ new\ thread\ action \Rightarrow 't\ list$

where

$enqueue\ new\ thread\ queue\ (NewThread\ t\ x\ m) = queue\ @\ [t]$
 $| enqueue\ new\ thread\ queue\ (ThreadExists\ t\ b) = queue$

definition $enqueue\ new\ threads :: 't\ list \Rightarrow ('t, 'x, 'm)\ new\ thread\ action\ list \Rightarrow 't\ list$

where

$enqueue\ new\ threads = foldl\ enqueue\ new\ thread$

primrec $round\ robin\ update\ state :: nat \Rightarrow 't\ list\ round\ robin \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o)\ thread\ action \Rightarrow 't\ list\ round\ robin$

where

$round\ robin\ update\ state\ n0\ (queue, n)\ t\ ta =$
 $(let\ queue' = enqueue\ new\ threads\ queue\ \{ta\}_t$
 $in\ if\ n = 0 \vee Yield \in set\ \{ta\}_c\ then\ (rotate1\ queue', n0)\ else\ (queue', n - 1))$

context $multithreaded\ base\ begin$

abbreviation $round\ robin\ step :: nat \Rightarrow 't\ list\ round\ robin \Rightarrow ('l, 't, 'x, 'm, 'w)\ state \Rightarrow 't \Rightarrow ('t \times (('l, 't, 'x, 'm, 'w, 'o)\ thread\ action \times 'x \times 'm)\ option \times 't\ list\ round\ robin)\ option$

where

$round\ robin\ step\ n0\ \sigma\ s\ t \equiv step\ thread\ (round\ robin\ update\ state\ n0\ \sigma\ t)\ s\ t$

partial-function $(option)\ round\ robin\ reschedule :: 't \Rightarrow$

$'t\ list \Rightarrow nat \Rightarrow ('l, 't, 'x, 'm, 'w)\ state \Rightarrow ('t \times (('l, 't, 'x, 'm, 'w, 'o)\ thread\ action \times 'x \times 'm)\ option \times 't\ list\ round\ robin)\ option$

where

$round\ robin\ reschedule\ t0\ queue\ n0\ s =$
 $(let$
 $\quad t = hd\ queue;$
 $\quad queue' = tl\ queue$
 in
 $\quad if\ t = t0\ then$
 $\quad\quad None$
 $\quad else$
 $\quad\quad case\ round\ robin\ step\ n0\ (t\ \# queue', n0)\ s\ t\ of$
 $\quad\quad\quad None \Rightarrow round\ robin\ reschedule\ t0\ (queue' @ [t])\ n0\ s$
 $\quad\quad\quad | [ttaxm\ \sigma] \Rightarrow [ttaxm\ \sigma])$

fun $round\ robin :: nat \Rightarrow 't\ list\ round\ robin \Rightarrow ('l, 't, 'x, 'm, 'w)\ state \Rightarrow ('t \times (('l, 't, 'x, 'm, 'w, 'o)\ thread\ action \times 'x \times 'm)\ option \times 't\ list\ round\ robin)\ option$

where

$round\ robin\ n0\ ([], n)\ s = None$
 $| round\ robin\ n0\ (t\ \# queue, n)\ s =$
 $\quad (case\ round\ robin\ step\ n0\ (t\ \# queue, n)\ s\ t\ of$
 $\quad\quad [ttaxm\ \sigma] \Rightarrow [ttaxm\ \sigma]$
 $\quad\quad | None \Rightarrow round\ robin\ reschedule\ t\ (queue @ [t])\ n0\ s)$

end

primrec *round-robin-invar* :: 't list round-robin \Rightarrow 't set \Rightarrow bool
where *round-robin-invar* (queue, n) T \longleftrightarrow set queue = T \wedge distinct queue

lemma *set-enqueue-new-thread*:

set (enqueue-new-thread queue nta) = set queue \cup {t. \exists x m. nta = NewThread t x m}
 <proof>

lemma *set-enqueue-new-threads*:

set (enqueue-new-threads queue ntas) = set queue \cup {t. \exists x m. NewThread t x m \in set ntas}
 <proof>

lemma *enqueue-new-thread-eq-Nil* [simp]:

enqueue-new-thread queue nta = [] \longleftrightarrow queue = [] \wedge (\exists t b. nta = ThreadExists t b)
 <proof>

lemma *enqueue-new-threads-eq-Nil* [simp]:

enqueue-new-threads queue ntas = [] \longleftrightarrow queue = [] \wedge set ntas \subseteq {ThreadExists t b | t b. True}
 <proof>

lemma *distinct-enqueue-new-threads*:

fixes ts :: ('l,'t,'x) thread-info
and ntas :: ('t,'x,'m) new-thread-action list
assumes thread-oks ts ntas set queue = dom ts distinct queue
shows distinct (enqueue-new-threads queue ntas)
 <proof>

lemma *round-robin-reschedule-induct* [consumes 1, case-names head rotate]:

assumes major: t0 \in set queue
and head: \bigwedge queue. P (t0 $\#$ queue)
and rotate: \bigwedge queue t. \llbracket t \neq t0; t0 \in set queue; P (queue @ [t]) $\rrbracket \Longrightarrow$ P (t $\#$ queue)
shows P queue
 <proof>

context multithreaded-base **begin**

declare actions-ok-iff [simp del]

declare actions-ok.cases [rule del]

lemma *round-robin-step-invar-None*:

\llbracket round-robin-step n0 σ s t' = \llbracket (t, None, σ') \rrbracket ; round-robin-invar σ (dom (thr s)) \rrbracket
 \Longrightarrow round-robin-invar σ' (dom (thr s))
 <proof>

lemma *round-robin-step-invar-Some*:

\llbracket deterministic I; round-robin-step n0 σ s t' = \llbracket (t, \llbracket (ta, x', m') \rrbracket , σ') \rrbracket ; round-robin-invar σ (dom (thr s)); s \in I \rrbracket
 \Longrightarrow round-robin-invar σ' (dom (thr s) \cup {t. \exists x m. NewThread t x m \in set \llbracket ta \rrbracket_t })
 <proof>

lemma *round-robin-reschedule-Cons*:

round-robin-reschedule t0 (t0 $\#$ queue) n0 s = None

$t \neq t0 \implies \text{round-robin-reschedule } t0 \ (t \# \text{queue}) \ n0 \ s =$
 (case round-robin-step $n0 \ (t \# \text{queue}, n0) \ s \ t$ of
 None $\implies \text{round-robin-reschedule } t0 \ (\text{queue} \ @ \ [t]) \ n0 \ s$
 | Some $ttaxm\sigma \implies \text{Some } ttaxm\sigma$)
 <proof>

lemma round-robin-reschedule-NoneD:
assumes $rrr: \text{round-robin-reschedule } t0 \ \text{queue} \ n0 \ s = \text{None}$
and $t0: t0 \in \text{set queue}$
shows $\text{set } (\text{takeWhile } (\lambda t'. t' \neq t0) \ \text{queue}) \cap \text{active-threads } s = \{\}$
 <proof>

lemma round-robin-reschedule-Some-NoneD:
assumes $rrr: \text{round-robin-reschedule } t0 \ \text{queue} \ n0 \ s = \lfloor (t, \text{None}, \sigma') \rfloor$
and $t0: t0 \in \text{set queue}$
shows $\exists x \ ln \ n. \ \text{thr } s \ t = \lfloor (x, \ln) \rfloor \wedge \ln \ \$ \ n > 0 \wedge \neg \text{waiting } (\text{wset } s \ t) \wedge \text{may-acquire-all } (\text{locks } s)$
 $t \ ln$
 <proof>

lemma round-robin-reschedule-Some-SomeD:
assumes $\text{deterministic } I$
and $rrr: \text{round-robin-reschedule } t0 \ \text{queue} \ n0 \ s = \lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor$
and $t0: t0 \in \text{set queue}$
and $I: s \in I$
shows $\exists x. \ \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor \wedge t \vdash \langle x, \text{shr } s \rangle \text{-}ta \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s \ t \ ta$
 <proof>

lemma round-robin-reschedule-invar-None:
assumes $rrr: \text{round-robin-reschedule } t0 \ \text{queue} \ n0 \ s = \lfloor (t, \text{None}, \sigma') \rfloor$
and $\text{invar}: \text{round-robin-invar } (\text{queue}, n0) \ (\text{dom } (\text{thr } s))$
and $t0: t0 \in \text{set queue}$
shows $\text{round-robin-invar } \sigma' \ (\text{dom } (\text{thr } s))$
 <proof>

lemma round-robin-reschedule-invar-Some:
assumes $\text{deterministic } I$
and $rrr: \text{round-robin-reschedule } t0 \ \text{queue} \ n0 \ s = \lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor$
and $\text{invar}: \text{round-robin-invar } (\text{queue}, n0) \ (\text{dom } (\text{thr } s))$
and $t0: t0 \in \text{set queue}$
and $s \in I$
shows $\text{round-robin-invar } \sigma' \ (\text{dom } (\text{thr } s) \cup \{t. \exists x \ m. \ \text{NewThread } t \ x \ m \in \text{set } \{\!|ta|_t\}\})$
 <proof>

lemma round-robin-NoneD:
assumes $rr: \text{round-robin } n0 \ \sigma \ s = \text{None}$
and $\text{invar}: \text{round-robin-invar } \sigma \ (\text{dom } (\text{thr } s))$
shows $\text{active-threads } s = \{\}$
 <proof>

lemma round-robin-Some-NoneD:
assumes $rr: \text{round-robin } n0 \ \sigma \ s = \lfloor (t, \text{None}, \sigma') \rfloor$
shows $\exists x \ ln \ n. \ \text{thr } s \ t = \lfloor (x, \ln) \rfloor \wedge \ln \ \$ \ n > 0 \wedge \neg \text{waiting } (\text{wset } s \ t) \wedge \text{may-acquire-all } (\text{locks } s)$
 $t \ ln$
 <proof>

lemma *round-robin-Some-SomeD*:

assumes *deterministic I*

and *rr: round-robin n0 σ s = [(t, [(ta, x', m'), σ'])]*

and *s \in I*

shows $\exists x. \text{thr } s \ t = [(x, \text{no-wait-locks})] \wedge t \vdash \langle x, \text{shr } s \rangle \text{-ta} \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s \ t \ \text{ta}$

<proof>

lemma *round-robin-invar-None*:

assumes *rr: round-robin n0 σ s = [(t, None, σ')]*

and *invar: round-robin-invar σ (dom (thr s))*

shows *round-robin-invar σ' (dom (thr s))*

<proof>

lemma *round-robin-invar-Some*:

assumes *deterministic I*

and *rr: round-robin n0 σ s = [(t, [(ta, x', m'), σ'])]*

and *invar: round-robin-invar σ (dom (thr s)) s \in I*

shows *round-robin-invar σ' (dom (thr s) \cup {t. $\exists x \ m. \text{NewThread } t \ x \ m \in \text{set } \{ta\}_t$ })*

<proof>

end

locale *round-robin-base =*

scheduler-base-aux

final r convert-RA

thr- α thr-invar thr-lookup thr-update

ws- α ws-invar ws-lookup

is- α is-invar is-memb is-ins is-delete

for *final :: 'x \Rightarrow bool*

and *r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l,'t,'x,'m,'w,'o) thread-action \times 'x \times 'm) Predicate.pred*

and *convert-RA :: 'l released-locks \Rightarrow 'o list*

and *output :: 'queue round-robin \Rightarrow 't \Rightarrow ('l,'t,'x,'m,'w,'o) thread-action \Rightarrow 'q option*

and *thr- α :: 'm-t \Rightarrow ('l,'t,'x) thread-info*

and *thr-invar :: 'm-t \Rightarrow bool*

and *thr-lookup :: 't \Rightarrow 'm-t \rightarrow ('x \times 'l released-locks)*

and *thr-update :: 't \Rightarrow 'x \times 'l released-locks \Rightarrow 'm-t \Rightarrow 'm-t*

and *ws- α :: 'm-w \Rightarrow ('w,'t) wait-sets*

and *ws-invar :: 'm-w \Rightarrow bool*

and *ws-lookup :: 't \Rightarrow 'm-w \rightarrow 'w wait-set-status*

and *ws-update :: 't \Rightarrow 'w wait-set-status \Rightarrow 'm-w \Rightarrow 'm-w*

and *ws-delete :: 't \Rightarrow 'm-w \Rightarrow 'm-w*

and *ws-iterate :: 'm-w \Rightarrow ('t \times 'w wait-set-status, 'm-w) set-iterator*

and *ws-sel :: 'm-w \Rightarrow ('t \times 'w wait-set-status \Rightarrow bool) \rightarrow ('t \times 'w wait-set-status)*

and *is- α :: 's-i \Rightarrow 't interrupts*

and *is-invar :: 's-i \Rightarrow bool*

and *is-memb :: 't \Rightarrow 's-i \Rightarrow bool*

and *is-ins :: 't \Rightarrow 's-i \Rightarrow 's-i*

and *is-delete :: 't \Rightarrow 's-i \Rightarrow 's-i*

+

fixes *queue- α :: 'queue \Rightarrow 't list*

and *queue-invar :: 'queue \Rightarrow bool*

and *queue-empty :: unit \Rightarrow 'queue*

and *queue-isEmpty :: 'queue \Rightarrow bool*

and $queue\text{-}enqueue :: 't \Rightarrow 'queue \Rightarrow 'queue$
and $queue\text{-}dequeue :: 'queue \Rightarrow 't \times 'queue$
and $queue\text{-}push :: 't \Rightarrow 'queue \Rightarrow 'queue$
begin

definition $queue\text{-}rotate1 :: 'queue \Rightarrow 'queue$
where $queue\text{-}rotate1 = case\text{-}prod\ queue\text{-}enqueue \circ queue\text{-}dequeue$

primrec $enqueue\text{-}new\text{-}thread :: 'queue \Rightarrow ('t, 'x, 'm)\ new\text{-}thread\text{-}action \Rightarrow 'queue$
where
 $enqueue\text{-}new\text{-}thread\ ts\ (NewThread\ t\ x\ m) = queue\text{-}enqueue\ t\ ts$
 $| enqueue\text{-}new\text{-}thread\ ts\ (ThreadExists\ t\ b) = ts$

definition $enqueue\text{-}new\text{-}threads :: 'queue \Rightarrow ('t, 'x, 'm)\ new\text{-}thread\text{-}action\ list \Rightarrow 'queue$
where
 $enqueue\text{-}new\text{-}threads = foldl\ enqueue\text{-}new\text{-}thread$

primrec $round\text{-}robin\text{-}update\text{-}state :: nat \Rightarrow 'queue\ round\text{-}robin \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o)\ thread\text{-}action$
 $\Rightarrow 'queue\ round\text{-}robin$
where

$round\text{-}robin\text{-}update\text{-}state\ n0\ (queue,\ n)\ t\ ta =$
 $(let\ queue' = enqueue\text{-}new\text{-}threads\ queue\ \{\!|ta|\!\}_t$
 $in\ if\ n = 0 \vee Yield \in set\ \{\!|ta|\!\}_c\ then\ (queue\text{-}rotate1\ queue',\ n0)\ else\ (queue',\ n - 1))$

abbreviation $round\text{-}robin\text{-}step ::$
 $nat \Rightarrow 'queue\ round\text{-}robin \Rightarrow ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)\ state\text{-}refine \Rightarrow 't$
 $\Rightarrow ('t \times (('l, 't, 'x, 'm, 'w, 'o)\ thread\text{-}action \times 'x \times 'm)\ option \times 'queue\ round\text{-}robin)\ option$
where
 $round\text{-}robin\text{-}step\ n0\ \sigma\ s\ t \equiv step\text{-}thread\ (round\text{-}robin\text{-}update\text{-}state\ n0\ \sigma\ t)\ s\ t$

partial-function $(option)\ round\text{-}robin\text{-}reschedule ::$
 $'t \Rightarrow 'queue \Rightarrow nat \Rightarrow ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)\ state\text{-}refine$
 $\Rightarrow ('t \times (('l, 't, 'x, 'm, 'w, 'o)\ thread\text{-}action \times 'x \times 'm)\ option \times 'queue\ round\text{-}robin)\ option$

where
 $round\text{-}robin\text{-}reschedule\ t0\ queue\ n0\ s =$
 $(let$
 $(t,\ queue') = queue\text{-}dequeue\ queue$
 in
 $if\ t = t0\ then$
 $None$
 $else$
 $case\ round\text{-}robin\text{-}step\ n0\ (queue\text{-}push\ t\ queue',\ n0)\ s\ t\ of$
 $None \Rightarrow round\text{-}robin\text{-}reschedule\ t0\ (queue\text{-}enqueue\ t\ queue')\ n0\ s$
 $| [ttaxm\sigma] \Rightarrow [ttaxm\sigma])$

primrec $round\text{-}robin :: nat \Rightarrow ('l, 't, 'x, 'm, 'w, 'o, 'm\text{-}t, 'm\text{-}w, 's\text{-}i, 'queue\ round\text{-}robin)\ scheduler$
where

$round\text{-}robin\ n0\ (queue,\ n)\ s =$
 $(if\ queue\text{-}isEmpty\ queue\ then\ None$
 $else$
 let
 $(t,\ queue') = queue\text{-}dequeue\ queue$
 in
 $(case\ round\text{-}robin\text{-}step\ n0\ (queue\text{-}push\ t\ queue',\ n)\ s\ t\ of$

$[ttaxm\sigma] \Rightarrow [ttaxm\sigma]$
 $| \text{None} \Rightarrow \text{round-robin-reschedule } t \text{ (queue-enqueue } t \text{ queue')} \text{ n0 } s)$

primrec *round-robin-invar* :: $'queue \text{ round-robin} \Rightarrow 't \text{ set} \Rightarrow \text{bool}$
where *round-robin-invar* (queue, n) T \longleftrightarrow *queue-invar* queue \wedge *Round-Robin.round-robin-invar* (queue- α queue, n) T

definition *round-robin- α* :: $'queue \text{ round-robin} \Rightarrow 't \text{ list round-robin}$
where *round-robin- α* = *apfst* *queue- α*

definition *round-robin-start* :: $\text{nat} \Rightarrow 't \Rightarrow 'queue \text{ round-robin}$
where *round-robin-start* n0 t = (queue-enqueue t (queue-empty ()), n0)

lemma *round-robin-invar-correct*:
round-robin-invar σ T \Longrightarrow *Round-Robin.round-robin-invar* (round-robin- α σ) T
 <proof>

end

locale *round-robin* =
round-robin-base
final *r* *convert-RA* *output*
thr- α *thr-invar* *thr-lookup* *thr-update*
ws- α *ws-invar* *ws-lookup* *ws-update* *ws-delete* *ws-iterate* *ws-sel*
is- α *is-invar* *is-memb* *is-ins* *is-delete*
queue- α *queue-invar* *queue-empty* *queue-isEmpty* *queue-enqueue* *queue-dequeue* *queue-push*
 +
scheduler-aux
final *r* *convert-RA*
thr- α *thr-invar* *thr-lookup* *thr-update*
ws- α *ws-invar* *ws-lookup*
is- α *is-invar* *is-memb* *is-ins* *is-delete*
 +
ws: map-update *ws- α* *ws-invar* *ws-update* +
ws: map-delete *ws- α* *ws-invar* *ws-delete* +
ws: map-iteratei *ws- α* *ws-invar* *ws-iterate* +
ws: map-sel' *ws- α* *ws-invar* *ws-sel* +
queue: list *queue- α* *queue-invar* +
queue: list-empty *queue- α* *queue-invar* *queue-empty* +
queue: list-isEmpty *queue- α* *queue-invar* *queue-isEmpty* +
queue: list-enqueue *queue- α* *queue-invar* *queue-enqueue* +
queue: list-dequeue *queue- α* *queue-invar* *queue-dequeue* +
queue: list-push *queue- α* *queue-invar* *queue-push*
for *final* :: $'x \Rightarrow \text{bool}$
and *r* :: $'t \Rightarrow ('x \times 'm) \Rightarrow (('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ Predicate.pred}$
and *convert-RA* :: $'l \text{ released-locks} \Rightarrow 'o \text{ list}$
and *output* :: $'queue \text{ round-robin} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow 'q \text{ option}$
and *thr- α* :: $'m-t \Rightarrow ('l, 't, 'x) \text{ thread-info}$
and *thr-invar* :: $'m-t \Rightarrow \text{bool}$
and *thr-lookup* :: $'t \Rightarrow 'm-t \rightarrow ('x \times 'l \text{ released-locks})$
and *thr-update* :: $'t \Rightarrow 'x \times 'l \text{ released-locks} \Rightarrow 'm-t \Rightarrow 'm-t$
and *ws- α* :: $'m-w \Rightarrow ('w, 't) \text{ wait-sets}$
and *ws-invar* :: $'m-w \Rightarrow \text{bool}$
and *ws-lookup* :: $'t \Rightarrow 'm-w \rightarrow 'w \text{ wait-set-status}$

and *ws-update* :: 't ⇒ 'w wait-set-status ⇒ 'm-w ⇒ 'm-w
and *ws-delete* :: 't ⇒ 'm-w ⇒ 'm-w
and *ws-iterate* :: 'm-w ⇒ ('t × 'w wait-set-status, 'm-w) set-iterator
and *ws-sel* :: 'm-w ⇒ ('t × 'w wait-set-status ⇒ bool) → ('t × 'w wait-set-status)
and *is-α* :: 's-i ⇒ 't interrupts
and *is-invar* :: 's-i ⇒ bool
and *is-memb* :: 't ⇒ 's-i ⇒ bool
and *is-ins* :: 't ⇒ 's-i ⇒ 's-i
and *is-delete* :: 't ⇒ 's-i ⇒ 's-i
and *queue-α* :: 'queue ⇒ 't list
and *queue-invar* :: 'queue ⇒ bool
and *queue-empty* :: unit ⇒ 'queue
and *queue-isEmpty* :: 'queue ⇒ bool
and *queue-enqueue* :: 't ⇒ 'queue ⇒ 'queue
and *queue-dequeue* :: 'queue ⇒ 't × 'queue
and *queue-push* :: 't ⇒ 'queue ⇒ 'queue
begin

lemma *queue-rotate1-correct*:

assumes *queue-invar* *queue* *queue-α* *queue* ≠ []
shows *queue-α* (*queue-rotate1* *queue*) = *rotate1* (*queue-α* *queue*)
and *queue-invar* (*queue-rotate1* *queue*)
⟨*proof*⟩

lemma *enqueue-thread-correct*:

assumes *queue-invar* *queue*
shows *queue-α* (*enqueue-new-thread* *queue* *nta*) = *Round-Robin.enqueue-new-thread* (*queue-α* *queue*)
nta
and *queue-invar* (*enqueue-new-thread* *queue* *nta*)
⟨*proof*⟩

lemma *enqueue-threads-correct*:

assumes *queue-invar* *queue*
shows *queue-α* (*enqueue-new-threads* *queue* *ntas*) = *Round-Robin.enqueue-new-threads* (*queue-α* *queue*)
ntas
and *queue-invar* (*enqueue-new-threads* *queue* *ntas*)
⟨*proof*⟩

lemma *round-robin-update-thread-correct*:

assumes *round-robin-invar* σ T $t' \in T$
shows *round-robin-α* (*round-robin-update-state* *n0* σ t ta) = *Round-Robin.round-robin-update-state*
n0 (*round-robin-α* σ) t ta
⟨*proof*⟩

lemma *round-robin-step-correct*:

assumes *det*: α .*deterministic* I
and *invar*: *round-robin-invar* σ (*dom* (*thr-α* (*thr* s))) *state-invar* s *state-α* $s \in I$
shows
map-option (*apsnd* (*apsnd* *round-robin-α*)) (*round-robin-step* *n0* σ s t) =
 α .*round-robin-step* *n0* (*round-robin-α* σ) (*state-α* s) t (**is** ?*thesis1*)
and *case-option* *True* ($\lambda(t, \text{taxm}, \sigma).$ *round-robin-invar* σ (*case* *taxm* of *None* ⇒ *dom* (*thr-α* (*thr* s)) |
Some (ta, x', m') ⇒ *dom* (*thr-α* (*thr* s)) ∪ { t . ∃ x m . *NewThread* t x m ∈ *set* { ta } $_t$ })}) (*round-robin-step*
n0 σ s t)
(**is** ?*thesis2*)

<proof>

lemma *round-robin-reschedule-correct:*

assumes *det: α .deterministic I*

and *invar: round-robin-invar (queue, n) (dom (thr- α (thr s))) state-invar s state- α s \in I*

and *t0: t0 \in set (queue- α queue)*

shows *map-option (apsnd (apsnd round-robin- α)) (round-robin-reschedule t0 queue n0 s) =
 α .round-robin-reschedule t0 (queue- α queue) n0 (state- α s)*

and *case-option True ($\lambda(t, taxm, \sigma)$. round-robin-invar σ (case taxm of None \Rightarrow dom (thr- α (thr s)) | Some (ta, x', m') \Rightarrow dom (thr- α (thr s)) \cup {t. \exists x m. NewThread t x m \in set {ta}_t})) (round-robin-reschedule t0 queue n0 s)*

<proof>

lemma *round-robin-correct:*

assumes *det: α .deterministic I*

and *invar: round-robin-invar σ (dom (thr- α (thr s))) state-invar s state- α s \in I*

shows *map-option (apsnd (apsnd round-robin- α)) (round-robin n0 σ s) =
 α .round-robin n0 (round-robin- α σ) (state- α s)*

(is ?thesis1)

and *case-option True ($\lambda(t, taxm, \sigma)$. round-robin-invar σ (case taxm of None \Rightarrow dom (thr- α (thr s)) | Some (ta, x', m') \Rightarrow dom (thr- α (thr s)) \cup {t. \exists x m. NewThread t x m \in set {ta}_t})) (round-robin n0 σ s)*

(is ?thesis2)

<proof>

lemma *round-robin-scheduler-spec:*

assumes *det: α .deterministic I*

shows *scheduler-spec final r (round-robin n0) round-robin-invar thr- α thr-invar ws- α ws-invar is- α is-invar I*

<proof>

lemma *round-robin-start-invar:*

round-robin-invar (round-robin-start n0 t0) {t0}

<proof>

end

sublocale *round-robin-base <*

scheduler-base

final r convert-RA

round-robin n0 output pick-wakeup-via-sel (λ s P. ws-sel s ($\lambda(k,v)$. P k v)) round-robin-invar

thr- α thr-invar thr-lookup thr-update

ws- α ws-invar ws-lookup ws-update ws-delete ws-iterate

is- α is-invar is-memb is-ins is-delete

for *n0 <proof>*

sublocale *round-robin <*

pick-wakeup-spec

final r convert-RA

pick-wakeup-via-sel (λ s P. ws-sel s ($\lambda(k,v)$. P k v)) round-robin-invar

thr- α thr-invar

ws- α ws-invar

is- α is-invar

<proof>

context *round-robin* **begin**

lemma *round-robin-scheduler*:

assumes *det*: α .*deterministic* *I*

shows

scheduler

final *r* *convert-RA*

(round-robin *n0*) (*pick-wakeup-via-sel* ($\lambda s P. ws-sel\ s\ (\lambda(k,v). P\ k\ v)$)) *round-robin-invar*

thr- α *thr-invar* *thr-lookup* *thr-update*

ws- α *ws-invar* *ws-lookup* *ws-update* *ws-delete* *ws-iterate*

is- α *is-invar* *is-memb* *is-ins* *is-delete*

I

<proof>

end

lemmas [*code*] =

round-robin-base.queue-rotate1-def

round-robin-base.enqueue-new-thread.simps

round-robin-base.enqueue-new-threads-def

round-robin-base.round-robin-update-state.simps

round-robin-base.round-robin-reschedule.simps

round-robin-base.round-robin.simps

round-robin-base.round-robin-start-def

end

theory *SC-Schedulers*

imports

Random-Scheduler

Round-Robin

../MM/SC-Collections

../Basic/JT-ICF

begin

abbreviation *sc-start-state-refine* ::

$'m-t \Rightarrow (\text{thread-id} \Rightarrow ('x \times \text{addr released-locks}) \Rightarrow 'm-t \Rightarrow 'm-t) \Rightarrow 'm-w \Rightarrow 's-i$

$\Rightarrow (\text{cname} \Rightarrow \text{mname} \Rightarrow \text{ty list} \Rightarrow \text{ty} \Rightarrow 'md \Rightarrow \text{addr val list} \Rightarrow 'x) \Rightarrow 'md\ \text{prog} \Rightarrow \text{cname} \Rightarrow \text{mname}$

$\Rightarrow \text{addr val list}$

$\Rightarrow (\text{addr}, \text{thread-id}, \text{heap}, 'm-t, 'm-w, 's-i)$ *state-refine*

where

$\bigwedge is-empty.$

sc-start-state-refine *thr-empty* *thr-update* *ws-empty* *is-empty* *f* *P* \equiv

heap-base.start-state-refine *addr2thread-id* *sc-empty* (*sc-allocate* *P*) *thr-empty* *thr-update* *ws-empty*

is-empty *f* *P*

abbreviation *sc-state- α* ::

$(l, 't :: \text{linorder}, 'm, ('t, 'x \times l \Rightarrow f\ \text{nat})\ \text{rm}, ('t, 'w\ \text{wait-set-status})\ \text{rm}, 't\ \text{rs})$ *state-refine*

$\Rightarrow (l, 't, 'x, 'm, 'w)$ *state*

where *sc-state- α* \equiv *state-refine-base.state- α* *rm- α* *rm- α* *rs- α*

lemma *sc-state- α -sc-start-state-refine* [*simp*]:

$sc\text{-state-}\alpha$ ($sc\text{-start-state-refine}$ ($rm\text{-empty}$ ()) $rm\text{-update}$ ($rm\text{-empty}$ ()) ($rs\text{-empty}$ ()) $f P C M vs$) =
 $sc\text{-start-state}$ $f P C M vs$
 ⟨proof⟩

locale $sc\text{-scheduler}$ =
scheduler
final r *convert-RA*
schedule *output* *pick-wakeup* $\sigma\text{-invar}$
 $rm\text{-}\alpha$ $rm\text{-invar}$ $rm\text{-lookup}$ $rm\text{-update}$
 $rm\text{-}\alpha$ $rm\text{-invar}$ $rm\text{-lookup}$ $rm\text{-update}$ $rm\text{-delete}$ $rm\text{-iteratei}$
 $rs\text{-}\alpha$ $rs\text{-invar}$ $rs\text{-memb}$ $rs\text{-ins}$ $rs\text{-delete}$
invariant
for $final :: 'x \Rightarrow bool$
and $r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l, 't :: linorder, 'x, 'm, 'w, 'o) thread\text{-action} \times 'x \times 'm) Predicate.pred$
and $convert\text{-}RA :: 'l released\text{-locks} \Rightarrow 'o list$
and $schedule :: ('l, 't, 'x, 'm, 'w, 'o, ('t, 'x \times 'l \Rightarrow f nat) rm, ('t, 'w wait\text{-set}\text{-status}) rm, 't rs, 's) scheduler$
and $output :: 's \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) thread\text{-action} \Rightarrow 'q option$
and $pick\text{-}wakeup :: 's \Rightarrow 't \Rightarrow 'w \Rightarrow ('t, 'w wait\text{-set}\text{-status}) RBT.rbt \Rightarrow 't option$
and $\sigma\text{-invar} :: 's \Rightarrow 't set \Rightarrow bool$
and $invariant :: ('l, 't, 'x, 'm, 'w) state set$

locale $sc\text{-round-robin-base}$ =
round-robin-base
final r *convert-RA* *output*
 $rm\text{-}\alpha$ $rm\text{-invar}$ $rm\text{-lookup}$ $rm\text{-update}$
 $rm\text{-}\alpha$ $rm\text{-invar}$ $rm\text{-lookup}$ $rm\text{-update}$ $rm\text{-delete}$ $rm\text{-iteratei}$ $rm\text{-sel}$
 $rs\text{-}\alpha$ $rs\text{-invar}$ $rs\text{-memb}$ $rs\text{-ins}$ $rs\text{-delete}$
 $fifo\text{-}\alpha$ $fifo\text{-invar}$ $fifo\text{-empty}$ $fifo\text{-isEmpty}$ $fifo\text{-enqueue}$ $fifo\text{-dequeue}$ $fifo\text{-push}$
for $final :: 'x \Rightarrow bool$
and $r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l, 't :: linorder, 'x, 'm, 'w, 'o) thread\text{-action} \times 'x \times 'm) Predicate.pred$
and $convert\text{-}RA :: 'l released\text{-locks} \Rightarrow 'o list$
and $output :: 't fifo round\text{-robin} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) thread\text{-action} \Rightarrow 'q option$

locale $sc\text{-round-robin}$ =
round-robin
final r *convert-RA* *output*
 $rm\text{-}\alpha$ $rm\text{-invar}$ $rm\text{-lookup}$ $rm\text{-update}$
 $rm\text{-}\alpha$ $rm\text{-invar}$ $rm\text{-lookup}$ $rm\text{-update}$ $rm\text{-delete}$ $rm\text{-iteratei}$ $rm\text{-sel}$
 $rs\text{-}\alpha$ $rs\text{-invar}$ $rs\text{-memb}$ $rs\text{-ins}$ $rs\text{-delete}$
 $fifo\text{-}\alpha$ $fifo\text{-invar}$ $fifo\text{-empty}$ $fifo\text{-isEmpty}$ $fifo\text{-enqueue}$ $fifo\text{-dequeue}$ $fifo\text{-push}$
for $final :: 'x \Rightarrow bool$
and $r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l, 't :: linorder, 'x, 'm, 'w, 'o) thread\text{-action} \times 'x \times 'm) Predicate.pred$
and $convert\text{-}RA :: 'l released\text{-locks} \Rightarrow 'o list$
and $output :: 't fifo round\text{-robin} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) thread\text{-action} \Rightarrow 'q option$

sublocale $sc\text{-round-robin} < sc\text{-round-robin-base}$ ⟨proof⟩

locale $sc\text{-random-scheduler-base}$ =
random-scheduler-base
final r *convert-RA* *output*
 $rm\text{-}\alpha$ $rm\text{-invar}$ $rm\text{-lookup}$ $rm\text{-update}$ $rm\text{-iteratei}$
 $rm\text{-}\alpha$ $rm\text{-invar}$ $rm\text{-lookup}$ $rm\text{-update}$ $rm\text{-delete}$ $rm\text{-iteratei}$ $rm\text{-sel}$
 $rs\text{-}\alpha$ $rs\text{-invar}$ $rs\text{-memb}$ $rs\text{-ins}$ $rs\text{-delete}$
 $lsi\text{-}\alpha$ $lsi\text{-invar}$ $lsi\text{-empty}$ $lsi\text{-ins-dj}$ $lsi\text{-to-list}$

```

for final :: 'x ⇒ bool
and r :: 't ⇒ ('x × 'm) ⇒ (('l,'t :: linorder,'x,'m,'w,'o) thread-action × 'x × 'm) Predicate.pred
and convert-RA :: 'l released-locks ⇒ 'o list
and output :: random-scheduler ⇒ 't ⇒ ('l,'t,'x,'m,'w,'o) thread-action ⇒ 'q option

```

```

locale sc-random-scheduler =
  random-scheduler
  final r convert-RA output
  rm-α rm-invar rm-lookup rm-update rm-iteratei
  rm-α rm-invar rm-lookup rm-update rm-delete rm-iteratei rm-sel
  rs-α rs-invar rs-memb rs-ins rs-delete
  lsi-α lsi-invar lsi-empty lsi-ins-dj lsi-to-list
for final :: 'x ⇒ bool
and r :: 't ⇒ ('x × 'm) ⇒ (('l,'t :: linorder,'x,'m,'w,'o) thread-action × 'x × 'm) Predicate.pred
and convert-RA :: 'l released-locks ⇒ 'o list
and output :: random-scheduler ⇒ 't ⇒ ('l,'t,'x,'m,'w,'o) thread-action ⇒ 'q option

```

```

sublocale sc-random-scheduler < sc-random-scheduler-base ⟨proof⟩

```

No spurious wake-ups in generated code

```

overloading sc-spurious-wakeups ≡ sc-spurious-wakeups
begin
  definition sc-spurious-wakeups [code]: sc-spurious-wakeups ≡ False
end

end

```

9.5 Tabulation for lookup functions

```

theory TypeRelRefine
imports
  ../Common/TypeRel
  HOL-Library.AList-Mapping
begin

```

9.5.1 Auxiliary lemmata

```

lemma rtranclp-tranclpE:
  assumes r∗∗ x y
  obtains (refl) x = y
  | (trancl) r∗∗ x y
  ⟨proof⟩

```

```

lemma map-of-map2: map-of (map (λ(k, v). (k, f k v)) xs) k = map-option (f k) (map-of xs k)
  ⟨proof⟩

```

```

lemma map-of-map-K: map-of (map (λk. (k, c)) xs) k = (if k ∈ set xs then Some c else None)
  ⟨proof⟩

```

```

lift-definition map-values :: ('a ⇒ 'b ⇒ 'c) ⇒ ('a, 'b) mapping ⇒ ('a, 'c) mapping
is λf m k. map-option (f k) (m k) ⟨proof⟩

```

```

lemma map-values-Mapping [simp]:
  map-values f (Mapping.Mapping m) = Mapping.Mapping (λk. map-option (f k) (m k))

```

$\langle \text{proof} \rangle$

lemma *map-Mapping*: $\text{Mapping.map } f \ g \ (\text{Mapping.Mapping } m) = \text{Mapping.Mapping } (\text{map-option } g \circ m \circ f)$

$\langle \text{proof} \rangle$

abbreviation *subclst* :: $'m \ \text{prog} \Rightarrow \text{cname} \Rightarrow \text{cname} \Rightarrow \text{bool}$

where *subclst* $P \equiv (\text{subcls1 } P)^{++}$

9.5.2 Representation type for tabulated lookup functions

type-synonym

$'m \ \text{prog-impl}' =$

$'m \ \text{cdecl list} \times$

$(\text{cname}, 'm \ \text{class}) \ \text{mapping} \times$

$(\text{cname}, \text{cname set}) \ \text{mapping} \times$

$(\text{cname}, (\text{vname}, \text{cname} \times \text{ty} \times \text{fmod}) \ \text{mapping}) \ \text{mapping} \times$

$(\text{cname}, (\text{mname}, \text{cname} \times \text{ty list} \times \text{ty} \times 'm \ \text{option}) \ \text{mapping}) \ \text{mapping}$

lift-definition *tabulate-class* :: $'m \ \text{cdecl list} \Rightarrow (\text{cname}, 'm \ \text{class}) \ \text{mapping}$

is $\text{class} \circ \text{Program} \ \langle \text{proof} \rangle$

lift-definition *tabulate-subcls* :: $'m \ \text{cdecl list} \Rightarrow (\text{cname}, \text{cname set}) \ \text{mapping}$

is $\lambda P \ C. \ \text{if } \text{is-class } (\text{Program } P) \ C \ \text{then } \text{Some } \{D. \ \text{Program } P \vdash C \preceq^* D\} \ \text{else } \text{None} \ \langle \text{proof} \rangle$

lift-definition *tabulate-sees-field* :: $'m \ \text{cdecl list} \Rightarrow (\text{cname}, (\text{vname}, \text{cname} \times \text{ty} \times \text{fmod}) \ \text{mapping}) \ \text{mapping}$

is $\lambda P \ C. \ \text{if } \text{is-class } (\text{Program } P) \ C \ \text{then}$

$\text{Some } (\lambda F. \ \text{if } \exists T \ \text{fm } D. \ \text{Program } P \vdash C \ \text{sees } F:T \ (\text{fm}) \ \text{in } D \ \text{then } \text{Some } (\text{field } (\text{Program } P) \ C \ F) \ \text{else } \text{None})$

$\text{else } \text{None} \ \langle \text{proof} \rangle$

lift-definition *tabulate-Method* :: $'m \ \text{cdecl list} \Rightarrow (\text{cname}, (\text{mname}, \text{cname} \times \text{ty list} \times \text{ty} \times 'm \ \text{option}) \ \text{mapping}) \ \text{mapping}$

is $\lambda P \ C. \ \text{if } \text{is-class } (\text{Program } P) \ C \ \text{then}$

$\text{Some } (\lambda M. \ \text{if } \exists Ts \ T \ \text{mthd } D. \ \text{Program } P \vdash C \ \text{sees } M:Ts \rightarrow T = \text{mthd} \ \text{in } D \ \text{then } \text{Some } (\text{method } (\text{Program } P) \ C \ M) \ \text{else } \text{None})$

$\text{else } \text{None} \ \langle \text{proof} \rangle$

fun *wf-prog-impl'* :: $'m \ \text{prog-impl}' \Rightarrow \text{bool}$

where

$\text{wf-prog-impl}' (P, c, s, f, m) \longleftrightarrow$

$c = \text{tabulate-class } P \wedge$

$s = \text{tabulate-subcls } P \wedge$

$f = \text{tabulate-sees-field } P \wedge$

$m = \text{tabulate-Method } P$

9.5.3 Implementation type for tabulated lookup functions

typedef $'m \ \text{prog-impl} = \{P :: 'm \ \text{prog-impl}'. \ \text{wf-prog-impl}' P\}$

morphisms *impl-of ProgRefine*

$\langle \text{proof} \rangle$

lemma *impl-of-ProgImpl* [*simp*]:

$wf\text{-prog-impl}' Pfsm \implies \text{impl-of } (ProgRefine Pfsm) = Pfsm$
 ⟨proof⟩

definition $\text{program} :: 'm \text{ prog-impl} \Rightarrow 'm \text{ prog}$
where $\text{program} = Program \circ \text{fst} \circ \text{impl-of}$

code-datatype program

lemma prog-impl-eq-iff :

$Pi = Pi' \iff \text{program } Pi = \text{program } Pi'$ **for** $Pi Pi'$
 ⟨proof⟩

lemma $wf\text{-prog-impl}'\text{-impl-of}$ [*simp, intro!*]:

$wf\text{-prog-impl}' (\text{impl-of } Pi)$ **for** Pi
 ⟨proof⟩

lemma $ProgImpl\text{-impl-of}$ [*simp, code abstype*]:

$ProgRefine (\text{impl-of } Pi) = Pi$ **for** Pi
 ⟨proof⟩

lemma $\text{program-ProgRefine}$ [*simp*]: $wf\text{-prog-impl}' Pfsm \implies \text{program } (ProgRefine Pfsm) = Program$
 ($\text{fst } Pfsm$)

⟨proof⟩

lemma classes-program [*code*]: $\text{classes } (\text{program } P) = \text{fst } (\text{impl-of } P)$

⟨proof⟩

lemma class-program [*code*]: $\text{class } (\text{program } Pi) = Mapping.lookup (\text{fst } (\text{snd } (\text{impl-of } Pi)))$ **for** Pi

⟨proof⟩

9.5.4 Refining sub class and lookup functions to use precomputed mappings

declare $\text{subcls'}.equation$ [*code del*]

lemma subcls' - program [*code*]:

$\text{subcls}' (\text{program } Pi) C D \iff$
 $C = D \vee$

$(\text{case } Mapping.lookup (\text{fst } (\text{snd } (\text{snd } (\text{impl-of } Pi)))) C \text{ of } None \Rightarrow \text{False}$
 | $\text{Some } m \Rightarrow D \in m)$ **for** Pi

⟨proof⟩

lemma subcls' - i - i - program [*code*]:

$\text{subcls}'\text{-}i\text{-}i\text{-}P C D = (\text{if } \text{subcls}' P C D \text{ then } Predicate.single () \text{ else } bot)$

⟨proof⟩

lemma subcls' - i - i - o - program [*code*]:

$\text{subcls}'\text{-}i\text{-}i\text{-}o (\text{program } Pi) C =$

$\text{sup } (Predicate.single C) (\text{case } Mapping.lookup (\text{fst } (\text{snd } (\text{snd } (\text{impl-of } Pi)))) C \text{ of } None \Rightarrow bot \mid \text{Some}$
 $m \Rightarrow \text{pred-of-set } m)$ **for** Pi

⟨proof⟩

lemma rtranclp-FioB - i - i - subcls1 - i - i - o - code [*code-unfold*]:

$\text{rtranclp-FioB}\text{-}i\text{-}i (\text{subcls1}\text{-}i\text{-}i\text{-}o P) = \text{subcls}'\text{-}i\text{-}i\text{-}i P$

⟨proof⟩

declare *Method.equation*[code del]

lemma *Method-program* [code]:

program Pi \vdash *C* sees $M:Ts \rightarrow T = \text{meth}$ in *D* \longleftrightarrow
 (case *Mapping.lookup* (snd (snd (snd (snd (impl-of *Pi*)))))) *C* of
 None \Rightarrow *False*
 | *Some m* \Rightarrow
 (case *Mapping.lookup m M* of
 None \Rightarrow *False*
 | *Some (D', Ts', T', meth')* $\Rightarrow Ts = Ts' \wedge T = T' \wedge \text{meth} = \text{meth}' \wedge D = D'$) **for** *Pi*
 <proof>

lemma *Method-i-i-i-o-o-o-program* [code]:

Method-i-i-i-o-o-o (*program Pi*) *C M* =
 (case *Mapping.lookup* (snd (snd (snd (snd (impl-of *Pi*)))))) *C* of
 None \Rightarrow *bot*
 | *Some m* \Rightarrow
 (case *Mapping.lookup m M* of
 None \Rightarrow *bot*
 | *Some (D, Ts, T, meth)* \Rightarrow *Predicate.single* (*Ts, T, meth, D*)) **for** *Pi*
 <proof>

lemma *Method-i-i-i-o-o-o-i-program* [code]:

Method-i-i-i-o-o-o-i (*program Pi*) *C M D* =
 (case *Mapping.lookup* (snd (snd (snd (snd (impl-of *Pi*)))))) *C* of
 None \Rightarrow *bot*
 | *Some m* \Rightarrow
 (case *Mapping.lookup m M* of
 None \Rightarrow *bot*
 | *Some (D', Ts, T, meth)* \Rightarrow if $D = D'$ then *Predicate.single* (*Ts, T, meth*) else *bot*) **for** *Pi*
 <proof>

declare *sees-field.equation*[code del]

lemma *sees-field-program* [code]:

program Pi \vdash *C* sees $F:T$ (*fd*) in *D* \longleftrightarrow
 (case *Mapping.lookup* (*fst* (snd (snd (snd (impl-of *Pi*)))))) *C* of
 None \Rightarrow *False*
 | *Some m* \Rightarrow
 (case *Mapping.lookup m F* of
 None \Rightarrow *False*
 | *Some (D', T', fd')* $\Rightarrow T = T' \wedge \text{fd} = \text{fd}' \wedge D = D'$) **for** *Pi*
 <proof>

lemma *sees-field-i-i-i-o-o-o-program* [code]:

sees-field-i-i-i-o-o-o (*program Pi*) *C F* =
 (case *Mapping.lookup* (*fst* (snd (snd (snd (impl-of *Pi*)))))) *C* of
 None \Rightarrow *bot*
 | *Some m* \Rightarrow
 (case *Mapping.lookup m F* of
 None \Rightarrow *bot*
 | *Some (D, T, fd)* \Rightarrow *Predicate.single*(*T, fd, D*)) **for** *Pi*
 <proof>

lemma *sees-field-i-i-i-o-o-i-program* [code]:
sees-field-i-i-i-o-o-i (program *Pi*) *C F D* =
(case *Mapping.lookup* (fst (snd (snd (snd (impl-of *Pi*)))))) *C* of
 None ⇒ bot
 | Some *m* ⇒
 (case *Mapping.lookup* *m F* of
 None ⇒ bot
 | Some (*D'*, *T*, *fd*) ⇒ if *D* = *D'* then *Predicate.single*(*T*, *fd*) else bot) **for** *Pi*
⟨proof⟩

lemma *field-program* [code]:
field (program *Pi*) *C F* =
(case *Mapping.lookup* (fst (snd (snd (snd (impl-of *Pi*)))))) *C* of
 None ⇒ *Code.abort* (*STR "not-unique"*) (λ-. *Predicate.the* bot)
 | Some *m* ⇒
 (case *Mapping.lookup* *m F* of
 None ⇒ *Code.abort* (*STR "not-unique"*) (λ-. *Predicate.the* bot)
 | Some (*D'*, *T*, *fd*) ⇒ (*D'*, *T*, *fd*)) **for** *Pi*
⟨proof⟩

9.5.5 Implementation for precomputing mappings

definition *tabulate-program* :: 'm cdecl list ⇒ 'm prog-impl
where *tabulate-program* *P* = *ProgRefine* (*P*, *tabulate-class* *P*, *tabulate-subcls* *P*, *tabulate-sees-field* *P*, *tabulate-Method* *P*)

lemma *impl-of-tabulate-program* [code abstract]:
impl-of (*tabulate-program* *P*) = (*P*, *tabulate-class* *P*, *tabulate-subcls* *P*, *tabulate-sees-field* *P*, *tabulate-Method* *P*)
⟨proof⟩

lemma *Program-code* [code]:
Program = *program* ∘ *tabulate-program*
⟨proof⟩

class

lemma *tabulate-class-code* [code]:
tabulate-class = *Mapping.of-alist*
⟨proof⟩

subcls

inductive *subcls1'* :: 'm cdecl list ⇒ cname ⇒ cname ⇒ bool
where
 find: *C* ≠ *Object* ⇒ *subcls1'* ((*C*, *D*, *rest*) # *P*) *C D*
 | *step*: [*C* ≠ *Object*; *C* ≠ *C'*; *subcls1'* *P C D*] ⇒ *subcls1'* ((*C'*, *D'*, *rest*) # *P*) *C D*

code-pred
(*modes*: *i* ⇒ *i* ⇒ *o* ⇒ bool)
subcls1' ⟨proof⟩

lemma *subcls1-into-subcls1'*:
assumes *subcls1* (*Program* *P*) *C D*
shows *subcls1'* *P C D*

<proof>

lemma *subcls1'-into-subcls1*:

assumes *subcls1' P C D*

shows *subcls1 (Program P) C D*

<proof>

lemma *subcls1-eq-subcls1'*:

subcls1 (Program P) = subcls1' P

<proof>

definition *subcls'' :: 'm cdecl list \Rightarrow cname \Rightarrow cname \Rightarrow bool*

where *subcls'' P = (subcls1' P)^{**}*

code-pred

(modes: i \Rightarrow i \Rightarrow i \Rightarrow bool)

[inductify]

subcls'' <proof>

lemma *subcls''-eq-subcls: subcls'' P = subcls (Program P)*

<proof>

lemma *subclst-snd-classD*:

assumes *subclst (Program P) C D*

shows *D \in fst ' snd ' set P*

<proof>

definition *check-acyclicity :: (cname, cname set) mapping \Rightarrow 'm cdecl list \Rightarrow unit*

where *check-acyclicity - - = ()*

definition *cyclic-class-hierarchy :: unit*

where *[code del]: cyclic-class-hierarchy = ()*

declare *[[code abort: cyclic-class-hierarchy]]*

lemma *check-acyclicity-code*:

check-acyclicity mapping P =

(let - =

map ($\lambda(C, D, -)$.

if C = Object then ()

else

(case Mapping.lookup mapping D of

None \Rightarrow ()

| Some Cs \Rightarrow if C \in Cs then cyclic-class-hierarchy else ()))

P

in ())

<proof>

lemma *tabulate-subcls-code [code]*:

tabulate-subcls P =

(let cnames = map fst P;

cnames' = map (fst \circ snd) P;

mapping = Mapping.tabulate cnames (λC . set (C # [D \leftarrow cnames'. subcls'' P C D]));

- = check-acyclicity mapping P

```

    in mapping
  )
⟨proof⟩

```

Fields

Problem: Does not terminate for cyclic class hierarchies! This problem already occurs in Jinja's well-formedness checker: *wf-cdecl* calls *wf-mdecl* before checking for acyclicity, but *wf-J-mdecl* involves the type judgements, which in turn requires *Fields* (via *sees-field*). Checking acyclicity before executing *Fields'* for tabulation is difficult because we would have to intertwine tabulation and well-formedness checking. Possible (local) solution: additional termination parameter (like memoisation for *rtranclp*) and list option as error return parameter.

inductive

Fields' :: 'm cdecl list ⇒ cname ⇒ ((vname × cname) × (ty × fmod)) list ⇒ bool

for *P* :: 'm cdecl list

where

rec:

```

[[ map-of P C = Some(D,fs,ms); C ≠ Object; Fields' P D FDTs;
   FDTs' = map (λ(F,Tm). ((F,C),Tm)) fs @ FDTs ]]
⇒ Fields' P C FDTs'

```

| *Object*:

```

[[ map-of P Object = Some(D,fs,ms); FDTs = map (λ(F,T). ((F,Object),T)) fs ]]
⇒ Fields' P Object FDTs

```

lemma *Fields'-into-Fields*:

assumes *Fields' P C FDTs*

shows *Program P ⊢ C has-fields FDTs*

⟨proof⟩

lemma *Fields-into-Fields'*:

assumes *Program P ⊢ C has-fields FDTs*

shows *Fields' P C FDTs*

⟨proof⟩

lemma *Fields'-eq-Fields*:

Fields' P = Fields (Program P)

⟨proof⟩

code-pred

(*modes*: *i* ⇒ *i* ⇒ *o* ⇒ bool)

Fields' ⟨proof⟩

definition *fields'* :: 'm cdecl list ⇒ cname ⇒ ((vname × cname) × (ty × fmod)) list

where *fields' P C = (if ∃ FDTs. Fields' P C FDTs then THE FDTs. Fields' P C FDTs else [])*

lemma *eval-Fields'-conv*:

Predicate.eval (Fields'-i-i-o P C) = Fields' P C

⟨proof⟩

lemma *fields'-code* [*code*]:

fields' P C =

(let FDTs = Fields'-i-i-o P C in if Predicate.holds (FDTs ≫ (λ-. Predicate.single ())) then Predicate.the FDTs else [])

<proof>

lemma *The-Fields* [*simp*]:

$P \vdash C \text{ has-fields FDTs} \implies \text{The (Fields } P \ C) = \text{FDTs}$

<proof>

lemma *tabulate-sees-field-code* [*code*]:

tabulate-sees-field $P =$

$\text{Mapping.tabulate (map fst } P) (\lambda C. \text{Mapping.of-alist (map } (\lambda((F, D), \text{Tfm}). (F, (D, \text{Tfm}))) (\text{fields}' P \ C)))$

<proof>

Methods

Same termination problem as for *Fields'*

inductive *Methods'* :: 'm cdecl list \Rightarrow cname \Rightarrow (mname \times (ty list \times ty \times 'm option) \times cname) list \Rightarrow bool

for $P :: 'm \text{ cdecl list}$

where

$\llbracket \text{map-of } P \ \text{Object} = \text{Some}(D,fs,ms); \text{Mm} = \text{map } (\lambda(M, \text{rest}). (M, (\text{rest}, \text{Object}))) \ ms \rrbracket$
 $\implies \text{Methods}' \ P \ \text{Object} \ \text{Mm}$

$\mid \llbracket \text{map-of } P \ C = \text{Some}(D,fs,ms); C \neq \text{Object}; \text{Methods}' \ P \ D \ \text{Mm};$

$\text{Mm}' = \text{map } (\lambda(M, \text{rest}). (M, (\text{rest}, C))) \ ms \ @ \ \text{Mm} \rrbracket$

$\implies \text{Methods}' \ P \ C \ \text{Mm}'$

lemma *Methods'-into-Methods*:

assumes *Methods'* $P \ C \ \text{Mm}$

shows *Program* $P \vdash C \text{ sees-methods (map-of } \text{Mm})$

<proof>

lemma *Methods-into-Methods'*:

assumes *Program* $P \vdash C \text{ sees-methods } \text{Mm}$

shows $\exists \text{Mm}'. \text{Methods}' \ P \ C \ \text{Mm}' \wedge \text{Mm} = \text{map-of } \text{Mm}'$

<proof>

code-pred

(modes: $i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$)

Methods'

<proof>

definition *methods'* :: 'm cdecl list \Rightarrow cname \Rightarrow (mname \times (ty list \times ty \times 'm option) \times cname) list

where *methods'* $P \ C = (\text{if } \exists \text{Mm}. \text{Methods}' \ P \ C \ \text{Mm} \text{ then } \text{THE } \text{Mm}. \text{Methods}' \ P \ C \ \text{Mm} \text{ else } \llbracket \rrbracket)$

lemma *methods'-code* [*code*]:

methods' $P \ C =$

(let $\text{Mm} = \text{Methods}'\text{-i-i-o } P \ C$

in if *Predicate.holds* ($\text{Mm} \ggg (\lambda-. \text{Predicate.single } ())$) then *Predicate.the* Mm else $\llbracket \rrbracket$)

<proof>

lemma *Methods'-fun*:

assumes *Methods'* $P \ C \ \text{Mm}$

shows *Methods'* $P \ C \ \text{Mm}' \implies \text{Mm} = \text{Mm}'$

<proof>

lemma *The-Methods'* [simp]: *Methods' P C Mm* \implies *The (Methods' P C) = Mm*
 ⟨proof⟩

lemma *methods-def2* [simp]: *Methods' P C Mm* \implies *methods' P C = Mm*
 ⟨proof⟩

lemma *tabulate-Method-code* [code]:

tabulate-Method P =
Mapping.tabulate (map fst P) ($\lambda C.$ Mapping.of-alist (map ($\lambda(M, (rest, D)). (M, D, rest)$)) (methods'
P C)))
 ⟨proof⟩

Merge modules TypeRel, Decl and TypeRelRefine to avoid cyclic modules

code-identifier

code-module *TypeRel* \rightarrow
 (*SML*) *TypeRel* **and** (*Haskell*) *TypeRel* **and** (*OCaml*) *TypeRel*
 | **code-module** *TypeRelRefine* \rightarrow
 (*SML*) *TypeRel* **and** (*Haskell*) *TypeRel* **and** (*OCaml*) *TypeRel*
 | **code-module** *Decl* \rightarrow
 (*SML*) *TypeRel* **and** (*Haskell*) *TypeRel* **and** (*OCaml*) *TypeRel*

⟨ML⟩

end

theory *PCompilerRefine*

imports

TypeRelRefine
 ../Compiler/PCompiler

begin

9.5.6 *compP*

Applying the compiler to a tabulated program either compiles every method twice (once for the program itself and once for method lookup) or recomputes the class and method lookup tabulation from scratch. We follow the second approach.

fun *compP-code'* :: (*cname* \Rightarrow *mname* \Rightarrow *ty list* \Rightarrow *ty* \Rightarrow 'a \Rightarrow 'b) \Rightarrow 'a *prog-impl'* \Rightarrow 'b *prog-impl'*
where

compP-code' f (P, Cs, s, F, m) =
(let P' = map (compC f) P
in (P', tabulate-class P', s, F, tabulate-Method P'))

definition *compP-code* :: (*cname* \Rightarrow *mname* \Rightarrow *ty list* \Rightarrow *ty* \Rightarrow 'a \Rightarrow 'b) \Rightarrow 'a *prog-impl* \Rightarrow 'b *prog-impl*
where *compP-code f P = ProgRefine (compP-code' f (impl-of P))*

declare *compP.simps* [simp del] *compP.simps*[symmetric, simp]

lemma *compP-code-code* [code abstract]:

impl-of (compP-code f P) = compP-code' f (impl-of P)
 ⟨proof⟩

declare *compP.simps* [simp] *compP.simps*[symmetric, simp del]

lemma *compP-program* [code]:
compP f (program P) = program (compP-code f P)
 ⟨proof⟩

Merge module names to avoid cycles in module dependency

code-identifier

code-module *PCompiler* \rightarrow
 (*SML*) *PCompiler* **and** (*OCaml*) *PCompiler* **and** (*Haskell*) *PCompiler*
 | **code-module** *PCompilerRefine* \rightarrow
 (*SML*) *PCompiler* **and** (*OCaml*) *PCompiler* **and** (*Haskell*) *PCompiler*

⟨ML⟩

end

9.6 Executable semantics for J

theory *J-Execute*

imports

SC-Schedulers
 ../J/Threaded

begin

interpretation *sc*:

J-heap-base
addr2thread-id
thread-id2addr
sc-spurious-wakeups
sc-empty
sc-allocate P
sc-typeof-addr
sc-heap-read
sc-heap-write
for *P* ⟨proof⟩

abbreviation *sc-red* ::

$((addr, thread-id, heap) \text{ external-thread-action} \Rightarrow (addr, thread-id, 'o, heap) \text{ Jinja-thread-action})$
 $\Rightarrow addr \text{ J-prog} \Rightarrow thread-id \Rightarrow addr \text{ expr} \Rightarrow heap \times addr \text{ locals}$
 $\Rightarrow (addr, thread-id, 'o, heap) \text{ Jinja-thread-action} \Rightarrow addr \text{ expr} \Rightarrow heap \times addr \text{ locals} \Rightarrow bool$
 ($\langle -, -, - \vdash sc ((1 \langle -, - \rangle) \dashrightarrow / (1 \langle -, - \rangle)) \rangle [51, 51, 0, 0, 0, 0, 0, 0] 81$)

where

sc-red extTA P $\equiv sc.red (TYPE(addr \text{ J-mb})) P \text{ extTA } P$

fun *sc-red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o*

where

sc-red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o P t ((e, xs), h) =
red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o
addr2thread-id thread-id2addr sc-spurious-wakeups
sc-empty (sc-allocate P) sc-typeof-addr sc-heap-read-i-i-i-i-o sc-heap-write-i-i-i-i-o
(extTA2J P) P t e (h, xs)
 $\gg (\lambda (ta, e, h, xs). \text{ Predicate.single } (ta, (e, xs), h))$

abbreviation *sc-J-start-state-refine* ::

$addr\ J\text{-}prog \Rightarrow cname \Rightarrow mname \Rightarrow addr\ val\ list \Rightarrow$
 $(addr, thread\text{-}id, heap, (thread\text{-}id, (addr\ expr \times addr\ locals) \times addr\ released\text{-}locks) rm, (thread\text{-}id,$
 $addr\ wait\text{-}set\text{-}status) rm, thread\text{-}id\ rs) state\text{-}refine$

where

$sc\text{-}J\text{-}start\text{-}state\text{-}refine \equiv$

$sc\text{-}start\text{-}state\text{-}refine$

$(rm\text{-}empty\ ())\ rm\text{-}update\ (rm\text{-}empty\ ())\ (rs\text{-}empty\ ())$

$(\lambda C\ M\ Ts\ T\ (pns, body)\ vs.\ (blocks\ (this\ \# pns)\ (Class\ C\ \# Ts)\ (Null\ \# vs)\ body, Map.empty))$

lemma $eval\text{-}sc\text{-}red\text{-}i\text{-}i\text{-}i\text{-}i\text{-}i\text{-}Fii\text{-}i\text{-}oB\text{-}Fii\text{-}i\text{-}i\text{-}oB\text{-}i\text{-}i\text{-}i\text{-}i\text{-}o\text{-}o\text{-}o$:

$(\lambda t\ xm\ ta\ x'm'. Predicate.eval\ (sc\text{-}red\text{-}i\text{-}i\text{-}i\text{-}i\text{-}i\text{-}i\text{-}Fii\text{-}i\text{-}oB\text{-}Fii\text{-}i\text{-}i\text{-}oB\text{-}i\text{-}i\text{-}i\text{-}i\text{-}o\text{-}o\text{-}o\ P\ t\ xm)\ (ta, x'm'))$

$=$

$(\lambda t\ ((e, xs), h)\ ta\ ((e', xs'), h'). extTA2J\ P, P, t \vdash_{sc}\ \langle e, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle)$

$\langle proof \rangle$

lemma $sc\text{-}J\text{-}start\text{-}state\text{-}invar$: $(\lambda\cdot. True)\ (sc\text{-}state\text{-}\alpha\ (sc\text{-}J\text{-}start\text{-}state\text{-}refine\ P\ C\ M\ vs))$

$\langle proof \rangle$

9.6.1 Round-robin scheduler

interpretation $J\text{-}rr$:

$sc\text{-}round\text{-}robin\text{-}base$

$final\text{-}expr\ sc\text{-}red\text{-}i\text{-}i\text{-}i\text{-}i\text{-}i\text{-}i\text{-}Fii\text{-}i\text{-}oB\text{-}Fii\text{-}i\text{-}i\text{-}oB\text{-}i\text{-}i\text{-}i\text{-}i\text{-}o\text{-}o\text{-}o\ P\ convert\text{-}RA\ Jinja\text{-}output$

for P

$\langle proof \rangle$

definition $sc\text{-}rr\text{-}J\text{-}start\text{-}state$:: $nat \Rightarrow 'm\ prog \Rightarrow thread\text{-}id\ fifo\ round\text{-}robin$

where $sc\text{-}rr\text{-}J\text{-}start\text{-}state\ n0\ P = J\text{-}rr.round\text{-}robin\text{-}start\ n0\ (sc\text{-}start\text{-}tid\ P)$

definition $exec\text{-}J\text{-}rr$::

$nat \Rightarrow addr\ J\text{-}prog \Rightarrow cname \Rightarrow mname \Rightarrow addr\ val\ list \Rightarrow$

$(thread\text{-}id \times (addr, thread\text{-}id)\ obs\text{-}event\ list,$

$(addr, thread\text{-}id)\ locks \times ((thread\text{-}id, (addr\ expr \times addr\ locals) \times addr\ released\text{-}locks) rm \times heap)$

\times

$(thread\text{-}id, addr\ wait\text{-}set\text{-}status) rm \times thread\text{-}id\ rs) tllist$

where

$exec\text{-}J\text{-}rr\ n0\ P\ C\ M\ vs = J\text{-}rr.exec\ P\ n0\ (sc\text{-}rr\text{-}J\text{-}start\text{-}state\ n0\ P)\ (sc\text{-}J\text{-}start\text{-}state\text{-}refine\ P\ C\ M\ vs)$

interpretation $J\text{-}rr$:

$sc\text{-}round\text{-}robin$

$final\text{-}expr\ sc\text{-}red\text{-}i\text{-}i\text{-}i\text{-}i\text{-}i\text{-}i\text{-}Fii\text{-}i\text{-}oB\text{-}Fii\text{-}i\text{-}i\text{-}oB\text{-}i\text{-}i\text{-}i\text{-}i\text{-}o\text{-}o\text{-}o\ P\ convert\text{-}RA\ Jinja\text{-}output$

for P

$\langle proof \rangle$

interpretation $J\text{-}rr$:

$sc\text{-}scheduler$

$final\text{-}expr\ sc\text{-}red\text{-}i\text{-}i\text{-}i\text{-}i\text{-}i\text{-}i\text{-}Fii\text{-}i\text{-}oB\text{-}Fii\text{-}i\text{-}i\text{-}oB\text{-}i\text{-}i\text{-}i\text{-}i\text{-}o\text{-}o\text{-}o\ P\ convert\text{-}RA$

$J\text{-}rr.round\text{-}robin\ P\ n0\ Jinja\text{-}output\ pick\text{-}wakeup\text{-}via\text{-}sel\ (\lambda s\ P.\ rm\text{-}sel\ s\ (\lambda(k, v).\ P\ k\ v))\ J\text{-}rr.round\text{-}robin\text{-}invar$

$UNIV$

for $P\ n0$

$\langle proof \rangle$

9.6.2 Random scheduler

interpretation *J-rnd*:

sc-random-scheduler-base
final-expr sc-red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o P convert-RA Jinja-output
for *P*
 ⟨*proof*⟩

definition *sc-rnd-J-start-state* :: *Random.seed* ⇒ *random-scheduler*

where *sc-rnd-J-start-state seed* = *seed*

definition *exec-J-rnd* ::

Random.seed ⇒ *addr J-prog* ⇒ *cname* ⇒ *mname* ⇒ *addr val list* ⇒
 (*thread-id* × (*addr, thread-id*) *obs-event list*,
 (*addr, thread-id*) *locks* × ((*thread-id, (addr expr* × *addr locals*) × *addr released-locks*) *rm* × *heap*)
 ×
 (*thread-id, addr wait-set-status*) *rm* × *thread-id rs*) *tlist*

where

exec-J-rnd seed P C M vs = *J-rnd.exec P (sc-rnd-J-start-state seed) (sc-J-start-state-refine P C M vs)*

interpretation *J-rnd*:

sc-random-scheduler
final-expr sc-red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o P convert-RA Jinja-output
for *P*
 ⟨*proof*⟩

interpretation *J-rnd*:

sc-scheduler
final-expr sc-red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o P convert-RA
J-rnd.random-scheduler P Jinja-output pick-wakeup-via-sel (λs P. rm-sel s (λ(k,v). P k v)) λ- -.
True
UNIV
for *P*
 ⟨*proof*⟩

⟨*ML*⟩

end

9.7 Executable semantics for the JVM

theory *ExternalCall-Execute*

imports

../Common/ExternalCall
../Basic/Set-without-equal

begin

9.7.1 Translated versions of external calls for the JVM

locale *heap-execute* = *addr-base* +

constrains *addr2thread-id* :: ('*addr* :: *addr*) ⇒ '*thread-id*

and *thread-id2addr* :: '*thread-id* ⇒ '*addr*

fixes *spurious-wakeups* :: *bool*

and *empty-heap* :: 'heap
and *allocate* :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) set
and *typeof-addr* :: 'heap \Rightarrow 'addr \Rightarrow htype option
and *heap-read* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val set
and *heap-write* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow 'heap set

sublocale *heap-execute* < *execute*: *heap-base*
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate typeof-addr
 $\lambda h a ad v. v \in \text{heap-read } h a ad \ \lambda h a ad v h'. h' \in \text{heap-write } h a ad v$
 <proof>

context *heap-execute* **begin**

definition *heap-copy-loc* :: 'addr \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'heap \Rightarrow (('addr, 'thread-id) obs-event list \times 'heap) set

where [*simp*]:
 $\text{heap-copy-loc } a a' al h = \{(obs, h'). \text{execute.heap-copy-loc } a a' al h obs h'\}$

lemma *heap-copy-loc-code*:
 $\text{heap-copy-loc } a a' al h =$
 (do {
 $v \leftarrow \text{heap-read } h a al;$
 $h' \leftarrow \text{heap-write } h a' al v;$
 $\{([ReadMem a al v, WriteMem a' al v], h')\}$
 })
 <proof>

definition *heap-copies* :: 'addr \Rightarrow 'addr \Rightarrow addr-loc list \Rightarrow 'heap \Rightarrow (('addr, 'thread-id) obs-event list \times 'heap) set

where [*simp*]: $\text{heap-copies } a a' al h = \{(obs, h'). \text{execute.heap-copies } a a' al h obs h'\}$

lemma *heap-copies-code*:
shows *heap-copies-Nil*:
 $\text{heap-copies } a a' [] h = \{([], h)\}$
and *heap-copies-Cons*:
 $\text{heap-copies } a a' (al \# als) h =$
 (do {
 $(ob, h') \leftarrow \text{heap-copy-loc } a a' al h;$
 $(obs, h'') \leftarrow \text{heap-copies } a a' als h';$
 $\{(ob @ obs, h'')\}$
 })
 <proof>

definition *heap-clone* :: 'm prog \Rightarrow 'heap \Rightarrow 'addr \Rightarrow ('heap \times (('addr, 'thread-id) obs-event list \times 'addr) option) set

where [*simp*]: $\text{heap-clone } P h a = \{(h', obsa). \text{execute.heap-clone } P h a h' obsa\}$

lemma *heap-clone-code*:
 $\text{heap-clone } P h a =$
 (case *typeof-addr* $h a$ of
 [*Class-type* C] \Rightarrow
 let $HA = \text{allocate } h$ (*Class-type* C)

```

in if HA = {} then {(h, None)} else do {
  (h', a') ← HA;
  FDTs ← set-of-pred (Fields-i-i-o P C);
  (obs, h'') ← heap-copies a a' (map (λ((F, D), Tfm). CField D F) FDTs) h';
  {(h'', [(NewHeapElem a' (Class-type C) # obs, a')])}
}
| [Array-type T n] ⇒
  let HA = allocate h (Array-type T n)
  in if HA = {} then {(h, None)} else do {
    (h', a') ← HA;
    FDTs ← set-of-pred (Fields-i-i-o P Object);
    (obs, h'') ← heap-copies a a' (map (λ((F, D), Tfm). CField D F) FDTs @ map ACell [0..<n])
  }
h';
  {(h'', [(NewHeapElem a' (Array-type T n) # obs, a')])}
}
| - ⇒ {}
⟨proof⟩

```

definition *red-external-aggr* ::

```

'm prog ⇒ 'thread-id ⇒ 'addr ⇒ mname ⇒ 'addr val list ⇒ 'heap ⇒
  (('addr, 'thread-id, 'heap) external-thread-action × 'addr extCallRet × 'heap) set

```

where [*simp*]:

```

red-external-aggr P t a M vs h = execute.red-external-aggr P t a M vs h

```

lemma *red-external-aggr-code*:

```

red-external-aggr P t a M vs h =
  (if M = wait then
    let ad-t = thread-id2addr t
    in {(⊥Unlock→a, Lock→a, IsInterrupted t True, ClearInterrupt t, ObsInterrupted t}, execute.RetEXC InterruptedException, h),
    {(⊥Suspend a, Unlock→a, Lock→a, ReleaseAcquire→a, IsInterrupted t False, SyncUnlock a}, RetStaySame, h),
    {(⊥UnlockFail→a}, execute.RetEXC IllegalMonitorState, h),
    {(⊥Notified}, RetVal Unit, h),
    {(⊥WokenUp, ClearInterrupt t, ObsInterrupted t}, execute.RetEXC InterruptedException, h)} ∪
    (if spurious-wakeups then {(⊥Unlock→a, Lock→a, ReleaseAcquire→a, IsInterrupted t False, SyncUnlock a}, RetVal Unit, h)} else {})
  else if M = notify then
    {(⊥Notify a, Unlock→a, Lock→a}, RetVal Unit, h),
    {(⊥UnlockFail→a}, execute.RetEXC IllegalMonitorState, h)}
  else if M = notifyAll then
    {(⊥NotifyAll a, Unlock→a, Lock→a}, RetVal Unit, h),
    {(⊥UnlockFail→a}, execute.RetEXC IllegalMonitorState, h)}
  else if M = clone then
    do {
      (h', obsa) ← heap-clone P h a;
      {case obsa of None ⇒ (ε, execute.RetEXC OutOfMemory, h')
      | Some (obs, a') ⇒ ((K$ [], [], [], [], [], obs), RetVal (Addr a'), h')}
    }
  else if M = hashcode then {(ε, RetVal (Intg (word-of-int (hash-addr a))), h)}
  else if M = print then {(⊥ExternalCall a M vs Unit}, RetVal Unit, h)}
  else if M = currentThread then {(ε, RetVal (Addr (thread-id2addr t)), h)}
  else if M = interrupted then
    {(⊥IsInterrupted t True, ClearInterrupt t, ObsInterrupted t}, RetVal (Bool True), h),

```

```

    ({IsInterrupted t False}, RetVal (Bool False), h)}
  else if M = yield then {({Yield}, RetVal Unit, h)}
  else
    let T = ty-of-htype (the (typeof-addr h a))
    in if P ⊢ T ≤ Class Thread then
      let t-a = addr2thread-id a
      in if M = start then
        {({NewThread t-a (the-Class T, run, a) h, ThreadStart t-a}, RetVal Unit, h),
         {({ThreadExists t-a True}, execute.RetEXC IllegalThreadState, h)}
      else if M = join then
        {({Join t-a, IsInterrupted t False, ThreadJoin t-a}, RetVal Unit, h),
         {({IsInterrupted t True, ClearInterrupt t, ObsInterrupted t}, execute.RetEXC InterruptedEx-
ception, h)}
      else if M = interrupt then
        {({ThreadExists t-a True, WakeUp t-a, Interrupt t-a, ObsInterrupt t-a}, RetVal Unit, h),
         {({ThreadExists t-a False}, RetVal Unit, h)}
      else if M = isInterrupted then
        {({IsInterrupted t-a False}, RetVal (Bool False), h),
         {({IsInterrupted t-a True, ObsInterrupted t-a}, RetVal (Bool True), h)}
      else {({}, undefined)}
    else {({}, undefined)}
  ⟨proof⟩

```

end

```

lemmas [code] =
  heap-execute.heap-copy-loc-code
  heap-execute.heap-copies-code
  heap-execute.heap-clone-code
  heap-execute.red-external-aggr-code

```

end

9.8 An optimized JVM

```

theory JVMExec-Execute2
imports
  ../BV/BVNoTypeError
  ExternalCall-Execute
begin

```

This JVM must lookup the method declaration of the top call frame at every step to find the next instruction. It is more efficient to refine it such that the instruction list and the exception table are cached in the call frame. Even further, this theory adds keeps track of *drop pc ins*, whose head is the next instruction to execute.

```

locale JVM-heap-execute = heap-execute +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'heap ⇒ 'addr ⇒ htype option
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val set

```

and *heap-write* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow 'heap set

sublocale *JVM-heap-execute* < *execute*: *JVM-heap-base*
 addr2thread-id thread-id2addr
 spurious-wakeups
 empty-heap allocate typeof-addr
 $\lambda h a ad v. v \in \text{heap-read } h a ad \ \lambda h a ad v h'. h' \in \text{heap-write } h a ad v$
 {proof}

type-synonym

'addr frame' = ('addr instr list \times 'addr instr list \times ex-table) \times 'addr val list \times 'addr val list \times cname
 \times mname \times pc

type-synonym

('addr, 'heap) jvm-state' = 'addr option \times 'heap \times 'addr frame' list

type-synonym

'addr jvm-thread-state' = 'addr option \times 'addr frame' list

type-synonym

('addr, 'thread-id, 'heap) jvm-thread-action' = ('addr, 'thread-id, 'addr jvm-thread-state', 'heap) Jinja-thread-action

type-synonym

('addr, 'thread-id, 'heap) jvm-ta-state' = ('addr, 'thread-id, 'heap) jvm-thread-action' \times ('addr, 'heap) jvm-state'

fun frame'-of-frame :: 'addr jvm-prog \Rightarrow 'addr frame \Rightarrow 'addr frame'

where

frame'-of-frame P (stk, loc, C, M, pc) =
 ((drop pc (instrs-of P C M), instrs-of P C M, ex-table-of P C M), stk, loc, C, M, pc)

fun jvm-state'-of-jvm-state :: 'addr jvm-prog \Rightarrow ('addr, 'heap) jvm-state \Rightarrow ('addr, 'heap) jvm-state'

where jvm-state'-of-jvm-state P (xcp, h, frs) = (xcp, h, map (frame'-of-frame P) frs)

fun jvm-thread-state'-of-jvm-thread-state :: 'addr jvm-prog \Rightarrow 'addr jvm-thread-state \Rightarrow 'addr jvm-thread-state'

where

jvm-thread-state'-of-jvm-thread-state P (xcp, frs) = (xcp, map (frame'-of-frame P) frs)

definition jvm-thread-action'-of-jvm-thread-action ::

'addr jvm-prog \Rightarrow ('addr, 'thread-id, 'heap) jvm-thread-action \Rightarrow ('addr, 'thread-id, 'heap) jvm-thread-action'

where

jvm-thread-action'-of-jvm-thread-action P = convert-extTA (jvm-thread-state'-of-jvm-thread-state P)

fun jvm-ta-state'-of-jvm-ta-state ::

'addr jvm-prog \Rightarrow ('addr, 'thread-id, 'heap) jvm-ta-state \Rightarrow ('addr, 'thread-id, 'heap) jvm-ta-state'

where

jvm-ta-state'-of-jvm-ta-state P (ta, s) = (jvm-thread-action'-of-jvm-thread-action P ta, jvm-state'-of-jvm-state P s)

abbreviation (input) frame-of-frame' :: 'addr frame' \Rightarrow 'addr frame

where frame-of-frame' \equiv snd

definition jvm-state-of-jvm-state' :: ('addr, 'heap) jvm-state' \Rightarrow ('addr, 'heap) jvm-state

where [simp]:

$jvm\text{-state-of-jvm-state}' = \text{map-prod id (map-prod id (map frame-of-frame}'))$

definition $jvm\text{-thread-state-of-jvm-thread-state}' :: 'addr\ jvm\text{-thread-state}' \Rightarrow 'addr\ jvm\text{-thread-state}$
where $[simp]$:

$jvm\text{-thread-state-of-jvm-thread-state}' = \text{map-prod id (map frame-of-frame}')$

definition $jvm\text{-thread-action-of-jvm-thread-action}' ::$

$('addr, 'thread\text{-id}, 'heap)\ jvm\text{-thread-action}' \Rightarrow ('addr, 'thread\text{-id}, 'heap)\ jvm\text{-thread-action}$

where $[simp]$:

$jvm\text{-thread-action-of-jvm-thread-action}' = \text{convert-extTA } jvm\text{-thread-state-of-jvm-thread-state}'$

definition $jvm\text{-ta-state-of-jvm-ta-state}' ::$

$('addr, 'thread\text{-id}, 'heap)\ jvm\text{-ta-state}' \Rightarrow ('addr, 'thread\text{-id}, 'heap)\ jvm\text{-ta-state}$

where $[simp]$:

$jvm\text{-ta-state-of-jvm-ta-state}' = \text{map-prod } jvm\text{-thread-action-of-jvm-thread-action}'\ jvm\text{-state-of-jvm-state}'$

fun $frame'\text{-ok} :: 'addr\ jvm\text{-prog} \Rightarrow 'addr\ frame' \Rightarrow \text{bool}$

where

$frame'\text{-ok } P ((ins', insxt), stk, loc, C, M, pc) \longleftrightarrow$

$ins' = \text{drop } pc\ (\text{instrs-of } P\ C\ M) \wedge insxt = \text{snd (snd (the (snd (snd (snd (method } P\ C\ M))))))$

lemma $frame'\text{-ok-frame'-of-frame}$ $[iff]$:

$frame'\text{-ok } P (frame'\text{-of-frame } P\ f)$

$\langle \text{proof} \rangle$

lemma $frames'\text{-ok-inverse}$ $[simp]$:

$\forall x \in \text{set } frs. frame'\text{-ok } P\ x \implies \text{map (frame'\text{-of-frame } P \circ \text{frame-of-frame}') } frs = frs$

$\langle \text{proof} \rangle$

fun $jvm\text{-state}'\text{-ok} :: 'addr\ jvm\text{-prog} \Rightarrow ('addr, 'heap)\ jvm\text{-state}' \Rightarrow \text{bool}$

where $jvm\text{-state}'\text{-ok } P (xcp, h, frs) = (\forall f \in \text{set } frs. frame'\text{-ok } P\ f)$

lemma $jvm\text{-state}'\text{-ok-jvm-state}'\text{-of-jvm-state}$ $[iff]$:

$jvm\text{-state}'\text{-ok } P (jvm\text{-state}'\text{-of-jvm-state } P\ s)$

$\langle \text{proof} \rangle$

fun $jvm\text{-thread-state}'\text{-ok} :: 'addr\ jvm\text{-prog} \Rightarrow 'addr\ jvm\text{-thread-state}' \Rightarrow \text{bool}$

where $jvm\text{-thread-state}'\text{-ok } P (xcp, frs) \longleftrightarrow (\forall f \in \text{set } frs. frame'\text{-ok } P\ f)$

lemma $jvm\text{-thread-state}'\text{-ok-jvm-thread-state}'\text{-of-jvm-thread-state}$ $[iff]$:

$jvm\text{-thread-state}'\text{-ok } P (jvm\text{-thread-state}'\text{-of-jvm-thread-state } P\ s)$

$\langle \text{proof} \rangle$

definition $jvm\text{-thread-action}'\text{-ok} :: 'addr\ jvm\text{-prog} \Rightarrow ('addr, 'thread\text{-id}, 'heap)\ jvm\text{-thread-action}' \Rightarrow$
 bool

where $jvm\text{-thread-action}'\text{-ok } P\ ta \longleftrightarrow (\forall nt \in \text{set } \{ta\}_t. \forall t\ x\ h. nt = \text{NewThread } t\ x\ h \longrightarrow jvm\text{-thread-state}'\text{-ok } P\ x)$

lemma $jvm\text{-thread-action}'\text{-ok-jvm-thread-action}'\text{-of-jvm-thread-action}$ $[iff]$:

$jvm\text{-thread-action}'\text{-ok } P (jvm\text{-thread-action}'\text{-of-jvm-thread-action } P\ ta)$

$\langle \text{proof} \rangle$

lemma $jvm\text{-thread-action}'\text{-ok-}\varepsilon$ $[simp]$: $jvm\text{-thread-action}'\text{-ok } P\ \varepsilon$

$\langle \text{proof} \rangle$

fun *jvm-ta-state'-ok* :: 'addr jvm-prog \Rightarrow ('addr, 'thread-id, 'heap) jvm-ta-state' \Rightarrow bool
where *jvm-ta-state'-ok* P (ta, s) \longleftrightarrow *jvm-thread-action'-ok* P ta \wedge *jvm-state'-ok* P s

lemma *jvm-ta-state'-ok-jvm-ta-state'-of-jvm-ta-state* [iff]:
jvm-ta-state'-ok P (*jvm-ta-state'-of-jvm-ta-state* P tas)
 ⟨proof⟩

lemma *frame-of-frame'-inverse* [simp]: *frame-of-frame'* \circ *frame'-of-frame* P = id
 ⟨proof⟩

lemma *convert-new-thread-action-frame-of-frame'-inverse* [simp]:
convert-new-thread-action (map-prod id (map *frame-of-frame'*)) \circ *convert-new-thread-action* (*jvm-thread-state'-of-jvm-thread*
 P) = id
 ⟨proof⟩

primrec *extRet2JVM'* ::
 'addr instr list \Rightarrow 'addr instr list \Rightarrow ex-table
 \Rightarrow nat \Rightarrow 'heap \Rightarrow 'addr val list \Rightarrow 'addr val list \Rightarrow cname \Rightarrow mname \Rightarrow pc \Rightarrow 'addr frame' list
 \Rightarrow 'addr extCallRet \Rightarrow ('addr, 'heap) jvm-state'

where
extRet2JVM' ins' ins xt n h stk loc C M pc frs (RetVal v) = (None, h, ((tl ins', ins, xt), v # drop
 (Suc n) stk, loc, C, M, pc + 1) # frs)
 | *extRet2JVM'* ins' ins xt n h stk loc C M pc frs (RetExc a) = ([a], h, ((ins', ins, xt), stk, loc, C, M,
 pc) # frs)
 | *extRet2JVM'* ins' ins xt n h stk loc C M pc frs RetStaySame = (None, h, ((ins', ins, xt), stk, loc, C,
 M, pc) # frs)

definition *extNTA2JVM'* :: 'addr jvm-prog \Rightarrow (cname \times mname \times 'addr) \Rightarrow 'addr jvm-thread-state'
where *extNTA2JVM'* P \equiv (λ (C, M, a). let (D, Ts, T, meth) = method P C M; (m_{xs}, m_{xl0}, ins, xt) =
 the meth
 in (None, [((ins, ins, xt), [], Addr a # replicate m_{xl0} undefined-value, D,
 M, 0)]))

abbreviation *extTA2JVM'* ::
 'addr jvm-prog \Rightarrow ('addr, 'thread-id, 'heap) external-thread-action \Rightarrow ('addr, 'thread-id, 'heap) jvm-thread-action'
where *extTA2JVM'* P \equiv *convert-extTA* (*extNTA2JVM'* P)

lemma *jvm-state'-ok-extRet2JVM'* [simp]:
assumes [simp]: ins = instrs-of P C M xt = ex-table-of P C M \forall f \in set frs. frame'-ok P f
shows *jvm-state'-ok* P (*extRet2JVM'* (drop pc ins) ins xt n h stk loc C M pc frs va)
 ⟨proof⟩

lemma *jvm-state'-of-jvm-state-extRet2JVM* [simp]:
assumes [simp]: ins = instrs-of P C M xt = ex-table-of P C M \forall f \in set frs. frame'-ok P f
shows
jvm-state'-of-jvm-state P (*extRet2JVM* n h' stk loc C M pc (map *frame-of-frame'* frs) va) =
extRet2JVM' (drop pc (instrs-of P C M)) ins xt n h' stk loc C M pc frs va
 ⟨proof⟩

lemma *extRet2JVM'-extRet2JVM* [simp]:
jvm-state-of-jvm-state' (*extRet2JVM'* ins' ins xt n h' stk loc C M pc frs va) =
extRet2JVM n h' stk loc C M pc (map *frame-of-frame'* frs) va
 ⟨proof⟩

lemma *jvm-ta-state'-ok-inverse*:

assumes *jvm-ta-state'-ok P tas*

shows *jvm-ta-state-of-jvm-ta-state' tas ∈ A ⟷ tas ∈ jvm-ta-state'-of-jvm-ta-state P ' A*

⟨*proof*⟩

context *JVM-heap-execute begin*

primrec *exec-instr* ::

'addr instr list ⇒ 'addr instr list ⇒ ex-table

⇒ 'addr instr ⇒ 'addr jvm-prog ⇒ 'thread-id ⇒ 'heap ⇒ 'addr val list ⇒ 'addr val list

⇒ cname ⇒ mname ⇒ pc ⇒ 'addr frame' list

⇒ (('addr, 'thread-id, 'heap) jvm-ta-state') set

where

exec-instr ins' ins xt (Load n) P t h stk loc C₀ M₀ pc frs =

{(ε, (None, h, ((tl ins', ins, xt), (loc ! n) # stk, loc, C₀, M₀, pc+1)#frs))}

| *exec-instr ins' ins xt (Store n) P t h stk loc C₀ M₀ pc frs =*

{(ε, (None, h, ((tl ins', ins, xt), tl stk, loc[n:=hd stk], C₀, M₀, pc+1)#frs))}

| *exec-instr ins' ins xt (Push v) P t h stk loc C₀ M₀ pc frs =*

{(ε, (None, h, ((tl ins', ins, xt), v # stk, loc, C₀, M₀, pc+1)#frs))}

| *exec-instr ins' ins xt (New C) P t h stk loc C₀ M₀ pc frs =*

(let HA = allocate h (Class-type C)

in if HA = {} then {(ε, [execute.addr-of-sys-xcpt OutOfMemory], h, ((ins', ins, xt), stk, loc, C₀, M₀, pc) # frs)}

else do { (h', a) ← HA;

{(NewHeapElem a (Class-type C)}, None, h', ((tl ins', ins, xt), Addr a # stk, loc, C₀, M₀, pc + 1)#frs)}

| *exec-instr ins' ins xt (NewArray T) P t h stk loc C₀ M₀ pc frs =*

(let si = the-Intg (hd stk);

i = nat (sint si)

in if si < s 0

then {(ε, [execute.addr-of-sys-xcpt NegativeArraySize], h, ((ins', ins, xt), stk, loc, C₀, M₀, pc) # frs)}

else let HA = allocate h (Array-type T i) in

if HA = {} then {(ε, [execute.addr-of-sys-xcpt OutOfMemory], h, ((ins', ins, xt), stk, loc, C₀, M₀, pc) # frs)}

else do { (h', a) ← HA;

{(NewHeapElem a (Array-type T i)}, None, h', ((tl ins', ins, xt), Addr a # tl stk, loc, C₀, M₀, pc + 1) # frs)}

| *exec-instr ins' ins xt ALoad P t h stk loc C₀ M₀ pc frs =*

(let va = hd (tl stk)

in (if va = Null then {(ε, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C₀, M₀, pc) # frs)}

else

let i = the-Intg (hd stk);

a = the-Addr va;

len = alen-of-htype (the (typeof-addr h a))

in if i < s 0 ∨ int len ≤ sint i then

{(ε, [execute.addr-of-sys-xcpt ArrayIndexOutOfBounds], h, ((ins', ins, xt), stk, loc, C₀, M₀, pc) # frs)}

else do {

v ← heap-read h a (ACell (nat (sint i)));


```

      {(\ReadMem a (ACell (nat (sint i))) v\}, None, h, ((tl ins', ins, xt), v # tl (tl stk), loc,
C0, M0, pc + 1) # frs)}
    ))
| exec-instr ins' ins xt AStore P t h stk loc C0 M0 pc frs =
  (let ve = hd stk;
    vi = hd (tl stk);
    va = hd (tl (tl stk))
    in (if va = Null then {(\epsilon, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C0,
M0, pc) # frs)}
      else (let i = the-Intg vi;
        idx = nat (sint i);
        a = the-Addr va;
        hT = the (typeof-addr h a);
        T = ty-of-htype hT;
        len = alen-of-htype hT;
        U = the (execute.typeof-h h ve)
        in (if i < s 0 \ve int len \le sint i then
          {(\epsilon, [execute.addr-of-sys-xcpt ArrayIndexOutOfBounds], h, ((ins', ins, xt), stk, loc,
C0, M0, pc) # frs)}
          else if P \vdash U \le the-Array T then
            do {
              h' \leftarrow heap-write h a (ACell idx) ve;
              {(\WriteMem a (ACell idx) ve\}, None, h', ((tl ins', ins, xt), tl (tl (tl stk)), loc,
C0, M0, pc+1) # frs)}
            }
          else {(\epsilon, ([execute.addr-of-sys-xcpt ArrayStore], h, ((ins', ins, xt), stk, loc, C0, M0, pc)
# frs))}))))))
| exec-instr ins' ins xt ALength P t h stk loc C0 M0 pc frs =
  {(\epsilon, (let va = hd stk
    in if va = Null
      then ([execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C0, M0, pc) # frs)
      else (None, h, ((tl ins', ins, xt), Intg (word-of-int (int (alen-of-htype (the (typeof-addr h
(the-Addr va)))))) # tl stk, loc, C0, M0, pc+1) # frs))}
| exec-instr ins' ins xt (Getfield F C) P t h stk loc C0 M0 pc frs =
  (let v = hd stk
    in if v = Null then {(\epsilon, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C0, M0,
pc) # frs)}
    else let a = the-Addr v
      in do {
        v' \leftarrow heap-read h a (CField C F);
        {(\ReadMem a (CField C F) v'\}, None, h, ((tl ins', ins, xt), v' # (tl stk), loc, C0, M0,
pc + 1) # frs)}
      })
| exec-instr ins' ins xt (Putfield F C) P t h stk loc C0 M0 pc frs =
  (let v = hd stk;
    r = hd (tl stk)
    in if r = Null then {(\epsilon, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C0, M0,
pc) # frs)}
    else let a = the-Addr r
      in do {
        h' \leftarrow heap-write h a (CField C F) v;
        {(\WriteMem a (CField C F) v\}, None, h', ((tl ins', ins, xt), tl (tl stk), loc, C0, M0, pc
+ 1) # frs)}
      })

```

```

| exec-instr ins' ins xt (CAS F C) P t h stk loc C0 M0 pc frs =
  (let v'' = hd stk; v' = hd (tl stk); v = hd (tl (tl stk))
   in if v = Null then {(\epsilon, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C0, M0,
pc) # frs)}
   else let a = the-Addr v
        in do {
          v''' \leftarrow heap-read h a (CField C F);
          if v''' = v' then do {
            h' \leftarrow heap-write h a (CField C F) v'';
            {(\{ReadMem a (CField C F) v', WriteMem a (CField C F) v''\}, None, h', ((tl ins', ins,
xt), Bool True # tl (tl (tl stk)), loc, C0, M0, pc + 1) # frs)}
            } else {(\{ReadMem a (CField C F) v'''\}, None, h, ((tl ins', ins, xt), Bool False # tl (tl
(tl stk)), loc, C0, M0, pc + 1) # frs)}
          })
| exec-instr ins' ins xt (Checkcast T) P t h stk loc C0 M0 pc frs =
  {(\epsilon, let U = the (typeof_h (hd stk))
   in if P \vdash U \leq T then (None, h, ((tl ins', ins, xt), stk, loc, C0, M0, pc + 1) # frs)
   else ([execute.addr-of-sys-xcpt ClassCast], h, ((ins', ins, xt), stk, loc, C0, M0, pc) # frs))}
| exec-instr ins' ins xt (Instanceof T) P t h stk loc C0 M0 pc frs =
  {(\epsilon, None, h, ((tl ins', ins, xt), Bool (hd stk \neq Null \wedge P \vdash the (typeof_h (hd stk)) \leq T) # tl stk,
loc, C0, M0, pc + 1) # frs)}
| exec-instr ins' ins xt (Invoke M n) P t h stk loc C0 M0 pc frs =
  (let r = stk ! n
   in (if r = Null then {(\epsilon, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C0,
M0, pc) # frs)}
   else (let ps = rev (take n stk);
        a = the-Addr r;
        T = the (typeof-addr h a);
        (D,Ts,T, meth) = method P (class-type-of T) M
        in case meth of
          Native \Rightarrow
            do {
              (ta, va, h') \leftarrow red-external-aggr P t a M ps h;
              {(\text{extTA2JVM}' P ta, \text{extRet2JVM}' ins' ins xt n h' stk loc C0 M0 pc frs va)}
            }
          | [(mxs, mxl0, ins'', xt'')] \Rightarrow
            let f' = ((ins'', ins'', xt''), [], [r]@ps@(replicate mxl0 undefined-value), D, M, 0)
            in {(\epsilon, None, h, f' # ((ins', ins, xt), stk, loc, C0, M0, pc) # frs)}))}
| exec-instr ins' ins xt Return P t h stk0 loc0 C0 M0 pc frs =
  {(\epsilon, (if frs = [] then (None, h, []))
   else
    let v = hd stk0;
        ((ins', ins, xt), stk, loc, C, m, pc) = hd frs;
        n = length (fst (snd (method P C0 M0)))
        in (None, h, ((tl ins', ins, xt), v # (drop (n+1) stk), loc, C, m, pc+1) # tl frs))}
| exec-instr ins' ins xt Pop P t h stk loc C0 M0 pc frs =
  {(\epsilon, (None, h, ((tl ins', ins, xt), tl stk, loc, C0, M0, pc+1) # frs))}
| exec-instr ins' ins xt Dup P t h stk loc C0 M0 pc frs =
  {(\epsilon, (None, h, ((tl ins', ins, xt), hd stk # stk, loc, C0, M0, pc+1) # frs))}
| exec-instr ins' ins xt Swap P t h stk loc C0 M0 pc frs =
  {(\epsilon, (None, h, ((tl ins', ins, xt), hd (tl stk) # hd stk # tl (tl stk), loc, C0, M0, pc+1) # frs))}
| exec-instr ins' ins xt (BinOpInstr bop) P t h stk loc C0 M0 pc frs =
  {(\epsilon,
   case the (execute.binop bop (hd (tl stk)) (hd stk)) of

```

$Inl\ v \Rightarrow (None, h, ((tl\ ins', ins, xt), v \# tl\ (tl\ stk), loc, C_0, M_0, pc + 1) \# frs)$
 $| Inr\ a \Rightarrow (Some\ a, h, ((ins', ins, xt), stk, loc, C_0, M_0, pc) \# frs))$
 $| exec\text{-}instr\ ins'\ ins\ xt\ (IfFalse\ i)\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $\{\varepsilon, (let\ pc' = if\ hd\ stk = Bool\ False\ then\ nat(int\ pc+i)\ else\ pc+1$
 $\quad in\ (None, h, ((drop\ pc'\ ins, ins, xt), tl\ stk, loc, C_0, M_0, pc')\#frs))\}$
 $| exec\text{-}instr\ ins'\ ins\ xt\ (Goto\ i)\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $\{let\ pc' = nat(int\ pc+i)$
 $\quad in\ (\varepsilon, (None, h, ((drop\ pc'\ ins, ins, xt), stk, loc, C_0, M_0, pc')\#frs))\}$
 $| exec\text{-}instr\ ins'\ ins\ xt\ ThrowExc\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $\{\varepsilon, (let\ xp' = if\ hd\ stk = Null\ then\ [execute.addr-of-sys-xcpt\ NullPointer]\ else\ [the-Addr(hd\ stk)]$
 $\quad in\ (xp', h, ((ins', ins, xt), stk, loc, C_0, M_0, pc)\#frs))\}$
 $| exec\text{-}instr\ ins'\ ins\ xt\ MEnter\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $\{let\ v = hd\ stk$
 $\quad in\ if\ v = Null$
 $\quad then\ (\varepsilon, [execute.addr-of-sys-xcpt\ NullPointer], h, ((ins', ins, xt), stk, loc, C_0, M_0, pc) \# frs)$
 $\quad else\ (\{\!|Lock\rightarrow the-Addr\ v, SyncLock\ (the-Addr\ v)\!\}, None, h, ((tl\ ins', ins, xt), tl\ stk, loc, C_0, M_0,$
 $pc + 1) \# frs)\}$
 $| exec\text{-}instr\ ins'\ ins\ xt\ MExit\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$
 $(let\ v = hd\ stk$
 $\quad in\ if\ v = Null$
 $\quad then\ \{\varepsilon, [execute.addr-of-sys-xcpt\ NullPointer], h, ((ins', ins, xt), stk, loc, C_0, M_0, pc)\#frs\}$
 $\quad else\ \{\{\!|Unlock\rightarrow the-Addr\ v, SyncUnlock\ (the-Addr\ v)\!\}, None, h, ((tl\ ins', ins, xt), tl\ stk, loc, C_0,$
 $M_0, pc + 1) \# frs\},$
 $\quad (\{\!|UnlockFail\rightarrow the-Addr\ v\!\}, [execute.addr-of-sys-xcpt\ IllegalMonitorState], h, ((ins', ins, xt),$
 $stk, loc, C_0, M_0, pc) \# frs)\}$

fun *exception-step* :: 'addr jvm-prog \Rightarrow 'addr \Rightarrow 'heap \Rightarrow 'addr frame' \Rightarrow 'addr frame' list \Rightarrow ('addr, 'heap) jvm-state'

where

$exception\text{-}step\ P\ a\ h\ ((ins', ins, xt), stk, loc, C, M, pc)\ frs =$
 $(case\ match\text{-}ex\text{-}table\ P\ (execute.cname\text{-}of\ h\ a)\ pc\ xt\ of$
 $\quad None \Rightarrow ([a], h, frs)$
 $\quad | Some\ (pc', d) \Rightarrow (None, h, ((drop\ pc'\ ins, ins, xt), Addr\ a \# drop\ (size\ stk - d)\ stk, loc, C,$
 $M, pc') \# frs))$

fun *exec* :: 'addr jvm-prog \Rightarrow 'thread-id \Rightarrow ('addr, 'heap) jvm-state' \Rightarrow ('addr, 'thread-id, 'heap) jvm-ta-state' set

where

$exec\ P\ t\ (xcp, h, []) = \{\}$
 $| exec\ P\ t\ (None, h, ((ins', ins, xt), stk, loc, C, M, pc) \# frs) =$
 $\quad exec\text{-}instr\ ins'\ ins\ xt\ (hd\ ins')\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$
 $| exec\ P\ t\ ([a], h, fr \# frs) = \{\varepsilon, exception\text{-}step\ P\ a\ h\ fr\ frs\}$

definition *exec-1* ::

'addr jvm-prog \Rightarrow 'thread-id \Rightarrow ('addr, 'heap) jvm-state'
 $\Rightarrow ((\text{'addr, 'thread-id, 'heap) jvm-thread-action}' \times (\text{'addr, 'heap) jvm-state'})\ Predicate.pred$

where *exec-1* $P\ t\ \sigma = pred\text{-}of\text{-}set\ (exec\ P\ t\ \sigma)$

lemma *check-exec-instr-ok*:

assumes *wf*: wf-prog wf-md P

and *execute.check-instr* $i\ P\ h\ stk\ loc\ C\ M\ pc\ (map\ frame\text{-}of\text{-}frame'\ frs)$

and $P \vdash C\ sees\ M:Ts \rightarrow T = [m]\ in\ D$

and *jvm-state'-ok* $P\ (None, h, ((ins', ins, xt), stk, loc, C, M, pc) \# frs)$

and $tas \in exec\text{-}instr\ ins'\ ins\ xt\ i\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$

shows *jvm-ta-state'-ok P tas*
 ⟨*proof*⟩

lemma *check-exec-instr-complete:*

assumes *wf: wf-prog wf-md P*
and *execute.check-instr i P h stk loc C M pc (map frame-of-frame' frs)*
and *P ⊢ C sees M: Ts→T = [m] in D*
and *jvm-state'-ok P (None, h, ((ins', ins, xt), stk, loc, C, M, pc) # frs)*
and *tas ∈ execute.exec-instr i P t h stk loc C M pc (map frame-of-frame' frs)*
shows *jvm-ta-state'-of-jvm-ta-state P tas ∈ exec-instr ins' ins xt i P t h stk loc C M pc frs*
 ⟨*proof*⟩

lemma *check-exec-instr-refine:*

assumes *wf: wf-prog wf-md P*
and *execute.check-instr i P h stk loc C M pc (map frame-of-frame' frs)*
and *P ⊢ C sees M: Ts→T = [m] in D*
and *jvm-state'-ok P (None, h, ((ins', ins, xt), stk, loc, C, M, pc) # frs)*
and *tas ∈ exec-instr ins' ins xt i P t h stk loc C M pc frs*
shows *tas ∈ jvm-ta-state'-of-jvm-ta-state P ' execute.exec-instr i P t h stk loc C M pc (map frame-of-frame' frs)*
 ⟨*proof*⟩

lemma *exception-step-ok:*

assumes *frame'-ok P fr ∀f∈set frs. frame'-ok P f*
shows *jvm-state'-ok P (exception-step P a h fr frs)*
and *exception-step P a h fr frs = jvm-state'-of-jvm-state P (execute.exception-step P a h (snd fr) (map frame-of-frame' frs))*
 ⟨*proof*⟩

lemma *exec-step-conv:*

assumes *wf-prog wf-md P*
and *jvm-state'-ok P s*
and *execute.check P (jvm-state-of-jvm-state' s)*
shows *exec P t s = jvm-ta-state'-of-jvm-ta-state P ' execute.exec P t (jvm-state-of-jvm-state' s)*
 ⟨*proof*⟩

lemma *exec-step-ok:*

assumes *wf-prog wf-md P*
and *jvm-state'-ok P s*
and *execute.check P (jvm-state-of-jvm-state' s)*
and *tas ∈ exec P t s*
shows *jvm-ta-state'-ok P tas*
 ⟨*proof*⟩

end

locale *JVM-heap-execute-conf-read = JVM-heap-execute +*
execute: JVM-conf-read

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate typeof-addr

λh a ad v. v ∈ heap-read h a ad λh a ad v h'. h' ∈ heap-write h a ad v

+

```

constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr ⇒ htype option
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val set
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap set
and hconf :: 'heap ⇒ bool
and P :: 'addr jvm-prog
begin

lemma exec-correct-state:
  assumes wt: wf-jvm-progΦ P
  and correct: execute.correct-state Φ t (jvm-state-of-jvm-state' s)
  and ok: jvm-state'-ok P s
  shows exec P t s = jvm-ta-state'-of-jvm-ta-state P ' execute.exec P t (jvm-state-of-jvm-state' s)
  (is ?thesis1)
  and (ta, s') ∈ exec P t s ⇒ execute.correct-state Φ t (jvm-state-of-jvm-state' s') (is - ⇒ ?thesis2)
  and tas ∈ exec P t s ⇒ jvm-ta-state'-ok P tas
  ⟨proof⟩

end

lemmas [code] =
  JVM-heap-execute.exec-instr.simps
  JVM-heap-execute.exception-step.simps
  JVM-heap-execute.exec.simps
  JVM-heap-execute.exec-1-def

end

theory JVM-Execute2
imports
  SC-Schedulers
  JVMExec-Execute2
  ../BV/BVProgressThreaded
begin

abbreviation sc-heap-read-cset :: heap ⇒ addr ⇒ addr-loc ⇒ addr val set
where sc-heap-read-cset h ad al ≡ set-of-pred (sc-heap-read-i-i-i-o h ad al)

abbreviation sc-heap-write-cset :: heap ⇒ addr ⇒ addr-loc ⇒ addr val ⇒ heap set
where sc-heap-write-cset h ad al v ≡ set-of-pred (sc-heap-write-i-i-i-i-o h ad al v)

interpretation sc:
  JVM-heap-execute
  addr2thread-id
  thread-id2addr
  sc-spurious-wakeups
  sc-empty
  sc-allocate P
  sc-typeof-addr

```

sc-heap-read-cset
sc-heap-write-cset
rewrites $\bigwedge h \text{ ad al } v. v \in \text{sc-heap-read-cset } h \text{ ad al} \equiv \text{sc-heap-read } h \text{ ad al } v$
and $\bigwedge h \text{ ad al } v \text{ h}'. h' \in \text{sc-heap-write-cset } h \text{ ad al } v \equiv \text{sc-heap-write } h \text{ ad al } v \text{ h}'$
for P
 ⟨proof⟩

interpretation *sc*:

JVM-heap-execute-conf-read
addr2thread-id
thread-id2addr
sc-spurious-wakeups
sc-empty
sc-allocate P
sc-typeof-addr
sc-heap-read-cset
sc-heap-write-cset
sc-hconf P
 P
rewrites $\bigwedge h \text{ ad al } v. v \in \text{sc-heap-read-cset } h \text{ ad al} \equiv \text{sc-heap-read } h \text{ ad al } v$
and $\bigwedge h \text{ ad al } v \text{ h}'. h' \in \text{sc-heap-write-cset } h \text{ ad al } v \equiv \text{sc-heap-write } h \text{ ad al } v \text{ h}'$
for P
 ⟨proof⟩

abbreviation *sc-JVM-start-state* $:: \text{addr jvm-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{addr val list} \Rightarrow (\text{addr, thread-id, addr jvm-thread-state, heap, addr}) \text{ state}$

where *sc-JVM-start-state* $P \equiv \text{sc.execute.JVM-start-state TYPE(addr jvm-method)} P P$

abbreviation *sc-exec* $:: \text{addr jvm-prog} \Rightarrow \text{thread-id} \Rightarrow (\text{addr, heap}) \text{ jvm-state}' \Rightarrow (\text{addr, thread-id, heap}) \text{ jvm-ta-state}' \text{ set}$

where *sc-exec* $P \equiv \text{sc.exec TYPE(addr jvm-method)} P P$

abbreviation *sc-execute-mexec* $:: \text{addr jvm-prog} \Rightarrow \text{thread-id} \Rightarrow (\text{addr jvm-thread-state} \times \text{heap}) \Rightarrow (\text{addr, thread-id, heap}) \text{ jvm-thread-action} \Rightarrow (\text{addr jvm-thread-state} \times \text{heap}) \Rightarrow \text{bool}$

where *sc-execute-mexec* $P \equiv \text{sc.execute.mexec TYPE(addr jvm-method)} P P$

fun *sc-mexec* $::$

$\text{addr jvm-prog} \Rightarrow \text{thread-id} \Rightarrow (\text{addr jvm-thread-state}' \times \text{heap})$
 $\Rightarrow ((\text{addr, thread-id, heap}) \text{ jvm-thread-action}' \times \text{addr jvm-thread-state}' \times \text{heap}) \text{ Predicate.pred}$

where

sc-mexec $P \text{ t } ((xcp, frs), h) =$
 $\text{sc.exec-1 } (\text{TYPE(addr jvm-method)}) P P \text{ t } (xcp, h, frs) \gg (\lambda(ta, xcp, h, frs). \text{Predicate.single } (ta, (xcp, frs), h))$

abbreviation *sc-jvm-start-state-refine* $::$

$\text{addr jvm-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{addr val list} \Rightarrow$
 $(\text{addr, thread-id, heap, } (\text{thread-id, } (\text{addr jvm-thread-state}') \times \text{addr released-locks}) \text{ rbt, } (\text{thread-id, addr wait-set-status}) \text{ rbt, thread-id rs}) \text{ state-refine}$

where

sc-jvm-start-state-refine \equiv
 $\text{sc-start-state-refine } (\text{rm-empty } ()) \text{ rm-update } (\text{rm-empty } ()) (\text{rs-empty } ()) (\lambda C M Ts T (mxs, mxl0, ins, xt) \text{ vs. } (\text{None, } [((ins, ins, xt), []), \text{Null \# vs @ replicate mxl0 undefined-value, C, M, 0}])))$

fun *jvm-mstate-of-jvm-mstate'* $::$

$(addr, thread-id, addr\ jvm-thread-state', heap, addr)\ state \Rightarrow (addr, thread-id, addr\ jvm-thread-state, heap, addr)$
state

where

$jvm-mstate-of-jvm-mstate' (ls, (ts, m), ws) = (ls, (\lambda t. map-option (map-prod\ jvm-thread-state-of-jvm-thread-state'\ id) (ts\ t), m), ws)$

definition $sc-jvm-state-invar :: addr\ jvm-prog \Rightarrow ty_P \Rightarrow (addr, thread-id, addr\ jvm-thread-state', heap, addr)$
state set

where

$sc-jvm-state-invar\ P\ \Phi \equiv$
 $\{s. jvm-mstate-of-jvm-mstate'\ s \in sc.execute.correct-jvm-state\ P\ \Phi\} \cap$
 $\{s. ts-ok\ (\lambda t\ (xcp, frs)\ h. jvm-state'-ok\ P\ (xcp, h, frs))\ (thr\ s)\ (shr\ s)\}$

fun $JVM-final' :: 'addr\ jvm-thread-state' \Rightarrow bool$

where $JVM-final' (xcp, frs) \longleftrightarrow frs = []$

lemma $shr-jvm-mstate-of-jvm-mstate' [simp]: shr\ (jvm-mstate-of-jvm-mstate'\ s) = shr\ s$
 $\langle proof \rangle$

lemma $jvm-mstate-of-jvm-mstate'-sc-start-state [simp]:$

$jvm-mstate-of-jvm-mstate'$
 $(sc-start-state\ (\lambda C\ M\ Ts\ T\ (mxs, mxl0, ins, xt)\ vs. (None, [(ins, ins, xt), []], Null\ \# vs\ @\ replicate\ mxl0\ undefined-value, C, M, 0)))\ P\ C\ M\ vs) = sc-JVM-start-state\ P\ C\ M\ vs$
 $\langle proof \rangle$

lemma $sc-jvm-start-state-invar:$

assumes $wf-jvm-prog_{\Phi}\ P$
and $sc-wf-start-state\ P\ C\ M\ vs$
shows $sc-state-\alpha\ (sc-jvm-start-state-refine\ P\ C\ M\ vs) \in sc-jvm-state-invar\ P\ \Phi$
 $\langle proof \rangle$

lemma $invariant3p-sc-jvm-state-invar:$

assumes $wf-jvm-prog_{\Phi}\ P$
shows $invariant3p\ (multithreaded-base.redT\ JVM-final'\ (\lambda t\ xm\ ta\ x'm'. Predicate.eval\ (sc-mexec\ P\ t\ xm)\ (ta, x'm'))\ convert-RA)\ (sc-jvm-state-invar\ P\ \Phi)$
 $\langle proof \rangle$

lemma $sc-exec-deterministic:$

assumes $wf-jvm-prog_{\Phi}\ P$
shows $multithreaded-base.deterministic\ JVM-final'\ (\lambda t\ xm\ ta\ x'm'. Predicate.eval\ (sc-mexec\ P\ t\ xm)\ (ta, x'm'))\ convert-RA$
 $(sc-jvm-state-invar\ P\ \Phi)$
 $\langle proof \rangle$

9.8.1 Round-robin scheduler

interpretation $JVM-rr:$

$sc-round-robin-base$
 $JVM-final'\ sc-mexec\ P\ convert-RA\ Jinja-output$
for P
 $\langle proof \rangle$

definition $sc-rr-JVM-start-state :: nat \Rightarrow 'm\ prog \Rightarrow thread-id\ fifo\ round-robin$

where $sc-rr-JVM-start-state\ n0\ P = JVM-rr.round-robin-start\ n0\ (sc-start-tid\ P)$

definition *exec-JVM-rr* ::

nat \Rightarrow *addr jvm-prog* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *addr val list* \Rightarrow
 (*thread-id* \times (*addr*, *thread-id*) *obs-event list*,
 (*addr*, *thread-id*) *locks* \times ((*thread-id*, *addr jvm-thread-state'* \times *addr released-locks*) *RBT.rbt* \times *heap*)
 \times
 (*thread-id*, *addr wait-set-status*) *RBT.rbt* \times *thread-id rs*) *tllist*

where

exec-JVM-rr *n0 P C M vs* = *JVM-rr.exec P n0 (sc-rr-JVM-start-state n0 P) (sc-jvm-start-state-refine P C M vs)*

interpretation *JVM-rr*:

sc-round-robin
JVM-final' sc-mexec P convert-RA Jinja-output
for *P*
 \langle *proof* \rangle

lemma *JVM-rr*:

assumes *wf-jvm-prog* Φ *P*
shows
sc-scheduler
JVM-final' (sc-mexec P) convert-RA
 (*JVM-rr.round-robin P n0*) (*pick-wakeup-via-sel* ($\lambda s P. rm-sel s (\lambda(k,v). P k v)$)) *JVM-rr.round-robin-invar*
 (*sc-jvm-state-invar P* Φ)
 \langle *proof* \rangle

9.8.2 Random scheduler

interpretation *JVM-rnd*:

sc-random-scheduler-base
JVM-final' sc-mexec P convert-RA Jinja-output
for *P*
 \langle *proof* \rangle

definition *sc-rnd-JVM-start-state* :: *Random.seed* \Rightarrow *random-scheduler*

where *sc-rnd-JVM-start-state seed* = *seed*

definition *exec-JVM-rnd* ::

Random.seed \Rightarrow *addr jvm-prog* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *addr val list* \Rightarrow
 (*thread-id* \times (*addr*, *thread-id*) *obs-event list*,
 (*addr*, *thread-id*) *locks* \times ((*thread-id*, *addr jvm-thread-state'* \times *addr released-locks*) *RBT.rbt* \times *heap*)
 \times
 (*thread-id*, *addr wait-set-status*) *RBT.rbt* \times *thread-id rs*) *tllist*

where *exec-JVM-rnd seed P C M vs* = *JVM-rnd.exec P (sc-rnd-JVM-start-state seed) (sc-jvm-start-state-refine P C M vs)*

interpretation *JVM-rnd*:

sc-random-scheduler
JVM-final' sc-mexec P convert-RA Jinja-output
for *P*
 \langle *proof* \rangle

lemma *JVM-rnd*:

assumes *wf-jvm-prog* Φ *P*


```

shows
  sc-scheduler
    JVM-final' (sc-mexec P) convert-RA
    (JVM-rnd.random-scheduler P) (pick-wakeup-via-sel ( $\lambda s P. rm-sel s (\lambda(k,v). P k v)$ )) ( $\lambda- -. True$ )
    (sc-jvm-state-invar P  $\Phi$ )
<proof>

<ML>

end

```

9.9 Code generator setup

theory *Code-Generation*

imports

```

  J-Execute
  JVM-Execute2
  ../Compiler/Preprocessor
  ../BV/BCVExec
  ../Compiler/Compiler
  Coinductive.Lazy-TLList
  HOL-Library.Code-Cardinality
  HOL-Library.Code-Target-Int
  HOL-Library.Code-Target-Numeral

```

begin

Avoid module dependency cycles.

code-identifier

```

  code-module More-Set  $\rightarrow$  (SML) Set
| code-module Set  $\rightarrow$  (SML) Set
| code-module Complete-Lattices  $\rightarrow$  (SML) Set
| code-module Complete-Partial-Order  $\rightarrow$  (SML) Set

```

new code equation for *insort-insert-key* to avoid module dependency cycle with *set*.

lemma *insort-insert-key-code* [code]:

```

  insort-insert-key f x xs =
  (if List.member (map f xs) (f x) then xs else insort-key f x xs)
<proof>

```

equations on predicate operations for code inlining

lemma *eq-i-o-conv-single*: *eq-i-o* = *Predicate.single*

<proof>

lemma *eq-o-i-conv-single*: *eq-o-i* = *Predicate.single*

<proof>

lemma *sup-case-exp-case-exp-same*:

```

  sup-class.sup
  (case-exp cNew cNewArray cCast cInstanceOf cVal cBinOp cVar cLAss cAAss cALen cFAcc
  cFAss cCAS cCall cBlock cSync cInSync cSeq cCond cWhile cThrow cTry e)
  (case-exp cNew' cNewArray' cCast' cInstanceOf' cVal' cBinOp' cVar' cLAss' cAAss' cAAss'
  cALen' cFAcc' cFAss' cCAS' cCall' cBlock' cSync' cInSync' cSeq' cCond' cWhile' cThrow' cTry' e)
  =

```

(case e of
 new C \Rightarrow sup-class.sup (cNew C) (cNew' C)
 | newArray T e \Rightarrow sup-class.sup (cNewArray T e) (cNewArray' T e)
 | Cast T e \Rightarrow sup-class.sup (cCast T e) (cCast' T e)
 | InstanceOf e T \Rightarrow sup-class.sup (cInstanceOf e T) (cInstanceOf' e T)
 | Val v \Rightarrow sup-class.sup (cVal v) (cVal' v)
 | BinOp e bop e' \Rightarrow sup-class.sup (cBinOp e bop e') (cBinOp' e bop e')
 | Var V \Rightarrow sup-class.sup (cVar V) (cVar' V)
 | LAss V e \Rightarrow sup-class.sup (cLAss V e) (cLAss' V e)
 | AAcc a e \Rightarrow sup-class.sup (cAAcc a e) (cAAcc' a e)
 | AAss a i e \Rightarrow sup-class.sup (cAAss a i e) (cAAss' a i e)
 | ALen a \Rightarrow sup-class.sup (cALen a) (cALen' a)
 | FAcc e F D \Rightarrow sup-class.sup (cFAcc e F D) (cFAcc' e F D)
 | FAss e F D e' \Rightarrow sup-class.sup (cFAss e F D e') (cFAss' e F D e')
 | CompareAndSwap e D F e' e'' \Rightarrow sup-class.sup (cCAS e D F e' e'') (cCAS' e D F e' e'')
 | Call e M es \Rightarrow sup-class.sup (cCall e M es) (cCall' e M es)
 | Block V T vo e \Rightarrow sup-class.sup (cBlock V T vo e) (cBlock' V T vo e)
 | Synchronized v e e' \Rightarrow sup-class.sup (cSync v e e') (cSync' v e e')
 | InSynchronized v a e \Rightarrow sup-class.sup (cInSync v a e) (cInSync' v a e)
 | Seq e e' \Rightarrow sup-class.sup (cSeq e e') (cSeq' e e')
 | Cond b e e' \Rightarrow sup-class.sup (cCond b e e') (cCond' b e e')
 | While b e \Rightarrow sup-class.sup (cWhile b e) (cWhile' b e)
 | throw e \Rightarrow sup-class.sup (cThrow e) (cThrow' e)
 | TryCatch e C V e' \Rightarrow sup-class.sup (cTry e C V e') (cTry' e C V e')

\langle proof \rangle

lemma sup-case-exp-case-exp-other:

fixes p :: 'a :: semilattice-sup **shows**

sup-class.sup

(case-exp cNew cNewArray cCast cInstanceOf cVal cBinOp cVar cLAss cAAcc cAAss cALen cFAcc
 cFAss cCAS cCall cBlock cSync cInSync cSeq cCond cWhile cThrow cTry e)

(sup-class.sup (case-exp cNew' cNewArray' cCast' cInstanceOf' cVal' cBinOp' cVar' cLAss' cAAcc'
 cAAss' cALen' cFAcc' cFAss' cCAS' cCall' cBlock' cSync' cInSync' cSeq' cCond' cWhile' cThrow'
 cTry' e) p) =

sup-class.sup (case e of

new C \Rightarrow sup-class.sup (cNew C) (cNew' C)
 | newArray T e \Rightarrow sup-class.sup (cNewArray T e) (cNewArray' T e)
 | Cast T e \Rightarrow sup-class.sup (cCast T e) (cCast' T e)
 | InstanceOf e T \Rightarrow sup-class.sup (cInstanceOf e T) (cInstanceOf' e T)
 | Val v \Rightarrow sup-class.sup (cVal v) (cVal' v)
 | BinOp e bop e' \Rightarrow sup-class.sup (cBinOp e bop e') (cBinOp' e bop e')
 | Var V \Rightarrow sup-class.sup (cVar V) (cVar' V)
 | LAss V e \Rightarrow sup-class.sup (cLAss V e) (cLAss' V e)
 | AAcc a e \Rightarrow sup-class.sup (cAAcc a e) (cAAcc' a e)
 | AAss a i e \Rightarrow sup-class.sup (cAAss a i e) (cAAss' a i e)
 | ALen a \Rightarrow sup-class.sup (cALen a) (cALen' a)
 | FAcc e F D \Rightarrow sup-class.sup (cFAcc e F D) (cFAcc' e F D)
 | FAss e F D e' \Rightarrow sup-class.sup (cFAss e F D e') (cFAss' e F D e')
 | CompareAndSwap e D F e' e'' \Rightarrow sup-class.sup (cCAS e D F e' e'') (cCAS' e D F e' e'')
 | Call e M es \Rightarrow sup-class.sup (cCall e M es) (cCall' e M es)
 | Block V T vo e \Rightarrow sup-class.sup (cBlock V T vo e) (cBlock' V T vo e)
 | Synchronized v e e' \Rightarrow sup-class.sup (cSync v e e') (cSync' v e e')
 | InSynchronized v a e \Rightarrow sup-class.sup (cInSync v a e) (cInSync' v a e)
 | Seq e e' \Rightarrow sup-class.sup (cSeq e e') (cSeq' e e')

| *Cond* $b\ e\ e' \Rightarrow \text{sup-class.sup } (c\text{Cond } b\ e\ e')\ (c\text{Cond}'\ b\ e\ e')$
 | *While* $b\ e \Rightarrow \text{sup-class.sup } (c\text{While } b\ e)\ (c\text{While}'\ b\ e)$
 | *throw* $e \Rightarrow \text{sup-class.sup } (c\text{Throw } e)\ (c\text{Throw}'\ e)$
 | *TryCatch* $e\ C\ V\ e' \Rightarrow \text{sup-class.sup } (c\text{Try } e\ C\ V\ e')\ (c\text{Try}'\ e\ C\ V\ e')$ p
 <proof>

lemma *sup-bot1*: $\text{sup-class.sup } \text{bot } a = (a :: 'a :: \{\text{semilattice-sup, order-bot}\})$
 <proof>

lemma *sup-bot2*: $\text{sup-class.sup } a\ \text{bot} = (a :: 'a :: \{\text{semilattice-sup, order-bot}\})$
 <proof>

lemma *sup-case-val-case-val-same*:

$\text{sup-class.sup } (\text{case-val } c\text{Unit } c\text{Null } c\text{Bool } c\text{Intg } c\text{Addr } v)\ (\text{case-val } c\text{Unit}'\ c\text{Null}'\ c\text{Bool}'\ c\text{Intg}'\ c\text{Addr}'\ v) =$
 $(\text{case } v\ \text{of}$
 $\text{Unit} \Rightarrow \text{sup-class.sup } c\text{Unit } c\text{Unit}'$
 | $\text{Null} \Rightarrow \text{sup-class.sup } c\text{Null } c\text{Null}'$
 | $\text{Bool } b \Rightarrow \text{sup-class.sup } (c\text{Bool } b)\ (c\text{Bool}'\ b)$
 | $\text{Intg } i \Rightarrow \text{sup-class.sup } (c\text{Intg } i)\ (c\text{Intg}'\ i)$
 | $\text{Addr } a \Rightarrow \text{sup-class.sup } (c\text{Addr } a)\ (c\text{Addr}'\ a))$
 <proof>

lemma *sup-case-bool-case-bool-same*:

$\text{sup-class.sup } (\text{case-bool } t\ f\ b)\ (\text{case-bool } t'\ f'\ b) =$
 $(\text{if } b\ \text{then } \text{sup-class.sup } t\ t'\ \text{else } \text{sup-class.sup } f\ f')$
 <proof>

lemmas *predicate-code-inline* [*code-unfold*] =

Predicate.single-bind Predicate.bind-single split
eq-i-o-conv-single eq-o-i-conv-single
sup-case-exp-case-exp-same sup-case-exp-case-exp-other unit.case
sup-bot1 sup-bot2 sup-case-val-case-val-same sup-case-bool-case-bool-same

lemma *op-case-ty-case-ty-same*:

$f\ (\text{case-ty } c\text{Void } c\text{Boolean } c\text{Integer } c\text{NT } c\text{Class } c\text{Array } e)$
 $(\text{case-ty } c\text{Void}'\ c\text{Boolean}'\ c\text{Integer}'\ c\text{NT}'\ c\text{Class}'\ c\text{Array}'\ e) =$
 $(\text{case } e\ \text{of}$
 $\text{Void} \Rightarrow f\ c\text{Void } c\text{Void}'$
 | $\text{Boolean} \Rightarrow f\ c\text{Boolean } c\text{Boolean}'$
 | $\text{Integer} \Rightarrow f\ c\text{Integer } c\text{Integer}'$
 | $\text{NT} \Rightarrow f\ c\text{NT } c\text{NT}'$
 | $\text{Class } C \Rightarrow f\ (c\text{Class } C)\ (c\text{Class}'\ C)$
 | $\text{Array } T \Rightarrow f\ (c\text{Array } T)\ (c\text{Array}'\ T))$
 <proof>

declare *op-case-ty-case-ty-same*[**where** $f = \text{sup-class.sup}$, *code-unfold*]

lemma *op-case-bop-case-bop-same*:

$f\ (\text{case-bop } c\text{Eq } c\text{NotEq } c\text{LessThan } c\text{LessOrEqual } c\text{GreaterThan } c\text{GreaterOrEqual } c\text{Add } c\text{Subtract}$
 $c\text{Mult } c\text{Div } c\text{Mod } c\text{BinAnd } c\text{BinOr } c\text{BinXor } c\text{ShiftLeft } c\text{ShiftRightZeros } c\text{ShiftRightSigned } \text{bop})$
 $(\text{case-bop } c\text{Eq}'\ c\text{NotEq}'\ c\text{LessThan}'\ c\text{LessOrEqual}'\ c\text{GreaterThan}'\ c\text{GreaterOrEqual}'\ c\text{Add}'\ c\text{Subtract}'\ c\text{Mult}'\ c\text{Div}'\ c\text{Mod}'\ c\text{BinAnd}'\ c\text{BinOr}'\ c\text{BinXor}'\ c\text{ShiftLeft}'\ c\text{ShiftRightZeros}'\ c\text{ShiftRightSigned}'\ \text{bop})$

```

= case-bop (f cEq cEq') (f cNotEq cNotEq') (f cLessThan cLessThan') (f cLessOrEqual cLessOrEqual')
(f cGreaterThan cGreaterThan') (f cGreaterOrEqual cGreaterOrEqual') (f cAdd cAdd') (f cSubtract cSubtract')
(f cMult cMult') (f cDiv cDiv') (f cMod cMod') (f cBinAnd cBinAnd') (f cBinOr cBinOr')
(f cBinXor cBinXor') (f cShiftLeft cShiftLeft') (f cShiftRightZeros cShiftRightZeros') (f cShiftRightSigned cShiftRightSigned') bop
⟨proof⟩

```

lemma *sup-case-bop-case-bop-other* [code-unfold]:

fixes $p :: 'a :: \text{semilattice-sup}$ **shows**

```

sup-class.sup (case-bop cEq cNotEq cLessThan cLessOrEqual cGreaterThan cGreaterOrEqual cAdd
cSubtract cMult cDiv cMod cBinAnd cBinOr cBinXor cShiftLeft cShiftRightZeros cShiftRightSigned
bop)

```

```

(sup-class.sup (case-bop cEq' cNotEq' cLessThan' cLessOrEqual' cGreaterThan' cGreaterOrEqual'
cAdd' cSubtract' cMult' cDiv' cMod' cBinAnd' cBinOr' cBinXor' cShiftLeft' cShiftRightZeros'
cShiftRightSigned' bop) p)

```

```

= sup-class.sup (case-bop (sup-class.sup cEq cEq') (sup-class.sup cNotEq cNotEq') (sup-class.sup
cLessThan cLessThan') (sup-class.sup cLessOrEqual cLessOrEqual') (sup-class.sup cGreaterThan cGreaterThan')
(sup-class.sup cGreaterOrEqual cGreaterOrEqual') (sup-class.sup cAdd cAdd') (sup-class.sup cSubtract cSubtract')
(sup-class.sup cMult cMult') (sup-class.sup cDiv cDiv') (sup-class.sup cMod cMod')
(sup-class.sup cBinAnd cBinAnd') (sup-class.sup cBinOr cBinOr') (sup-class.sup cBinXor cBinXor')
(sup-class.sup cShiftLeft cShiftLeft') (sup-class.sup cShiftRightZeros cShiftRightZeros') (sup-class.sup
cShiftRightSigned cShiftRightSigned') bop) p

```

⟨proof⟩

declare *op-case-bop-case-bop-same*[**where** $f = \text{sup-class.sup}$, code-unfold]

end

theory *JVMExec-Execute*

imports

../JVM/JVMExec

ExternalCall-Execute

begin

9.9.1 Manual translation of the JVM to use sets instead of predicates

```

locale JVM-heap-execute = heap-execute +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr ⇒ htype option
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val set
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap set

```

sublocale *JVM-heap-execute* < *execute*: *JVM-heap-base*

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate typeof-addr

$\lambda h a ad v. v \in \text{heap-read } h a ad \ \lambda h a ad v h'. h' \in \text{heap-write } h a ad v$

⟨proof⟩

context *JVM-heap-execute* **begin**

definition *exec-instr* ::

'*addr instr* ⇒ '*addr jvm-prog* ⇒ '*thread-id* ⇒ '*heap* ⇒ '*addr val list* ⇒ '*addr val list*
 ⇒ *cname* ⇒ *mname* ⇒ *pc* ⇒ '*addr frame list*
 ⇒ (('addr, '*thread-id*, '*heap*) *jvm-thread-action* × ('addr, '*heap*) *jvm-state*) *set*

where [*simp*]: *exec-instr* = *execute.exec-instr*

lemma *exec-instr-code* [*code*]:

exec-instr (*Load n*) *P t h stk loc C₀ M₀ pc frs* =
 { $(\varepsilon, (None, h, ((loc ! n) \# stk, loc, C_0, M_0, pc+1)\#frs))$ }

exec-instr (*Store n*) *P t h stk loc C₀ M₀ pc frs* =
 { $(\varepsilon, (None, h, (tl\ stk, loc[n:=hd\ stk], C_0, M_0, pc+1)\#frs))$ }

exec-instr (*Push v*) *P t h stk loc C₀ M₀ pc frs* =
 { $(\varepsilon, (None, h, (v \# stk, loc, C_0, M_0, pc+1)\#frs))$ }

exec-instr (*New C*) *P t h stk loc C₀ M₀ pc frs* =
 (let *HA* = *allocate h (Class-type C)* in
 if *HA* = {} then { $(\varepsilon, \lfloor execute.addr-of-sys-xcpt\ OutOfMemory \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)$ }

else do { (*h'*, *a*) ← *HA*; { $(\lfloor NewHeapElem\ a\ (Class-type\ C) \rfloor, None, h', (Addr\ a \# stk, loc, C_0,$
M₀, pc + 1)\#frs)}

exec-instr (*NewArray T*) *P t h stk loc C₀ M₀ pc frs* =
 (let *si* = *the-Intg (hd stk)*;
i = *nat (sint si)*
 in if *si* < *s 0*
 then { $(\varepsilon, \lfloor execute.addr-of-sys-xcpt\ NegativeArraySize \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)$ }

else let *HA* = *allocate h (Array-type T i)* in
 if *HA* = {} then { $(\varepsilon, \lfloor execute.addr-of-sys-xcpt\ OutOfMemory \rfloor, h, (stk, loc, C_0, M_0, pc) \#$
frs)}

else do { (*h'*, *a*) ← *HA*; { $(\lfloor NewHeapElem\ a\ (Array-type\ T\ i) \rfloor, None, h', (Addr\ a \# tl\ stk, loc,$
C₀, M₀, pc + 1) \# frs)}

exec-instr *ALoad P t h stk loc C₀ M₀ pc frs* =
 (let *va* = *hd (tl stk)*
 in (if *va* = *Null* then { $(\varepsilon, \lfloor execute.addr-of-sys-xcpt\ NullPointer \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)$ }

else
 let *i* = *the-Intg (hd stk)*;
a = *the-Addr va*;
len = *alen-of-htype (the (typeof-addr h a))*
 in if *i* < *s 0* ∨ *int len* ≤ *sint i* then
 { $(\varepsilon, \lfloor execute.addr-of-sys-xcpt\ ArrayIndexOutOfBounds \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)$ }

else do {
v ← *heap-read h a (ACell (nat (sint i)))*;
 { $(\lfloor ReadMem\ a\ (ACell\ (nat\ (sint\ i)))\ v \rfloor, None, h, (v \# tl (tl\ stk), loc, C_0, M_0, pc +$
1) \# frs)}

}}))

exec-instr *AStore P t h stk loc C₀ M₀ pc frs* =
 (let *ve* = *hd stk*;
vi = *hd (tl stk)*;
va = *hd (tl (tl stk))*
 in (if *va* = *Null* then { $(\varepsilon, \lfloor execute.addr-of-sys-xcpt\ NullPointer \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)$ }

else (let *i* = *the-Intg vi*;
idx = *nat (sint i)*;
a = *the-Addr va*;
hT = *the (typeof-addr h a)*;
T = *ty-of-htype hT*;

```

      len = alen-of-htype hT;
      U = the (execute.typeof-h h ve)
    in (if i < s 0 ∨ int len ≤ sint i then
      {(\ε, [execute.addr-of-sys-xcpt ArrayIndexOutOfBounds], h, (stk, loc, C0, M0, pc) #
frs)})
    else if P ⊢ U ≤ the-Array T then
      do {
        h' ← heap-write h a (ACell idx) ve;
        {(\WriteMem a (ACell idx) ve}, None, h', (tl (tl (tl stk)), loc, C0, M0, pc+1) #
frs)})
      }
    else {(\ε, ([execute.addr-of-sys-xcpt ArrayStore], h, (stk, loc, C0, M0, pc) # frs))})
exec-instr ALength P t h stk loc C0 M0 pc frs =
  {(\ε, (let va = hd stk
    in if va = Null
      then ([execute.addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)
      else (None, h, (Intg (word-of-int (int (alen-of-htype (the (typeof-addr h (the-Addr va)))))) #
tl stk, loc, C0, M0, pc+1) # frs))})
exec-instr (Getfield F C) P t h stk loc C0 M0 pc frs =
  (let v = hd stk
    in if v = Null then {(\ε, [execute.addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)}
    else let a = the-Addr v
      in do {
        v' ← heap-read h a (CField C F);
        {(\ReadMem a (CField C F) v'}, None, h, (v' # (tl stk), loc, C0, M0, pc + 1) # frs)}
      })
exec-instr (Putfield F C) P t h stk loc C0 M0 pc frs =
  (let v = hd stk;
    r = hd (tl stk)
    in if r = Null then {(\ε, [execute.addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)}
    else let a = the-Addr r
      in do {
        h' ← heap-write h a (CField C F) v;
        {(\WriteMem a (CField C F) v}, None, h', (tl (tl stk), loc, C0, M0, pc + 1) # frs)}
      })
exec-instr (Checkcast T) P t h stk loc C0 M0 pc frs =
  {(\ε, let U = the (typeofh (hd stk))
    in if P ⊢ U ≤ T then (None, h, (stk, loc, C0, M0, pc + 1) # frs)
    else ([execute.addr-of-sys-xcpt ClassCast], h, (stk, loc, C0, M0, pc) # frs))}
exec-instr (Instanceof T) P t h stk loc C0 M0 pc frs =
  {(\ε, None, h, (Bool (hd stk ≠ Null ∧ P ⊢ the (typeofh (hd stk)) ≤ T) # tl stk, loc, C0, M0, pc +
1) # frs)}
exec-instr (Invoke M n) P t h stk loc C0 M0 pc frs =
  (let r = stk ! n
    in (if r = Null then {(\ε, [execute.addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)}
    else (let ps = rev (take n stk);
      a = the-Addr r;
      T = the (typeof-addr h a);
      (D,M',Ts, meth) = method P (class-type-of T) M
      in case meth of
        Native ⇒
          do {
            (ta, va, h') ← red-external-aggr P t a M ps h;
            {(\extTA2JVM P ta, extRet2JVM n h' stk loc C0 M0 pc frs va)}
          }

```

$$\begin{aligned}
& \} \\
& | [(m\text{xs}, m\text{x}l_0, i\text{ns}, x\text{t})] \Rightarrow \\
& \quad \text{let } f' = ([, [r]@\text{ps}@\text{(replicate } m\text{x}l_0 \text{ undefined-value)}, D, M, 0) \\
& \quad \text{in } \{(\varepsilon, \text{None}, h, f' \# (\text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs})\}) \\
\text{exec-instr Return } P \text{ t h stk}_0 \text{ loc}_0 \text{ C}_0 \text{ M}_0 \text{ pc frs} = \\
& \{(\varepsilon, (\text{if } \text{frs} = [] \text{ then } (\text{None}, h, [])) \\
& \quad \text{else} \\
& \quad \text{let } v = \text{hd } \text{stk}_0; \\
& \quad (\text{stk}, \text{loc}, C, m, \text{pc}) = \text{hd } \text{frs}; \\
& \quad n = \text{length } (\text{fst } (\text{snd } (\text{method } P \text{ C}_0 \text{ M}_0))) \\
& \quad \text{in } (\text{None}, h, (v \# (\text{drop } (n+1) \text{ stk}), \text{loc}, C, m, \text{pc}+1) \# \text{tl } \text{frs}))\} \\
\text{exec-instr Pop } P \text{ t h stk loc C}_0 \text{ M}_0 \text{ pc frs} = \{(\varepsilon, (\text{None}, h, (\text{tl } \text{stk}, \text{loc}, C_0, M_0, \text{pc}+1) \# \text{frs}))\} \\
\text{exec-instr Dup } P \text{ t h stk loc C}_0 \text{ M}_0 \text{ pc frs} = \{(\varepsilon, (\text{None}, h, (\text{hd } \text{stk} \# \text{stk}, \text{loc}, C_0, M_0, \text{pc}+1) \# \text{frs}))\} \\
\text{exec-instr Swap } P \text{ t h stk loc C}_0 \text{ M}_0 \text{ pc frs} = \{(\varepsilon, (\text{None}, h, (\text{hd } (\text{tl } \text{stk}) \# \text{hd } \text{stk} \# \text{tl } (\text{tl } \text{stk}), \text{loc}, \\
C_0, M_0, \text{pc}+1) \# \text{frs}))\} \\
\text{exec-instr (BinOpInstr bop) } P \text{ t h stk loc C}_0 \text{ M}_0 \text{ pc frs} = \\
\{(\varepsilon, \\
\quad \text{case the (execute.binop bop (hd (tl stk)) (hd stk)) of} \\
\quad \text{Inl } v \Rightarrow (\text{None}, h, (v \# \text{tl } (\text{tl } \text{stk}), \text{loc}, C_0, M_0, \text{pc} + 1) \# \text{frs}) \\
\quad | \text{Inr } a \Rightarrow (\text{Some } a, h, (\text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs}))\} \\
\text{exec-instr (IfFalse i) } P \text{ t h stk loc C}_0 \text{ M}_0 \text{ pc frs} = \\
\{(\varepsilon, (\text{let } \text{pc}' = \text{if } \text{hd } \text{stk} = \text{Bool False} \text{ then } \text{nat}(\text{int } \text{pc}+i) \text{ else } \text{pc}+1 \\
\quad \text{in } (\text{None}, h, (\text{tl } \text{stk}, \text{loc}, C_0, M_0, \text{pc}') \# \text{frs}))\} \\
\text{exec-instr (Goto i) } P \text{ t h stk loc C}_0 \text{ M}_0 \text{ pc frs} = \{(\varepsilon, (\text{None}, h, (\text{stk}, \text{loc}, C_0, M_0, \text{nat}(\text{int } \text{pc}+i)) \# \text{frs}))\} \\
\text{exec-instr ThrowExc } P \text{ t h stk loc C}_0 \text{ M}_0 \text{ pc frs} = \\
\{(\varepsilon, (\text{let } \text{xp}' = \text{if } \text{hd } \text{stk} = \text{Null} \text{ then } [\text{execute.addr-of-sys-xcpt NullPointer}] \text{ else } [\text{the-Addr}(\text{hd } \text{stk})] \\
\quad \text{in } (\text{xp}', h, (\text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs}))\} \\
\text{exec-instr MEnter } P \text{ t h stk loc C}_0 \text{ M}_0 \text{ pc frs} = \\
\{(\text{let } v = \text{hd } \text{stk} \\
\quad \text{in if } v = \text{Null} \\
\quad \text{then } (\varepsilon, [\text{execute.addr-of-sys-xcpt NullPointer}], h, (\text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs}) \\
\quad \text{else } (\{\text{Lock} \rightarrow \text{the-Addr } v, \text{SyncLock } (\text{the-Addr } v)\}, \text{None}, h, (\text{tl } \text{stk}, \text{loc}, C_0, M_0, \text{pc} + 1) \# \\
\text{frs}))\} \\
\text{exec-instr MExit } P \text{ t h stk loc C}_0 \text{ M}_0 \text{ pc frs} = \\
(\text{let } v = \text{hd } \text{stk} \\
\quad \text{in if } v = \text{Null} \\
\quad \text{then } \{(\varepsilon, [\text{execute.addr-of-sys-xcpt NullPointer}], h, (\text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs})\} \\
\quad \text{else } (\{\text{Unlock} \rightarrow \text{the-Addr } v, \text{SyncUnlock } (\text{the-Addr } v)\}, \text{None}, h, (\text{tl } \text{stk}, \text{loc}, C_0, M_0, \text{pc} + 1) \\
\# \text{frs}), \\
\quad (\{\text{UnlockFail} \rightarrow \text{the-Addr } v\}, [\text{execute.addr-of-sys-xcpt IllegalMonitorState}], h, (\text{stk}, \text{loc}, C_0, \\
M_0, \text{pc}) \# \text{frs}))\} \\
\langle \text{proof} \rangle
\end{aligned}$$

definition $\text{exec} :: 'addr \text{ jvm-prog} \Rightarrow 'thread\text{-id} \Rightarrow ('addr, 'heap) \text{ jvm-state} \Rightarrow ('addr, 'thread\text{-id}, 'heap) \text{ jvm-ta-state set}$

where $\text{exec} = \text{execute.exec}$

lemma exec-code :

$$\begin{aligned}
& \text{exec } P \text{ t } (x\text{cp}, h, []) = \{\} \\
& \text{exec } P \text{ t } (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs}) = \text{exec-instr } (\text{instrs-of } P \text{ C M ! pc}) \text{ P t h stk loc C M} \\
& \text{pc frs} \\
& \text{exec } P \text{ t } ([a], h, \text{fr} \# \text{frs}) = \{(\varepsilon, \text{execute.exception-step } P \text{ a h fr frs})\} \\
& \langle \text{proof} \rangle
\end{aligned}$$

definition *exec-1* ::

'addr jvm-prog \Rightarrow 'thread-id \Rightarrow ('addr, 'heap) jvm-state
 \Rightarrow (('addr, 'thread-id, 'heap) jvm-thread-action \times ('addr, 'heap) jvm-state) Predicate.pred
where *exec-1* P t σ = pred-of-set (*exec* P t σ)

lemma *exec-1I*: *execute.exec-1* P t σ ta σ' \Longrightarrow Predicate.eval (*exec-1* P t σ) (ta, σ')
 <proof>

lemma *exec-1E*:

assumes Predicate.eval (*exec-1* P t σ) (ta, σ')

obtains *execute.exec-1* P t σ ta σ'

<proof>

lemma *exec-1-eq* [*simp*]:

Predicate.eval (*exec-1* P t σ) (ta, σ') \longleftrightarrow *execute.exec-1* P t σ ta σ'

<proof>

lemma *exec-1-eq'*:

Predicate.eval (*exec-1* P t σ) = (λ (ta, σ'). *execute.exec-1* P t σ ta σ')

<proof>

end

lemmas [*code*] =

JVM-heap-execute.exec-instr-code

JVM-heap-base.exception-step.simps

JVM-heap-execute.exec-code

JVM-heap-execute.exec-1-def

end

theory JVM-Execute

imports

SC-Schedulers

JVMExec-Execute

../BV/BVProgressThreaded

begin

abbreviation *sc-heap-read-cset* :: heap \Rightarrow addr \Rightarrow addr-loc \Rightarrow addr val set

where *sc-heap-read-cset* h ad al \equiv set-of-pred (*sc-heap-read-i-i-i-o* h ad al)

abbreviation *sc-heap-write-cset* :: heap \Rightarrow addr \Rightarrow addr-loc \Rightarrow addr val \Rightarrow heap set

where *sc-heap-write-cset* h ad al v \equiv set-of-pred (*sc-heap-write-i-i-i-i-o* h ad al v)

interpretation *sc*:

JVM-heap-execute

addr2thread-id

thread-id2addr

sc-spurious-wakeups

sc-empty

sc-allocate P

sc-typeof-addr

sc-heap-read-cset

sc-heap-write-cset
rewrites $\bigwedge h \text{ ad al } v. v \in \text{sc-heap-read-cset } h \text{ ad al} \equiv \text{sc-heap-read } h \text{ ad al } v$
and $\bigwedge h \text{ ad al } v \text{ h}'. h' \in \text{sc-heap-write-cset } h \text{ ad al } v \equiv \text{sc-heap-write } h \text{ ad al } v \text{ h}'$
for P
 $\langle \text{proof} \rangle$

interpretation *sc-execute*:

JVM-conf-read
addr2thread-id
thread-id2addr
sc-spurious-wakeups
sc-empty
sc-allocate P
sc-typeof-addr
sc-heap-read
sc-heap-write
sc-hconf P
for P
 $\langle \text{proof} \rangle$

fun *sc-mexec* ::

$\text{addr } \text{jvm-prog} \Rightarrow \text{thread-id} \Rightarrow (\text{addr } \text{jvm-thread-state} \times \text{heap})$
 $\Rightarrow ((\text{addr}, \text{thread-id}, \text{heap}) \text{ jvm-thread-action} \times \text{addr } \text{jvm-thread-state} \times \text{heap}) \text{ Predicate.pred}$
where
 $\text{sc-mexec } P \text{ t } ((\text{xcp}, \text{frs}), \text{h}) =$
 $\text{sc.exec-1 } (\text{TYPE}(\text{addr } \text{jvm-method})) \text{ P P t } (\text{xcp}, \text{h}, \text{frs}) \gg (\lambda(\text{ta}, \text{xcp}, \text{h}, \text{frs}). \text{Predicate.single } (\text{ta},$
 $(\text{xcp}, \text{frs}), \text{h}))$

abbreviation *sc-jvm-start-state-refine* ::

$\text{addr } \text{jvm-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{addr val list} \Rightarrow$
 $(\text{addr}, \text{thread-id}, \text{heap}, (\text{thread-id}, (\text{addr } \text{jvm-thread-state}) \times \text{addr released-locks}) \text{ rm}, (\text{thread-id}, \text{addr}$
 $\text{wait-set-status}) \text{ rm}, \text{thread-id } \text{rs}) \text{ state-refine}$

where

$\text{sc-jvm-start-state-refine} \equiv$
 $\text{sc-start-state-refine } (\text{rm-empty } ()) \text{ rm-update } (\text{rm-empty } ()) (\text{rs-empty } ()) (\lambda C \text{ M } \text{Ts } \text{T } (\text{mxs}, \text{mxl0},$
 $\text{b}) \text{ vs. } (\text{None}, [([], \text{Null } \# \text{vs } @ \text{replicate } \text{mxl0 } \text{undefined-value}, \text{C}, \text{M}, \text{0})])$

abbreviation *sc-jvm-state-invar* :: $\text{addr } \text{jvm-prog} \Rightarrow \text{ty}_P \Rightarrow (\text{addr}, \text{thread-id}, \text{addr } \text{jvm-thread-state}, \text{heap}, \text{addr})$
state set

where $\text{sc-jvm-state-invar } P \Phi \equiv \{s. \text{sc-execute.correct-state-ts } P \Phi (\text{thr } s) (\text{shr } s)\}$

lemma *eval-sc-mexec*:

$(\lambda t \text{ xm } \text{ta } \text{x}'\text{m}'. \text{Predicate.eval } (\text{sc-mexec } P \text{ t } \text{xm}) (\text{ta}, \text{x}'\text{m}')) =$
 $(\lambda t ((\text{xcp}, \text{frs}), \text{h}) \text{ta } ((\text{xcp}', \text{frs}'), \text{h}'). \text{sc.execute.exec-1 } (\text{TYPE}(\text{addr } \text{jvm-method})) \text{ P P t } (\text{xcp}, \text{h},$
 $\text{frs}) \text{ta } (\text{xcp}', \text{h}', \text{frs}'))$
 $\langle \text{proof} \rangle$

lemma *sc-jvm-start-state-invar*:

assumes $\text{wf-jvm-prog}_\Phi \text{ P}$
and $\text{sc-wf-start-state } P \text{ C M vs}$
shows $\text{sc-state-}\alpha (\text{sc-jvm-start-state-refine } P \text{ C M vs}) \in \text{sc-jvm-state-invar } P \Phi$
 $\langle \text{proof} \rangle$

9.9.2 Round-robin scheduler

interpretation *JVM-rr*:

sc-round-robin-base
JVM-final sc-mexec P convert-RA Jinja-output
for *P*
 ⟨*proof*⟩

definition *sc-rr-JVM-start-state* :: $\text{nat} \Rightarrow 'm \text{ prog} \Rightarrow \text{thread-id} \text{ fifo} \text{ round-robin}$
where *sc-rr-JVM-start-state* $n0 P = \text{JVM-rr.round-robin-start } n0 (\text{sc-start-tid } P)$

definition *exec-JVM-rr* ::

$\text{nat} \Rightarrow \text{addr} \text{ jvm-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{addr} \text{ val list} \Rightarrow$
 $(\text{thread-id} \times (\text{addr}, \text{thread-id}) \text{ obs-event list},$
 $(\text{addr}, \text{thread-id}) \text{ locks} \times ((\text{thread-id}, \text{addr} \text{ jvm-thread-state} \times \text{addr} \text{ released-locks}) \text{ rm} \times \text{heap}) \times$
 $(\text{thread-id}, \text{addr} \text{ wait-set-status}) \text{ rm} \times \text{thread-id} \text{ rs}) \text{ tllist}$

where

exec-JVM-rr $n0 P C M vs = \text{JVM-rr.exec } P n0 (\text{sc-rr-JVM-start-state } n0 P) (\text{sc-jvm-start-state-refine } P C M vs)$

interpretation *JVM-rr*:

sc-round-robin
JVM-final sc-mexec P convert-RA Jinja-output
for *P*
 ⟨*proof*⟩

lemma *JVM-rr*:

assumes *wf-jvm-prog* ΦP
shows
sc-scheduler
JVM-final (sc-mexec P) convert-RA
 $(\text{JVM-rr.round-robin } P n0) (\text{pick-wakeup-via-sel } (\lambda s P. \text{rm-sel } s (\lambda(k,v). P k v))) \text{JVM-rr.round-robin-invar}$
 $(\text{sc-jvm-state-invar } P \Phi)$
 ⟨*proof*⟩

9.9.3 Random scheduler

interpretation *JVM-rnd*:

sc-random-scheduler-base
JVM-final sc-mexec P convert-RA Jinja-output
for *P*
 ⟨*proof*⟩

definition *sc-rnd-JVM-start-state* :: $\text{Random.seed} \Rightarrow \text{random-scheduler}$

where *sc-rnd-JVM-start-state* $\text{seed} = \text{seed}$

definition *exec-JVM-rnd* ::

$\text{Random.seed} \Rightarrow \text{addr} \text{ jvm-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{addr} \text{ val list} \Rightarrow$
 $(\text{thread-id} \times (\text{addr}, \text{thread-id}) \text{ obs-event list},$
 $(\text{addr}, \text{thread-id}) \text{ locks} \times ((\text{thread-id}, \text{addr} \text{ jvm-thread-state} \times \text{addr} \text{ released-locks}) \text{ rm} \times \text{heap}) \times$
 $(\text{thread-id}, \text{addr} \text{ wait-set-status}) \text{ rm} \times \text{thread-id} \text{ rs}) \text{ tllist}$

where *exec-JVM-rnd* $\text{seed } P C M vs = \text{JVM-rnd.exec } P (\text{sc-rnd-JVM-start-state } \text{seed}) (\text{sc-jvm-start-state-refine } P C M vs)$

interpretation *JVM-rnd*:

```

sc-random-scheduler
  JVM-final sc-mexec P convert-RA Jinja-output
for P
⟨proof⟩

```

lemma *JVM-rnd*:

```

assumes wf-jvm-prog $\Phi$  P
shows
  sc-scheduler
  JVM-final (sc-mexec P) convert-RA
  (JVM-rnd.random-scheduler P) (pick-wakeup-via-sel ( $\lambda s P. rm-sel s (\lambda(k,v). P k v)$ )) ( $\lambda- -. True$ )
  (sc-jvm-state-invar P  $\Phi$ )
⟨proof⟩

```

⟨*ML*⟩

end

9.10 String representation of types

theory *ToString* **imports**

```

  ../J/Expr
  ../JVM/JVMInstructions

```

```

  ../Basic/JT-ICF

```

begin

class *toString* =

```

  fixes toString :: 'a  $\Rightarrow$  String.literal

```

instantiation *bool* :: *toString* **begin**

definition [*code*]: *toString b* = (*case b of True \Rightarrow STR "True" | False \Rightarrow STR "False"*)

instance ⟨*proof*⟩

end

instantiation *char* :: *toString* **begin**

definition [*code*]: *toString (c :: char)* = *String.implode [c]*

instance ⟨*proof*⟩

end

instantiation *String.literal* :: *toString* **begin**

definition [*code*]: *toString (s :: String.literal)* = *s*

instance ⟨*proof*⟩

end

fun *list-toString* :: *String.literal* \Rightarrow 'a :: *toString list* \Rightarrow *String.literal list*

where

```

  list-toString sep [] = []
| list-toString sep [x] = [toString x]
| list-toString sep (x#xs) = toString x # sep # list-toString sep xs

```

instantiation *list* :: (*toString*) *toString* **begin**

definition [*code*]:

```

  toString (xs :: 'a list) = sum-list (STR "[" # list-toString (STR ",") xs @ [STR "]" )
instance <proof>
end

```

definition *digit-toString* :: *int* ⇒ *String.literal*

where

```

digit-toString k = (if k = 0 then STR "0"
  else if k = 1 then STR "1"
  else if k = 2 then STR "2"
  else if k = 3 then STR "3"
  else if k = 4 then STR "4"
  else if k = 5 then STR "5"
  else if k = 6 then STR "6"
  else if k = 7 then STR "7"
  else if k = 8 then STR "8"
  else if k = 9 then STR "9"
  else undefined)

```

function *int-toString* :: *int* ⇒ *String.literal list*

where

```

int-toString n =
  (if n < 0 then STR "-" # int-toString (- n)
  else if n < 10 then [digit-toString n]
  else int-toString (n div 10) @ [digit-toString (n mod 10)])

```

<proof>

termination <proof>

instantiation *int* :: *toString begin*

definition [code]: *toString i* = *sum-list (int-toString i)*

instance <proof>

end

instantiation *nat* :: *toString begin*

definition [code]: *toString n* = *toString (int n)*

instance <proof>

end

instantiation *option* :: (*toString*) *toString begin*

primrec *toString-option* :: 'a *option* ⇒ *String.literal* **where**

```

  toString None = STR "None"

```

```

| toString (Some a) = sum-list [STR "Some (" , toString a, STR ")"]

```

instance <proof>

end

instantiation *finfun* :: ({*toString*, *card-UNIV*, *equal*, *linorder*}, *toString*) *toString begin*

definition [code]:

```

  toString (f :: 'a ⇒f 'b) =

```

```

  sum-list

```

```

  (STR "("

```

```

  # toString (finfun-default f)

```

```

  # concat (map (λx. [STR ", " , toString x, STR "|->" , toString (f $ x)]) (finfun-to-list f))

```

```

  @ [STR ")"]

```

instance <proof>

end

```

instantiation word :: (len) toString begin
definition [code]: toString (w :: 'a word) = toString (sint w)
instance ⟨proof⟩
end

```

```

instantiation fun :: (type, type) toString begin
definition [code]: toString (f :: 'a ⇒ 'b) = STR "fn"
instance ⟨proof⟩
end

```

```

instantiation val :: (toString) toString begin
fun toString-val :: ('a :: toString) val ⇒ String.literal
where
  toString Unit = STR "Unit"
| toString Null = STR "Null"
| toString (Bool b) = sum-list [STR "Bool ", toString b]
| toString (Intg i) = sum-list [STR "Intg ", toString i]
| toString (Addr a) = sum-list [STR "Addr ", toString a]
instance ⟨proof⟩
end

```

```

instantiation ty :: toString begin
primrec toString-ty :: ty ⇒ String.literal
where
  toString Void = STR "Void"
| toString Boolean = STR "Boolean"
| toString Integer = STR "Integer"
| toString NT = STR "NT"
| toString (Class C) = sum-list [STR "Class ", toString C]
| toString (T[]) = sum-list [toString T, STR "[]"]
instance ⟨proof⟩
end

```

```

instantiation bop :: toString begin
primrec toString-bop :: bop ⇒ String.literal where
  toString Eq = STR "=="
| toString NotEq = STR "!="
| toString LessThan = STR "<"
| toString LessOrEqual = STR "<="
| toString GreaterThan = STR ">"
| toString GreaterOrEqual = STR ">="
| toString Add = STR "+"
| toString Subtract = STR "-"
| toString Mult = STR "*"
| toString Div = STR "/"
| toString Mod = STR "%"
| toString BinAnd = STR "&"
| toString BinOr = STR "|"
| toString BinXor = STR "^"
| toString ShiftLeft = STR "<<"
| toString ShiftRightZeros = STR ">>"
| toString ShiftRightSigned = STR ">>>"
instance ⟨proof⟩

```

end

instantiation *addr-loc* :: *toString* **begin**

primrec *toString-addr-loc* :: *addr-loc* ⇒ *String.literal* **where**

toString (*CField C F*) = *sum-list* [*STR* "*CField* ", *F*, *STR* "{", *C*, *STR* "}"]
 | *toString* (*ACell n*) = *sum-list* [*STR* "*ACell* ", *toString n*]

instance ⟨*proof*⟩

end

instantiation *hType* :: *toString* **begin**

fun *toString-hType* :: *hType* ⇒ *String.literal* **where**

toString (*Class-type C*) = *C*
 | *toString* (*Array-type T n*) = *sum-list* [*toString T*, *STR* "[", *toString n*, *STR* "]"]

instance ⟨*proof*⟩

end

instantiation *obs-event* :: (*toString*, *toString*) *toString* **begin**

primrec *toString-obs-event* :: (*'a* :: *toString*, *'b* :: *toString*) *obs-event* ⇒ *String.literal*

where

toString (*ExternalCall ad M vs v*) =
sum-list [*STR* "*ExternalCall* ", *M*, *STR* "((", *toString vs*, *STR* ") = ", *toString v*]
 | *toString* (*ReadMem ad al v*) =
sum-list [*STR* "*ReadMem* ", *toString ad*, *STR* "@", *toString al*, *STR* "=", *toString v*]
 | *toString* (*WriteMem ad al v*) =
sum-list [*STR* "*WriteMem* ", *toString ad*, *STR* "@", *toString al*, *STR* "=", *toString v*]
 | *toString* (*NewHeapElem ad hT*) = *sum-list* [*STR* "*Allocate* ", *toString ad*, *STR* ":", *toString hT*]
 | *toString* (*ThreadStart t*) = *sum-list* [*STR* "*ThreadStart* ", *toString t*]
 | *toString* (*ThreadJoin t*) = *sum-list* [*STR* "*ThreadJoin* ", *toString t*]
 | *toString* (*SyncLock ad*) = *sum-list* [*STR* "*SyncLock* ", *toString ad*]
 | *toString* (*SyncUnlock ad*) = *sum-list* [*STR* "*SyncUnlock* ", *toString ad*]
 | *toString* (*ObsInterrupt t*) = *sum-list* [*STR* "*Interrupt* ", *toString t*]
 | *toString* (*ObsInterrupted t*) = *sum-list* [*STR* "*Interrupted* ", *toString t*]

instance ⟨*proof*⟩

end

instantiation *prod* :: (*toString*, *toString*) *toString* **begin**

definition *toString* = (λ(*a*, *b*). *sum-list* [*STR* "((", *toString a*, *STR* ", ", *toString b*, *STR* ")")])

instance ⟨*proof*⟩

end

instantiation *fmod-ext* :: (*toString*) *toString* **begin**

definition *toString fd* = *sum-list* [*STR* "{|volatile=", *toString (volatile fd)*, *STR* ", ", *toString (fmod.more fd)*, *STR* "|}"]

instance ⟨*proof*⟩

end

instantiation *unit* :: *toString* **begin**

definition *toString* (*u* :: *unit*) = *STR* "()"

instance ⟨*proof*⟩

end

instantiation *exp* :: (*toString*, *toString*, *toString*) *toString* **begin**

fun *toString-exp* :: (*'a* :: *toString*, *'b* :: *toString*, *'c* :: *toString*) *exp* ⇒ *String.literal*

where

```

toString (new C) = sum-list [STR "new ", C]
| toString (newArray T e) = sum-list [STR "new ", toString T, STR "[", toString e, STR "]"']
| toString (Cast T e) = sum-list [STR "("', toString T, STR ")"' ("', toString e, STR ")'"']
| toString (InstanceOf e T) = sum-list [STR "("', toString e, STR ")"' instanceof "', toString T]
| toString (Val v) = sum-list [STR "Val ("', toString v, STR ")'"']
| toString (e1 «bop» e2) = sum-list [STR "("', toString e1, STR ")"' ", toString bop, STR "("', toString
e2, STR ")'"']
| toString (Var V) = sum-list [STR "Var ", toString V]
| toString (V := e) = sum-list [toString V, STR " := ("', toString e, STR ")'"']
| toString (AAcc a i) = sum-list [STR "("', toString a, STR ")"'["', toString i, STR "]"']
| toString (AAss a i e) = sum-list [STR "("', toString a, STR ")"'["', toString i, STR "]" := ("', toString
e, STR ")'"']
| toString (ALen a) = sum-list [STR "("', toString a, STR ")"'.length'"']
| toString (FAcc e F D) = sum-list [STR "("', toString e, STR ")"'.", F, STR "{", D, STR "}'"']
| toString (FAss e F D e') = sum-list [STR "("', toString e, STR ")"'.", F, STR "{", D, STR "} :=
('', toString e', STR ")'"']
| toString (Call e M es) = sum-list ([STR "("', toString e, STR ")"'.", M, STR "("'" @ map toString es
@ [STR ")'"']
| toString (Block V T vo e) = sum-list ([STR "{", toString V, STR ":", toString T] @ (case vo of
None => [] | Some v => [STR "= ", toString v]) @ [STR "; ", toString e, STR ")'"']
| toString (Synchronized V e e') = sum-list [STR "synchronized-", toString V, STR "-(", toString e,
STR ") {", toString e', STR "}'"']
| toString (InSynchronized V ad e) = sum-list [STR "insynchronized-", toString V, STR "-(", toString
ad, STR ") {", toString e, STR "}'"']
| toString (e;;e') = sum-list [toString e, STR "; ", toString e']
| toString (if (e) e' else e'') = sum-list [STR "if ("', toString e, STR ") { ", toString e', STR " } else
{ ", toString e'', STR "}'"']
| toString (while (e) e') = sum-list [STR "while ("', toString e, STR ") { ", toString e', STR " }'"']
| toString (throw e) = sum-list [STR "throw ("', toString e, STR ")'"']
| toString (try e catch(C V) e') = sum-list [STR "try { ", toString e, STR " } catch ("', C, STR "
", toString V, STR ") { ", toString e', STR " }'"']
instance <proof>
end

```

instantiation *instr* :: (toString) toString **begin**

primrec toString-instr :: 'a instr => String.literal **where**

```

toString (Load i) = sum-list [STR "Load ("', toString i, STR ")'"']
| toString (Store i) = sum-list [STR "Store ("', toString i, STR ")'"']
| toString (Push v) = sum-list [STR "Push ("', toString v, STR ")'"']
| toString (New C) = sum-list [STR "New ", toString C]
| toString (NewArray T) = sum-list [STR "NewArray ", toString T]
| toString ALoad = STR "ALoad"
| toString AStore = STR "AStore"
| toString ALength = STR "ALength"
| toString (Getfield F D) = sum-list [STR "Getfield ", toString F, STR " ", toString D]
| toString (Putfield F D) = sum-list [STR "Putfield ", toString F, STR " ", toString D]
| toString (Checkcast T) = sum-list [STR "Checkcast ", toString T]
| toString (InstanceOf T) = sum-list [STR "InstanceOf ", toString T]
| toString (Invoke M n) = sum-list [STR "Invoke ", toString M, STR " ", toString n]
| toString Return = STR "Return"
| toString Pop = STR "Pop"
| toString Dup = STR "Dup"
| toString Swap = STR "Swap"
| toString (BinOpInstr bop) = sum-list [STR "BinOpInstr ", toString bop]

```

```

| toString (Goto i) = sum-list [STR "Goto ", toString i]
| toString (IfFalse i) = sum-list [STR "IfFalse ", toString i]
| toString ThrowExc = STR "ThrowExc"
| toString MEnter = STR "monitorenter"
| toString MExit = STR "monitorexit"
instance <proof>
end

instantiation trie :: (toString, toString) toString begin
definition [code]: toString (t :: ('a, 'b) trie) = toString (tm-to-list t)
instance <proof>
end

instantiation rbt :: ({toString, linorder}, toString) toString begin
definition [code]:
  toString (t :: ('a, 'b) rbt) =
    sum-list (list-toString (STR "◁") (rm-to-list t))
instance <proof>
end

instantiation assoc-list :: (toString, toString) toString begin
definition [code]: toString = toString ∘ Assoc-List.impl-of
instance <proof>
end

code-printing
  class-instance String.literal :: toString → (Haskell) –

end

```

9.11 Setup for converter Java2Jinja

```

theory Java2Jinja
imports
  Code-Generation
  ToString
begin

code-identifier
  code-module Java2Jinja → (SML) Code-Generation

definition j-Program :: addr J-mb cdecl list ⇒ addr J-prog
where j-Program = Program

export-code wf-J-prog' j-Program in SML file <JWellForm.ML>

  Functions for extracting calls to the native print method

definition purge where
  ∧run.
  purge run =
  lmap (λobs. case obs of ExternalCall - - (Cons (Intg i) -) v ⇒ i)
  (lfilter
    (λobs. case obs of ExternalCall - M (Cons (Intg i) Nil) - ⇒ M = print | - ⇒ False)
    (lconcat (lmap (llist-of ∘ snd) (llist-of-tlrun))))

```


Various other functions

```
instantiation heapobj :: toString begin
primrec toString-heapobj :: heapobj  $\Rightarrow$  String.literal where
  toString (Obj C fs) = sum-list [STR "(Obj ", toString C, STR ", ", toString fs, STR ")"]
| toString (Arr T si fs el) =
  sum-list [STR "([", toString si, STR "]", toString T, STR ", ", toString fs, STR ", ", toString
(map snd (rm-to-list el)), STR ")"]
instance <proof>
end
```

```
definition case-llist' where case-llist' = case-llist
definition case-tllist' where case-tllist' = case-tllist
definition terminal' where terminal' = terminal
definition llist-of-tllist' where llist-of-tllist' = llist-of-tllist
definition thr' where thr' = thr
definition shr' where shr' = shr
```

```
definition heap-toString :: heap  $\Rightarrow$  String.literal
where heap-toString = toString
```

```
definition thread-toString :: (thread-id, (addr expr  $\times$  addr locals)  $\times$  (addr  $\Rightarrow$  f nat)) rbt  $\Rightarrow$  String.literal
where thread-toString = toString
```

```
definition thread-toString' :: (thread-id, addr jvm-thread-state'  $\times$  (addr  $\Rightarrow$  f nat)) rbt  $\Rightarrow$  String.literal
where thread-toString' = toString
```

```
definition trace-toString :: thread-id  $\times$  (addr, thread-id) obs-event list  $\Rightarrow$  String.literal
where trace-toString = toString
```

code-identifier

```
code-module Cardinality  $\rightarrow$  (SML) Set
| code-module Code-Cardinality  $\rightarrow$  (SML) Set
| code-module Conditionally-Complete-Lattices  $\rightarrow$  (SML) Set
| code-module List  $\rightarrow$  (SML) Set
| code-module Predicate  $\rightarrow$  (SML) Set
| code-module Parity  $\rightarrow$  (SML) Bit-Operations
| type-class semiring-parity  $\rightarrow$  (SML) Bit-Operations.semiring-parity
| class-instance int :: semiring-parity  $\rightarrow$  (SML) Bit-Operations.semiring-parity-int
| class-instance int :: ring-parity  $\rightarrow$  (SML) Bit-Operations.semiring-parity-int
| constant member-i-i  $\rightarrow$  (SML) Set.member-i-i
```

export-code

```
wf-J-prog' exec-J-rr exec-J-rnd
j-Program
purge case-llist' case-tllist' terminal' llist-of-tllist'
thr' shr' heap-toString thread-toString trace-toString
in SML
file <J-Execute.ML>
```

```
definition j2jvm :: addr J-prog  $\Rightarrow$  addr jvm-prog where j2jvm = J2JVM
```

export-code

```
wf-jvm-prog' exec-JVM-rr exec-JVM-rnd j2jvm
j-Program
```

```
purge case-llist' case-tllist' terminal' llist-of-tllist'  
thr' shr' heap-toString thread-toString' trace-toString  
in SML  
file ⟨JVM-Execute2.ML⟩
```

```
end  
theory Execute-Main  
imports  
  SC-Schedulers  
  PCompilerRefine  
  Code-Generation  
  JVM-Execute  
  Java2Jinja  
begin  
  
end
```

Chapter 10

Examples

10.1 Apprentice challenge

```
theory ApprenticeChallenge
imports
  ../Execute/Code-Generation
begin
```

This theory implements the apprentice challenge by Porter and Moore [5].

```
definition ThreadC :: addr J-mb cdecl
where
  ThreadC =
    (Thread, Object, [],
     [(run, [], Void, [([], unit)]),
      (start, [], Void, Native),
      (join, [], Void, Native),
      (interrupt, [], Void, Native),
      (isInterrupted, [], Boolean, Native)])
```

```
definition Container :: cname
where Container = STR "Container"
```

```
definition ContainerC :: addr J-mb cdecl
where ContainerC = (Container, Object, [(STR "counter", Integer, (volatile=False))], [])
```

```
definition String :: cname
where String = STR "String"
```

```
definition StringC :: addr J-mb cdecl
where
  StringC = (String, Object, [], [])
```

```
definition Job :: cname
where Job = STR "Job"
```

```
definition JobC :: addr J-mb cdecl
where
  JobC =
    (Job, Thread, [(STR "objref", Class Container, (volatile=False))],
     [(STR "incr", [], Class Job, [([],
```

```

    sync(Var (STR "objref"))
      ((Var (STR "objref"))•STR "counter"{STR ""} := ((Var (STR "objref"))•STR "counter"{STR
""} «Add» Val (Intg 1))));
    Var this));
  (STR "setref", [Class Container], Void, [(STR "o"],
  LAss (STR "objref") (Var (STR "o"))),
  (run, [], Void, [([],
  while (true) (Var this•STR "incr"([])))]
  ])

```

definition *Apprentice* :: *cname*
where *Apprentice* = STR "Apprentice"

definition *ApprenticeC* :: *addr J-mb cdecl*

where

```

  ApprenticeC =
  (Apprentice, Object, [],
  [(STR "main", [Class String[]], Void, [(STR "args"],
  {STR "container":Class Container=None;
  (STR "container" := new Container);;
  (while (true)
  {STR "job":Class Job=None;
  (STR "job" := new Job);;
  (Var (STR "job")•STR "setref"([Var (STR "container")]));;
  (Var (STR "job")•Type.start([]))
  }
  )
  }))]])

```

definition *ApprenticeChallenge*

where

ApprenticeChallenge = Program (SystemClasses @ [StringC, ThreadC, ContainerC, JobC, ApprenticeC])

definition *ApprenticeChallenge-annotated*

where *ApprenticeChallenge-annotated* = *annotate-prog-code* *ApprenticeChallenge*

lemma *wf-J-prog ApprenticeChallenge-annotated*

<proof>

lemmas [*code-unfold*] =

Container-def Job-def String-def Apprentice-def

definition *main* :: *String.literal* **where** *main* = STR "main"

<ML>

end

10.2 Buffer example

theory *BufferExample* **imports**

../Execute/Code-Generation

begin

definition *ThreadC* :: *addr J-mb cdecl*

where

```
ThreadC =
(Thread, Object, [],
 [(run, [], Void, [([], unit)]),
 (start, [], Void, Native),
 (join, [], Void, Native),
 (interrupt, [], Void, Native),
 (isInterrupted, [], Boolean, Native)])
```

definition *IntegerC* :: *addr J-mb cdecl*

where *IntegerC* = (*STR* "Integer", Object, [(*STR* "value", Integer, (volatile=False))], [])

definition *Buffer* :: *cname*

where *Buffer* = *STR* "Buffer"

definition *BufferC* :: *addr J-mb cdecl*

where

```
BufferC =
(Buffer, Object,
 [(STR "buffer", Class Object[], (volatile=False)),
 (STR "front", Integer, (volatile=False)),
 (STR "back", Integer, (volatile=False)),
 (STR "size", Integer, (volatile=False))],
 [(STR "constructor", [Integer], Void, [(STR "size",
 (STR "buffer" := newA (Class Object)[Var (STR "size")]);
 (STR "front" := Val (Intg 0));
 (STR "back" := Val (Intg (- 1)));
 (Var this.(STR "size"){STR ""} := Val (Intg 0)))]),
 (STR "empty", [], Boolean, [([], sync(Var this) (Var (STR "size") «Eq» Val (Intg 0)))]),
 (STR "full", [], Boolean, [([],
 sync(Var this) (Var (STR "size") «Eq» ((Var (STR "buffer")).length)))]),
 (STR "get", [], Class Object, [([],
 sync(Var this) (
 (while (Var this.(STR "empty")())
 (try (Var this.wait()) catch(InterruptedException (STR "e") unit));
 (STR "size" := (Var (STR "size") «Subtract» Val (Intg 1))));
 {(STR "result"):Class Object=None;
 ((STR "result") := ((Var (STR "buffer"))[Var (STR "front")])];
 (STR "front" := (Var (STR "front") «Add» Val (Intg 1))];
 (if ((Var (STR "front") «Eq» ((Var (STR "buffer")).length))
 (STR "front" := Val (Intg 0))
 else unit));
 (Var this.notifyAll());
 Var (STR "result")
 }
 )])]),
 (STR "put", [Class Object], Void, [(STR "o",
 sync(Var this) (
 (while (Var this.STR "full")()
 (try (Var this.wait()) catch(InterruptedException STR "e") unit));
 (STR "back" := (Var (STR "back") «Add» Val (Intg 1))));
```

```

    (if (Var (STR "back") «Eq» ((Var (STR "buffer"))·length))
        (STR "back" := Val (Intg 0))
        else unit);;
    (AAss (Var (STR "buffer")) (Var (STR "back")) (Var (STR "o")));;
    (STR "size" := ((Var (STR "size")) «Add» Val (Intg 1)));;
    (Var this·notifyAll([]))
  )))
)

```

definition *Producer* :: *cname*
where *Producer* = STR "Producer"

definition *ProducerC* :: *int* ⇒ *addr J-mb cdecl*

where

```

  ProducerC n =
    (Producer, Thread, [(STR "buffer", Class Buffer, (volatile=False))],
    [(run, [], Void, [([],
      {STR "i":Integer=[Intg 0];
        while (Var (STR "i") «NotEq» Val (Intg (word-of-int n))) (
          (Var (STR "buffer"))·STR "put"({STR "temp":Class (STR "Integer")=None; (STR "temp"
:= new (STR "Integer"); ((FAss (Var (STR "temp")) (STR "value") (STR "")) (Var (STR "i")));
Var (STR "temp"))} )]);;
          STR "i" := (Var (STR "i") «Add» (Val (Intg 1))))
        }])])])

```

definition *Consumer* :: *cname*
where *Consumer* = STR "Consumer"

definition *ConsumerC* :: *int* ⇒ *addr J-mb cdecl*

where

```

  ConsumerC n =
    (Consumer, Thread, [(STR "buffer", Class Buffer, (volatile=False))],
    [(run, [], Void, [([],
      {STR "i":Integer=[Intg 0];
        while (Var (STR "i") «NotEq» Val (Intg (word-of-int n))) (
          {STR "o":Class Object=None;
            Seq (STR "o" := ((Var (STR "buffer"))·STR "get"([]))
              (STR "i" := (Var (STR "i") «Add» Val (Intg 1))))}
        }])])])

```

definition *String* :: *cname*
where *String* = STR "String"

definition *StringC* :: *addr J-mb cdecl*

where

```

  StringC = (String, Object, [], [])

```

definition *Test* :: *cname*
where *Test* = STR "Test"

definition *TestC* :: *addr J-mb cdecl*

where

```

  TestC =
    (Test, Object, [],

```

```

[[ (STR "main", [Class String[]], Void, [(STR "args"),
  {STR "b":Class Buffer=None; (STR "b" := new Buffer);;
  (Var (STR "b")·STR "constructor"([Val (Intg 10)]));;
  {STR "p":Class Producer=None; STR "p" := new Producer;;
  {STR "c":Class Consumer=None;
  (STR "c" := new Consumer);;
  (Var (STR "c")·STR "buffer"{STR ""} := Var (STR "b"));;
  (Var (STR "p")·STR "buffer"{STR ""} := Var (STR "b"));;
  (Var (STR "c")·Type.start([]));(Var (STR "p")·Type.start([]))
  }
  }
  }]]))

```

definition *BufferExample*

where

BufferExample n = Program (SystemClasses @ [ThreadC, StringC, IntegerC, BufferC, ProducerC n, ConsumerC n, TestC])

definition *BufferExample-annotated*

where

BufferExample-annotated n = annotate-prog-code (BufferExample n)

lemmas [*code-unfold*] =

*IntegerC-def Buffer-def Producer-def Consumer-def Test-def
String-def*

lemma *wf-J-prog (BufferExample-annotated 10)*

<proof>

definition *main* **where** *main = STR "main"*

definition *five :: int* **where** *five = 5*

<ML>

end

theory *Examples-Main*

imports

ApprenticeChallenge

BufferExample

begin

end

theory *JinjaThreads*

imports

Basic/Basic-Main

Common/Common-Main

J/J-Main

JVM/JVM-Main

BV/BV-Main

Compiler/Compiler-Main

MM/MM-Main

Execute/Execute-Main

Examples/Examples-Main

begin

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end

Bibliography

- [1] Andreas Lochbihler. Type safe nondeterminism - a formal semantics of Java threads. In *Proceedings of the 2008 International Workshop on Foundations of Object-Oriented Languages*, 2008.
- [2] Andreas Lochbihler. Verifying a compiler for Java threads. In Andrew D. Gordon, editor, *Programming Languages and Systems (ESOP 2010)*, volume 6012 of *Lecture Notes in Computer Science*, pages 427–447. Springer, 2010.
- [3] Andreas Lochbihler. Java and the Java memory model – a unified, machine-checked formalisation. In Helmut Seidl, editor, *Programming Languages and Systems*, volume 7211 of *Lecture Notes in Computer Science*, pages 493–513. Springer, 2012.
- [4] Andreas Lochbihler and Lukas Bulwahn. Animating the formalised semantics of a Java-like language. In Marko van Eekelen, Herman Geuvers, Julien Schmalz, and Freek Wiedijk, editors, *Interactive Theorem Proving (ITP 2011)*, volume 6898 of *Lecture Notes in Computer Science*, pages 216–232. Springer, 2011.
- [5] J Strother Moore and George Porter. The apprentice challenge. *ACM Trans. Program. Lang. Syst.*, 24(3):193–216, May 2002.